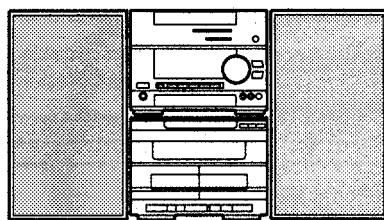


aiwa



XR-H100



CD STEREO SYSTEM

- BASIC TAPE MECHANISM : 2ZM-3MK2 PR2NM
- BASIC CD MECHANISM : 4ZG-1 WRNM
- TYPE: EZ

SYSTEM	AMPLIFIER/ TUNER	CASSETTE DECK/ CD PLAYER	SPEAKER	REMOTE CONTROLLER
XR-H100	RX-NH100	FD-NH100	SX-ANH100	RC-T501

- If requiring information about the CD mechanism, see service manual of 4ZG-1WR.
(S/M Code No. 09-965-128-10T)

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SPECIFICATIONS

STEREO RECEIVER RX-NH100

<FM tuner section>

Tuning range	87.5 MHz to 108 MHz
Usable sensitivity (IHF)	16.8 dBf
Antenna terminals	75 ohms (unbalanced)

<MW Tuner section>

Tuning range	531 kHz to 1602 kHz (9 kHz step)
Usable sensitivity	530 kHz to 1710 kHz (10 kHz step)
Antenna	350 μ V/m Loop antenna

<LW Tuner section>

Tuning range	144 kHz to 290 kHz
Usable sensitivity	1400 μ V/m Loop antenna

<Amplifier section>

Power output*	Rated: 100 W + 100 W (6 ohms, T.H.D. 1 %, 1 kHz/DIN 45500) Reference: 120 W + 120 W (6 ohms, T.H.D 10 %, 1 kHz/DIN 45324) DIN MUSIC POWER 200 W + 200 W
	200 W + 200 W

*without connecting to the SURROUND SPEAKERS

Total harmonic distortion	0.1 % (60 W, 1 kHz, 6 ohms, DIN AUDIO)
---------------------------	---

Inputs	VIDEO 1/MD IN: 200 mV (adjustable) VIDEO 2/AUX IN: 200 mV (adjustable)
--------	---

Outputs	MIC 1, MIC 2: 1 mV (10 kohms) REC OUT: 200 mV SUPER WOOFER: 2.6 V SPEAKERS: accept speakers of 6 ohms or more SURROUND SPEAKERS: accept speakers of 16 ohms or more PHONES (stereo jack): accepts headphones of 32 ohms or more
---------	--

<General>

Power requirements	230 V AC, 50Hz
Power consumption	140 W (System 160 W)
Dimensions of main unit (W x H x D)	260 x 199 x 333 mm
Weight of main unit	6.6 kg

STEREO CASSETTE DECK/COMPACT DISC PLAYER FD-NH100

<Cassette deck section>

Track format	4 tracks, 2 channels stereo
Frequency response	Metal tape: 50 Hz – 17000 Hz CrO_2 tape: 50 Hz – 16000 Hz Normal tape: 50 Hz – 15000 Hz
Signal-to-noise ratio	75 dB (Dolby C NR ON, Metal tape peak level)
Recording system	AC bias
Heads	Deck 1: Playhead x 1 Deck 2: Recording/playback/ erase head x 1

<Compact disc player section>

Laser	Semiconductor laser ($\lambda = 780 \text{ nm}$)
D-A converter	1 bit dual
Signal-to-noise ratio	85 dB (1 kHz, 0 dB)
Harmonic distortion	0.03% (1 kHz, 0 dB)
Wow and flutter	Unmeasurable

<General>

Dimensions (W x H x D)	260 x 204 X 320.2 mm
Weight	4 kg

<SPEAKER SYSTEM SX-ANH100>

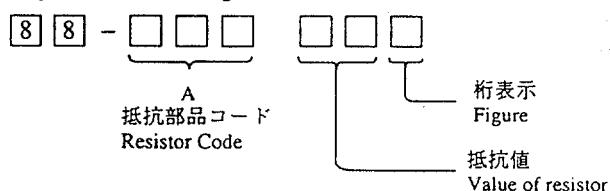
Cabinet type	4 way, bass reflex with surround speaker (magnetic shielded type)
Speakers	Woofer: 140 mm cone type Mid-range: 80 mm cone type Tweeter: 50 mm cone type Super tweeter: 20 mm ceramic type Surround speaker: 80 mm cone type Front speaker: 6 ohms Surround speaker: 16 ohms
Impedance	87 dB/W/m
Output sound pressure level	250 x 405 x 286 mm
Dimensions (W x H x D)	5 kg
Weight	

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- The word "BBE" and the "BBE symbol" are trademarks of BBE Sound, Inc.
Under license from BBE Sound, Inc.

REF. NO.	PART NO.	KANRI NO.	DESCRIPTION	REF. NO.	PART NO.	KANRI NO.	DESCRIPTION
C751	87-010-402-040		CAP, E 2.2-50 SME				AC2 C.B
C752	87-010-402-040		CAP, E 2.2-50 SME				△ PR101 87-A90-195-080 PROTECTOR, 7A 125V 491
C753	87-010-404-040		CAP, E 4.7-50 SME				△ PR102 87-A90-195-080 PROTECTOR, 7A 125V 491
C754	87-010-404-040		CAP, E 4.7-50 SME				△ PR105 87-026-682-080 PROTECTOR, 10A 60V491
C755	87-010-263-040		CAP, E 100-10 SME				△ PR106 87-026-682-080 PROTECTOR, 10A 60V491
C756	87-010-196-080		C-CAP, S 0.1-25 Z F				
C757	87-010-384-040		CAP, E 100-25 SME				
L601	87-005-481-080		COIL, 47UH J FLR50				
VR751	86-NT1-633-010		VR, 50KBX2 V-L20				
TRAY C.B							
S120	87-A90-095-080		SW, TACT EVQ11G04M				△ F101 87-035-191-010 FUSE, 3.15A 250V T218
S121	87-A90-095-080		SW, TACT EVQ11G04M				△ FC001 87-033-213-080 CLAMP, FUSE
S122	87-A90-095-080		SW, TACT EVQ11G04M				△ FC002 87-033-213-080 CLAMP, FUSE
S123	87-A90-095-080		SW, TACT EVQ11G04M				△ PT103 86-NT1-608-010 PT, 6NT1-E
S124	87-A90-095-080		SW, TACT EVQ11G04M				△ T001 87-A60-317-010 TERMINAL, 1P MSC
S125	87-A90-095-080		SW, TACT EVQ11G04M				△ T002 87-A60-317-010 TERMINAL, 1P MSC
S126	87-A90-095-080		SW, TACT EVQ11G04M				
S127	87-A90-095-080		SW, TACT EVQ11G04M				
S128	87-A90-095-080		SW, TACT EVQ11G04M				
S129	87-036-110-010		PUSH SWITCH				
S136	87-A90-095-080		SW, TACT EVQ11G04M				MOTOR C.B
S137	87-A90-095-080		SW, TACT EVQ11G04M				C210 87-010-263-040 CAP, E 100-10
S138	87-A90-095-080		SW, TACT EVQ11G04M				C211 87-010-263-040 CAP, E 100-10
S139	87-A90-095-080		SW, TACT EVQ11G04M				M107 87-045-383-010 MOT, M9I50T28-2
S140	87-A90-095-080		SW, TACT EVQ11G04M				
S141	87-A90-095-080		SW, TACT EVQ11G04M				
S144	87-036-110-010		PUSH SWITCH				

○ チップ抵抗部品コード / CHIP RESISTOR PART CODE

チップ抵抗部品コードの成り立ち
Chip Resistor Part Coding



チップ抵抗
Chip resistor

容量 Wattage	種類 Type	許容誤差 Tolerance	記号 Symbol	寸法 / Dimensions (mm)				抵抗コード : A Resistor Code: A
				外形 / Form	L	W	t	
1/16W	1608	±5%	CJ		1.6	0.8	0.45	108
1/10W	2125	±5%	CJ		2	1.25	0.45	118
1/8W	3216	±5%	CJ		3.2	1.6	0.55	128

TRANSISTOR ILLUSTRATION



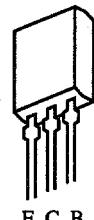
2SA1296GR
KTC3198GR



CSD1489B
2SA952
CSD655E



C2N5401



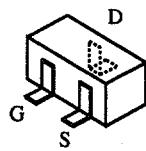
2SA935Q



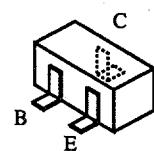
B C E



G D S



2SK2723

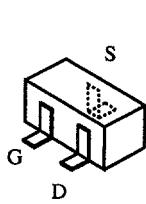


B E C

2SB1370
FN1016
FP1016

2SK2158

2SA1235F DTA144EK
2SC2714 DTA114YK
2SC3052F DTC114EK
CSD1306E DTA143XK
2SC2412 DTA124EK
DTA143EK DTA114EK
2SA1037 DTC114YK

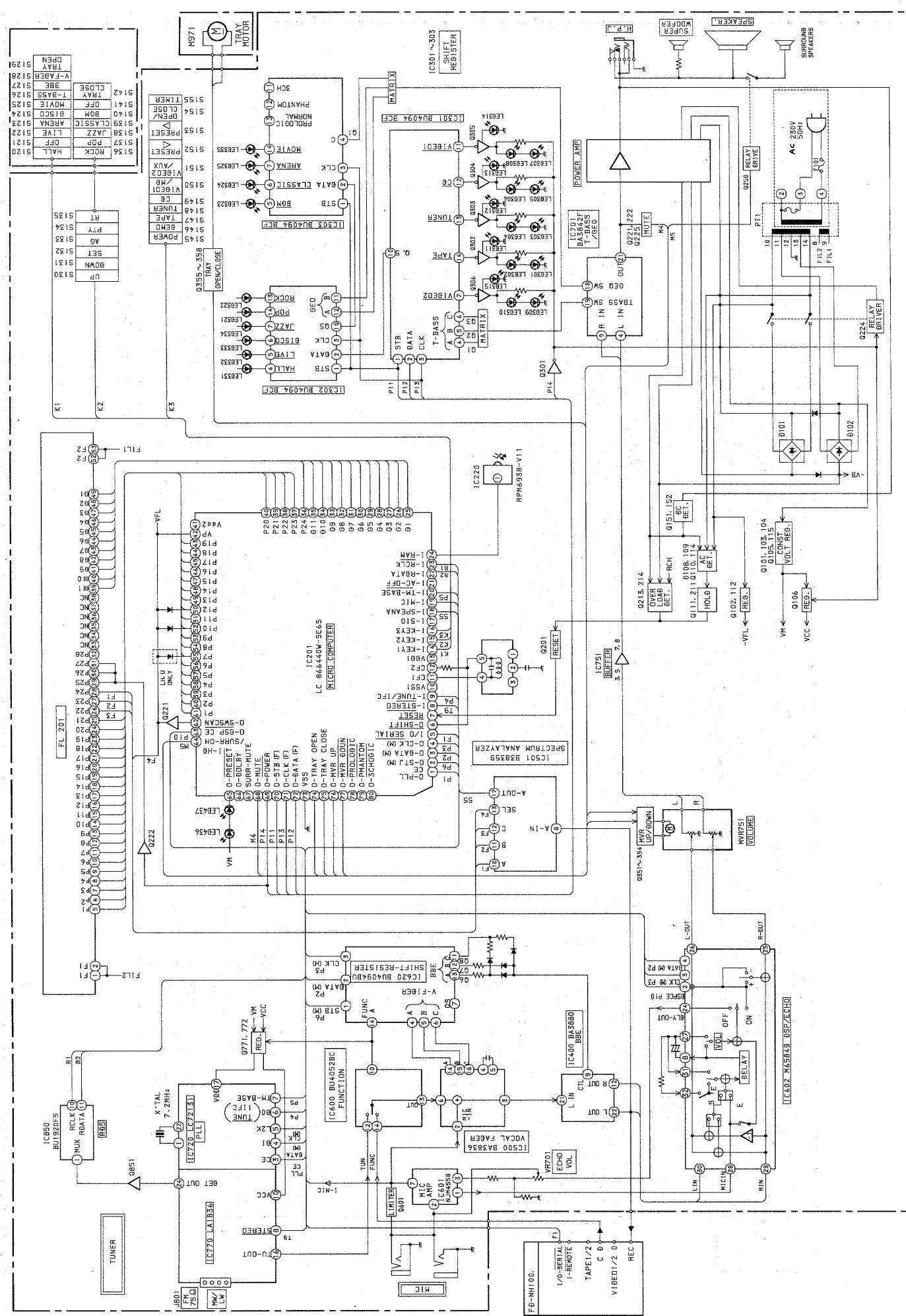


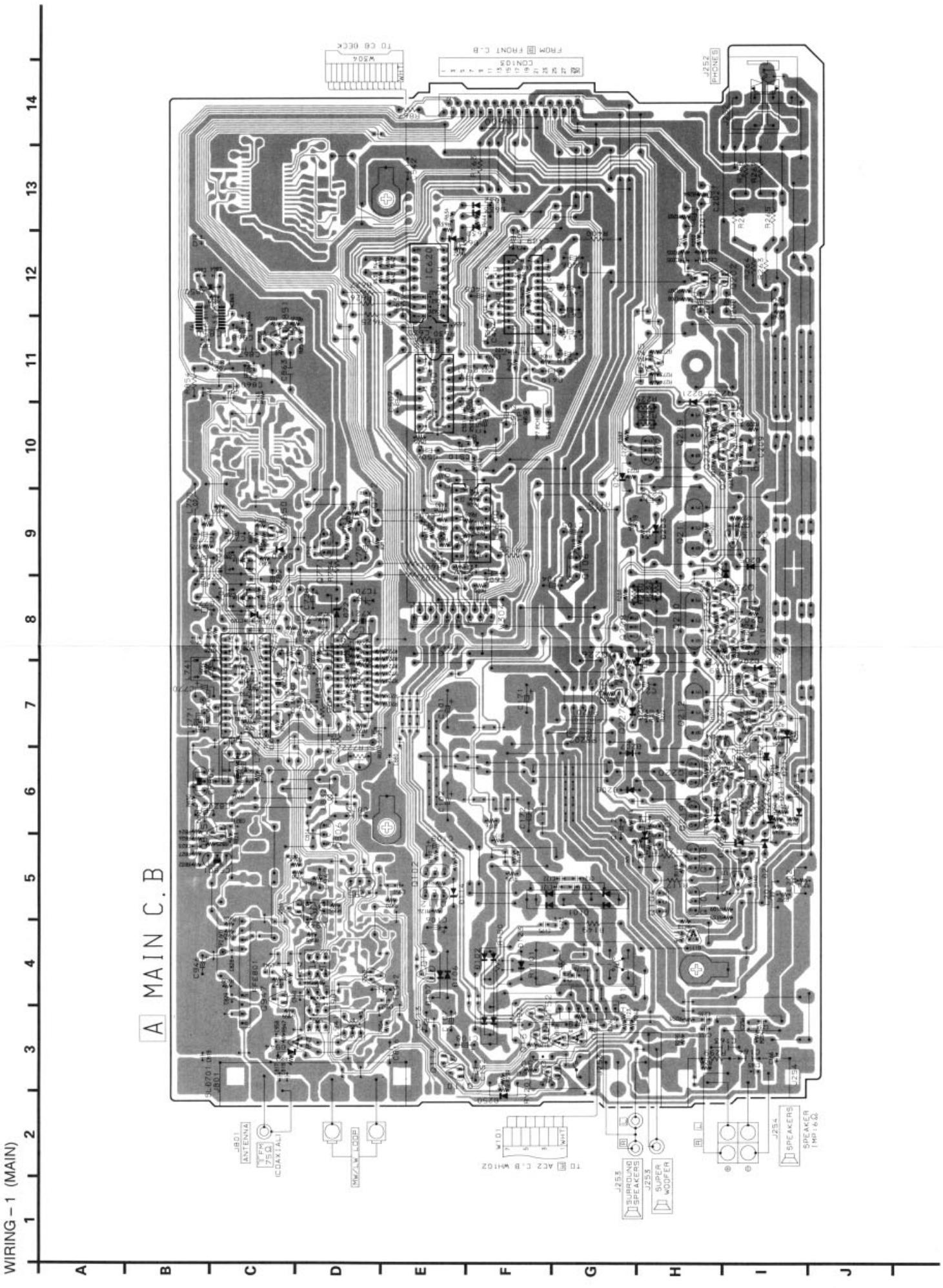
2SK543

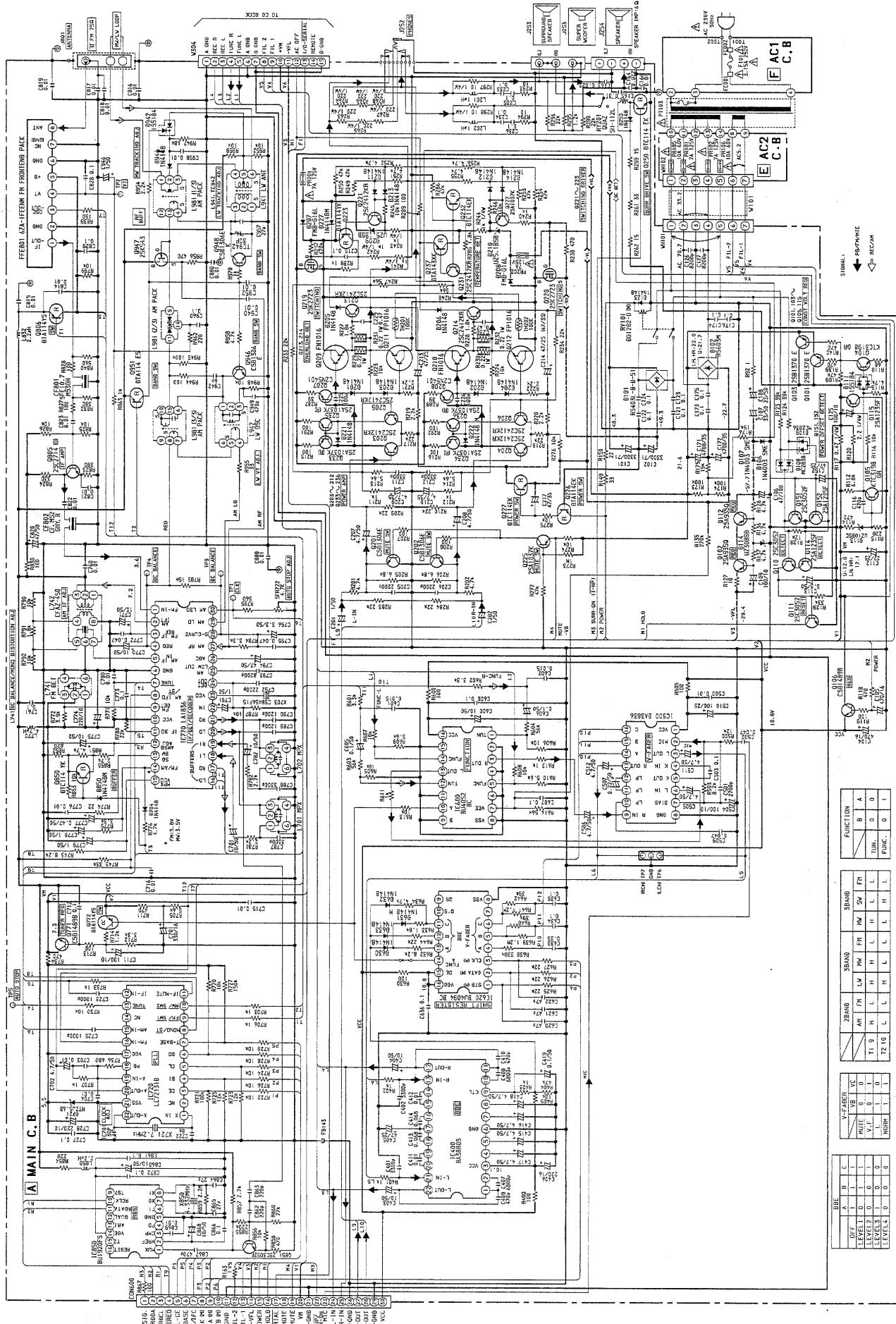


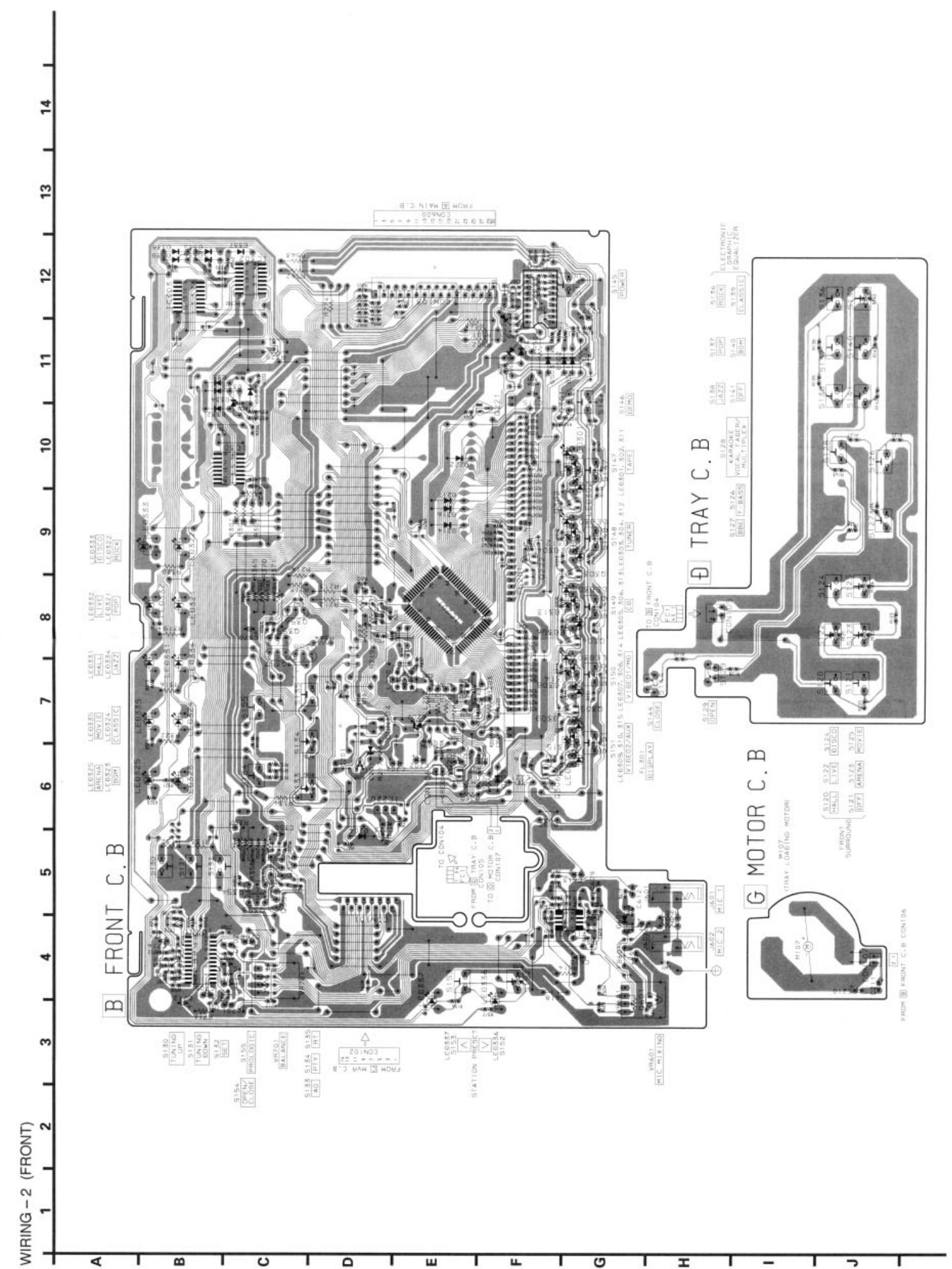
DTA114YS
DTA114ES

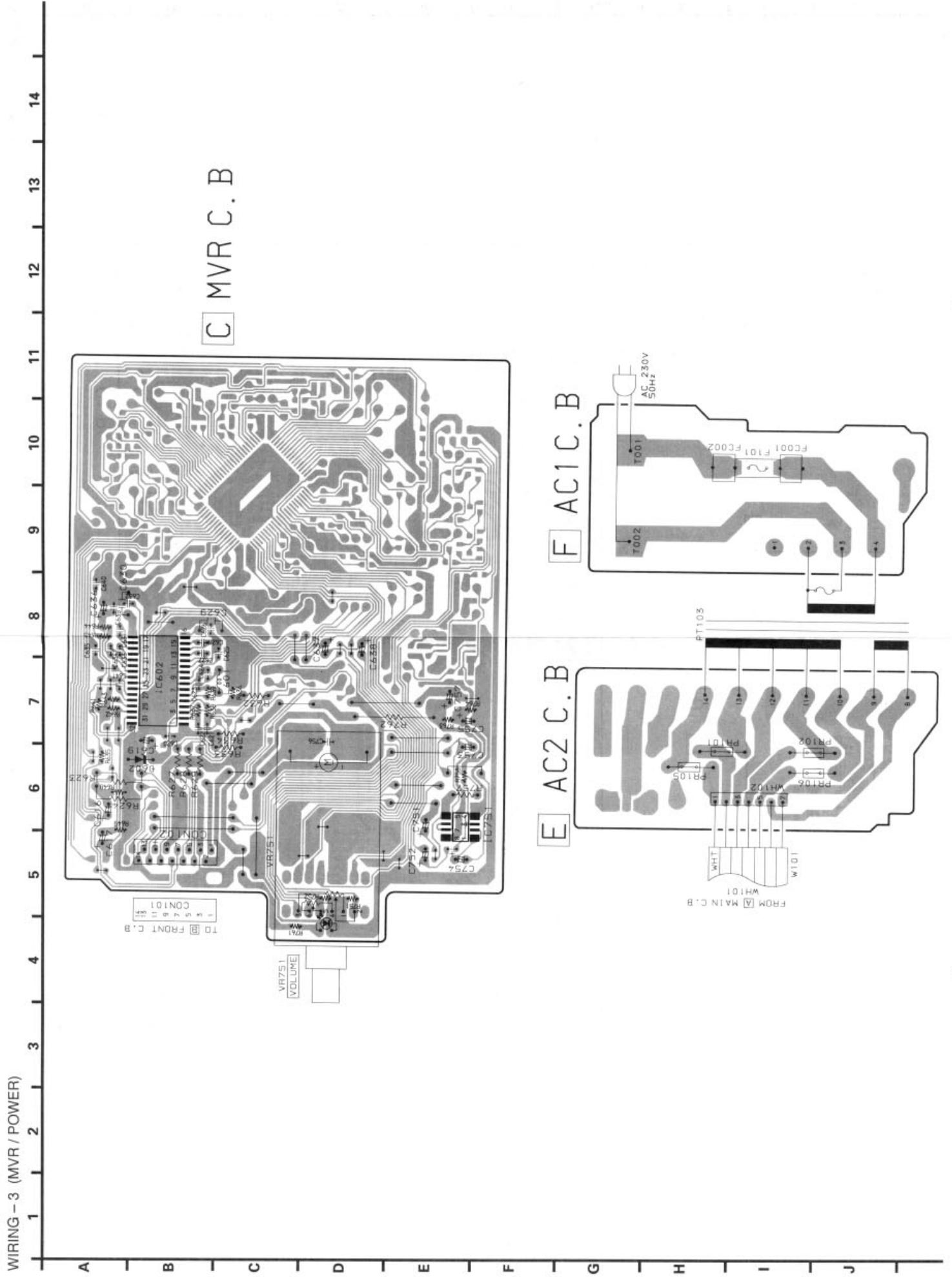
BLOCK DIAGRAM











IC DESCRIPTION

IC, LC866440W-5E65

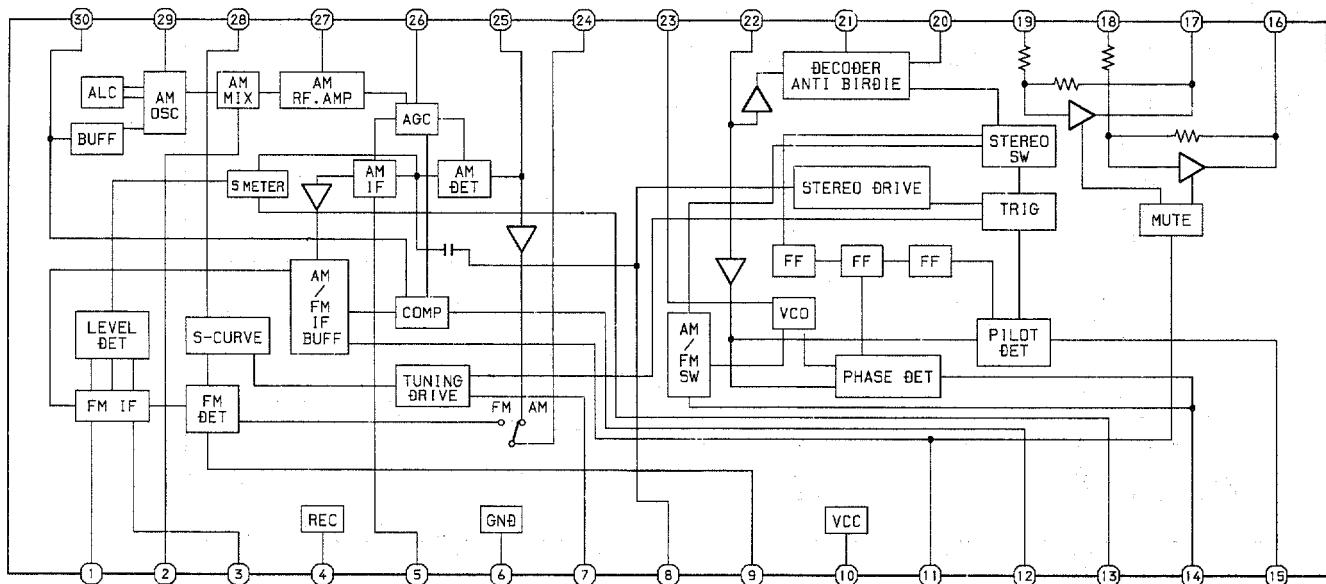
Pin No.	Pin Name	I/O	Description
1	O-PLLCE	O	PLL IC chip enable output.
2	O-STB(M)	O	Main shift register, data latch strobe output.
3	O-DATA(M)	O	Main shift register/PLL/DSP related, data output.
4	O-CLK(M)	O	Main shift register/PLL/DSP related, data transfer clock output.
5	I/O SERIAL	I/O	FD microprocessor, I/O serial.
6	O-SHIFT	O	Microprocessor clock shift output during tuner reception.
7	RESET	I	Reset input (Reset at "L").
8	I-STEREO	I	Tuner stereo sensing input
9	I-TUNE/IFC	I	Tuner, SD sensing input/IF count serial data input.
10	VSS1	-	GND.
11	CF1	-	5.76MHz oscillator.
12	CF2	-	5.76MHz oscillator.
13	VDD1	-	Power supply input.
14~16	I-KEY 1~3	I	Key 1 ~ 3 A/D input.
17	I-SIG	I	Signal level A/D input for RDS. (Not used)
18	I-SPEANA	I	Spectrum analyzer level A/D input.
19	I-MIC	I	Mic level A/D input for auto vocal fader.
20	I-TMBASE	I	Reference clock input for watch (Automatically supporting 8/50/60 Hz).
21	I-AC OFF	I	Power failure sensing input (Hold at "L").
22	I-RDATA	I	Data input for RDS.
23	I-RCLK	I	Clock input for RDS.
24	I-RMC	I	System remote control signal input (active low).
25~35	G1~G11	O	FL grid output G1~G11.
36~40	P24~P20	O	FL segment output P24~P40.
41	VDD2	-	Power supply input.
42	VP	-	Power supply for display.
43~48	P19~P14	O	FL segment output P19~14.
49	P13	O	FL segment output /Diode input supporting OIRT.
50	P12	O	FL segment output/Diode input supporting.
51	P11	O	FL segment output /Diode input supporting NTSC.
52	P10	O	FL segment output /Diode input supporting PRO.
53	P9	O	FL segment output /Diode input supporting LW.
54	P8	O	FL segment output /Diode input supporting SW.
55	P7	O	FL segment output /Diode input supporting AM 10K.
56	P6	O	FL segment output /Diode input supporting AM STEREO.
57	P5	O	FL segment output /Diode input supporting FM JPN.
58	P4	O	FL segment output /Diode input supporting RDS.
59	P3	I/O	FL segment output /Diode input supporting BBE.
60	P2	I/O	FL segment output /Diode input supporting DSP.
61	P1	I/O	FL segment output /Diode input supporting K-CON.
62	O-SWSCAN	O	CD turntable reverse direction rotation output/SW scan (timing output).
63	O-DSP CE	O	CD turntable forward direction rotation output/DSP chip enable.

Pin No.	Pin Name	I/O	Description
64	SUR ON	O	SUR ON(output at "H").
65	O-PRESET LED	O	Preset.
66	O-DOLBY	O	Not used.
67	SURR-MUTE	O	Not used.
68	O-MUTE	O	System Mute ON/OFF output.
69	O-POWER	O	System power supply ON/OFF output.
70	O-STB(F)	O	Front shift register, data latch strobe output.
71	O-CLK(F)	O	Front shift register, data clock output.
72	O-DATA(F)	O	Front shift register, data output.
73	VSS	-	GND.
74	O-TRAY OP	O	CD tray open output.
75	O-TRAY CL	O	CD tray close output.
76	O-VR UP	O	Vol up output.
77	O-VR DN	O	Vol down output.
78~80	NC	-	Not used.

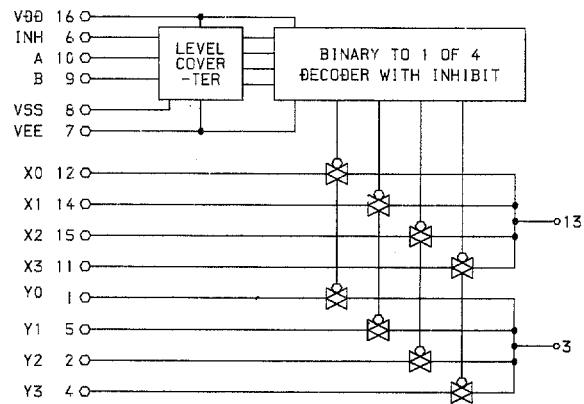
Pin No.	Pin Name	I/O	Description																														
1	XIN	I/O	A crystal oscillator (7.2MHz) is connected between these pins.																														
22	XOUT	I/O																															
2	NC	-	Not used.																														
3	CE	I	To enable the IC. Active "H".																														
4	DI	I	Digital data input from CPU (LC866440W-5E65) when relevant key is operated. Active "H".																														
5	CLK	I	To clock in the data DI.																														
6	DO	O	Digital data output to CPU (LC866440W-5E65).																														
7	TM-BASE	O	Outputs a reference clock signal (8Hz) for the clock.																														
8	MONO / BEAT	O	Outputs "H" when MONO / BEAT is switched.																														
9	FM / AM	O	Output "L" or "H" as follows:																														
			<table border="1"> <tr> <th colspan="2">2 BAND</th> <th colspan="3">3 BAND</th> <th colspan="3">3 BAND</th> </tr> <tr> <td>AM</td><td>FM</td> <td>LW</td><td>MW</td><td>FM</td> <td>MW</td><td>SW</td><td>FM</td> </tr> <tr> <td>H</td><td>L</td> <td>H</td><td>H</td><td>L</td> <td>H</td><td>L</td><td>L</td> </tr> </table>							2 BAND		3 BAND			3 BAND			AM	FM	LW	MW	FM	MW	SW	FM	H	L	H	H	L	H	L	L
2 BAND		3 BAND			3 BAND																												
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2 BAND		3 BAND			3 BAND																												
AM	FM	LW	MW	FM	MW	SW	FM																										
L	L	H	L	L	L	H	L																										
11	IF-MUTE	O	To control internal counter.																														
12	IFIN	I	General purpose counter input.																														
13	TUNE	I	Receives "L" when station is tuned.																														
14	NC	-	Not used.																														
15	A MIN	I	Receives the AM local oscillator frequency signal.																														
16	F MIN	I	Receives the FM local oscillator frequency signal.																														
17	VDD	-	Supply power to IC (+5V).																														
18	PD	O	PLL charge pump output.																														
19	AIN	I	The MOS transistor for PLL active low pass filter.																														
20	AOUT	O																															
21	VSS	-	Ground.																														

IC BLOCK DIAGRAM

IC, LA1836



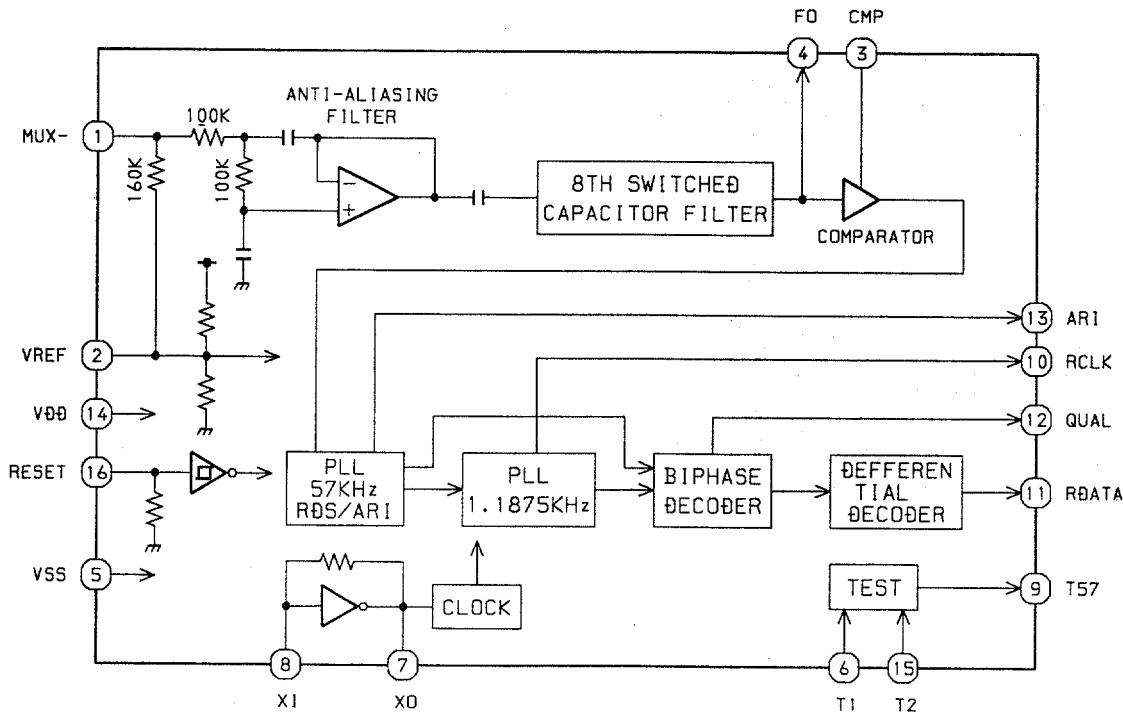
IC, BU4052BCP



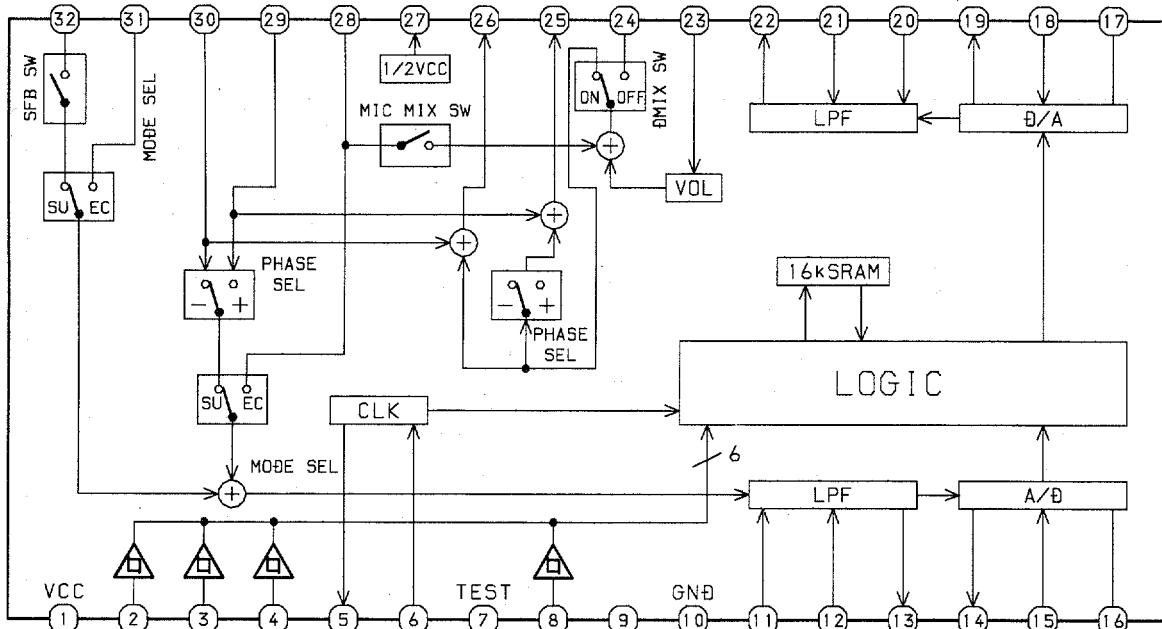
TRUTH TABLE

INHIBIT	A	B	ON SWITCH
L	L	L	X0 Y0
L	H	L	X1 Y1
L	L	H	X2 Y2
L	H	H	X3 Y3
H	X	X	NONE

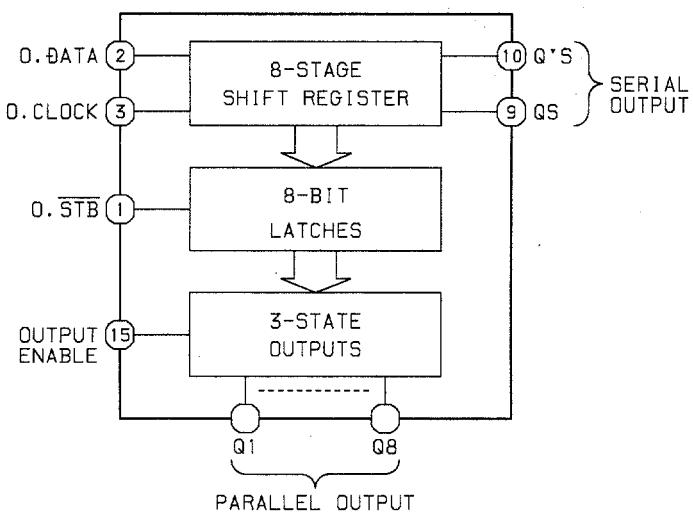
IC, BU1920FS



IC, M65849FP



IC, BU4094BCP / BCF



Q1: 0.DOLBY ON	Q5: 0.PLAY
Q2: 0.DOLBY C	Q6: 0.PB2
Q3: 0.EXT.REC	Q7: 0.LED
Q4: 0.INT.REC	Q8: 0.RMT

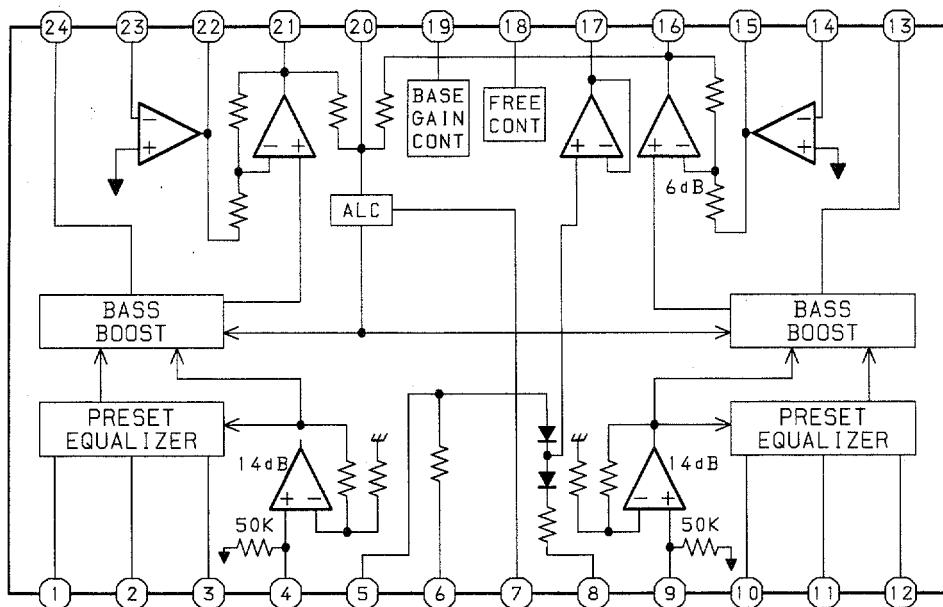
TRUTH TABLE

CLOCK	OUTPUT ENABLE	STROBE	DATA	PARALLEL OUTPUTS		SERIAL OUTPUTS	
				Q1	Qn	QS	Q'S
↓	L	X	X	Z	Z	Q7	NO CHG.
↑	L	X	X	Z	Z	NO CHG.	QS
↓	H	L	X	NO CHG.	NO CHG.	Q7	NO CHG.
↓	H	H	L	L	Qn-1	Q7	NO CHG.
↓	H	H	H	H	Qn-1	Q7	NO CHG.
↑	H	X	X	NO CHG.	NO CHG.	NO CHG.	QS

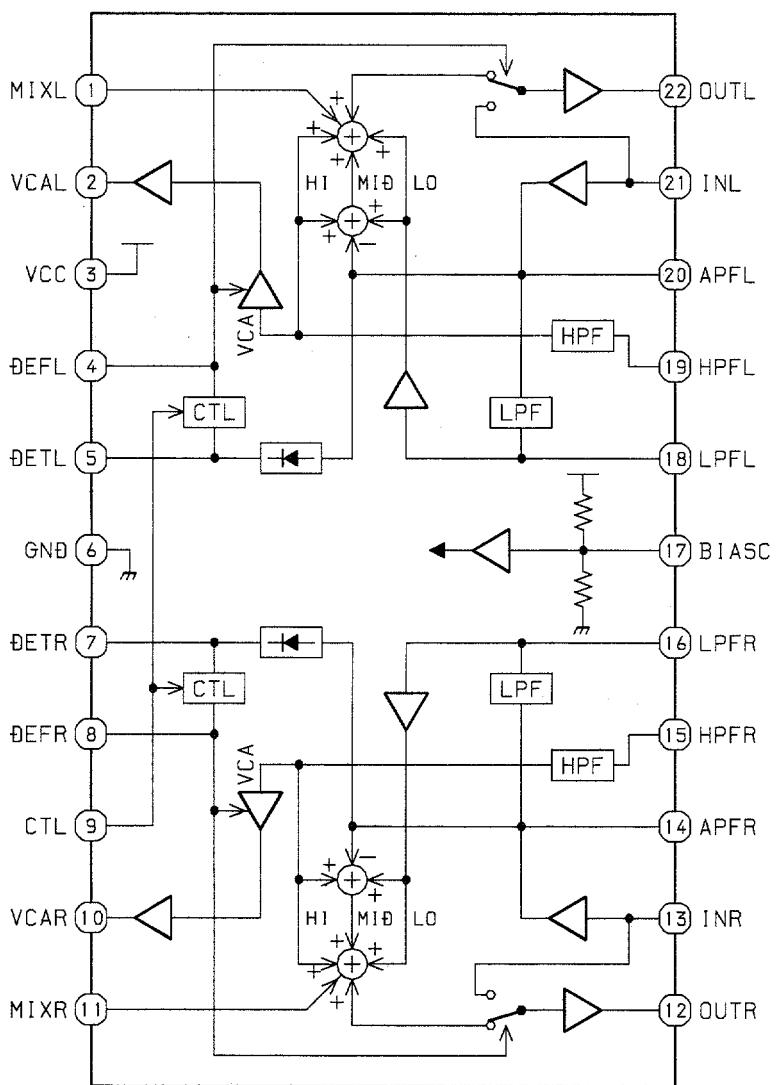
Z = HIGH IMPEDANCE

X = DON'T CARE

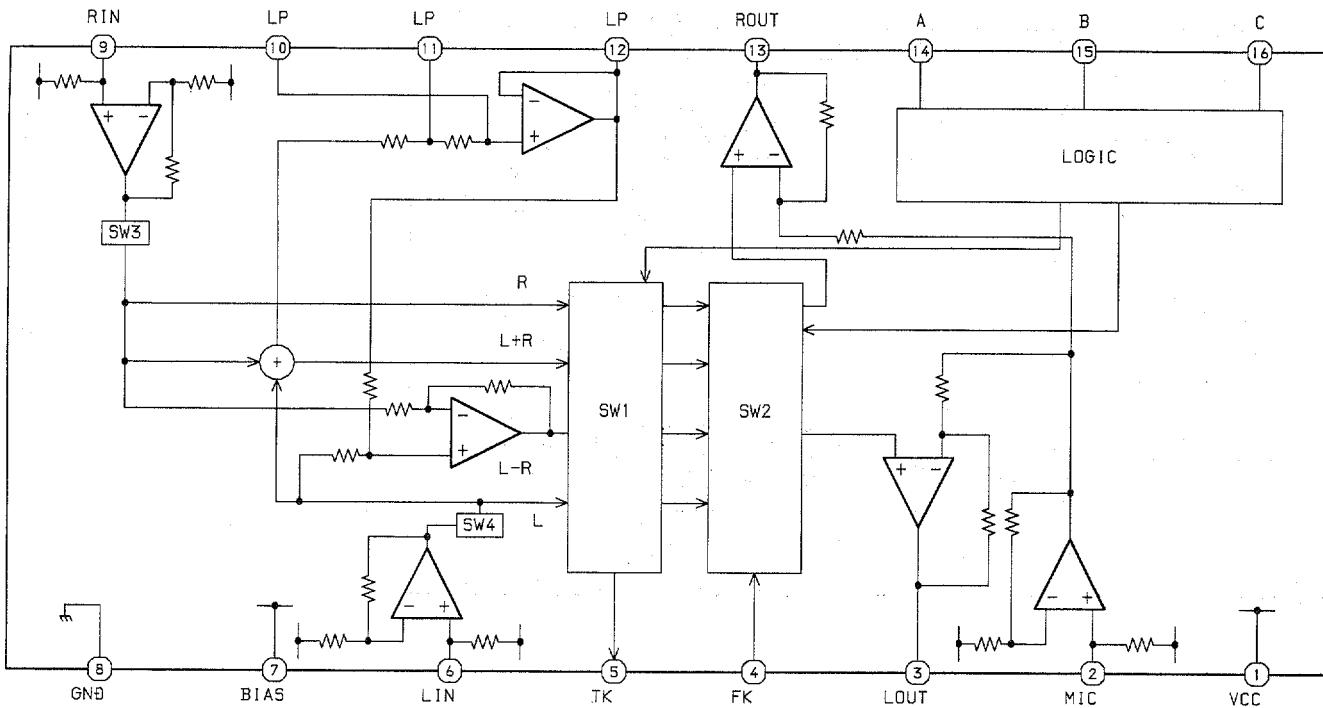
IC, BA3842F



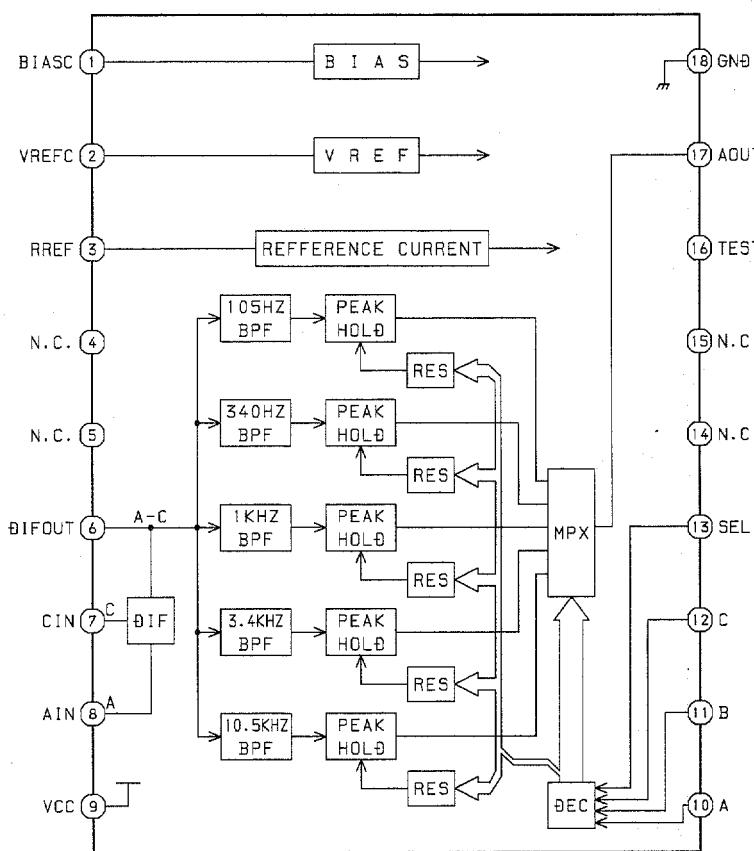
IC, BA3880S



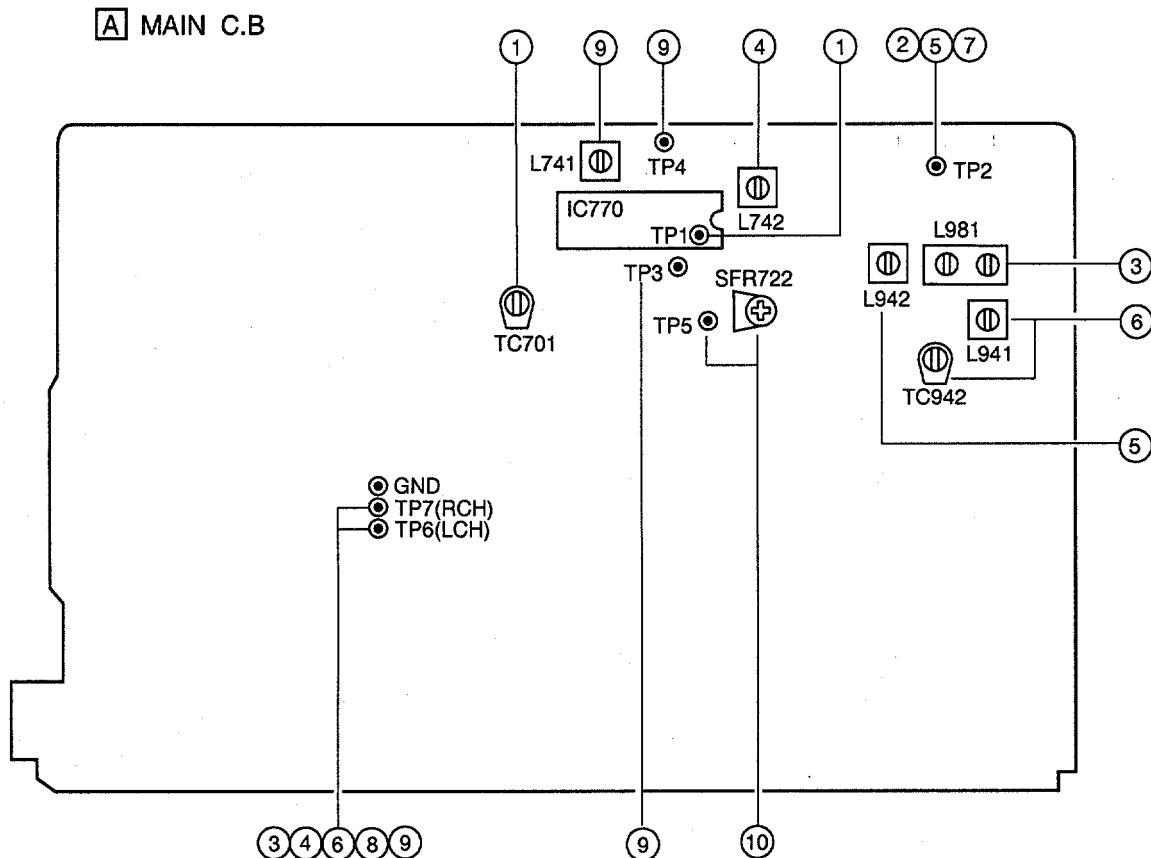
IC, BA3836



IC, BA3835S



ADJUSTMENT < TUNER >



< TUNER SECTION >

1. Clock Adjustment

Settings : • Test point : TP1 (CLK)

• Adjustment location : TC701

Method : Set to MW(AM) 1602kHz and adjust TC701 so that the test point becomes $2052\text{kHz} \pm 0.01\text{kHz}$.

2. MW(AM) VT Check

Settings : • Test point : TP2 (VT)

Method : Set to MW(AM) 1602kHz and check that the test point is $6.0V \pm 1.0V$.

3. MW(AM) Tracking Adjustment

Settings : • Test point : TP6(Lch), TP7(Rch)

• Adjustment location : L981

Method : Set to MW(AM) 999kHz and adjust L981 so that the test point becomes maximum.

4. AM IF Adjustment

Settings : • Test point : TP6(Lch), TP7(Rch)

L742.....450kHz

5. LW VT Adjustment

Settings : • Test point : TP2 (VT)

• Adjustment location : L942

Method : Set to LW 144kHz and adjust L942 so that the test point is $1.3V \pm 0.05V$.

6. LW Tracking Adjustment

Settings : • Test point : TP6(Lch), TP7(Rch)

• Input level : Adjustable

• Adjustment location :

L941.....144kHz

TC942.....290kHz

Method : Set up TC942 to center before adjustment.

The level at 144kHz is adjust to MAX by L941. Then the level at 290kHz is adjust to MAX by TC942.

7. FM VT Check

Settings : • Test point : TP2 (VT)

Method : Set to FM 87.5MHz and check that the test point is more than 1.5V.

Then set to FM 108MHz and check that the test point is less than 8.2V.

8. FM Tracking Check

Settings : • Test point : TP6(Lch), TP7(Rch)

Method : Check that the test point is 3~12dB and distortion is less than 3% at FM 98.0MHz.

9. DC Balance / Mono Distortion Adjustment

Settings : • Test point : TP3, TP4 (DC balance)
 : TP6, TP7 (Mono Distortion)

• Adjustment location : L741

• Input level : 54dB

Method : Set to FM 98.0MHz and adjust L741 so that the voltage between TP3 and TP4 becomes $0V \pm 0.04V$.

Next, check that the distortion is less than 1.3%.

PRACTICAL SERVICE FIGURE

10. Auto Stop Level Adjustment

- Settings :
 - Test point : TP5 (Auto Stop)
 - Adjustment location : SFR722
 - Input level : 54dB

Method : Set to FM 98.0MHz and adjust voltage low (about 0.1V) by SFR722. After that voltage high (about 7.0V) by 2.0dB down.

<TUNER SECTION>

<FM SECTION>

IHF Sensitivity :
(THD 3%) $6dB \pm 6dB$
[at 87.5 / 98.0 / 108.0MHz]

S/N 50dB Quieting sensitivity :
Less than 36dB
[at 87.5 / 98.0 / 108.0MHz]

Signal to noise ratio :
More than 66dB [STEREO]
More than 72dB [MONO]
[at 98.0MHz]

Distortion :
Less than 1.2% [at 98.0MHz]
Auto stop level :
 $25dB +10/-5dB$ [at 98.0MHz]

Stereo separation :
More than 20dB [at 98.0MHz]

Intermediate frequency :
10.7MHz

<MW (AM) SECTION>

Sensitivity :
(S/N 20dB) Less than 60dB
[at 603kHz]

Less than 58dB
[at 999kHz/1404kHz]

Signal to noise ratio :
More than 36dB
[at 999kHz]

Distortion :
Less than 1.5%
[at 999kHz]

Auto stop level :
Less than 60dB
[at 999kHz]

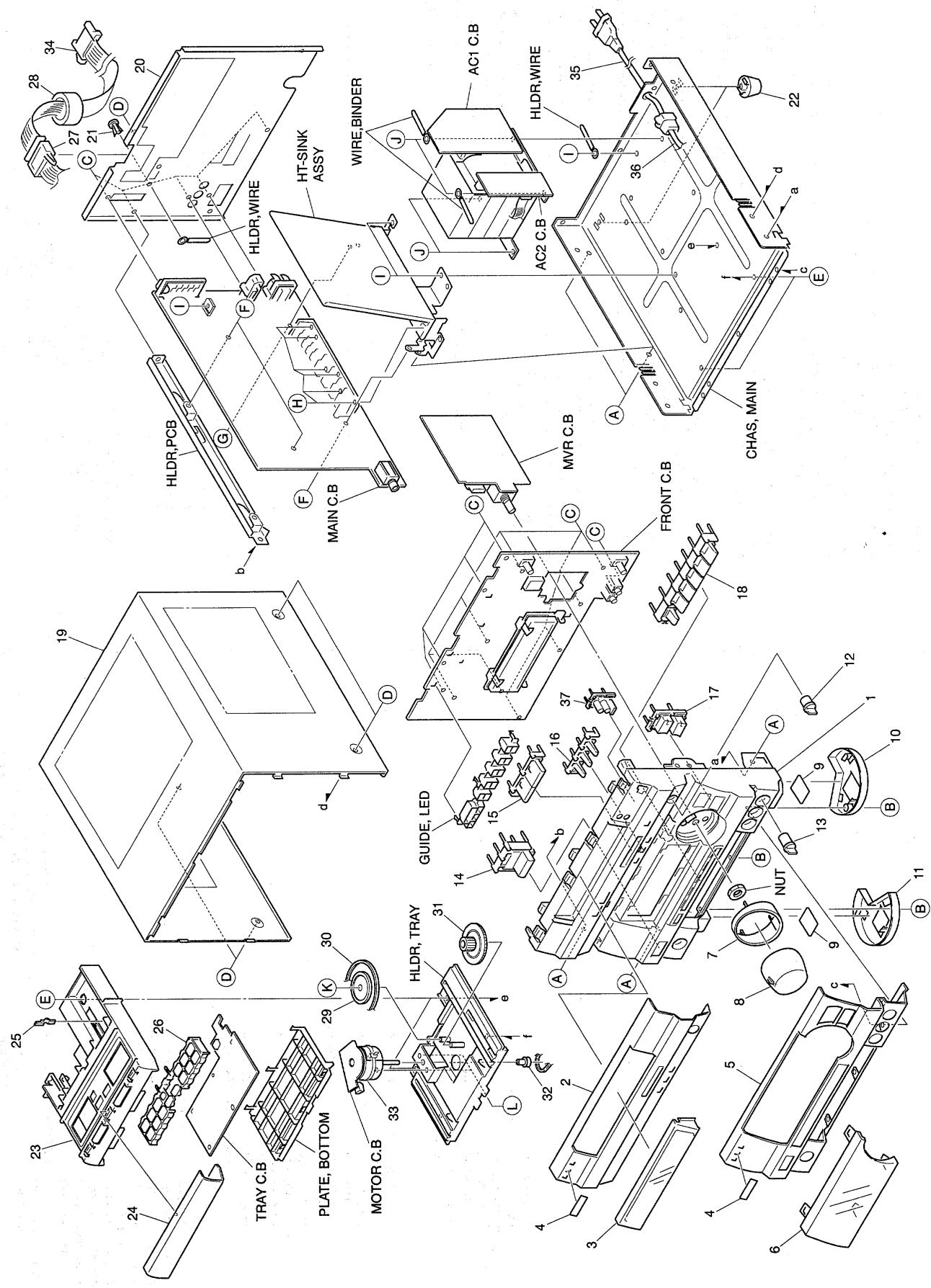
Intermediate frequency : 450kHz

<LW (AM) SECTION>

Sensitivity :
(S/N 20dB) Less than 70dB
[at 144kHz]

Less than 66dB
[at 198kHz/290kHz]

Signal to noise ratio :
More than 34dB
[at 198kHz]



MECHANICAL PARTS LIST 1 / 1

If can't understand for Description please kindly refer to "REFERENCE NAME LIST".

REF. NO.	PART NO.	KANRI NO.	DESCRIPTION	REF. NO.	PART NO.	KANRI NO.	DESCRIPTION
1	87-NT1-001-010	CABI, FR		26	87-NT1-014-010	KEY, GEQ	
2	87-NT1-004-010	PANEL, FR GEQ E		27	89-VT5-202-010	BUSHING, CORD	
3	87-B00-002-010	BADGE, AIWA 30 ABS SIL		28	87-003-317-010	F-BEAD, FOH2515-LG7	
4	87-NT1-019-010	WINDOW, GEQ		29	82-NT1-205-110	PULLEY, LOADING(*)	
5	87-NT1-002-010	PANEL, FR AMP		30	80-VW1-217-010	BELT, SQ 1.5	
6	87-NT1-036-010	WINDOW, DISPLAY RDS		31	82-NT1-204-010	GEAR, LOADING	
7	87-NT1-034-010	RING, VOL		32	80-VW1-204-010	PULLEY, MOTOR	
8	87-NT1-021-010	KNOB, RTRY VOL		33	87-045-383-010	MOT, M9150T28-2	
9	80-VT1-202-010	FELT, 12.5-15.5-2		34	87-NT1-650-010	CORD, FG15P	
10	87-NT1-035-010	RING, FOOT R		35	87-085-185-010	BUSHING, AC CORD (E)	
11	87-NT1-015-010	RING, FOOT L		36	87-050-079-010	AC-CORD ASSY, E	
12	87-NT1-024-010	KNOB, RTRY ECHO		37	87-NT1-009-010	KEY, TUNING	
13	87-NT1-023-010	KNOB, RTRY MIC		A	87-591-094-410	TAPPING SCREW, QIT+3-6	
14	87-NT1-007-010	KEY, POWER		B	87-067-777-010	BVTT+3-6 W, CONVEX BL	
15	87-NT1-011-010	KEY, TIMER		C	87-067-703-010	TAPPING SCREW, BVT2+3-10	
16	87-NT1-010-010	KEY, SET		D	87-067-641-010	UTT2+3-8(W/O SLOT)BL	
17	87-NT1-037-010	KEY, ASSY UP/ DOWN		E	87-067-584-010	TAPPING SCREW, BVT2+3-6	
18	87-NT1-013-010	KEY, FUN		F	87-078-084-010	BVTT+3-6 W, CONVEX	
19	87-NT1-043-010	CABI, STEEL		G	87-NF4-224-010	S-SCREW, IT3B+3-8 CU	
20	87-NT1-032-010	PANEL, REAR EZSNM		H	87-067-758-010	BVT2+3-12 W/O SLOT	
21	87-084-077-010	NYLON RIVET, 3.5-4.5		I	87-067-688-010	BVTT+3-6	
22	87-085-213-010	FOOT, H12.5		J	87-078-019-010	S-SCREW, IT+4-6	
23	87-NT1-005-010	TRAY, CONTROL		K	87-861-095-410	VFT2+3-8 SLOT	
24	87-NT1-006-010	PANEL, TRAY		L	87-261-073-410	V+2.6-6	
25	81-MT3-211-010	LEVER, OPEN					

MODEL NO.

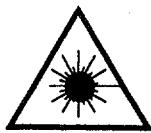
FD-NH100

PROTECTION OF EYES FROM LASER BEAM DURING SERVICING

This set employs laser. Therefore, be sure to follow carefully the instructions below when servicing.

WARNING!!

WHEN SERVICING, DO NOT APPROACH THE LASER EXIT WITH THE EYE TOO CLOSELY. IN CASE IT IS NECESSARY TO CONFIRM LASER BEAM EMISSION. BE SURE TO OBSERVE FROM A DISTANCE OF MORE THAN 30cm FROM THE SURFACE OF THE OBJECTIVE LENS ON THE OPTICAL PICK-UP BLOCK.



- Caution: Invisible laser radiation when open and interlocks defeated avoid exposure to beam.
- Advarsel: Usynlig laserstråling ved åbning, når sikkerhedsafbrydere er ude af funktion. Undgå udsættelse for stråling.

VAROITUS!

Laiteen Käyttäminen muulla kuin tässä käyttöohjeessa mainitulla tavalla saattaa altistaa käytäjän turvallisuusluokan 1 yli-täälle näkymättömälle lasersäteilylle.

VARNING!

Om apparaten används på annat sätt än vad som specificeras i denna bruksanvisning, kan användaren utsättas för osynlig laserstrålning, som överskrider gränsen för laserklass 1.

CAUTION

Use of controls or adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.

ATTENTION

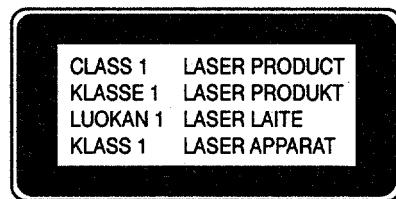
L'utilisation de commandes, réglages ou procédures autres que ceux spécifiés peut entraîner une dangereuse exposition aux radiations.

ADVARSEL!

Usynlig laserstråling ved åbning, når sikkerhedsafbrydere er ude af funktion. Undgå udsættelse for stråling.

This Compact Disc player is classified as a CLASS 1 LASER product.

The CLASS 1 LASER PRODUCT label is located on the rear exterior.



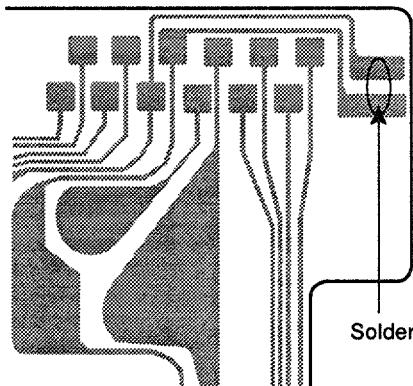
Precaution to replace Optical block

(KSS - 213B)

Body or clothes electrostatic potential could ruin laser diode in the optical block. Be sure ground body and workbench, and use care the clothes do not touch the diode.

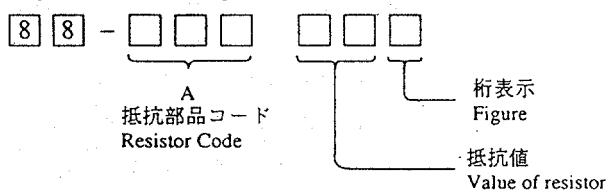
- 1) After the connection, remove solder shown in figure right.

PICK-UP Assy P.C.B



○ チップ抵抗部品コード／CHIP RESISTOR PART CODE

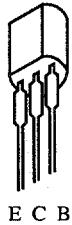
チップ抵抗部品コードの成り立ち
Chip Resistor Part Coding



チップ抵抗
Chip resistor

容量 Wattage	種類 Type	許容誤差 Tolerance	記号 Symbol	寸法／Dimensions (mm)				抵抗コード : A Resistor Code: A
				外形／Form	L	W	t	
1/16W	1608	±5%	CJ		1.6	0.8	0.45	108
1/10W	2125	±5%	CJ		2	1.25	0.45	118
1/8W	3216	±5%	CJ		3.2	1.6	0.55	128

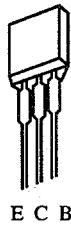
TRANSISTOR ILLUSTRATION



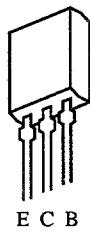
2SC1815
2SC3266
KTA1266GR
2SA1296



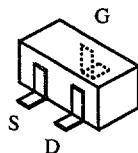
2SC2001
2SA952
CSD655E



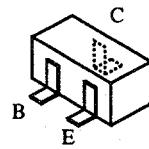
2SA933S



2SD2172

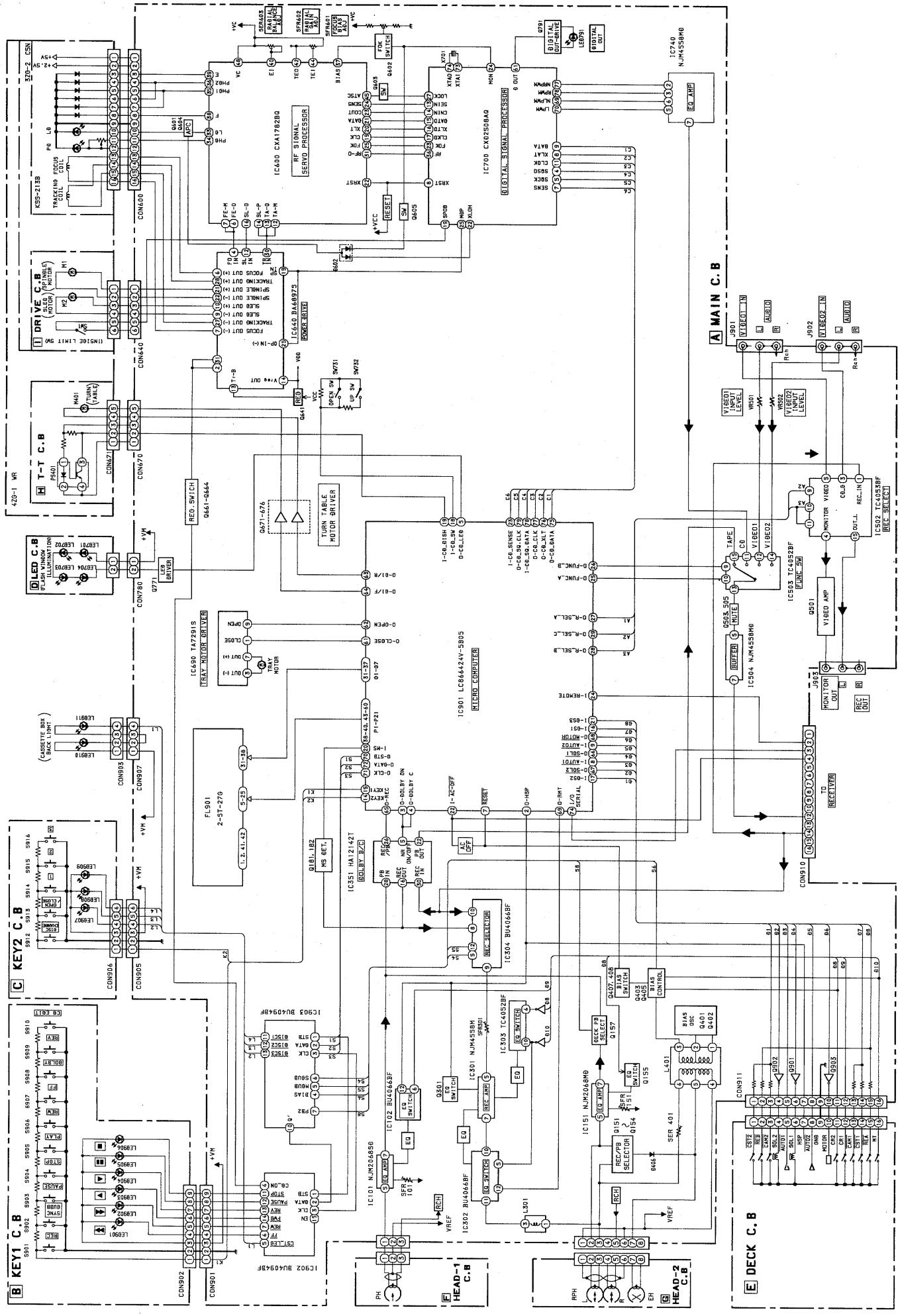


2SK368GR

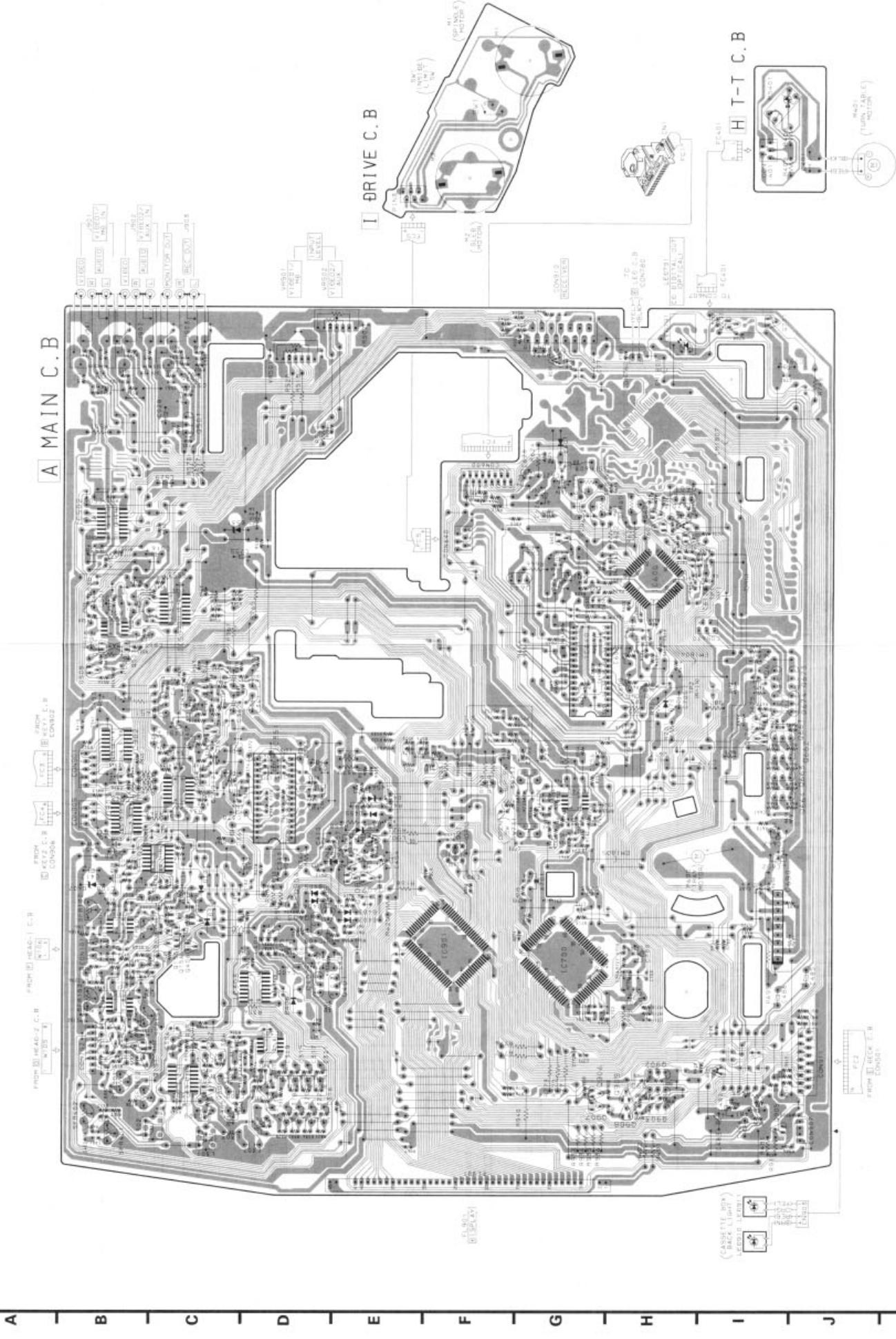


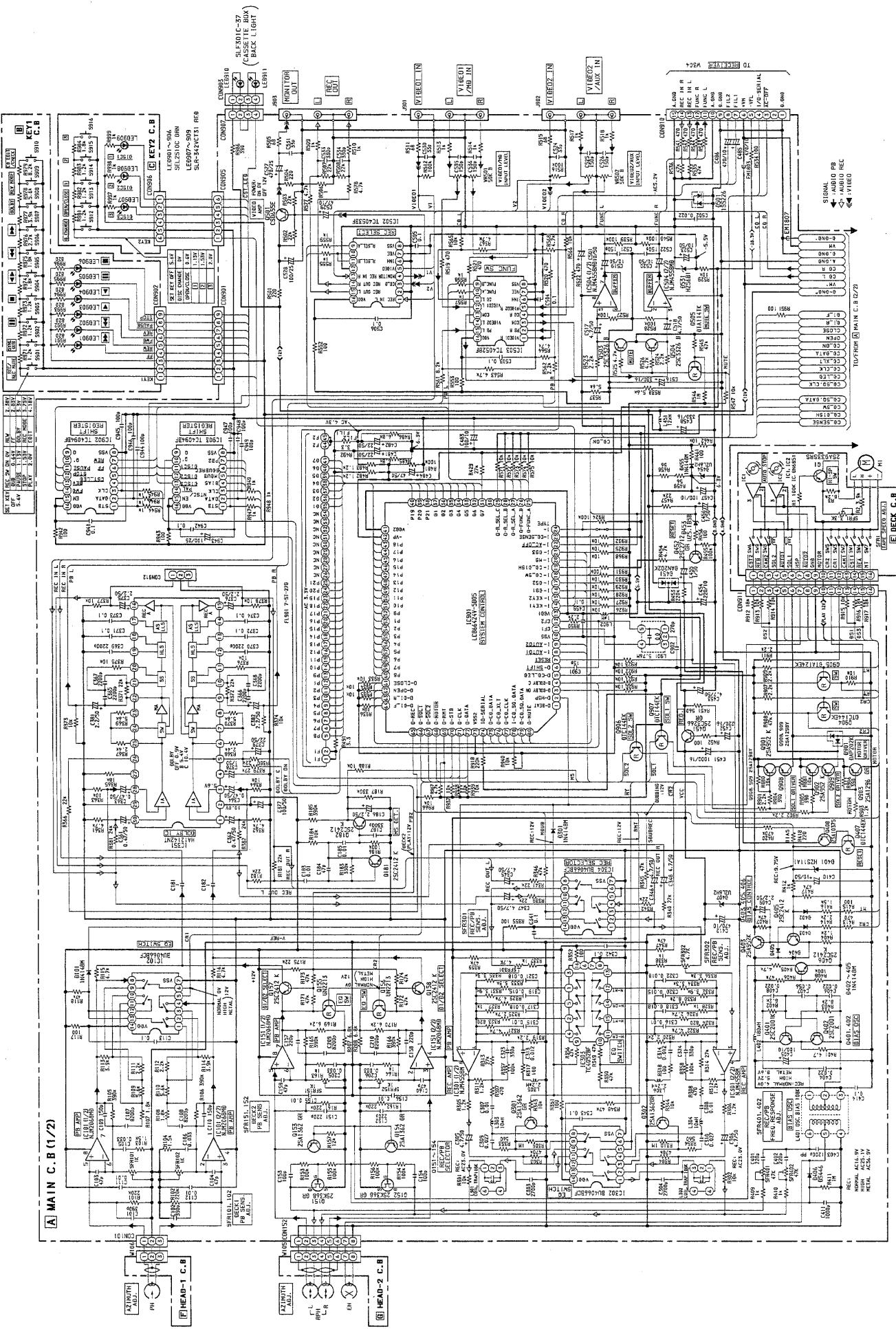
2SA1362GR	DTA144EK
UN2213	DTC114TK
2SC2412KR	DTC143TK
DTC144EK	DTC123JK
2SA1037S	DTA124EK
2SC3326	2SD1383
DTA114TK	2SA1298

BLOCK DIAGRAM

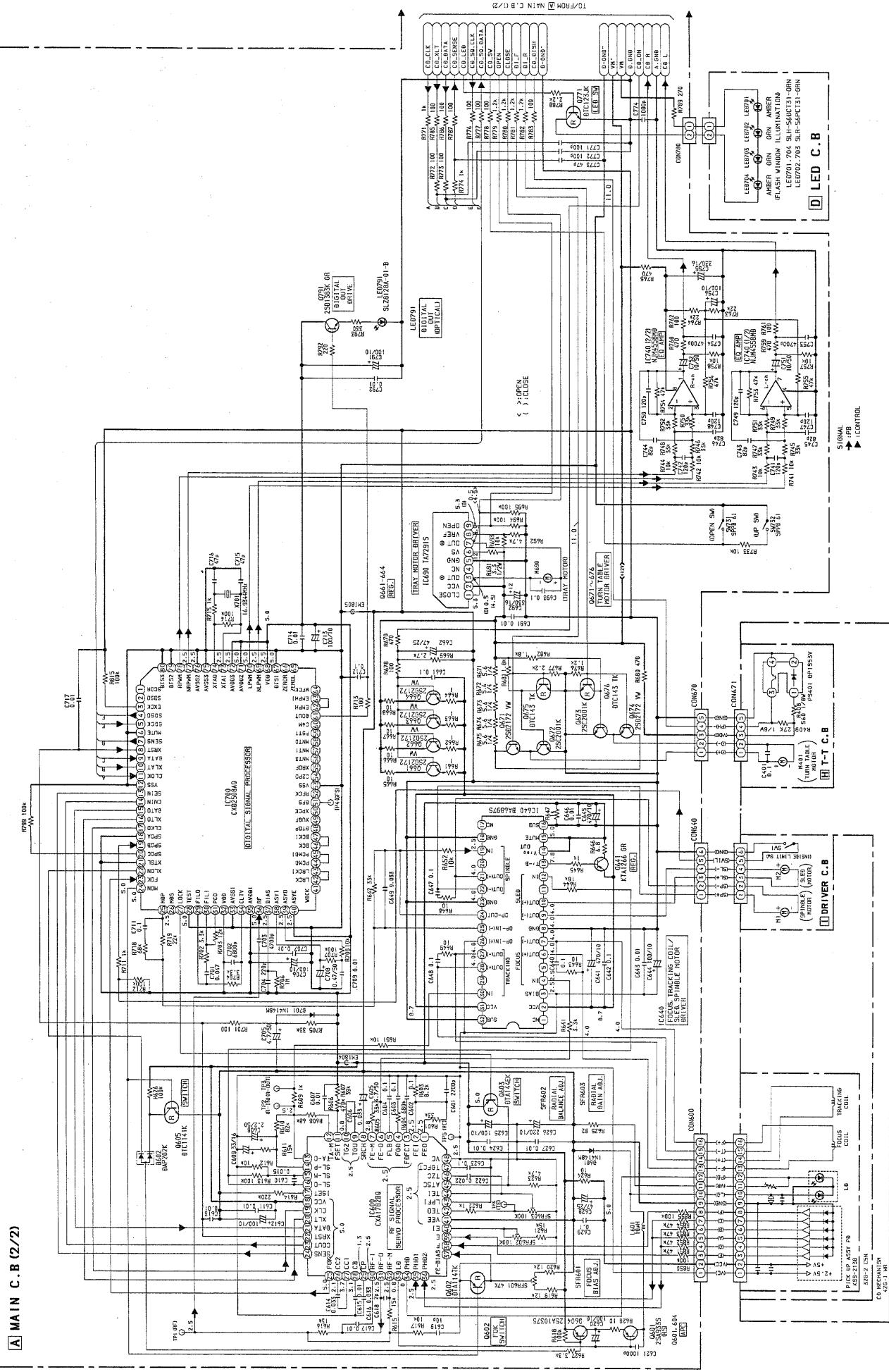


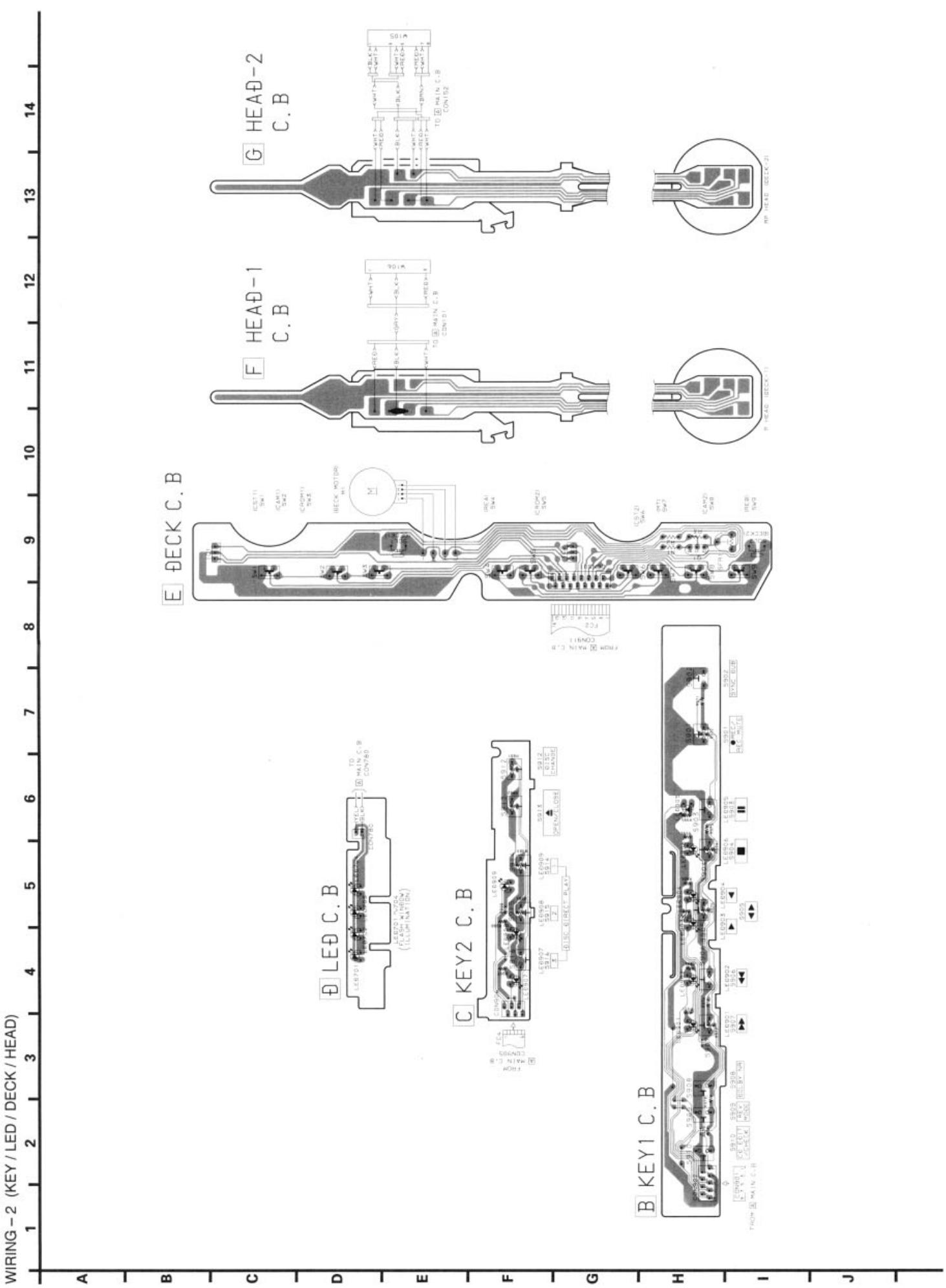
WIRING - 1 (MAIN) 1 2 3 4 5 6 7 8 9 10 11 12 13 14





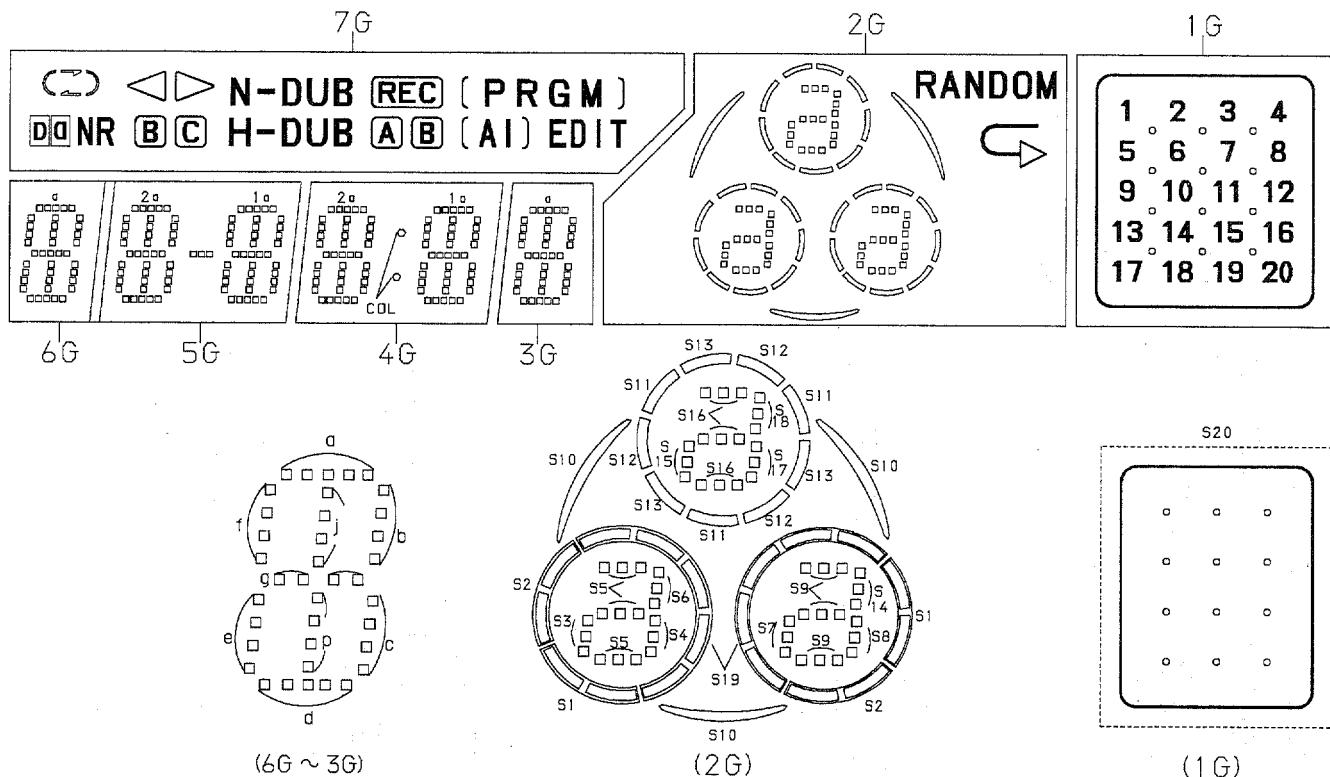
A MAIN C.B (2/2)





FL (7-ST-27G) GRID ASSIGNMENT AND ANODE CONNECTION

GRID ASSIGNMENT



ANODE CONNECTION

	7G	6G	5G	4G	3G	2G	1G
P1	NR	d	1d	1d	d	S1	20
P2	C	p	1p	1p	p	S2	19
P3	↔	e	1e	1e	e	S3	18
P4)	c	1c	1c	c	S4	17
P5	B (LEFT)	g	1g	1g	g	S5	16
P6	C	f	1f	1f	f	S6	15
P7	◀	b	1b	1b	b	S7	14
P8	▶	j	1j	1j	j	S8	13
P9	N-DUB	a	1a	1a	a	S9	12
P10	H-DUB	—	2d	2d	—	S10	11
P11	REC	—	2p	2p	—	S11	10
P12	A	—	2e	2e	—	S12	9
P13	B (RIGHT)	—	2c	2c	—	S13	8
P14	PRGM	—	2g	2g	—	S14	7
P15	AI	—	21	21	—	S15	6
P16	EDIT	—	2b	2b	—	S16	5
P17	(PRGM)	—	2j	2j	—	S17	4
P18	(AI)	—	2a	2a	—	S18	3
P19	—	—	...	COL (HIGH)	—	S19	2
P20	—	—	—	COL (LOW)	—	→	1
P21	—	—	—	—	RANDOM	S20	

IC DESCRIPTION

IC, LC866424V-5B05

Pin No.	Pin Name	I/O	Description				
1	O-BEAT	O	REC beat output. (ON/OFF)				
2	O-HSP	O	High speed dubbing switch. (HIGH/NORMAL)				
3	O-DOLBY/ON	O	DOLBY IC switch output. (DOLBY ON/OFF)				
4	O-DOLBY/C	O	DOLBY IC mode switch output. (DOLBY B/C)				
5	O-CD/LED	O	Flash window output. (ON/OFF)				
6	O-SHIFT	O	Microprocessor clock shift out during tuner reception.				
7	RESET	I	Reset input (Reset at "L").				
8	I-AUTO 1	I	Deck 1 auto stop input.				
9	I-AUTO 2	I	Deck 2 auto stop input.				
10	VSS 1	-	GND.				
11	CF 1	I	5.76 MHz oscillator.				
12	CF 2	O	5.76 MHz oscillator.				
13	VDD 1	-	Power supply input.				
14	I-KEY 1	I	Key 1 A/D input.				
15	I-KEY 2	I	Key 2 A/D input.				
16	I-DS 1	I	Deck 1 mechanism switch input.				
17	I-DS 2	I	Deck 2 mechanism switch input.				
18	I-CD/SW	I	CD mechanism switch A/D input.				
19	I-CD/DISH	I	CD turntable photo sensor A/D input.				
20	I-MS	I	Deck MS detection A/D input.				
21	I-DS 3	I	Deck mechanism switch input (REC enable A/D input).				
22	I-AC/OFF	I	HOLD input.				
23	I-CD/SENSE	I	CD microprocessor control SENSE input.				
24	I-TYPE	I	TYPE select A/D input. (H : DOLBY C / L : DOLBY B)				
25~26	O-FUNC/A~B	O	FUNCTION switch output.	AUX1	AUX2	TAPE	CD
				A	0	1	1
				B	0	0	1
27	O-R-SEL/A	O	Video signal switch. (VIDEO 1/2)				
28	O-R-SEL/B	O	REC output switch. (ON/MUTE)				
29	O-R-SEL/C	O	Monitor output switch. (VIDEO/CDG)				
30	-	-	Not used.				
31~37	G7~G1	O	FL grid output (G7~G1).				
38~40	P21~P19	O	FL segment output P21~P19.				
41	VDD2	-	Power supply input.				
42	-VP	-	Power supply for FL display .				
43~60	P18~P8	O	FL segment output P18~P8.				
61	O-CLOSE	O	CD tray close data output.				
62	O-OPEN	O	CD tray open data output.				
63	O-DI/R	O	CD turntable reverse rotation output.				
64	O-DI/F	O	CD turntable forward rotation output.				
65	O-REC	O	Deck REC switch output.				
66	O-SOL1	O	Deck 1 plunger ON/OFF output.				

67	O-SOL2	O	Deck 2 plunger ON/OFF output.
68	O-MOTOR	O	Deck motor ON/OFF output.
69	O-RMT	O	REC mute ON/OFF output.
70	O-STB	O	Front shift register, data latch strobe output.
71	O-CLK	O	Front shift register, data transfer clock output.
72	O-DATA	O	Front shift register, data output.
73	VSS2	-	GND.
74	I/O/SERIAL	I/O	Command input / output with the CD microprocessor.
75	O-CD/DATA	O	CD microprocessor control data output.
76	O-CD/XLT	O	CD microprocessor control latch output.
77	O-CD/CLK	O	CD microprocessor control clock output.
78	I-CD/SQ,DATA	I	CD SUB-Q data input.
79	O-CD/SQ,DATA	O	CD SUB-Q clock output.
80	O-MUTE	O	System mute ON/OFF output.

IC, CXD2508AQ

Pin No.	Pin Name	I/O	Description
1	SCOR	O	1H when the subcode sync S0 or S1 is detected.
2	SBSO	O	SUBP ~ W serial output.
3	EXCK	I	Clock input for SBSO read out.
4	SQSO	O	SUBQ 80-bit serial output.
5	SQCK	I	Clock input for SQSO read out.
6	MUTE	I	H to mute. L to cancel. (Connected to GND)
7	SENS	O	SENS signal output to MAIN CPU.
8	XRST	I	System reset. L to reset.
9	DATA	I	Serial data input from MAIN CPU.
10	XLAT	I	Latch input from MAIN CPU. Latching serial data at fall down.
11	CLOK	I	Clock input from MAIN CPU to transfer serial data.
12	VSS	-	GND.
13	SEIN	I	SENS input from SSP.
14	CNIN	I	Numbers of track jump are counted and input.
15	DATO	O	Serial data output to SSP.
16	XLTO	O	Serial data latched output to SSP. Latched at fall down edge.
17	CLKO	O	Clock input from SSP to transfer serial data.
18	TEST2	I	TEST. (Connected to +5V)
19~21	SPOB~D	I	Input from INSIDE LIMIT switch (SW1).
22	XLON	O	Mute control output.
23	FOK	I	Focus OK input pin. Used for SENS output and servo auto sequencer.
24	MON	O	Spindle motor ON/OFF control output.
25	MDP	O	Spindle motor servo control output.
26	MDS	O	Spindle motor servo control output.
27	LOCK	O	GFS is sampled by 460Hz. H output when GFS is H. L output when GFS is L for 8 consecutive times.
28	TEST1	I	TEST. (Connected to GND)
19	FILO	O	Filter output to master PLL. (Slave = digital PLL)
30	FILI	I	Filter input to master PLL.
31	PCO	O	Charge-pump output to master PLL.
32	VDD	-	Power supply input. (+5V)
33	AVSS1	-	GND.
34	CLTV	I	VCO control voltage input to master PLL.
35	AVDD1	-	Power supply input. (+5V)
36	RF	I	EFM signal input.
37	BIAS	I	Constant current input to asymmetry correction circuit.
38	ASYI	I	Comparator voltage input to asymmetry correction circuit.
39	ASYO	O	EFM full swing output. (L = VSS, H = VDD)
40	ASYE	I	L: asymmetry correction OFF. H: asymmetry correction ON. (Connected to +5V)
41	WCDK	O	D/A interface, word clock (2Fs) for 48-bit slot.

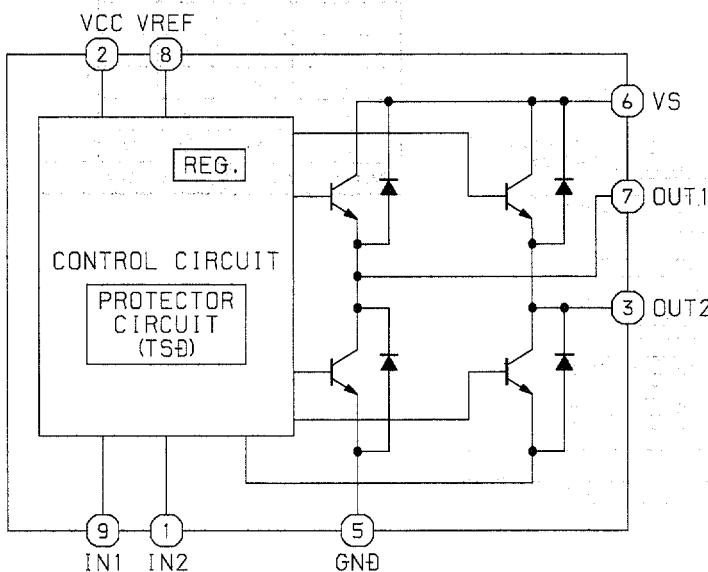
Pin No.	Pin Name	I/O	Description
42	LRCK	O	D/A interface, LR clock (FS) for 48-bit slot.
43	LRCKI	I	LR clock input to DAC. (48-bit slot)
44	PCMD	O	D/A interface, serial data. (2's complement, MSB first)
45	PCMDI	I	Audio data input to DAC. (48-bit slot)
46	BCK	O	D/A interface, bit clock.
47	BCK1	I	Bit clock input to DAC. (48-bit slot)
48	GTOP	O	GTOP output.
49	XUGF	O	XUGF output.
50	XPCK	O	XPLCK output.
51	GFS	O	GFS output.
52	RFCK	O	RFCK output.
53	VSS	-	GND.
54	C2PO	O	C2PO output.
55	XROF	O	XRAOF output.
56	MNT3	O	MNT3 output.
57	MNT1	O	MNT1 output.
58	MNT0	O	MNT0 output.
59	FSTT	O	Pins-73 and -74 divided-by 2/3 output.
60	C4M	O	4.2336MHz output.
61	DOUT	O	Digital Out connector output signal.
62	EMPH	O	H when the play back disk has emphasis. L when it does not.
63	EMPHI	I	DAC emphasis ON/OFF. H when ON, L when OFF.
64	WFCK	O	WFCK (WRITE FRAME CLOCK) output.
65	ZEROL	O	Not sound data detection output. H (L-ch) when no sound data is detected.
66	ZEROR	O	Not sound data detection output. H (L-ch) when no sound data is detected.
67	DTSI	I	TEST for DAC. (Connected to GND)
68	VDD	-	Power supply input. (+5V)
69	NLPWM	O	L-ch PWM output. (Reversed polarity)
70	LPWM	O	L-ch PWM output. (Normal polarity)
71	AVDD2	-	Power supply input to L-ch PWM driver. (Connected to +5V)
72	AVDD3	-	Power supply input to X'tal. (Connected to +5V)
73	XTAI	I	X'tal input to 33.8688MHz oscillator circuit.
74	XTAO	O	33.8688MHz X'tal oscillator circuit output.
75	AVSS1	-	Power supply input to X'tal. (Connected to GND)
76	AVSS2	-	Power supply input to PWM driver. (Connected to GND)
77	NRPWM	O	R-ch PWM output. (Reversed phase)
78	RPWM	O	R-ch PWM output. (Normal phase)
79	DTS2	I	TEST-2 for DAC. (Connected to GND)
80	DTS3	I	TEST-3 for DAC. (Connected to GND)

1	FEO	O	Focus error amplifier output pin. This pin is connected to the FZC comparator input internally.
2	FEI	I	Focus error input pin.
3	FDFCT	I	Capacitor connection pin for time constant used when there is defect.
4	FGD	I	This pin is connected to GND via capacitor when high frequency gain of the focus servo is attenuated.
5	FLB	I	This is a pin where the time constant is externally connected to raise the low frequency gain of the focus servo.
6	FEO	O	Focus drive output.
7	FEM	I	Focus amplifier inverted input pin.
8	SRCH	I	This is a pin where the time constant is externally connected to generate the focus search waveform.
9	TGU	I	This is a pin where the selection time constant is externally connected to set the tracking servo the high frequency gain.
10	TG2	I	This is a pin where the selection time constant is externally connected to set the tracking high frequency gain.
11	FSET	I	Pin for setting peak of the phase compensator of the focus tracking.
12	TAM	I	Tracking amplifier inverted input pin.
13	TAO	O	Tracking drive output.
14	SLP	I	Sled amplifier non-inverted input pin.
15	SLM	I	Sled amplifier inverted input pin.
16	SLO	O	Sled drive output.
17	ISET	I	The current which determines height of the focus search, track jump and sled kick is input.
18	VCC	—	+ 5 V power supply pin.
19	CLK	I	Serial data transfer clock input from CPU.
20	XLT	I	Latch input from CPU.
21	DATA	I	Serial data input from CPU.
22	XRST	I	Reset input pin. Reset at L.
23	COUT	O	Signal output to count the number of tracks.
24	SENS	O	FZC, DFCT, TZC, Gain or BAL is output depending on the command from CPU.
25	FOK	O	Output pin of the focus OK comparator.
26	CC2	O	Input pin where the DEFECT bottom hold output is capacitance coupled.
27	CC1	I	DEFECT bottom hold output pin.
28	CB	I	This is a pin where the DEFECT bottom hold capacitor is connected.
29	CP	I	This is a pin where the MIRR hold capacitor is connected and MIRR comparator non-inverted signal is input.
30	RFI	I	Input pin where the RF summing amplifier output is capacitance coupled.
31	RFO	O	RF summing amplifier output pin. (Eye pattern check point)
32	RFM	I	RF summing amplifier inverted input pin. Gain of RF amplifier is determined by the resistor connected between RFO and this pin.

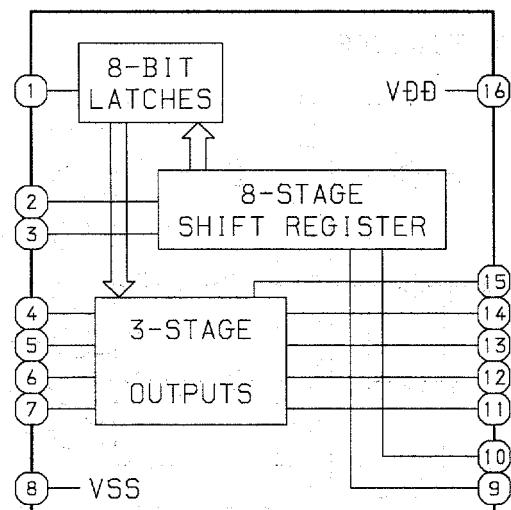
Pin No.	Pin Name	I/O	Description
33	LD	O	APC amplifier output pin.
34	PHD	I	APC amplifier input pin.
35~36	PHD1~2	I	RF I-V amplifier inverted input pin. These pins are connected to the A+C and B+D pins of the optical pickup.
37	FE BIAS	I	Bias adjustment pin of the focus error amplifier.
38~39	F~E	I	F and E IV amplifier non-inverted input pins. These pins are connected to the F and E of the optical pickup.
40	EI	—	Gain adjustment pin of the I-V amplifier E.
41	VEE	—	GND connection pin
42	TEO	O	Tracking error amplifier output pin. E-F signal is output.
43	LPFI	I	BAL adjustment comparator input pin.
44	TEI	I	Tracking error input pin.
45	ATSC	I	Window comparator input pin for detecting ATSC.
46	TZC	I	Tracking zero-cross comparator input pin.
47	TDFCT	I	Capacitor connection pin for the time constant used when there is defect.
48	VC	O	DC voltage output pin of VREF. (VDD/2)

IC BLOCK DIAGRAM

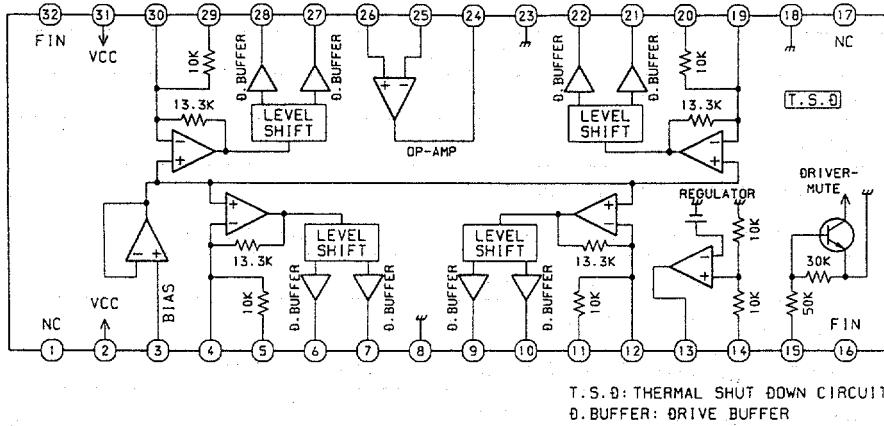
IC, TA7291S



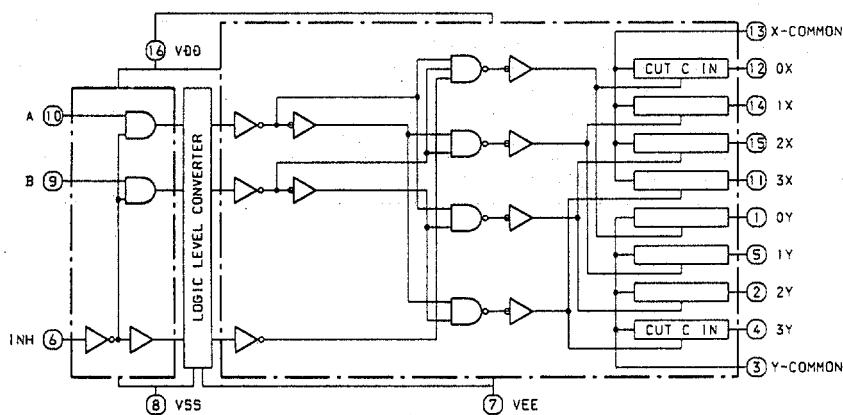
IC, TC4094BF



IC, BA6897S



IC, TC4052BF

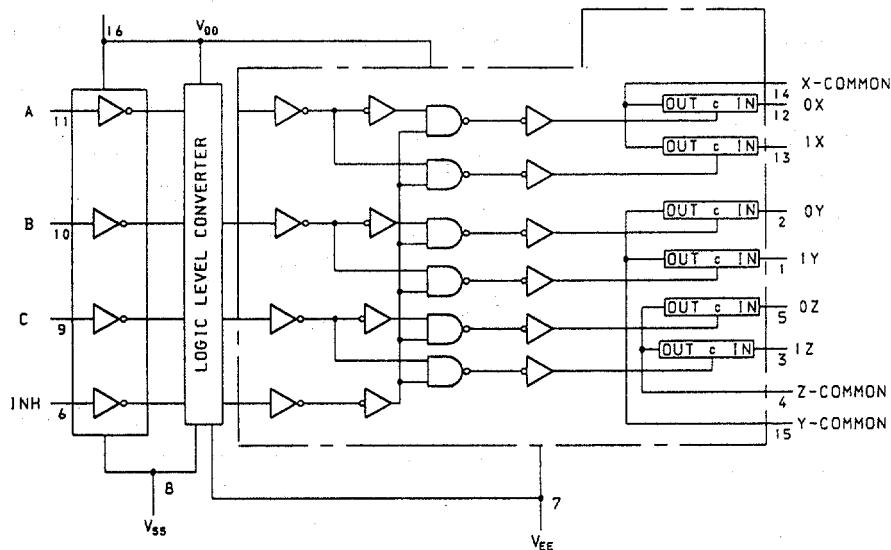


TRUTH TABLE

CONTROL INPUTS			
INHIBIT	C	B	A
L	L	L	L
L	L	L	H
L	L	H	L
L	L	H	H
L	H	L	L
L	H	H	L
L	H	H	H
H	*	*	*

*: DON't Care △: Expect TC4052B

IC, TC4053BF



TEST MODE

1. How to Activate CD Test Mode

Insert the AC plug while pressing the CD EDIT/CHECK/ button. All FL display tubes will light up, and the test mode will be activated.

2. How to cancel CD Test Mode

Either one of the following operations will cancel the CD test mode.

- Press the power switch button.
- Disconnect the AC plug.

3. CD Test Mode Functions

When test mode is activated, the following mode functions can be used by pressing the operation keys.

Mode	Operation	FL display	Operation	Contents
Start mode	Test mode activation	All FL light up	<ul style="list-style-type: none"> • Laser diode illuminated under normal circumstances (CD block power supply ON) 	Displays the machine mode that it is a test mode. All FL displays light up
Search mode	■ key	- - -	<ul style="list-style-type: none"> • Continual focus search * NOTE 1 (The pickup lens repeats the full-swing up-down motion.) * Avoid continual searches that last for more than 10 minutes. 	FOCUS SERVO <ul style="list-style-type: none"> • Laser current measurement (Across R628 resistor) • Check focus search waveform • Check focus error waveform * FOK / FZC are not monitored in the search mode.
Play mode	▶ key	/ -	<ul style="list-style-type: none"> • Normal playback • Focus search is continued if TOC cannot be read * NOTE 1 	FOCUS SERVO / TRACKING SERVO CLV SERVO / SLED SERVO Check FOK / FZC
Traverse mode	key	/ -	<ul style="list-style-type: none"> • During normal disc playback Press once; tracking servo OFF Press twice; tracking servo ON * NOTE 2	TRACKING SERVO ON / OFF Tracking balance (traverse) adjustment TP6(SFR602)
Sled mode	◀▶ key	All FL light up	<ul style="list-style-type: none"> • Pickup moves to the outermost track • Pickup moves to the innermost track * NOTE 3 (During playback, machine operates normally.)	SLED SERVO Check SLED mechanism operation

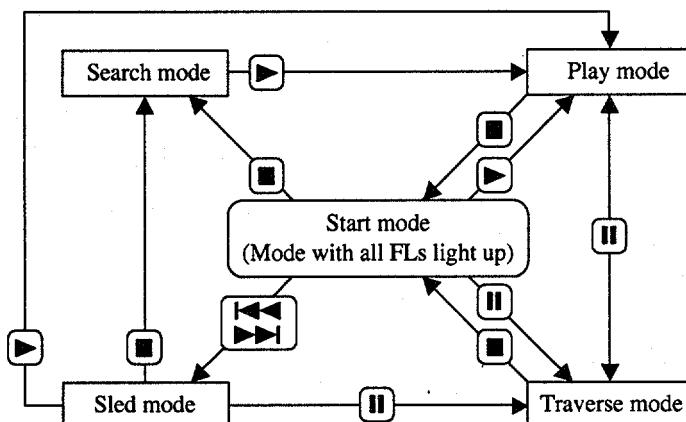
* NOTE 1: There are cases when the tracking servo cannot be locked owing to the protection circuit being operated when heat builds up in the driver IC if the focus search is operated continually for more than 10 minutes. In these cases, the power supply should be switched off for 10 minutes until heat has been reduced and then re-started.

* NOTE 2: Do not press the ▶◀ or ▶▶ keys when the machine is in the || status is active. If they are pressed, playback will not be possible after the || status has been canceled. If the ▶◀ or ▶▶ keys are pressed in the || status, press the ■ key and return to start mode (No. 1).

* NOTE 3: When pressing the ▶◀ or ▶▶ keys, take care to avoid damage to the gears. Because the sled motor is activated when the ▶◀ or ▶▶ keys are pressed, even when the pick-up is at the outermost or innermost track.

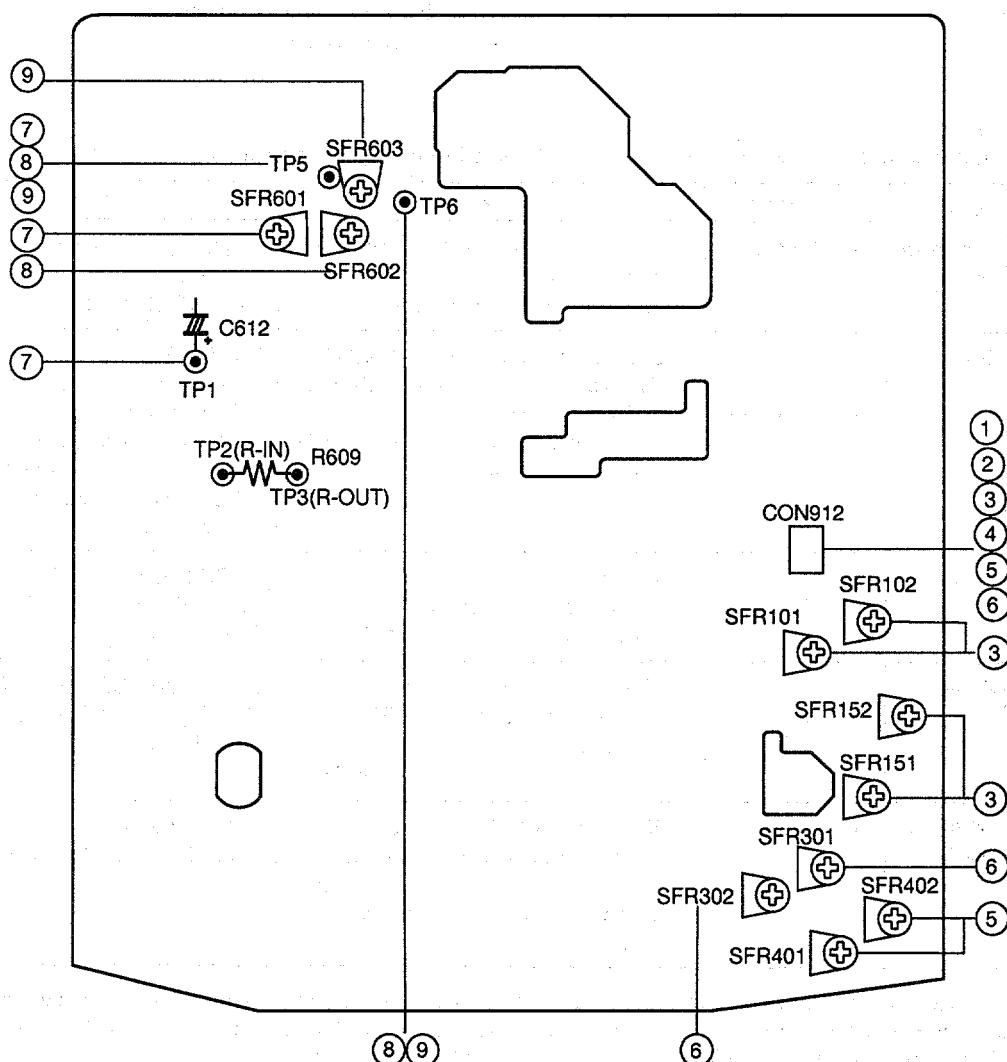
4. Operation Outline

The operation of each mode is carried out in the direction of the arrows from the start mode as indicated in the following illustration.

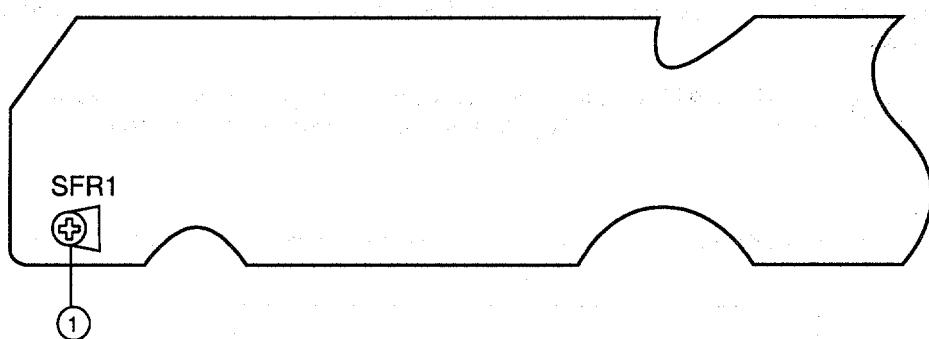


ADJUSTMENT <DECK>

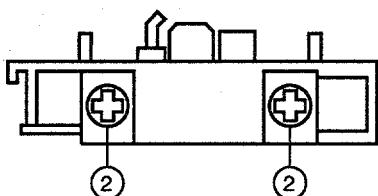
A MAIN C.B



E DECK C.B



DECK R / P E HEAD



<DECK SECTION>

1. Tape Speed Adjustment

Settings :

- Test tape : TTA-100

- Test point : TP CONN 3P(CON 912)

- Adjustment location : SFR1

Method : Play back the test tape and check for
3000Hz±5Hz.

(NOTE) : RVS SIDE SPEED SPECIFICATION FWD
SIDE SPECIFICATION±45Hz.

2. Head Azimuth Adjustment(DECK 1,2)

- Settings : • Test tape : TTA-300
• Test point : TP CONN 3P(CON912)
• Adjustment location : Head azimuth
adjustment screw

Method : Play back the 10kHz signal of the test tape and adjust screw so that the output becomes maximum. Next, perform on each FWD PLAY and REV PLAY mode.

3. PB Sensitivity Adjustment (DECK 1,2)

- Settings : • Test tape : TTA-200
• Test point : TP CONN 3P (CON 912)
• Adjustment location : SFR 101 (Lch,DECK1)
SFR 102 (Rch,DECK1)
SFR 151 (Lch,DECK2)
SFR 152 (Rch,DECK2)

Method : Play back the test tape and adjust SFRs so that the output level becomes 300 ± 5 mV

4. PB Frequency Response Check

- Settings : • Test tape : TTA-300
• Test point : TP CONN 3P (CON912)

Method : Play back the 315Hz and 10kHz signals of the test tape and check output difference to within $0dB \pm 2dB$, and the 10kHz signal with respect to that of the 315Hz signal is 2dB.

5. REC/PB Frequency Response Adjustment

- Settings : • Test tape : TTA-602
• Test point : TP CONN 3P (CON912)
• Input signal : 1kHz / 10kHz (VIDEO2/AUX IN)
• Adjustment location : SFR401 (Lch)
SFR402 (Rch)

Method : Establish the record mode. Adjust the CON 912 signal to 210mV and attenuate to -20dB. Record and playback 1kHz and 10kHz. Adjust SFR so that level difference between 1kHz and 10kHz is $0dB \pm 0.3dB$.

6. REC/PB Sensitivity Adjustment(DECK 2)

- Settings : • Test tape : TTA-602
• Test point : TP CONN 3P (CON 912)
• Input signal : 1kHz (VIDEO2/AUX IN)
• Adjustment location : SFR301 (Lch)
SFR302 (Rch)

Method : Apply a 1kHz signal and REC mode. Then adjust OSC attenuator so that the output level at the TP CONN 3P(CON912) becomes 21mV. Record and play back the 1kHz signals and adjust SFRs that the output is $21mV \pm 0.3dB$.

PRACTICAL SERVICE FIGURE

<DECK SECTION>

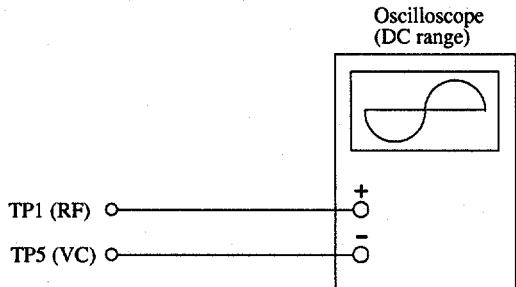
Tape speed :	3000Hz \pm 45Hz
Wow & flutter :	Less than 0.15% (W.R.M.S)
Take-up torque :	30 ~ 55g-cm (FWD REV)
F.F & REW torque :	75 ~ 160g-cm
Back tension :	2 ~ 7g-cm (FWD REV)
Distortion :	Less than 2.0% (REC/PB, AC)
Noise level(PB) :	Less than 1.2 / 0.4 mV (DOLBY OFF LINEAR/WTD,CrO2) Less than 1.8 / 0.6 mV (DOLBY OFF LINEAR/WTD,NORMAL) Less than 0.8 / 0.16 mV (DOLBY B LINEAR/WTD,CrO2) Less than 0.8 / 0.2 mV (DOLBY B LINEAR/WTD,NORMAL) Less than 0.6 / 0.06 mV (DOLBY C LINEAR/WTD,CrO2) Less than 0.6 / 0.08 mV (DOLBY C LINEAR/WTD,NORMAL)
Noise level(REC/PB) :	Less than 1.8 / 1.2 / 1.2 mV (DOLBY OFF LINEAR, NORMAL/CrO2/MT) Less than 0.8 / 0.8 / 0.8 mV (DOLBY B LINEAR, NORMAL/CrO2/MT) Less than 0.6 / 0.6 / 0.6mV (DOLBY C LINEAR, NORMAL/CrO2/MT)
Erasing ratio :	More than 60dB(at 125Hz)
Test tape :	TTA- 602(NORMAL) TTA-615(CrO2) TTA-635(MT)

<CD SECTION>

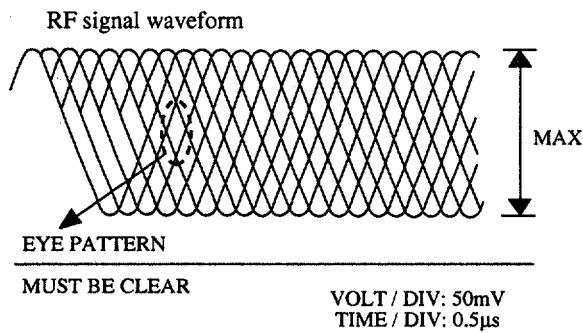
Note : Connect a probe (10:1) of the oscilloscope or the frequency counter to a test point.

7. Focus Bias Adjustment

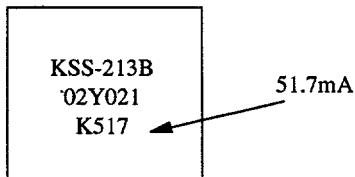
Make the focus bias adjustment when replacing and repairing the optical block.



- 1) Connect an oscilloscope to the test points TP1 (RF) and TP5 (VC).
- 2) Turn on the power switch.
- 3) Insert test disc TCD-782 (YEDS-18) and play back the second composition.
- 4) Adjust SFR601 so that the RF signal of the test point TP1 (RF) is MAX and CLEARREST.

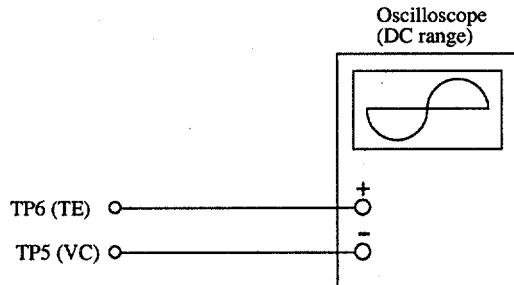


Note : The current of the laser signal can be checked with the voltages on both sides of R628 (10Ω). The difference for the specified value shown on the level must be within $\pm 6.0\text{mA}$.

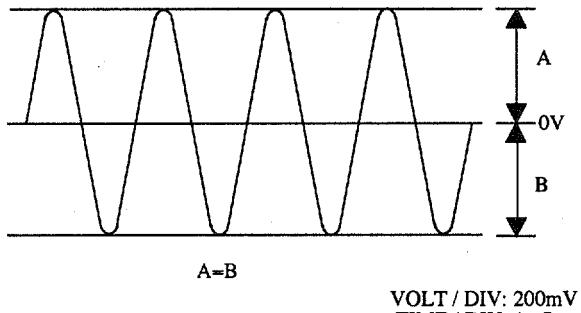


$$\text{Laser current } I_{op} = \frac{\text{Voltage across R628}}{10\Omega}$$

8. Tracking Balance Adjustment



- 1) Connect an oscilloscope to the test points TP6 (TE) and TP5 (VC).
- 2) Start the CD test mode.
- 3) Insert test disc TCD-782 (YEDS-18) and become traverse mode of CD test mode.
- 4) Adjust SFR602 so that the traverse waveform on the oscilloscope is vertically symmetrical as shown in the figure below.
- 5) After the adjustment is completed, remove the connected lead wires from the terminals.
- 6) Cancel the CD test mode.



9. Tracking Gain Adjustment

A servo analyzer is necessary in order to perform this adjustment exactly. However, this gain has a margin, so even if it is slightly off, there is no problem. Therefore, do not perform this adjustment.

Focus/tracking gain determines the pick-up follow-up (vertical and horizontal) relative to mechanical noise and mechanical shock when 2-axis device operates. However, as these gains are reciprocal, the adjustment is performed at the point where both gains are satisfied.

- When gain is raised, the noise increases when the 2-axis device operates increases.
- When gain is lowered, it is more susceptible to mechanical shock and skipping occurs more easily.

When gain adjustment is not satisfied, the symptoms below appear.

Symptoms	Gain	(Focus)	Tracking
• The time until music starts becomes longer for STOP → ►PLAY or automatic selection (◀▶ buttons pressed.) (Normally takes about 2 seconds.)	low	low or high	
• Music does not start and disc continues to rotate for STOP → ►PLAY or automatic selection (◀▶ buttons pressed.)	—	low	
• Disc stops to rotate shortly after STOP → ►PLAY.	low or high	—	
• Sound is interrupted during PLAY, or time counter display stops.	—	low	
• More noises during the 2-axis device operation.	high	high	

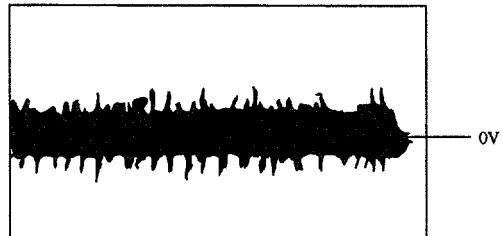
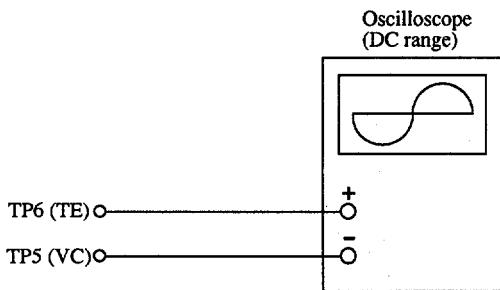
The following is simple adjustment method.

– Simple adjustment –

Note : Since exact adjustment cannot be performed, remember the positions of the controls before performing the adjustment.

If the positions after the simple adjustment are only a little different, return the controls to the original position.

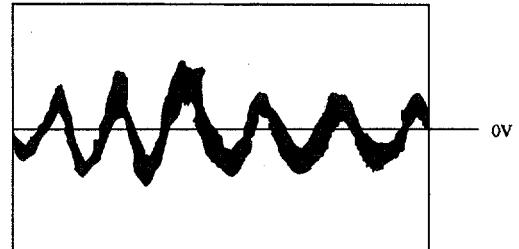
Procedure :



• Incorrect example

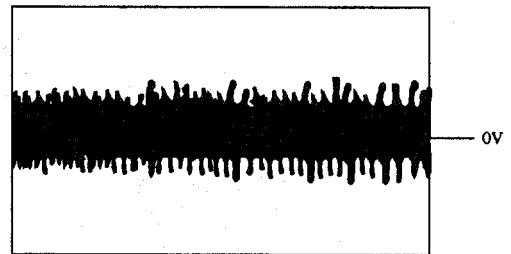
Low tracking gain

(The fundamental wave appears as compared with the waveform adjusted)



High tracking gain

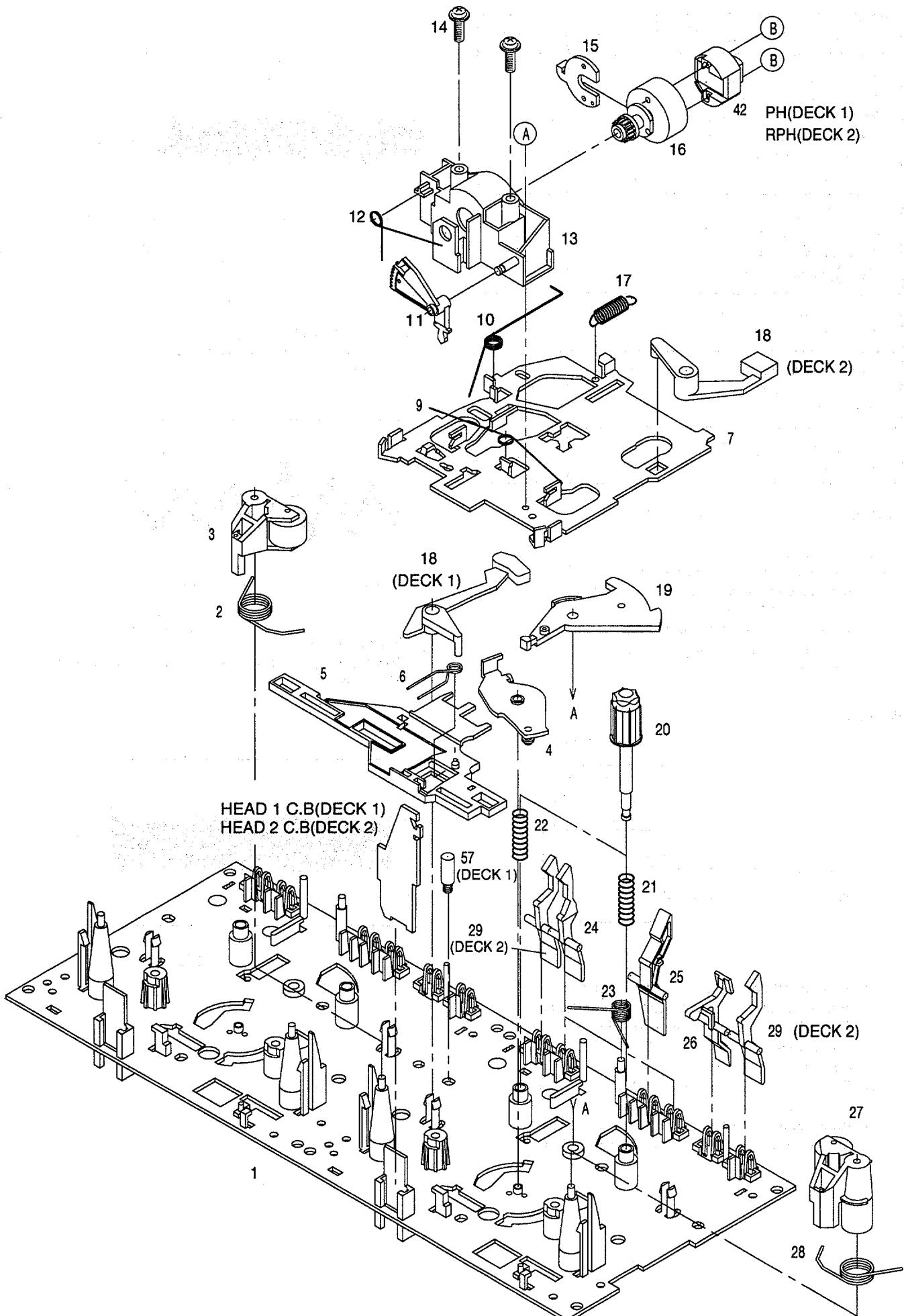
(The frequency of the fundamental wave is higher than in low gain)

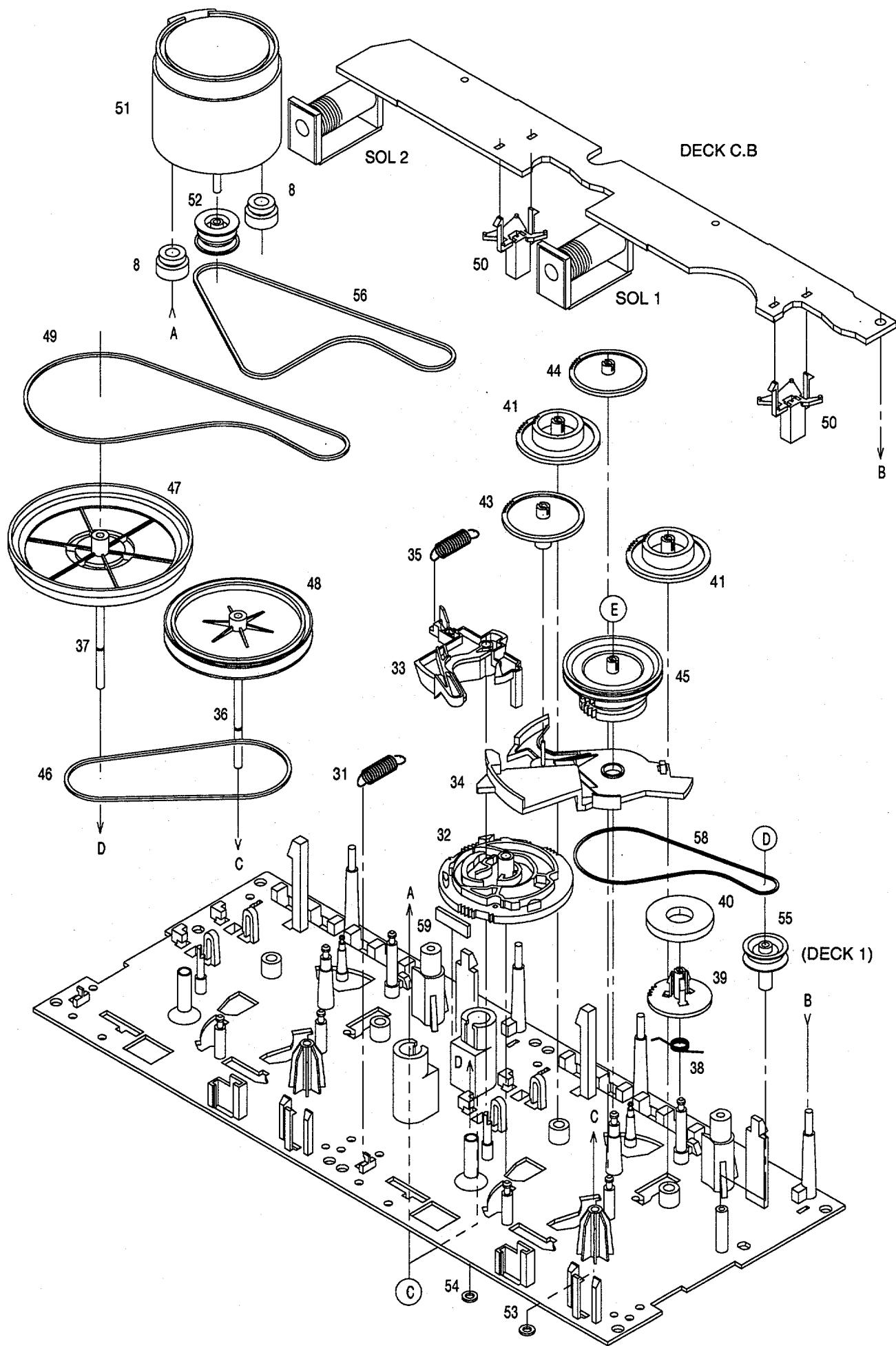


- 1) Keep the set horizontal. (If the set is not kept horizontally, this adjustment cannot be performed due to the gravity against the 2-axis device.)
- 2) Insert test disc TCD-782 (YEDS-18) and play back the second composition.
- 3) Connect an oscilloscope to TP6 (TE) of the 3CD MAIN C.B.
- 4) Adjust SFR603 so that the waveform appears as shown in the figure below. (tracking gain adjustment)

VOLT / DIV: 50mV
TIME / DIV: 1mS

TAPE MECHANISM EXPLODED VIEW 1 / 1

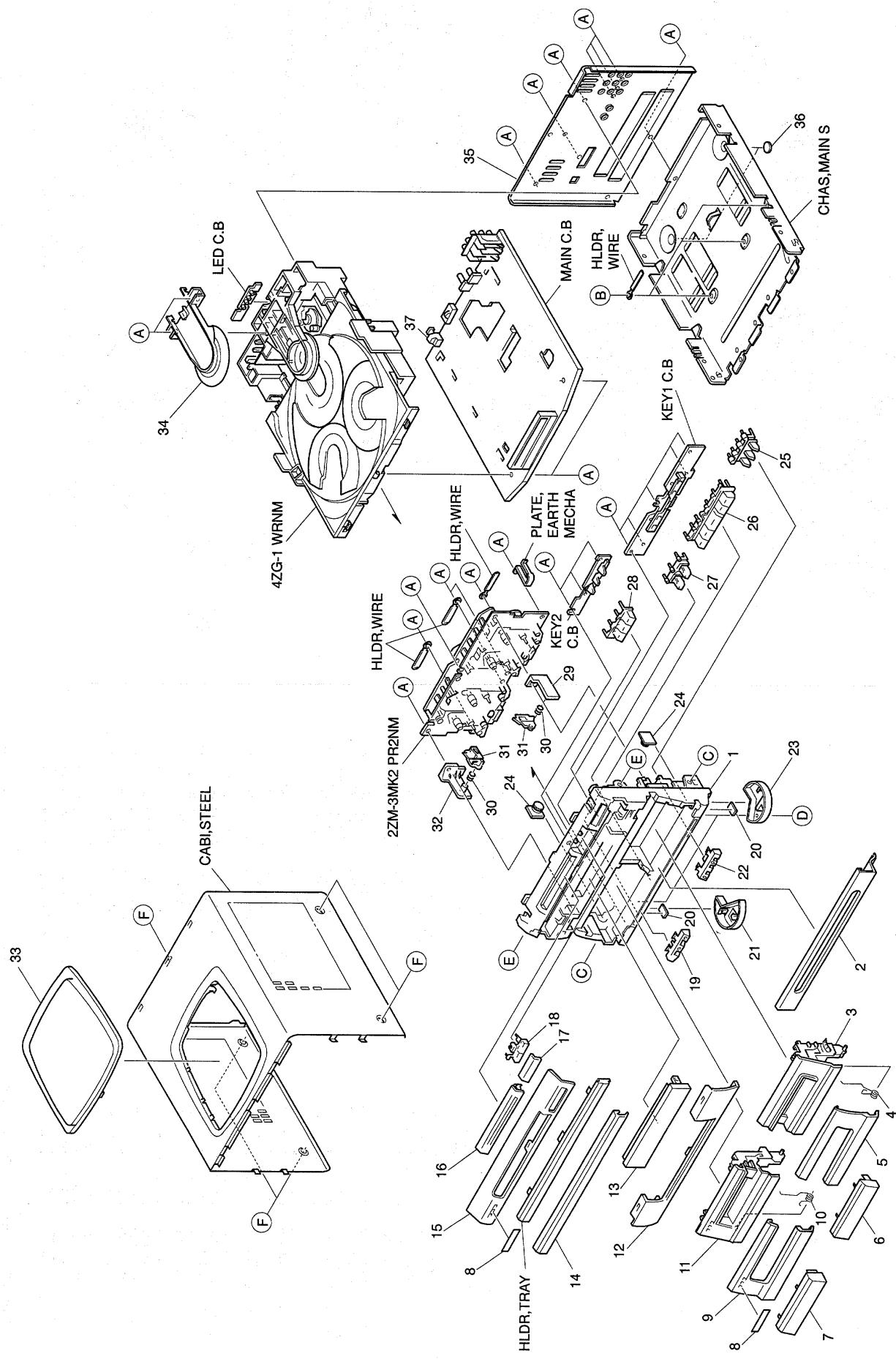




TAPE MECHANISM PARTS LIST 1 / 1

If can't understand for Description please kindly refer to "REFERENCE NAME LIST".

REF. NO.	PART NO.	KANRI NO.	DESCRIPTION	REF. NO.	PART NO.	KANRI NO.	DESCRIPTION
1	82-ZM3-301-519		CHAS ASSY,M2	36	82-ZM1-236-019		CAPSTAN N 2-41.5
2	82-ZM1-258-110		SPR-T,PINCH L	37	82-ZM1-239-019		CAPSTAN N 2.2-41.7
3	82-ZM1-341-110		LVR ASSY,PINCH L2	38	82-ZM1-322-019		SPR-T,FR60
4	82-ZM1-333-010		PLATE,LINK 2	39	82-ZM1-220-219		GEAR, IDLER
5	82-ZM1-266-11K		LVR,DIR	40	82-ZM3-616-019		RING MAGNET 4
6	82-ZM1-214-010		SPR-T,DIR	41	82-ZM1-216-31K		GEAR, REEL
7	82-ZM1-206-81K		CHAS,HEAD	42	87-A90-319-010		HEAD,PH HADKH2 FPC
8	82-ZM3-307-019		CUSH-G,DIA3.7-8-3.2	42	87-A90-320-010		HEAD,RPH HADKH5 FPC
9	82-ZM1-269-219		SPR-T,BRG	43	82-ZM1-225-21K		GEAR, FR
10	82-ZM1-219-119		SPR-T,LINK	44	82-ZM1-226-019		GEAR,REW
11	82-ZM1-210-119		GEAR,H T	45	82-ZM3-333-310		SLIP DISK ASSY 2
12	82-ZM1-213-019		SPR-T,HEAD	46	82-ZM1-338-010		BELT,FR4
13	82-ZM1-207-619		GUIDE,TAPE	47	82-ZM1-349-110		FLY-WHL,R W(DECK 2)
14	86-ZM4-206-010		S-SCREW,AZIMUTH	47	82-ZM3-338-110		FLY-WHL,R3 W(DECK 1)
15	82-ZM1-314-119		PLATE,HEAD	48	82-ZM1-348-010		FLY-WHL,L W(DECK 2)
16	82-ZM1-208-119		HLDR,HEAD	48	82-ZM1-348-010		FLY-WHL,L W(DECK 1)
17	82-ZM1-218-019		SPR-E,BB	49	82-ZM3-329-210		BELT,SBU R2
18	82-ZM1-263-110		LVR,EJECT L (DECK 1)	50	82-ZM1-245-210		HLDR,IC
18	82-ZM1-264-010		LVR,EJECT R (DECK 2)	51	87-045-347-019		MOT,SHU2L 70(M1)
19	82-ZM1-222-21K		LVR,PLAY	52	82-ZM3-221-010		PULLEY,MOT 2M
20	82-ZM1-217-319		REEL TABLE	53	82-ZM1-288-019		SH,1.63-3.2-0.5 SLT
21	82-ZM1-244-510		SPR-C,BT	54	80-ZM6-243-019		SH,1.75-3.6-0.5 SLT
22	82-ZM1-285-310		SPR-C,BT L	55	82-ZM3-335-210		PULLEY,COUPLER M3(DECK 1)
23	82-ZM1-257-019		SPR-T,CAS	56	82-ZM3-337-010		BELT,SBU MOT 2
24	82-ZM1-241-319		LVR,MC	57	82-ZM3-339-010		SHAFT,COUPLER N3(DECK 1)
25	82-ZM1-242-019		LVR,CAS	58	86-ZM1-206-010		BELT,MAIN L
26	82-ZM1-243-019		LVR,STOP	59	82-ZM3-340-010		SH,BELT D2
27	82-ZM1-344-110		LVR ASSY,PINCH R2	A	85-ZM3-202-010		S-SCREW,TG
28	82-ZM1-259-110		SPR-T,PINCH R	B	80-ZM6-207-019		V+1.6-7
29	82-ZM1-240-11K		LVR,REC (DECK 2)	C	82-ZM3-318-019		S-SCRW MOTOR M2
31	82-ZM1-255-319		SPR-E,LVR DIR	D	87-B10-043-010		W-P,0.99-4-0.25 SLT
32	82-ZM3-305-01K		GEAR,CAM M2	E	82-ZM3-334-010		PW,2.16-6-0.4
33	82-ZM1-227-21K		LVR,TRIG				
34	82-ZM3-306-11K		LVR,FR M2				
35	82-ZM1-265-119		SPR-E,TRIG				

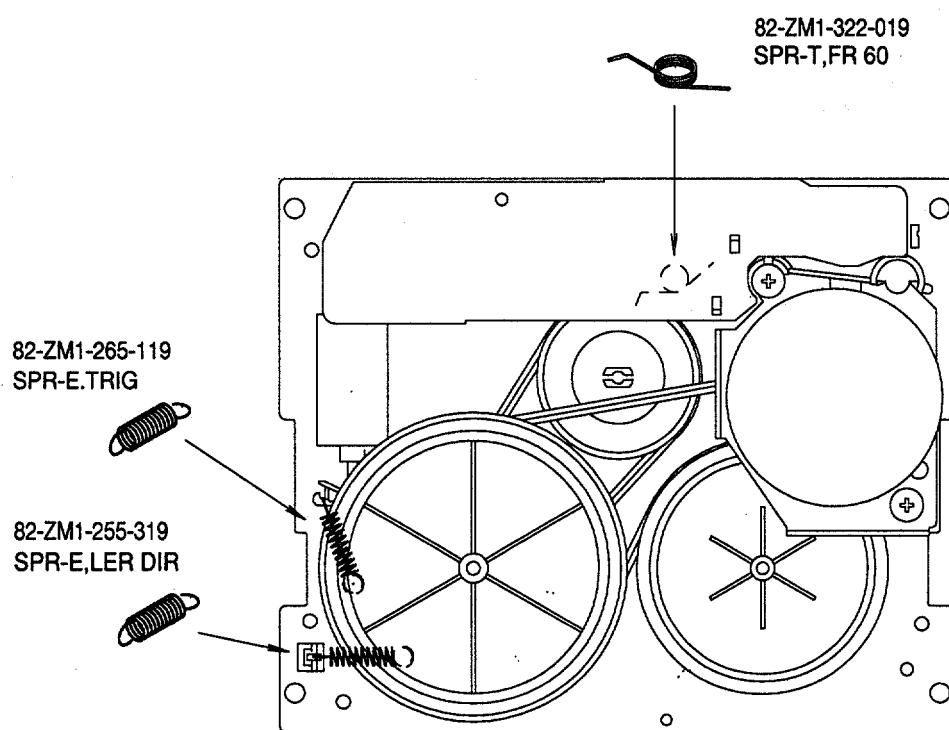
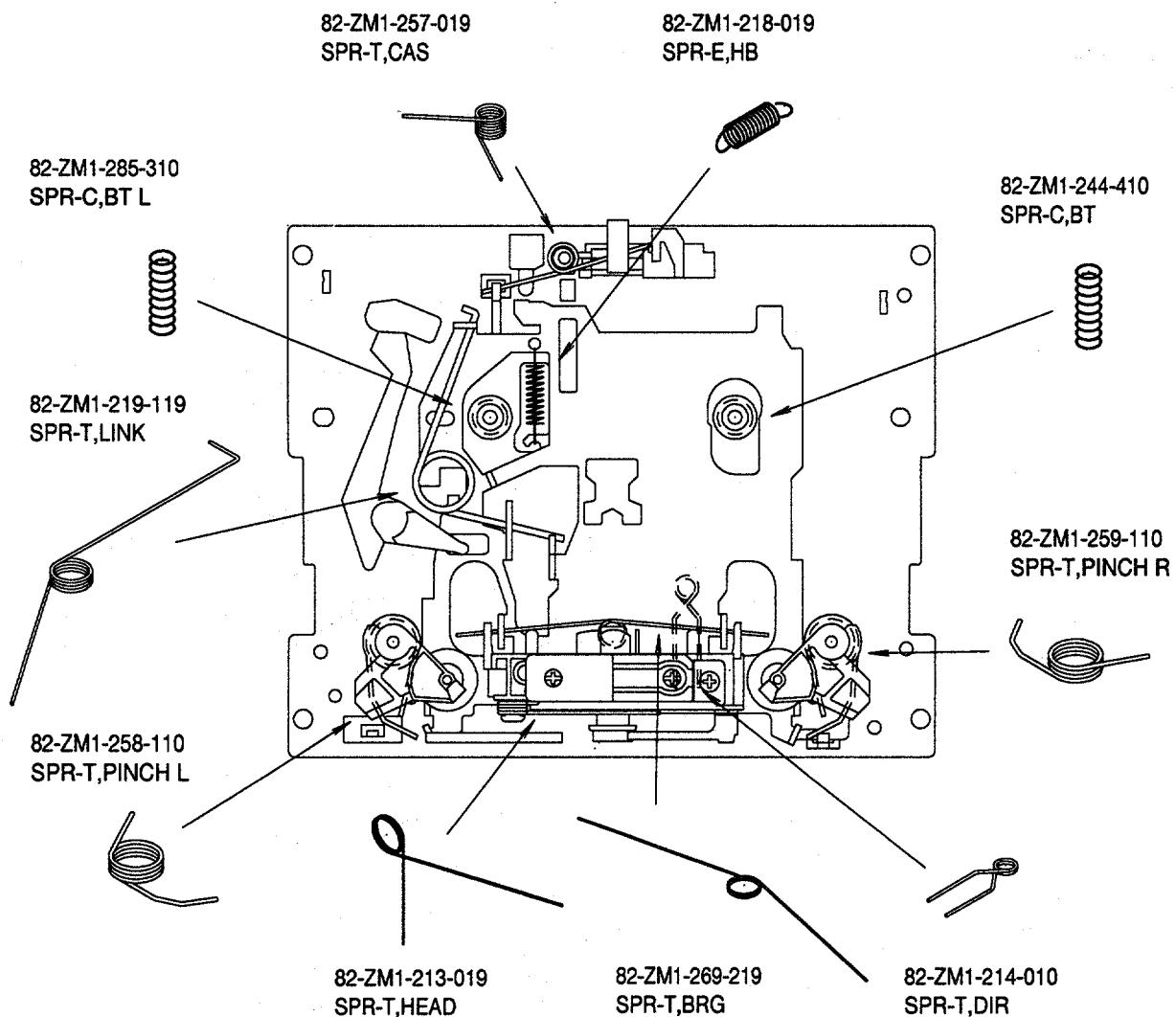


MECHANICAL PARTS LIST 1 / 1

If can't understand for Description please kindly refer to "REFERENCE NAME LIST".

REF. NO.	PART NO.	KANRI NO.	DESCRIPTION	REF. NO.	PART NO.	KANRI NO.	DESCRIPTION
1	87-NV1-001-010	CABI,FR		26	87-NV1-023-010		KEY,ASSY OPE
2	87-NV1-045-010	PANEL,CONTROL		27	87-NV1-013-010		KEY,REC
3	87-NV1-004-010	BOX,CASS 2		28	87-NV1-012-010		KEY,DISC
4	83-NV4-202-110	SPR-T,EJECT 2		29	87-NF4-217-010		HLDR,LOCK 2
5	87-NV1-044-010	PANEL,CASS 2		30	82-NF5-228-010		SPR-C,LOCK
6	87-NV1-009-010	WINDOW,CASS 2		31	82-NF5-229-010		PLATE,LOCK
7	87-NV1-008-010	WINDOW,CASS 1		32	87-NF4-216-010		HLDR,LOCK 1
8	87-B00-002-010	BADGE,AIWA 30 ABS SIL		33	86-NF6-007-010		WINDOW,TOP
9	87-NV1-043-010	PANEL,CASS 1		34	84-ZG1-011-010		REFLECTOR,CD
10	83-NV4-201-110	SPR-T,EJECT 1		35	87-NV1-002-010		PANEL,REAR YSNM
11	87-NV1-003-010	BOX,CASS 1		36	82-NV1-213-010		FELT,DIA12-2
12	87-NV1-042-010	PANEL,DISPLAY		37	84-ZG1-244-210		CABI,OPTICAL
13	87-NV1-006-010	WINDOW,DISPLAY		A	87-067-703-010		TAPPING SCREW, BVT2+3-10
14	87-NV1-041-010	PANEL,TRAY		B	87-571-092-410		TAPPING SCREW, VIT+3-4
15	87-NV1-040-010	PANEL,CD		C	87-591-094-410		TAPPING SCREW, QIT+3-6
16	87-NV1-007-010	WINDOW,CD		D	87-067-777-010		BVTT+3-6 BLK W/CONVEX
17	87-NV1-046-010	PANEL,OPEN		E	87-721-097-410		QT2+3-12 GLD
18	87-NV1-011-010	KEY,OPEN		F	87-067-641-010		UTT2+3-8(W/O SLOT)BL
19	87-NV1-047-010	PANEL,KEY REC					
20	80-VT1-202-010	FELT,12.5-15.5-2					
21	87-NT1-015-010	RING,FOOT L					
22	87-NV1-048-010	PANEL,KEY DOLBY					
23	87-NT1-035-010	RING,FOOT R					
24	87-063-165-010	OIL-DMPR 150					
25	87-NV1-014-010	KEY,DOLBY					

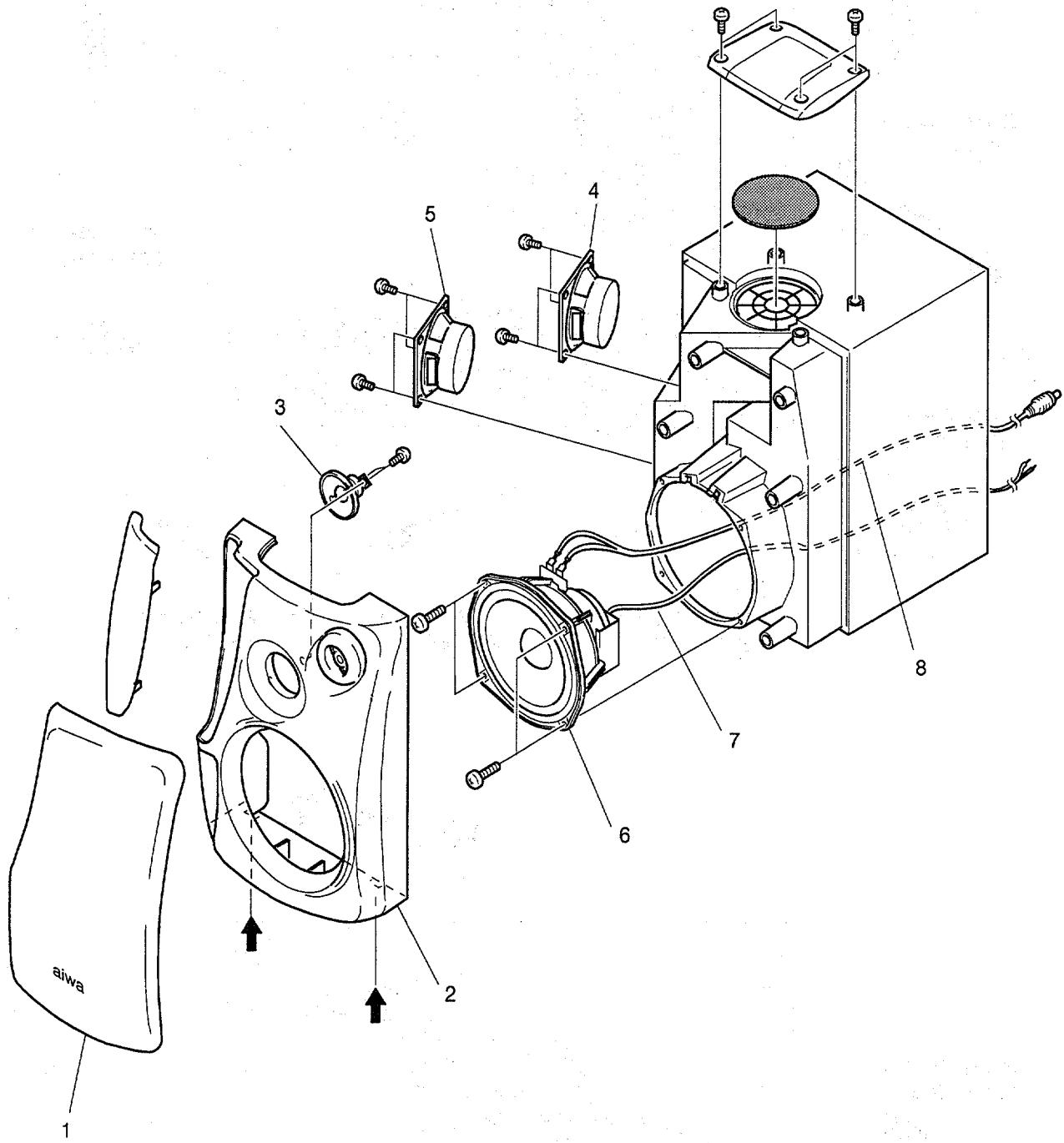
SPRING APPLICATION POSITION



MODEL NO. SX-ANH100

SPEAKER EXPLODED VIEW 1 / 1

Insert a flat-bladed screwdriver into the position indicated by the arrows and remove the panel.
Remove the screws of each speaker unit and then remove the speaker units.



SPEAKER PARTS LIST 1 / 1 (SX-ANH100)

If can't understand for Description please kindly refer to " REFERENCE NAME LIST ".

REF. NO.	PART NO.	KANRI NO.	DESCRIPTION	REF. NO.	PART NO.	KANRI NO.	DESCRIPTION
1	87-NS1-007-010		GRILLE, FRAME ASSY R	4	86-NS4-606-010		SPKR, F 80
1	87-NS1-009-010		GRILLE, FRAME ASSY L	5	86-NS4-604-010		SPKR, M 80
2	87-NS1-001-010		PANEL, FR ST R	6	86-NS1-602-010		SPKR, W140
2	87-NS1-002-010		PANEL, FR ST L	7	85-NS6-611-019		SPEAKER CORD Y/B
3	86-NS4-608-010		SPKR, T 50	8	83-NS5-613-019		SPEAKER CORD ASSY

ACCESSORIES / PACKAGE LIST

If can't understand for Description please kindly refer to " REFERENCE NAME LIST ".

REF. NO.	PART NO.	KANRI NO.	DESCRIPTION
1	87-NT1-906-010		IB,E(EGFSI)E
2	85-NF5-631-010		RC-T501
3	87-006-225-010		AM LOOP ANT NC2
4	87-043-106-010		WIRE, FM ANT (Z)

REFERENCE NAME LIST

ELECTRICAL SECTION		MECHANICAL SECTION	
DESCRIPTION	REFERENCE NAME	DESCRIPTION	REFERENCE NAME
ANT C- C-CAP C-CAP TN C-COIL	ANTENNAS CHIP CAP, CHIP CAP, CHIP TANTALUM COIL, CHIP	ADHESIVE AZ BAR-ANT BAT BATT	SHEET ADHESIVE AZIMUTH BAR-ANTENNA BATTERY BATTERY
C-DI C-DIODE C-FET C-FOTR C-JACK	DIODE, CHIP DIODE, CHIP FET, CHIP FILTER, CHIP JACK, CHIP	BRG BTN CAB CASS CHAS	BEARING BUTTON CABINET CASSETTE CHASSIS
C-LED C-RES C-SFR C-SLIDE SW C-SW	LED, CHIP RES, CHIP SFR, CHIP SLIDE SWITCH, CHIP SWITCH, CHIP	CLR CONT CRSR CU CUSH	COLLAR CONTROL CURSOR CUSHION CUSHION
C-TR C-VR C-ZENER CAP, CER CAP, E	TRANSISTOR, CHIP VOLUME, CHIP ZENER, CHIP CAP, CERA-SOL CAP, ELECT	DIR DUBB FL FR	DIRECTION DUBBING FRONT LOADING FLYWHEEL FRONT
CAP, M/F CAP, TC CAP, TC-U CAP, TN CERA FIL	CAP, FILM CAP, CERA-SOL CAP, CERA-SOL SS CAP, TANTALUM FILTER, CERAMIC	FUN G-CU HDL HIMERON HINGE, BAT	FUNCTION G-CUSHION HANDOL CLOTH HINGE, BATTERY
CF DL E/CAP FILT FLTR	FILTER, CERAMIC DELAY LINE CAP, ELECT FILTER FILTER	HLDR HT-SINK IB IDLE IND, L-R	HOLDER HEAT SINK INSTRUCTION BOOKLET IDLER INDICATOR, L-R
FUSE RES MOT P-DIODE P-SNSR P-TR	RES, FUSE MOTOR PHOTO DIODE PHOTO SENSER PHOTO TRANSISTOR	KEY, CONT KEY, PRGM KNOB, SL LBL LID, BATT	KEY, CONTROL KEY, PROGRAM KNOB, SLIDE LABEL LID, BATTERY
POLY VARI PPCAP PT PTR, RES RC	VARIABLE CAPACITOR CAP, PP POWER TRANSFORMER PTR, MEFL REMOTE CONTROLLER	LID, CASS LVR P-SP PANEL, CONT PANEL, FR	LID, CASSETTE LEVER P-SPRING PANEL, CONTROL PANEL, FRONT
RES NF RESO SHLD SOL SPKR	RES, NON-FLAMMABLE RESONATOR SHIELD SOLENOID SPEAKER	PRGM PULLY, LOAD MO RBN S- SEG	PROGRAM PULLY, LOAD MOTOR RIBBON SPECIAL SEGMENT
SW, LVR SW, RTRY SW, SL TC CAP THMS	SWITCH, LEVER SWITCH, ROTARY SWITCH, SLIDE CAP, CERA-SOL THERMISTOR	SH SHLD-SH SL SP SP-SCREW	SHEET SHIELD-SHEET SLIDE SPRING SPECIAL-SCREW
TR TRIMER TUN-CAP VIB, CER VIB, XTAL	TRANSISTOR CAP, TRIMMER VARIABLE CAPACITOR RESONATOR, CERAMIC RESONATOR, CRYSTAL	SPACER, BAT SPR SPR-P SPR-PC-PUSH T-SP	SPACER, BATTERY SPRING P-SPRING P-SPRING, C-PUSH T-SPRING
VR ZENER	VOLUME DIODE, ZENER	TERM TRIG TUN VOL W	TERMINAL TRIGGER TUNING VOLUME WASHER
		WHL WORM-WHL	WHEEL WORM-WHEEL

サービス技術ニュース	
番号	連絡内容
G- -	
G- -	
G- -	

**アイワ株式会社
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