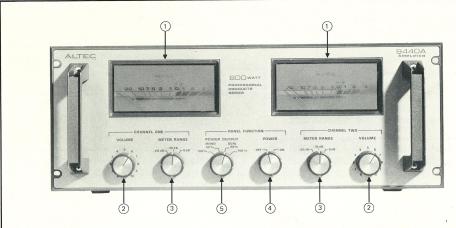
OPERATING INSTRUCTIONS



FRONT PANEL CONTROLS AND FEATURES



	2 3	5 4 3 2
1	VU meters	Display output for channels 1 and 2. Meters illuminate when power is on.
2	VOLUME controls	Adjust gain of channels 1 and 2. Rotate clockwise to increase gain.
3	METER RANGE switches	Select full scale range of VU meter. Selectable ranges are 0 dB, -10 dB and -20 dB. Maximum output of each range (0 dB, -10 dB and -20 dB) coincides with zero dB on meter scale.
4	POWER switch	Applies primary power to amplifier. VU meters illuminate when switch is turned to ON.
5	POWER OUTPUT switch	In DUAL position, channel 1 and channel 2 inputs are amplified and applied to CH 1 and CH 2 OUT jacks, for separate sound reproduction of each channel. In MONO position, channels are bridged at BAL MONO OUT jacks for 800-watt floating capability, or dual 200-watt capability through individual outputs; signal is provided from channel 1 input. Channel 2 input is removed during MONO operation. When fan accessories are not used, and amplifier is used for continuous service under heavy load, turn switch to 40% position to prevent amplifier from cycling due to excessive heat. In all other applications, use 100% position for full power output.

INSTALLATION

RACK MOUNTING

The 9440A amplifier is designed for mounting in a standard 19-inch equipment rack, requiring a vertical rack space of 7 inches. Use four 10-32 x ½" screws to secure the front panel against the rack.

VENTILATION

The 9440A must be adequately ventilated to prevent excessive temperature rise of output transistors. An inch or two of space should be left *behind*, *above and below* the 9440A to ensure adequate cooling. Four feet raise the amplifier from a flat surface to allow free passage of air beneath the chassis.

CAUTION -

Do not block ventilation slots in the top of the chassis, the bottom of the chassis, or the heat sink shroud at the back of the amplifier. Be sure free circulation of air is not restricted by other equipment, walls, curtains or furniture.

The 9440A should not be placed too close to other heat-generating equipment or in areas where ambient temperature exceeds 55°C (133°F).

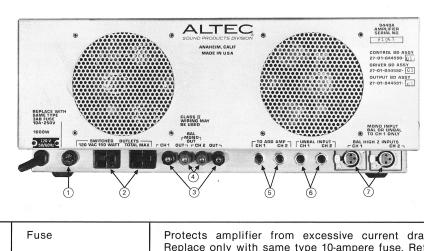
If the 9440A is mounted in an equipment rack or cabinet with other heat-producing equipment mounted above and/or below, space must be provided between the units to prevent excessive temperature rise. The ALTEC 10399 134" Perforated Panel is recommended for this purpose.

When several amplifiers or other heatproducing units are installed in a single rack or cabinet, acceptable air temperature may be in doubt. To determine temperature conditions, operate the system until temperature stabilizes, then measure air temperature with a bulb-type thermometer held at the bottom of the uppermost amplifier. Do not let the thermometer bulb touch metal because the metal may be hotter than the ambient air. If air temperature exceeds 55°C (133°F), the equipment should be spaced farther apart or a blower should be installed to ventilate the cabinet.

PARTINIS

Specifications and components subject to change without notice. Overall performance will be maintained or improved.





	1	Fuse	Protects amplifier from excessive current drain. Replace only with same type 10-ampere fuse. Refer to qualified service personnel if fuse blows re- peatedly. A 15-ampere fuse may be used for test purposes only.
	2	SWITCHED AC OUTLETS	Provide auxiliary line voltage, turned on by POWER switch on front panel. Total current from both outlets limited to 1.5A by internal fuse.
	3	CH 1 OUT CH 2 OUT speaker terminals	Connect channel 1 and channel 2 speakers for corresponding channels. Two pair of standard GR jacks have red (+) and black (-) polarity designations. When two or more speakers are connected in parallel, combined impedance should be not less than 4 ohms.
	4	BAL MONO OUT speaker terminals	Connect balanced output to speaker system. NEVER CONNECT EITHER TERMINAL TO GROUND. When two or more speakers are connected in parallel, combined impedance should be not less than 8 ohms. Operate POWER OUTPUT switch in MONO mode.
	5	TO ADD AMP jacks	Accept cables to connect another 9440A amplifier in parallel.
	6	UNBAL INPUT jacks	Accept cables from high-impedance (nominal 15K Ω) microphones, AM/FM tuners, tape recorders, etc.
	7	BAL HIGH Z INPUTS receptacles	Accept cables from high-impedance (nominal 15KΩ) microphones, AM/FM tuners, tape recorders, etc. Two transformer-isolated, balanced input channels

have XLR3-31 type connectors.

If the 9440A amplifier becomes too warm, due to continuous use at high output, two accessory fans should be installed on the rear of the heat sink shroud. Each fan should produce at least 30 cubic feet per minute at a static pressure of 0.04 inch of water. Fans provided in the ALTEC Model 105878 Fan Kit meet this requirement.

ELECTRICAL

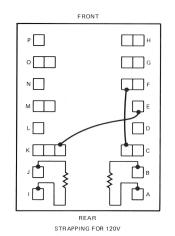
120-Volt, 50/60 Hz Power Connections

Equipment supplied for domestic use is provided with the power transformer primary strapped for 120 volts, as shown in Figure 1. The power input nameplate, adjacent to the power cord on the chassis, is mounted to show the appropriate side specifying connections. Verify that line voltage is in accordance with the voltage rating *before* connecting primary power.

240-Volt, 50/60 Hz Power Connections

Export equipment is provided with the power transformer primary strapped for

240 volts, as shown in Figure 1. The power input nameplate, adjacent to the power cord on the chassis, is mounted to show the appropriate side specifying connections.



For a 9440A previously wired for 120 volts ac primary power, use the following procedure to change wiring for 240 volts ac operation. Refer line voltage change to qualified service personnel only.

- Step 1. Turn POWER switch to OFF.
- Step 2. Remove power cord from primary power outlet.
- Step 3. Remove seven screws securing top cover. Lift off top cover to expose transformer terminal board.
- Step 4. Locate strap connecting terminals C and F (see Figure 1). Remove strap and store for possible later use.
- Step 5. Locate strap connecting terminals E and K. Remove spade connector from terminal K and connect it to terminal F. This strap now connects terminals E and F as shown in Figure 1.
- Step 6. Install top cover on amplifier and secure with seven screws removed in Step 3.
- Step 7. Remove voltage rating plate from chassis; reverse and reinstall to show 240-volt rating.

NOTE -

Switched ac outlets provide 120V ac when amplifier is strapped for 240V ac operation. Maximum acceptable power drain remains 150 watts combined.

Input Connections — Unbalanced

Connections to UNBAL INPUT for channels 1 and 2 are made with two-wire cables terminated with standard 1/4" diameter phone plugs.

The UNBAL INPUT connects preamplifiers, mixers, etc., to channels 1 and 2 (see Figure 2). Nominal input level should be approximately 0.5 volt rms. If the 9440A is used in a mono mode, use the channel 1 (CH 1) jack only; connection of an input to channel 2 (CH 2), with no connection to channel 1, will result in no output.

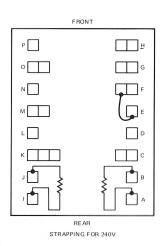


Figure 1. Strapping of Power Transformer for 120V and 240V Power

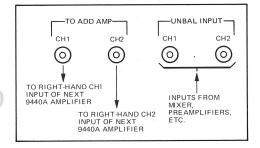


Figure 2. Unbalanced High-Impedance Input Connections

- NOTE -

The UNBAL INPUT overrides the BAL HIGH Z INPUTS when cable connections are made.

The TO ADD AMP jacks are in parallel with the respective CH 1 and CH 2 input jacks. These jacks are used to connect 9440A amplifiers in multiple configurations. Multiple amplifiers can be connected when the first amplifier receives input to either the UNBAL INPUT or the BAL HIGH Z INPUTS. Figure 3 shows a typical multiple configuration of 9440A amplifiers.

Unbalanced output at the TO ADD AMP jacks may be employed from a balanced input at the XLR3-31 jacks, as shown in Figure 4.

NOTE .

The input impedance presented to the program source is equal to 15K divided by the total number of amplifiers operating from that source. The volume control of the first amplifier does not control the other amplifiers.

Input Connections — Balanced

Connections to BAL HIGH Z INPUTS for channels 1 and 2 are made with shielded two-wire cables terminated with XLR3-type

TO ADD AMP -UNBAL INPUT CH2 NO. 1 (0, (•) ◐ (\mathbf{r}) TO ADD AMP UNBAL INPUT FROM MIXER, PREAMPLIFIER, CH1 FTC NO. 2 (GANGED \odot (E 9 CH1 CH₂ CH₁ CH₂ NO. 3 (GANGED TO NO. 2) • $oldsymbol{oldsymbol{arepsilon}}$ TO INPUTS OF NEXT AMPLIFIER

Figure 3. 9440A Amplifiers Connected in Multiple

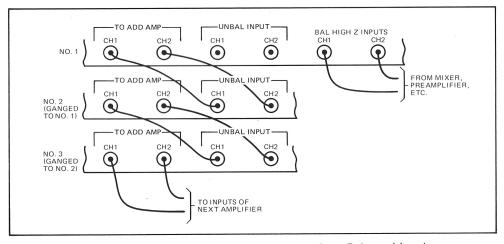


Figure 4. Application of Unbalanced Output from Balanced Input

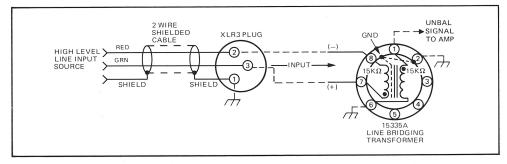


Figure 5. Balanced Input Conections to 9440A

plugs. Wiring of these plugs is shown in Figure 5.

Inputs may be from mixers, preamplifiers, tuners, phono or other high-level line sources. Nominal input level should be approximately 0.5 volt rms. If the 9440A is used in a mono mode, use channel 1 (CH1) jack only; connection of an input to channel 2 (CH 2), with no connection to channel 1, will result in no output.

- NOTE -

Inputs connected to the UNBAL IN-PUT override inputs to BAL HIGH Z INPUTS.

One plug-in ALTEC 15335A Line Transformer accessory is required for each of the two channels for BAL HIGH Z INPUTS. The transformer accessories are plugged into the corresponding receptacles within the chassis (see Figure 5). Refer transformer installation to qualified service personnel only.

Speaker Connections

Connections to channel 1 (CH 1 OUT) and channel 2 (CH 2 OUT) speaker outputs are made with two-wire cables. Both red (+) speaker output jacks are isolated from chassis ground. Do not place either of these jacks in a circuit to chassis ground. Combined impedance of the speaker system should be not less than 4 ohms for each of the two speaker outputs (see Figure 6).

Connections to BAL MONO OUT combine the two channels into one output. NEVER CONNECT EITHER RED OUTPUT TER-MINAL TO CHASSIS GROUND. Combined impedance of the speaker system should be not less than 8 ohms (see Figure 6).

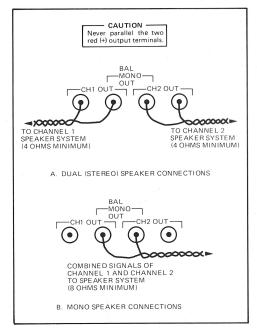


Figure 6. Mono and Dual Speaker Connections

BLOCK DIAGRAM

A system block diagram of the 9440A is shown in Figure 8.

SERVICE

This service information is for the use of authorized warranty stations (dealers) only. Service must be performed by an ALTEC Qualified Service Representative. For factory service, ship the 9440A prepaid to:

ALTEC Customer Service 131 West Katella Avenue Anaheim, California 92803

For additional information or technical assistance, call (714) 774-2900 or Telex 65-5415.

REPAIR PERFORMED BY OTHER THAN AUTHORIZED WARRANTY STATIONS (DEALERS) OR OTHER QUALIFIED PERSONNEL SHALL VOID THE WARRANTY PERIOD OF THIS UNIT. TO AVOID LOSS OF WARRANTY, SEE YOUR NEAREST ALTEC AUTHORIZED DEALER OR CALL ALTEC CUSTOMER SERVICE DIRECTLY AT (714) 774-2900, OR WRITE:

Customer Service

ALTEC 131 West Katella Avenue Anaheim, California 92803

Access

The top cover, heat sink assembly, and front panel assembly can be removed for access to corresponding areas of the chassis. Six screws secure the top cover to the chassis.

To remove the heat sink assembly, first take off the heat sink shroud, which is attached to the heat sink with four screws. Remove the four screws which secure the heat sink to the chassis. The heat sink assembly can then be laid back to the extent permitted by the wiring; use care not to bind the wiring at the sides of the chassis.

The four screws which fasten the handles also secure the front panel assembly to the chassis. Remove these four screws and lay back the front panel assembly to the extent permitted by the wiring. Use care to prevent binding the wires between the side supports of the chassis.

Adjustment of Output Null and Bias Potentiometers

- Step 1. Turn VOLUME control fully counterclockwise, METER RANGE switch to 0 dB, and POWER OUT-PUT switch to DUAL 100%.
- Step 2. Turn POWER switch to ON and allow a 5-minute thermal stabilization period.
- Step 3. Remove top cover from chassis for access to driver circuit boards.
- Step 4. Adjust Output Null potentiometer R30 (vertically oriented) on channel 1 (CH-1) driver circuit board for an output of ±3 millivolts dc at CH-1 output terminals. Adjust R30 of channel 2 by similar procedure.
- Step 5a. Insert a clamp-on dc milliammeter over blue wire from terminal E21 of output circuit board of channel

1 (CH-1) (see Figure 7). Set Bias Adjust potentiometer R22 (horizontally oriented) on CH-1 driver circuit board for a current of 75 \pm 5 mA dc. Adjust R22 of channel 2 by similar procedure, inserting clamp-on dc milliammeter over red wire from terminal E20' of CH-2 output circuit board.

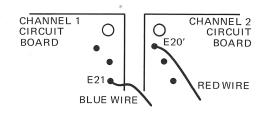


Figure 7. Output Wiring for Use in Setting
Bias Current

- Step 5b. If a clamp-on type dc milliammeter is not available, a standard dc milliammeter may be inserted in series with power leads to CH-1 and CH-2 output circuit boards:
 - (a) Turn off power to 9440A and locate two wires leading from terminal E5 of capacitor C2 to terminals E20 or E20' on output circuit boards.
 - (b) Unsolder these two leads from terminal E5. Identify by continuity check to which output board each wire connects.
 - (c) Insert dc milliammeter in series with one lead, reconnecting the other lead to terminal E5. Turn on power and allow 1 minute for thermal stabilization.
 - (d) Set Bias Adjust potentiometer R22 (horizontally oriented) of corresponding channel for a current of 75 ±5 mA dc.
 - (e) Repeat steps (3) and (4) to adjust R22 of other channel, inserting dc milliammeter in series with other lead. Always turn off power when connecting or disconnecting dc milliammeter.
 - (f) Shut off power and resolder both leads securely to terminal E5.

Step 6. Install top cover on chassis.

Fuse Replacement

If replacement of any fuse is required, determine and correct any cause of failure before installing another fuse. Install an identical fuse. See Parts List.

The primary power fuse is located on the rear of the chassis. See "Rear Panel Connections and Features" of Operating Instructions.

A pigtail 1.5-ampere fuse protects the power transformer against excessive power drain from the SWITCHED OUTLETS at the

rear of the chassis. Maximum acceptable power drain is 150 watts for both outlets combined. For access to the fuse, located on the top of the power transformer, remove the top cover from the chassis.

Lamp Replacement

Four lamps, located behind each VU meter, are accessible after removing the top cover from the chassis. Remove the failed lamp with long-nose pliers and install an identical lamp. See Parts List.

Removal of Printed Circuit Boards

- CAUTION -

Do not warp, bend or twist the circuit boards during handling, or conductors may fracture or open.

When reconnecting cables to the circuit boards, be sure ends of the plastic connectors are flush with the ends of the circuit board connectors, or individual pin plugs will not mate with the proper pin receptacles. Damage as a result of wrong connection is not covered by warranty.

Driver Circuit Boards

Two identical driver circuit boards are installed on each side of the chassis, and are accessible after removing the top cover from the chassis. Grasp the connector (not the wires) and pull off with a rocking motion. The board may then be freed by removing the four screws which fasten it to the side of the chassis.

Output Circuit Boards

Two identical output circuit boards are installed on the heat sink. Remove the top cover and the heat sink shroud from the chassis. Remove the four screws which secure the heat sink to the chassis: lav the heat sink assembly back to the extent permitted by the wiring to gain access to the front side of the board. For access to the back side of the board, and to resistors associated with the driver/output transistors, first remove applicable 14 transistors (channel 1 or channel 2 board) from the heat sink. Then remove the two screws securing the board to the heat sink, and the screw which attaches the connector strap to the board. The board may then be laid back from the heat sink to the extent permitted by the wiring. Do not unsolder wires from the board unless absolutely necessary.

Control Circuit Board

Remove the top cover from the chassis. Disconnect Plug P1 from each driver circuit board at J1. Disconnect three plugs from harness wiring from control circuit board. Remove two screws from each handle on the front panel. Carefully free each side of the panel assembly from the chassis, ensuring that the wire leads do not bind against the side of the chassis. The front panel assembly may be completely separated from the chassis by passing the loose cable connectors through the openings in the chassis.

To separate the circuit board from the front panel, first remove all knobs (except POWER switch) by loosening the set

Figure 8. 9440A Power Amplifier Block Diagram (6D714)

screws with a 3/32" allen wrench. Remove the four nuts on the meter posts. Remove the three screws securing the board to the standoffs; be sure to retain the fiber washers with the screws. Remove the two screws securing the switch bracket to the standoffs (insert screwdriver through holes in the circuit board). The control circuit board now may be freed from the front panel.

If it is necessary to remove the switch bracket from the circuit board, first remove all shaft nuts from the switches and controls. Remove the four screws, washers, and nuts that secure the bracket to the board; be sure to retain the fiber washers with the screws.

Power Output Transistors

Fourteen power output transistors for each channel are located on the rear of the heat sink, as shown in Figure 9. Faulty transistors can be located by using a low-power ohmmeter, such as the Triplett Model 603 FET-VOM, to check the transistor groups given in Table I. Open circuit voltage of the ohmmeter should be less than the 0.6V dc that turns on a silicon PN junction.

With power off to the 9440A, disconnect plug P2 from the driver circuit board. Check the junctions of the transistor groups at the appropriate terminals of plug P2, in the order presented by Table I.

Once a defective indication is found for a given transistor group, remove one transistor at a time from the group for individual check.

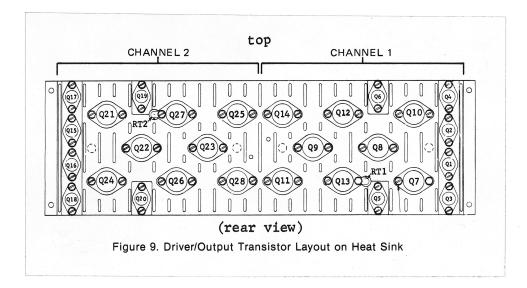
When installing transistors on the heat sink, check that the following conditions are met:

- Mica insulator is not damaged. If damaged, use new insulator.
- 2. No grit or metal particles are lodged between transistor and heat sink.
- 3. Both sides of mica insulator are covered with silicone grease or fluid.
- Mounting screws of transistor are tight (threads not stripped in transistor socket).

An optional testing device is a PN junctioninsensitive continuity checker, easily assembled from common components. A schematic for the checker is given in Figure 10. The checker sounds a tone upon detecing a short circuit or low-resistance path. Since open-circuit voltage of the checker is 200 mV, and short-circuit current is 10 mA or less, any solid state device can be tested for shorts without damage, and without turning on an associated junction.

Table I. Driver/Output Transistor Connections

Plug P2 Terminals	Junction	Channel 1 Transistors	Channel 2 Transistors
1 – 2	EB	Q15, 17, 19	Q1, 3, 5
8 – 9	EB	Q16, 18, 20	Q2, 4, 6
2 – 5	вс	Q15, 17, 19	Q1, 3, 5
5 – 8	ВС	Q16, 18, 20	Q2, 4, 6
1 – 5	EC	Q21, 23, 25, 27, CR1	Q7, 9, 11, 13, CR1
5 – 9	EC	Q22, 24, 26, 28, CR2	Q8, 10, 12, 14, CR2



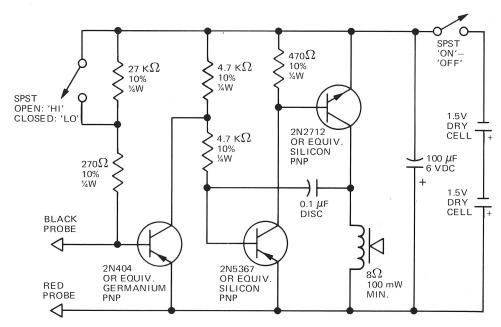


Figure 10. Schematic for PN Junction-insensitive Continuity Checker

PARTS LIST MAIN CHASSIS

Reference Designator	Ordering Number	Name and Description
— A1 A2 A3 C1, 2 CR1, 2, 5, 6 CR3, 4 F1 F2 K1 Q1, 3, 5,15, 17, 19 Q2, 4, 6, 16, 18, 20	24-04-044732-02 27-01-044591-05 27-01-044590-03 15-01-120228-01 48-04-120227-01 51-04-105890-01 51-04-120411-01 45-01-044782-01 48-03-120157-01	screw PCB assy., driver PCB assy., power output

Reference Designator	Ordering Number	Name and Description
Q7, 8, 9, 10, 11, 12, 13, 14, 21, 22, 23, 24, 25, 26, 27, 28	48-03-120392-02	Transistor, 2N6259, selected
R1, 2 RT1, 2 S1	47-01-102151-01 47-09-120248-01 53-01-120484-02	Resistor, 100Ω ± 10%, ¼W Resistor, temp. sensing (moxie) Switch, thermo, open 210°F, close 180°F
S1A, 1B T1 VU1, 2	26-03-044684-01 56-08-007552-01 29-01-044604-07	Switch, power Transformer, power Meter, VU
×		¥

DRIVER PCB ASSEMBLY

Reference Designator	Ordering Number	Name and Description
C1 C2 C3 C4 C5 C6, 17, 21 C7 C8, 9, 20 C10, 11 C12, 15, 16, 19 C13, 14 C18 C22 CR1, 2, 6 CR3, 4, 5, 9 CR7, 8 CR10, 11, 17, 18, 19	15-01-107221-01 15-02-107453-01 15-01-119335-02 15-02-100012-01 15-01-100204-01 15-02-100304-01 15-02-107455-01 15-06-108173-01 15-01-102575-01 15-02-100307-01 15-02-100110-01 15-02-100036-01 48-01-100850-02 48-01-100881-02 48-01-107017-01	Cap., 5 μF, 25V Cap., 150 pF \pm 10%, 100V Cap., 50 μF, 16V Cap., 20 pF \pm 10%, 500V Cap., 10 μF, 25V Cap., 0.001 μF \pm 10%, 100V Cap., 47 pF \pm 10%, 100V Cap., 0.47 μF \pm 20%, 100V Cap., 10μF, 100V Cap., 0.01 μF \pm 20%, 100V Cap., 0.033 μF \pm 20%, 100V Cap., 560 pF \pm 10%, 500V Diode, Zener, 6.2V \pm 5%, 1W Diode, 1N746 Diode, 1N456A
CR12, 13 CR14 CR15, 16 Q1, 3 Q2, 4 Q5, 12 Q6, 13, 16 Q7 Q8 Q9 Q10, 17 Q11 Q14 Q15	48-01-100876-01 not used 48-02-042787-01 48-03-109714-01 48-03-120245-01 48-03-120234-01 48-03-120234-01 48-03-120159-01 48-03-120235-01 48-03-120233-01 48-03-101098-03 48-03-120226-01 48-03-120157-01	Diode, 1N270 Rectifier, silicon, 1A, 400 PIV Transistor, TZ81 Transistor, 2N5550 Transistor, 2N3906, selected Transistor, MPS A93, selected Transistor, MPS U60, selected Transistor, MPS U10, selected Transistor, TZ255 w/heat sink Transistor, MPS A43, selected Transistor, 2N2712, selected Transistor, SJ 4118, selected Transistor, SJ 4117, selected

Reference Designator	Ordering Number	Name and Description
R1, 12, 16 R2, 52 R3, 17, 49 R4 R5, 10, 11 R6 R7, 9 R8 R13 R14 R15 R18 R19, 45, 48 R20 R21 R22, 30 R23 R24, 25, 46, 47, 56 R26, 27 R28, 29 R31, 41 R32, 55 R33, 54 R34, 38 R35, 37 R36 R39, 40 R42, 43 R44 R50, 51	Number 47-01-102163-01 47-01-102182-01 47-01-102151-01 47-01-102179-01 47-01-102181-01 47-03-109430-01 47-01-102167-01 47-01-102167-01 47-01-102149-01 47-01-102159-01 47-01-102156-01 47-01-102156-01 47-01-102156-01 47-01-102180-01 47-01-102180-01 47-01-102180-01 47-01-102180-01 47-01-102186-01 47-01-102186-01 47-01-102186-01 47-01-102186-01 47-01-102061-01 47-01-102061-01 47-01-102065-01 47-01-102065-01 47-01-102065-01 47-01-102186-01 47-01-102186-01 47-01-102085-01 47-01-102085-01 47-01-102085-01 47-01-102085-01 47-01-102085-01 47-01-102085-01 47-01-102085-01 47-01-102085-01 47-01-102085-01 47-01-102184-01	Res., $1KΩ \pm 10\%$, $1/4W$ Res., $39KΩ \pm 10\%$, $1/4W$ Res., $100Ω \pm 10\%$, $1/4W$ Res., $100Ω \pm 10\%$, $1/4W$ Res., $7.5KΩ \pm 5\%$, $1/4W$ Res., $22KΩ \pm 10\%$, $1/4W$ Res., $33KΩ \pm 10\%$, $1/4W$ Res., $40.2KΩ \pm 1\%$, $1/4W$ Res., $511Ω \pm 1\%$, $1/4W$ Res., $6.8KΩ \pm 10\%$, $2/4W$ Res., $6.8KΩ \pm 10\%$, $1/4W$ Res., $100KΩ \pm 10\%$, $1/4W$ Res., $43Ω \pm 5\%$, $1/4W$ Res., $68Ω \pm 10\%$, $1/4W$ Res., $68Ω \pm 10\%$, $1/4W$ Res., $68Ω \pm 10\%$, $1/4W$ Res., $47ΩΩ \pm 10\%$, $1/4W$ Res., $27KΩ \pm 10\%$, $1/4W$ Res., $17KΩ \pm 10\%$, $1/4W$
R53	47-01-102189-01	Res., 150KΩ ± 10%, ¼W

POWER OUTPUT PCB ASSEMBLY

Reference Designator	Ordering Number	Name and Description
CR1, 2 L1 R1, 3, 4, 5, 6,	56-01-018207-01	Rectifier, silicon, 1A, 400 PIV Choke, 5 μ H, air core Res., 1 Ω ± 10%, $1/2$ W
R2, 8	47-01-102925-01	Res., 4.7Ω ± 10%, 1W

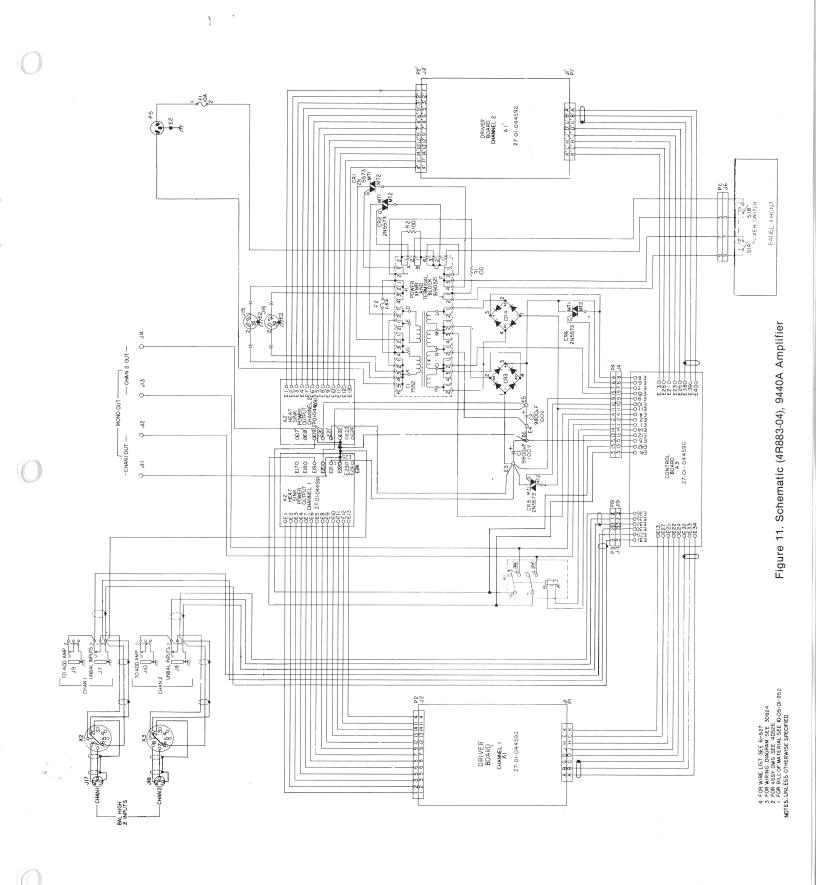
 Reference Designator	Ordering Number	Name and Description
R9, 10, 11, 12, 13, 14, 15, 16, 17	47-01-120244-01	Res., 0.5Ω ±5%, 3W
R18, 19 R20		Res., $12Ω \pm 10\%$, $2W$ Res., $5Ω \pm 10\%$, $5W$

PARTS LIST (Continued)

CONTROL PCB ASSEMBLY

Reference Designator	Ordering Number	Name and Description
C1, 6 C2 C3 C4 C5 C7, 8 C9 CR1, 23 CR2, 3, 4, 5, 7, 8, 9, 10 CR6, 11 CR12, 15 CR13 CR14, 16, 17,	15-06-108173-01 15-01-107452-01 15-01-119381-01 15-02-113220-01 15-01-107500-01 15-01-119229-01 15-02-100109-01 48-01-100858-01 48-01-100876-01 48-02-120246-01 48-01-100881-01 48-02-042787-01	Cap., $0.47 \mu\text{F}, \pm 20\%$, 100V Cap., $10 \mu\text{F}$, 50V Cap., $200 \mu\text{F}$, 25V Cap., $0.01 \mu\text{F} \pm 20\%$, 100V Cap., $100 \mu\text{F}$, 50V Cap., $100 \mu\text{F}$, 50V Cap., $16 \mu\text{F} \pm 10\%$, 50V NP Cap., $16 \mu\text{F} \pm 20\%$, 100V Diode, Zener, $12\text{V} \pm 5\%$, 2W Diode, germanium, $1\text{N}270$ Diode, $1\text{N}456\text{A}$ SCR, C103YY, 0.8A , 60V Stabistor, STB-567 Rectifier, 1A , 400 PIV
18, 20, 21, 22 CR19 DS1, 2, 3, 4, 5, 6, 7, 8 Q1 Q2 Q3 Q4 Q5 Q6 Q7 Q8	48-01-108575-01 39-01-107475-02 48-03-107448-03 48-03-041440-02 48-03-120233-01 48-03-120234-01 48-03-113716-01 48-03-107447-03 48-03-041840-02 48-03-119140-02	Diode, Zener, 30V ± 5%, 2W Lamp, midg. flange, 28V, 40 mA Transistor, 2N5322, selected Transistor, 2N3906, selected Transistor, MPS A43 Transistor, MPS A93 Transistor, unijunction, 2N2646 Transistor, 2N5320, selected Transistor, 2N3055, selected Transistor, 2N5308, selected

Reference Designator	Ordering Number	Name and Description
Q9 R1, 8 R2, 4, 9, 11 R3, 10 R5, 12 R6, 13 R7, 14 R15, 16, 20, 22, 27, 29, 37	48-03-101098-03 47-01-102065-01 47-01-102090-01 47-01-102078-01 47-01-102066-01 47-01-102091-01 47-01-102058-01 47-01-102163-01	Transistor, 2N2712, selected Res., $300\Omega \pm 5\%$, $1/4$ W Res., 3.3 KΩ $\pm 5\%$, $1/4$ W Res., 1 KΩ $\pm 5\%$, $1/4$ W Res., $330\Omega \pm 5\%$, $1/4$ W Res., 3.6 KΩ $\pm 5\%$, $1/4$ W Res., $150\Omega \pm 5\%$, $1/4$ W Res., $150\Omega \pm 10\%$, $1/4$ W Res., $16\Omega \pm 10\%$, $1/4$ W
R17, 28 R18, 38 R19 R21, 40 R23, 44, 45 R24 R25, 26 R30 R31 R33 R34 R35, 36 R39, 41, 42, 43 S1, 2 S3	47-01-102625-01 47-01-102181-01 47-01-104512-01 47-01-102167-01 47-01-102175-01 47-01-102151-01 47-01-102184-01 47-01-102184-01 47-01-102147-01 47-01-102172-01 47-01-102893-01 47-06-120422-02 47-01-102185-01 51-01-044588-03	Res., $10\text{K}\Omega \pm 10\%$, 1W Res., $33\text{K}\Omega \pm 10\%$, $1/4\text{W}$ Res., $3.3\text{K}\Omega \pm 10\%$, 2W Res., $10\text{K}\Omega \pm 10\%$, $1/4\text{W}$ Res., $10\text{K}\Omega \pm 10\%$, $1/4\text{W}$ Res., $100\Omega \pm 10\%$, $1/4\text{W}$ Res., $100\Omega \pm 10\%$, $1/4\text{W}$ Res., $56\text{K}\Omega \pm 10\%$, $1/4\text{W}$ Res., $56\text{K}\Omega \pm 10\%$, $1/4\text{W}$ Res., $56\text{K}\Omega \pm 10\%$, $1/4\text{W}$ Res., $100\Omega \pm 10\%$, $1/4\text{W}$ Switch, rotary, 1 pole, 3 pos. Switch, rotary, 1 pole, 4 pos.
		, , , , , , , , , , , , , , , , , , , ,



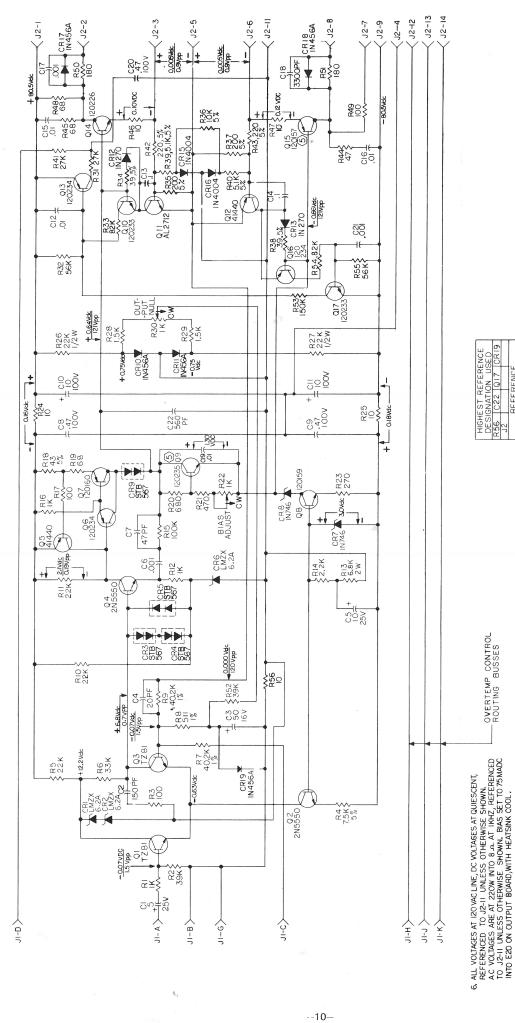


Figure 12. Schematic (4D885-06), Driver Board

REFERENCE DESIGNATIONS NOT USED

© 99¢015 HAVE THERMAL CONTACT BETWEEN THEM 4. FOR ASSY, DWG, SEE 40866. 2. FOR SEPARATE BILL OF MATERIAL SEE 27-01-044592 2. ALL CAPACITORS ARE IN MICROFARADS. I. ALL RESISTORS ARE IN OHMS, I/4W, IO% NOTES, UNLESS OTHERWS FROMED

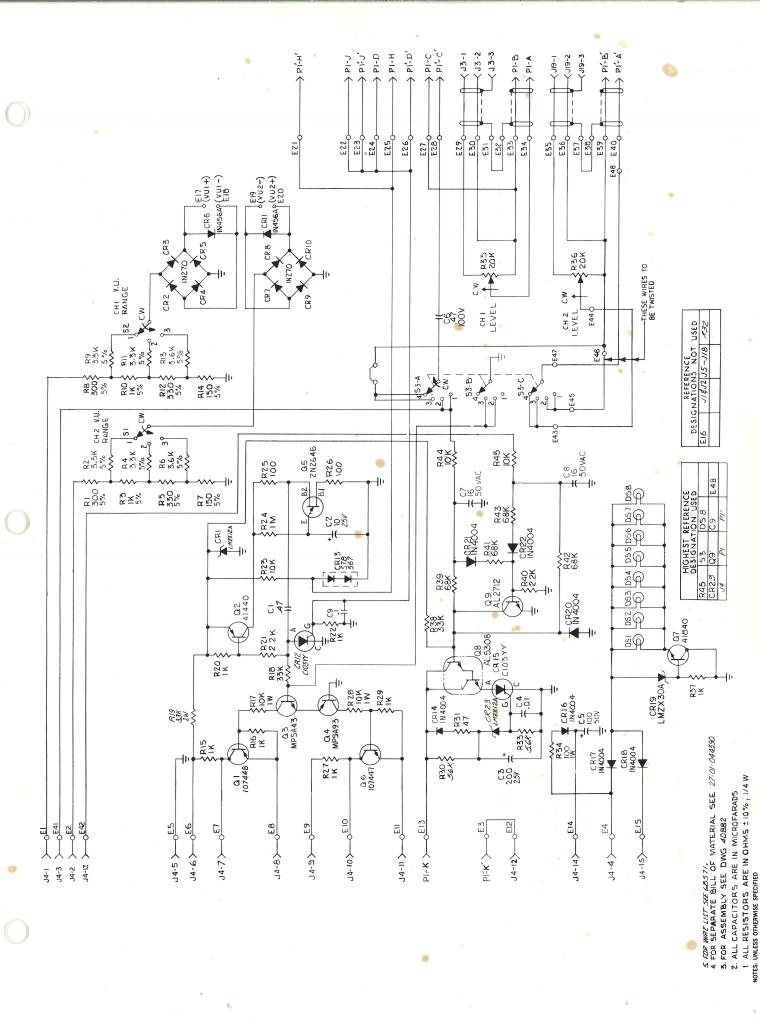


Figure 13. Schematic (4D881-05), Control Board

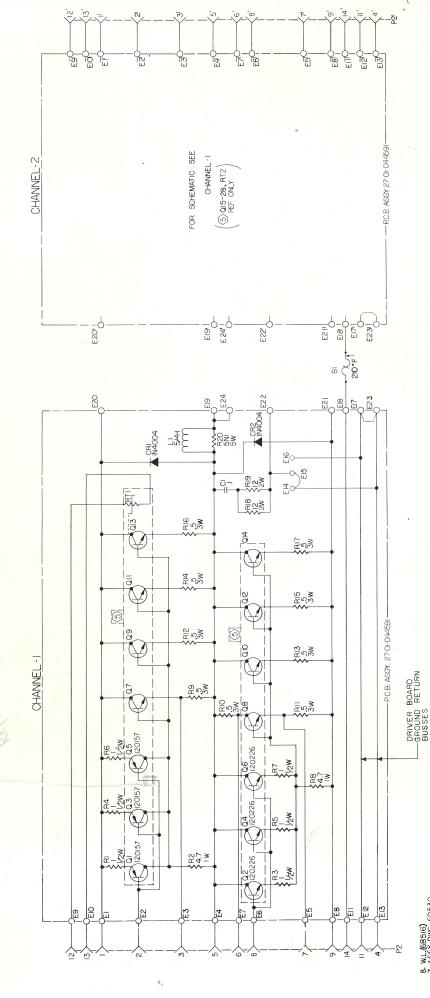


Figure 14. Schematic (4D944-03), Heat Sink Power Output

8. WLL(6B516)
7. ASSY DWC 60530
6. SEE B.VM. 10-05-01-252 SSA 204
6. SEE B.VM. 10-05-04608 ATH RT2 ARE RATH OF HEAT SINK ASSY 14-05-044608 AND
4. RON FERENCE ONLY. ON THIS SCHEMIC.

3. FOR PCB. ASSY. BILL OF MATERIAL SEE 27-01-044591.

2. ALL CAPACITORS ARE IN MICROFARADS. I ALL RESISTORS ARE IN OHMS NOTES, UNLESS OTHERWISE SPECIFIED