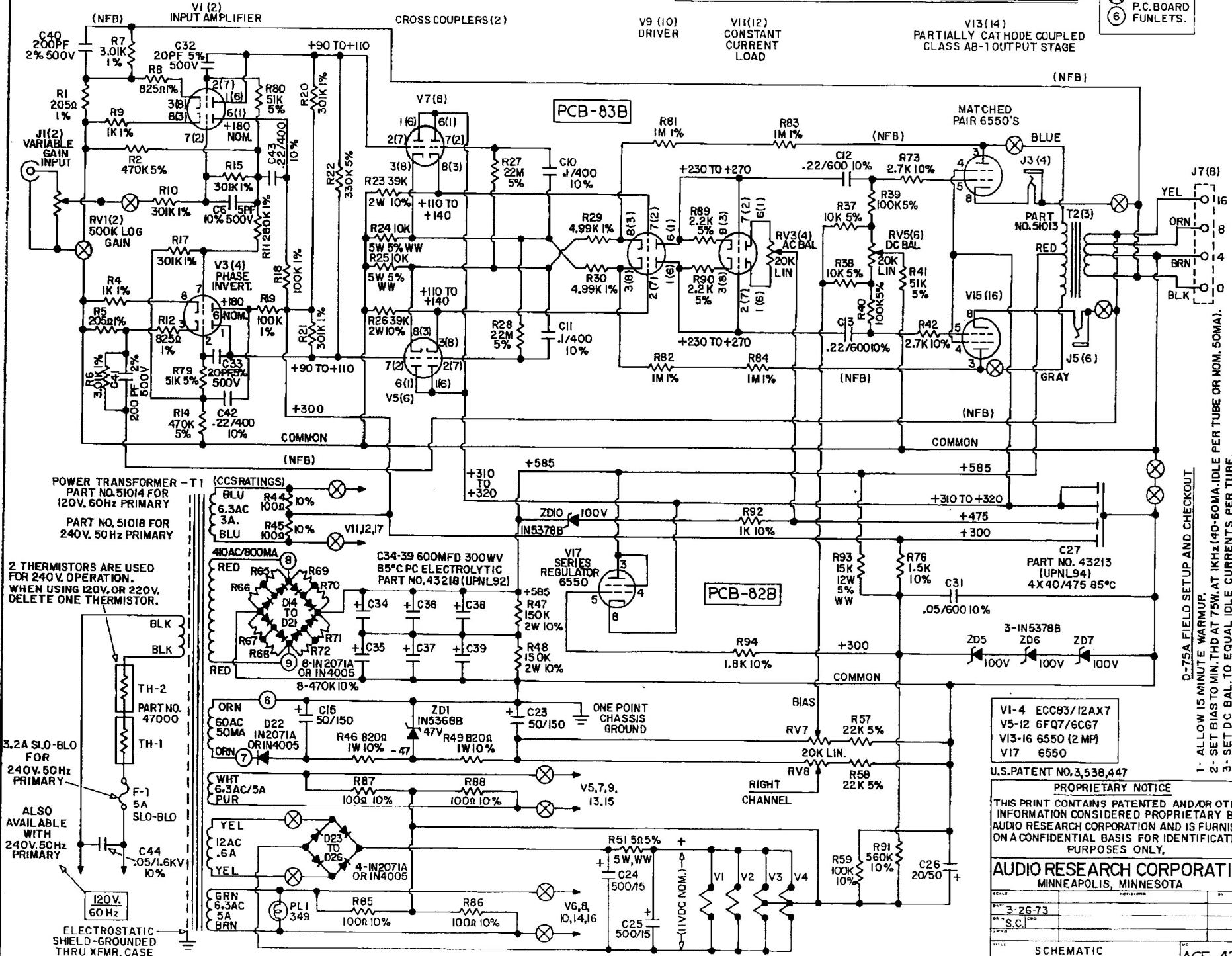


LEFT CHANNEL SHOWN (RIGHT CHANNEL TUBE PINS, WHERE DIFFERENT, ARE IN PARENTHESIS)

ALL RESISTORS 1/2 WATT AND ALL CAPACITORS IN MF EXCEPT AS NOTED.

⊗ DENOTES P.C. BOARD FUNNELTS.
⑥



2 THERMISTORS ARE USED FOR 240V OPERATION. WHEN USING 120V, OR 220V, DELETE ONE THERMISTOR.

3.2A SLO-BLO FOR 240V, 50Hz PRIMARY

ALSO AVAILABLE WITH 240V, 50Hz PRIMARY

ELECTROSTATIC SHIELD-GROUNDED THRU XFMR. CASE

D-75A FIELD SETUP AND CHECKOUT
1- ALLOW 15 MINUTE WARMUP.
2- SET BIAS TO MIN. THD AT 75W. AT 1KHZ (40-60MA. IDLE PER TUBE OR NOM. 50MA).
3- SET DC BAL. TO EQUAL IDLE CURRENTS PER TUBE.
4- SET AC BAL. TO MIN. THD AT 75W. AT 1KHZ (-1% TO .07 NOM.).
(5- IF WITHOUT DISTORTION MEASURING EQUIPMENT, SET ALL 4 OUTPUT TUBES TO 50MA EACH AFTER 30 MINUTE WARMUP.)

V1-4 ECC83/12AX7
V5-12 6FQ7/6CG7
V13-16 6550 (2 MF)
V17 6550

U.S. PATENT NO. 3,538,447

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AUDIO RESEARCH CORPORATION
MINNEAPOLIS, MINNESOTA

SCALE: 3-26-73
REV: S.C.

SCHEMATIC D-75A AMPLIFIER
ACE-423