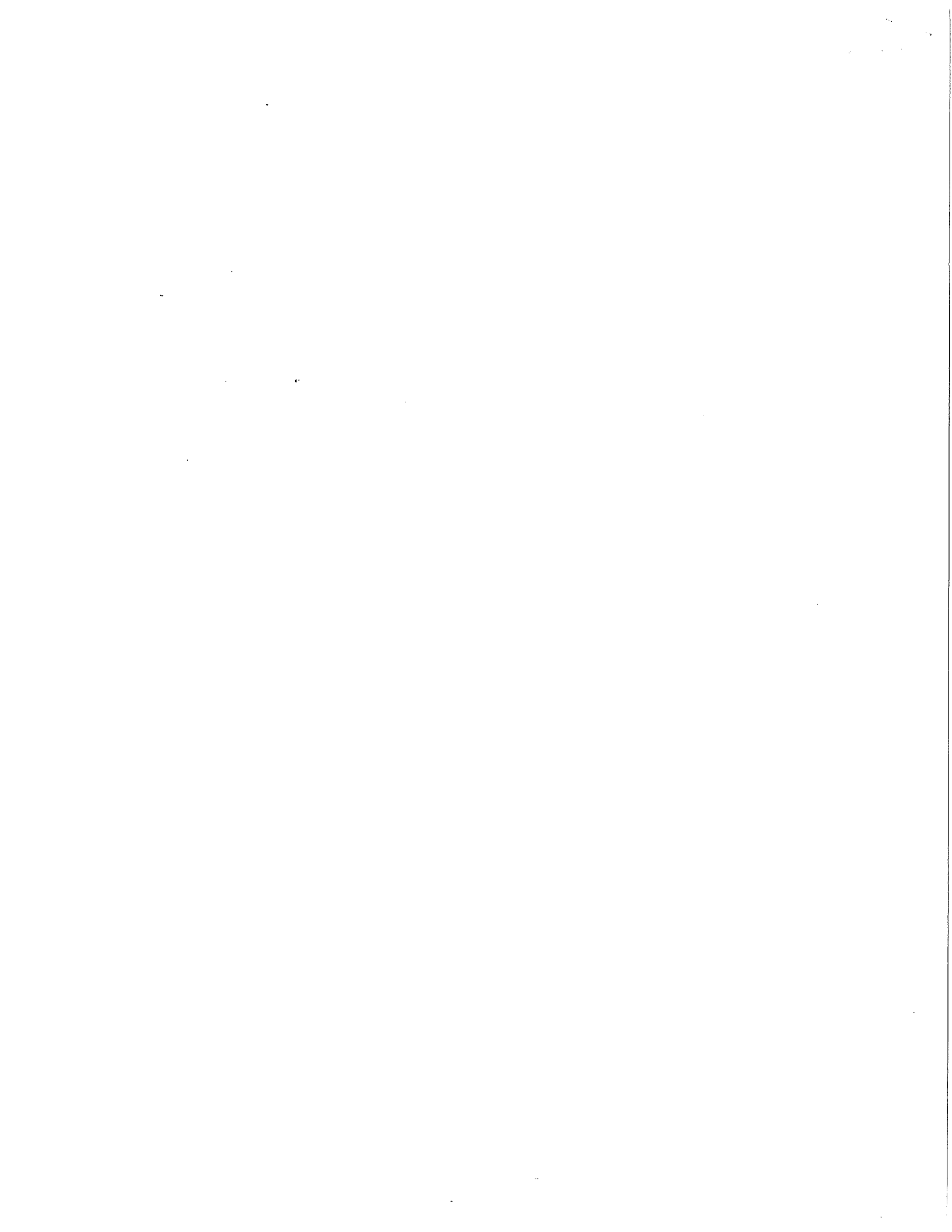


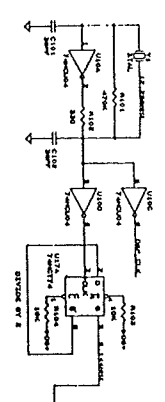


ADVANTAGE DDL11
SCHEMATICS

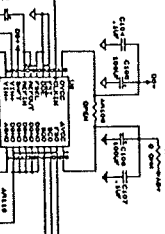




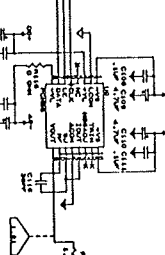
CLOCK OSCILLATOR



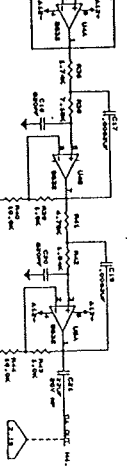
A/D CONVERTER



D/A CONVERTER

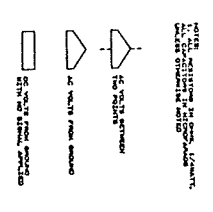
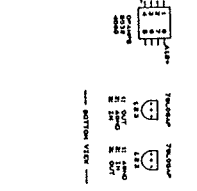
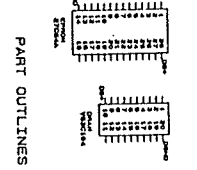
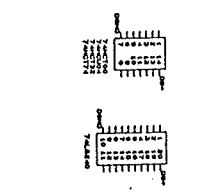
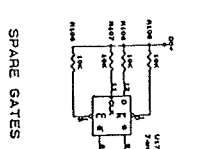
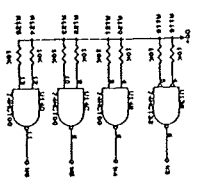
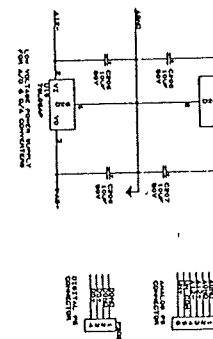
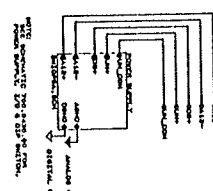
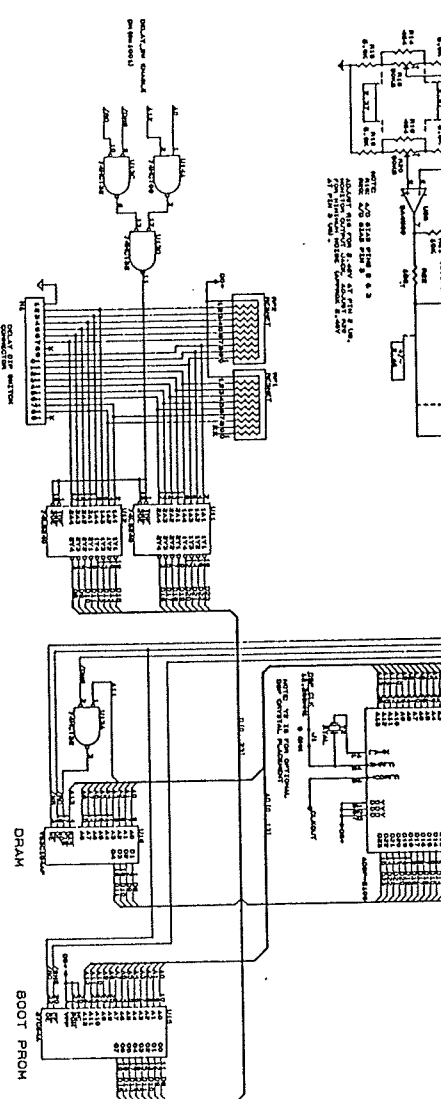
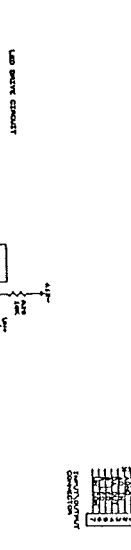
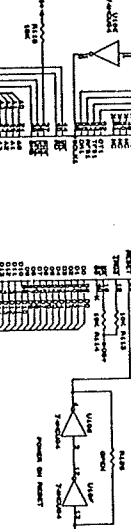
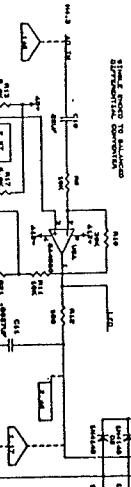


RECONSTRUCTION FILTER



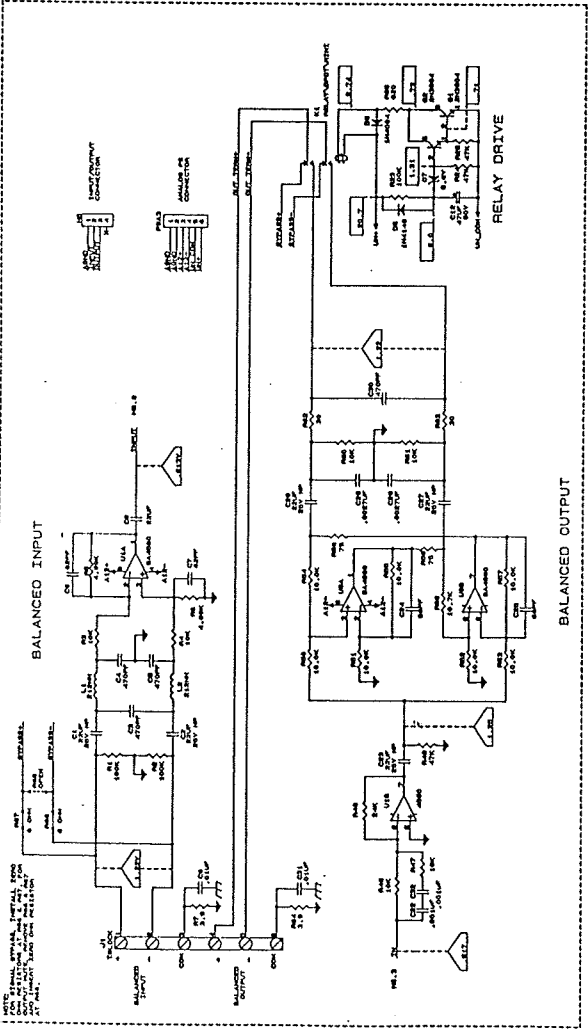
NOTE: DO NOT EXCEED 5V ON ANY INPUTS TO THE A/D CONVERTER.

NOTE: DO NOT EXCEED 5V ON ANY INPUTS TO THE D/A CONVERTER.

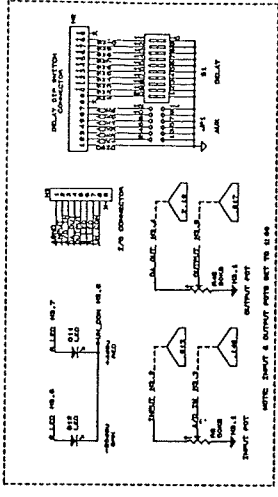


DESIGNED BY: J. L. COOPER	DATE: 1/15/84
CHECKED BY: J. L. COOPER	DATE: 1/15/84
DDL11 DSP PCB	
MULTI PROCESSOR & CONVERTER	
QUANTITY: 100	PRICE: \$700.0434.00

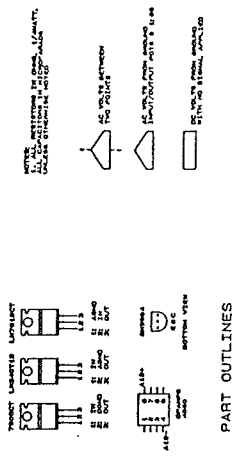
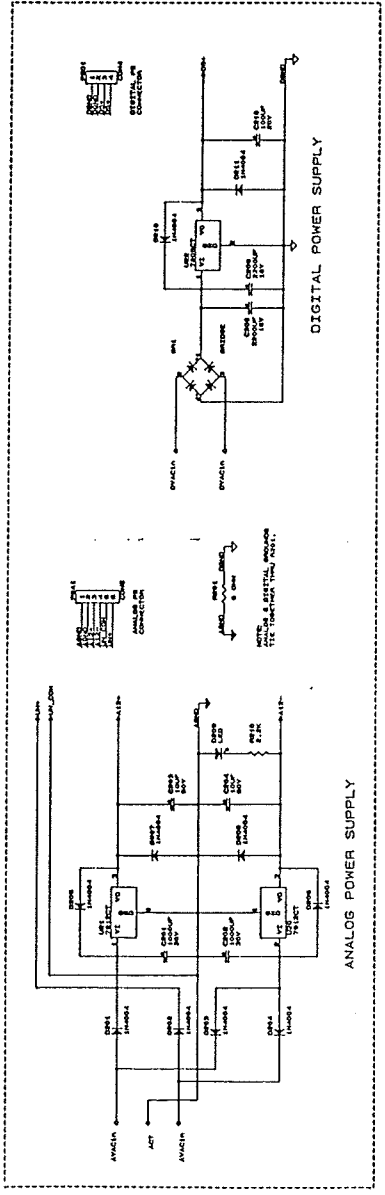
INPUT/OUTPUT



SWITCH - LEDS - VOLUME CONTROLS



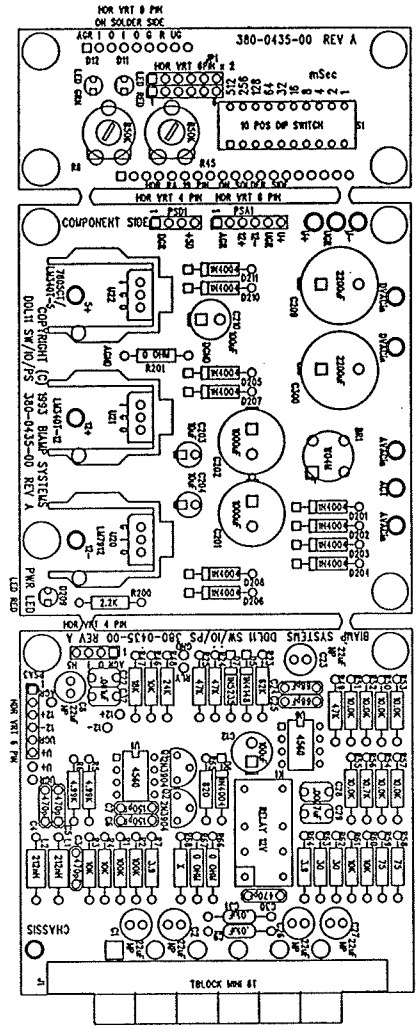
POWER SUPPLY



PART OUTLINES

DESIGNED BY:	DATE:	REV:	NO.
APPROVED BY:	DATE:	REV:	NO.
<p>DDLL11 SW/IO/PS PCB</p> <p>REVISED - SWITCH/OUTPUT CONTROLS - POWER SUPPLY</p>			
PRICE PER UNIT:	700.0435.00	REV:	A
DATE:	10/1983	SHEET:	1 OF 1

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BIAAMP SYSTEMS
 14270 N.W. Science Park Drive
 Portland, Oregon 97229
 (503) 641-7287

DATE	REV	DESCRIPTION	BY	CHKD
07/23	01	INITIAL RELEASE	RC	RC
07/23	02	REVISION	RC	RC
07/23	03	REVISION	RC	RC
07/23	04	REVISION	RC	RC
07/23	05	REVISION	RC	RC
07/23	06	REVISION	RC	RC
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