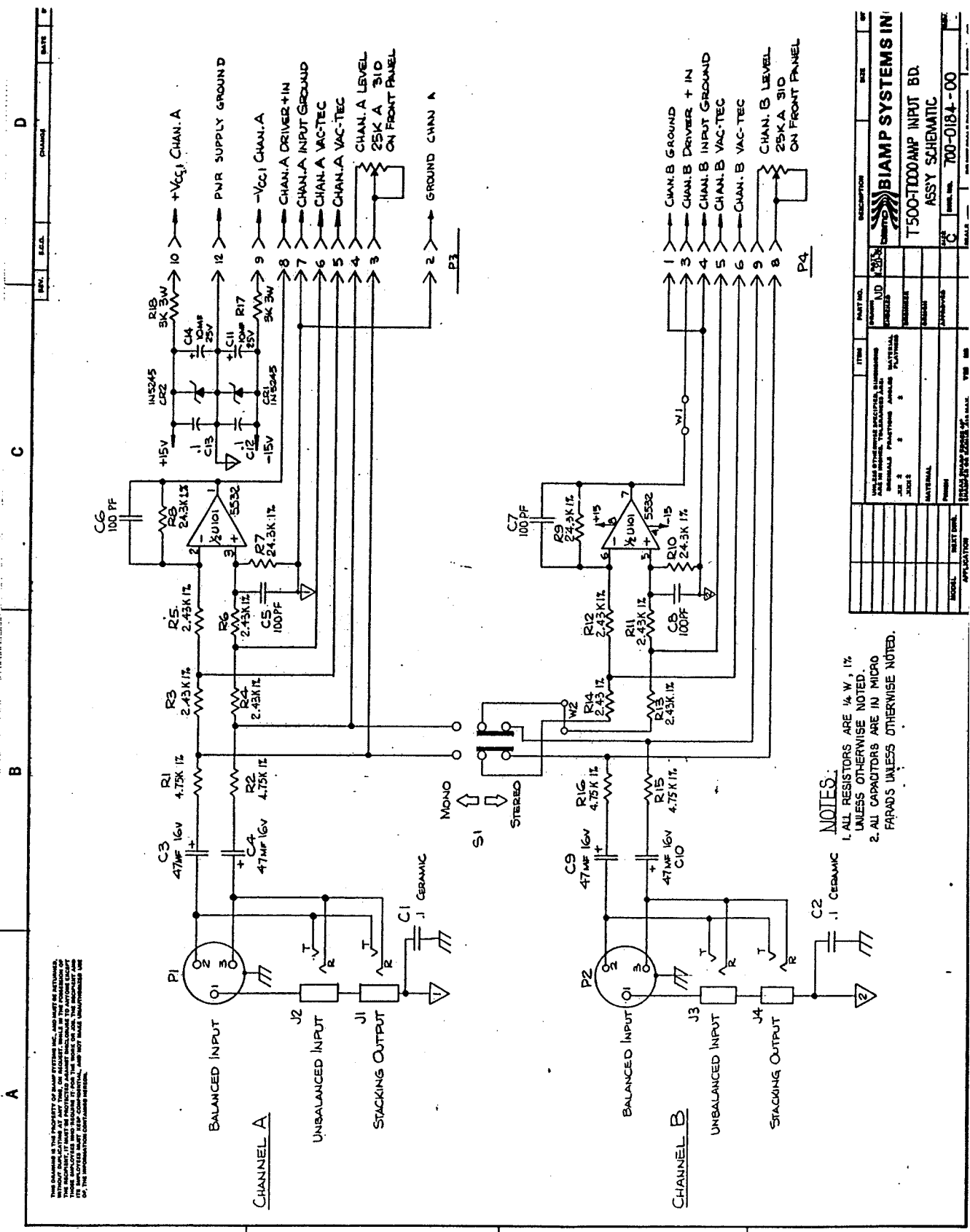


T-Series  
**Schematic**

**B I A M P**<sup>®</sup>  
S Y S T E M S



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**NOTES:**  
 1. ALL RESISTORS ARE 1/4 W, 1% UNLESS OTHERWISE NOTED.  
 2. ALL CAPACITORS ARE IN MICRO FARADS UNLESS OTHERWISE NOTED.

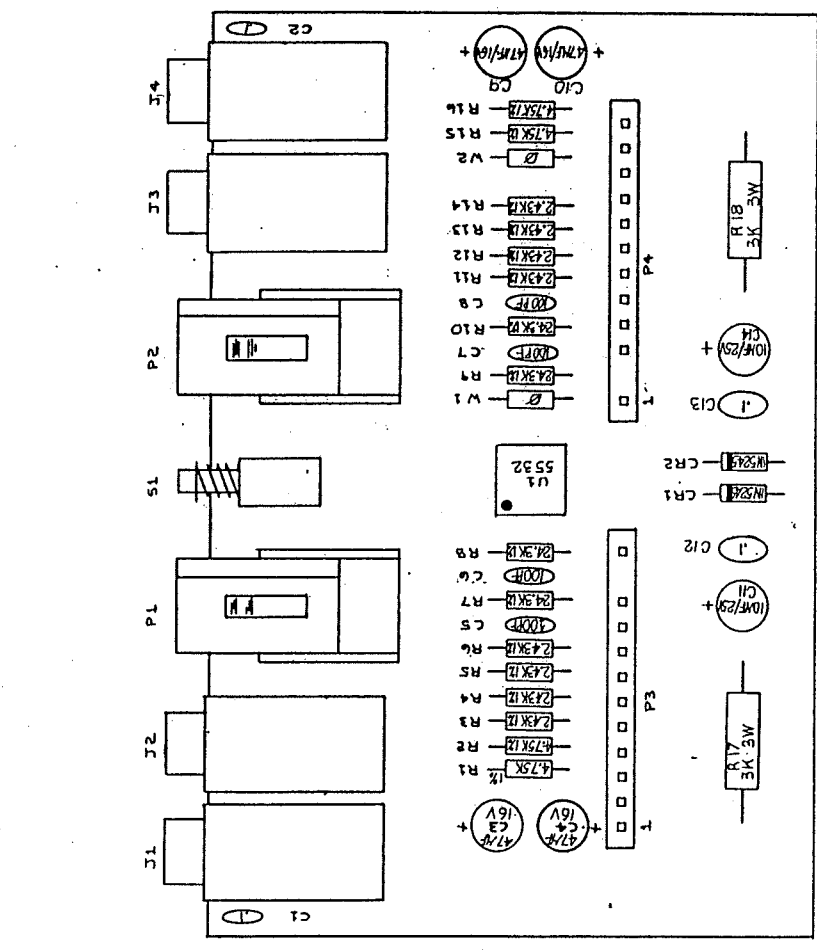
REV.	DATE	CHANGE	DESCRIPTION
1			BIAMP SYSTEMS INC. T500-T1000AMP INPUT BD. ASSY SCHEMATIC
2			
3			
4			

ITEM	QTY	PART NO.	DESCRIPTION
1	1	5532	OP AMP
2	1	5532	OP AMP
3	1	5532	OP AMP
4	1	5532	OP AMP
5	1	5532	OP AMP
6	1	5532	OP AMP
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95	1	5532	OP AMP
96	1	5532	OP AMP
97	1	5532	OP AMP
98	1	5532	OP AMP
99	1	5532	OP AMP
100	1	5532	OP AMP

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REV.	E.S.A.	CHANGE	DATE
1			



**P3. PIN ASSIGNMENTS**

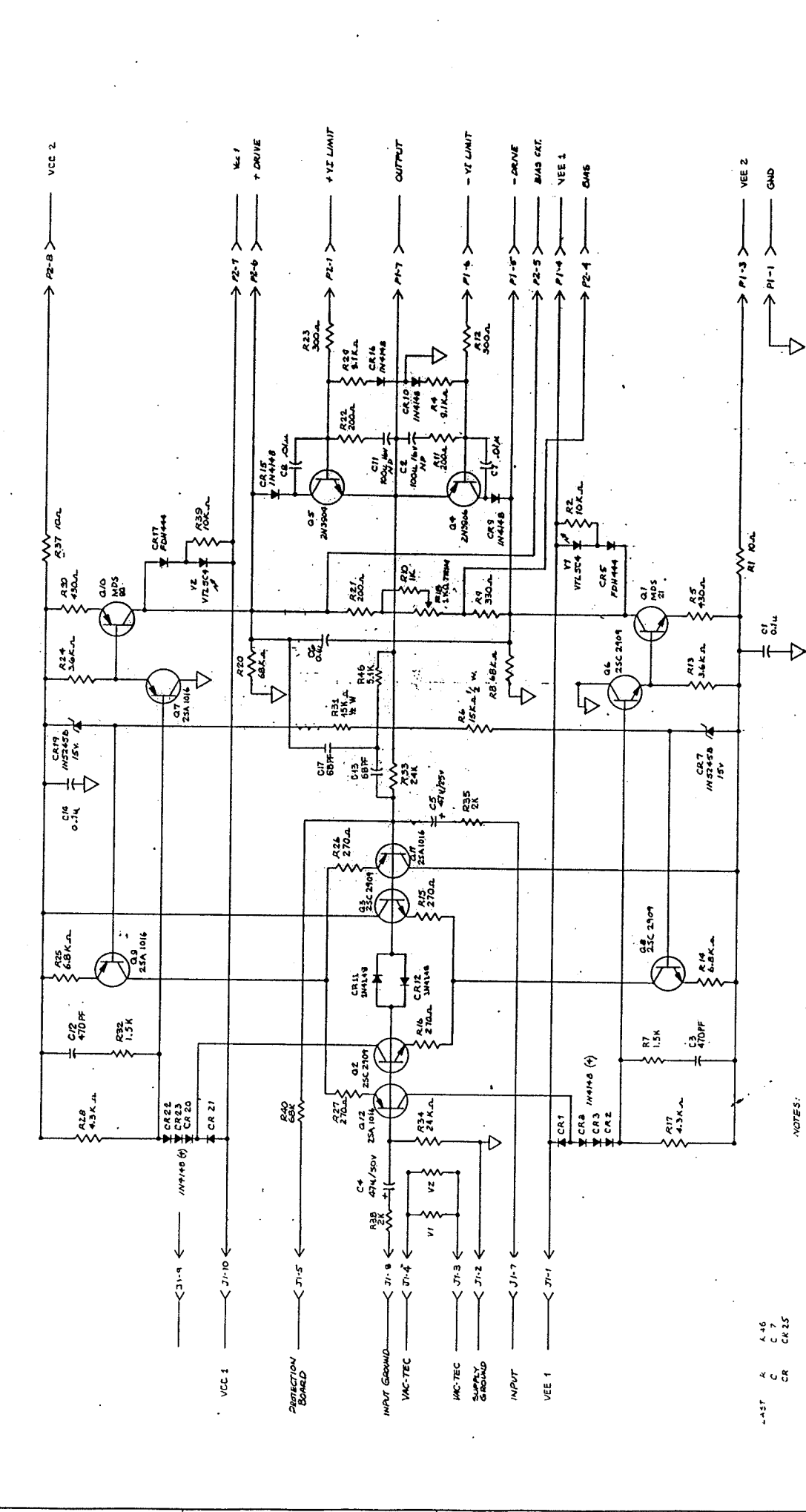
1. N/C
2. CHAN A GROUND
3. CHAN A VOLUME LEVEL CONTROL
4. CHAN A VOLUME LEVEL CONTROL
5. CHAN A DRIVER VACTEC
6. CHAN A DRIVER VACTEC
7. CHAN A DRIVER INPUT GND
8. CHAN A DRIVER INPUT
9. CHAN A VEE 1
10. CHAN A VCC 1
11. BLANK
12. CHAN A PWR SUPPLY GND.

**P4. PIN ASSIGNMENTS**

1. CHAN B GROUND
2. BLANK
3. CHAN B DRIVER INPUT GND
4. CHAN B DRIVER INPUT GND
5. CHAN B DRIVER VACTEC
6. CHAN B DRIVER VACTEC
7. N/C
8. CHAN B VOLUME LEVEL CONTROL
9. CHAN B VOLUME LEVEL CONTROL
10. N/C
11. N/C
12. N/C

PART NO.	ITEM	DESCRIPTION	REV.	DATE
701	1	BIAMP SYSTEMS INC. COMPONENT ASSEMBLY		
701	2	T500/T1000 AMP INPUT PCB		
701	3	REV. 701 - 0184 - 00		
701	4	REV. 701 - 0184 - 00		
701	5	REV. 701 - 0184 - 00		
701	6	REV. 701 - 0184 - 00		
701	7	REV. 701 - 0184 - 00		
701	8	REV. 701 - 0184 - 00		
701	9	REV. 701 - 0184 - 00		
701	10	REV. 701 - 0184 - 00		
701	11	REV. 701 - 0184 - 00		
701	12	REV. 701 - 0184 - 00		
701	13	REV. 701 - 0184 - 00		
701	14	REV. 701 - 0184 - 00		
701	15	REV. 701 - 0184 - 00		
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701	24	REV. 701 - 0184 - 00		
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701	28	REV. 701 - 0184 - 00		
701	29	REV. 701 - 0184 - 00		
701	30	REV. 701 - 0184 - 00		
701	31	REV. 701 - 0184 - 00		
701	32	REV. 701 - 0184 - 00		
701	33	REV. 701 - 0184 - 00		
701	34	REV. 701 - 0184 - 00		
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701	74	REV. 701 - 0184 - 00		
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701	95	REV. 701 - 0184 - 00		
701	96	REV. 701 - 0184 - 00		
701	97	REV. 701 - 0184 - 00		
701	98	REV. 701 - 0184 - 00		
701	99	REV. 701 - 0184 - 00		
701	100	REV. 701 - 0184 - 00		

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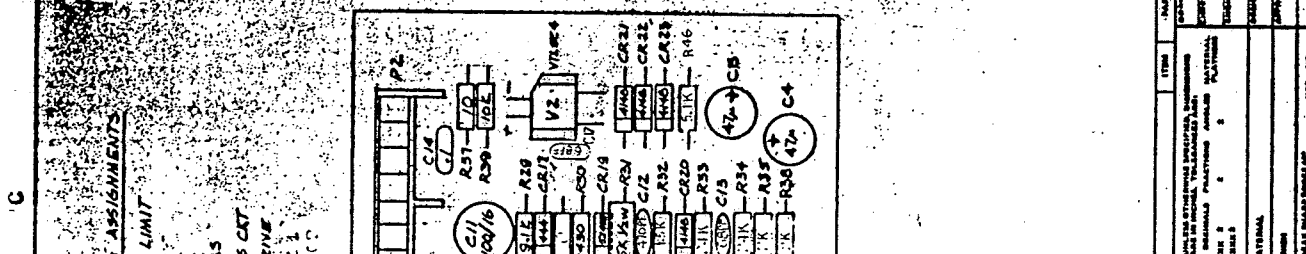


- A A-46
- C C-7
- CR CR-25

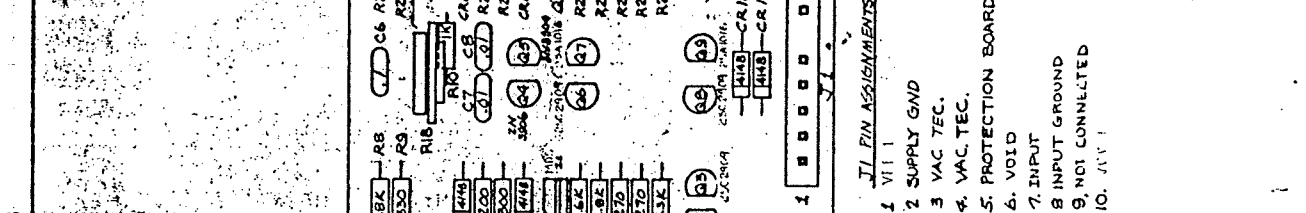
NOTES:  
 ALL RESISTORS  $\frac{1}{4}$  W 5% UNLESS NOTED  
 ALL CAPACITORS IN FARADS

REV.	DATE	BY	CHKD.	DESCRIPTION
1				SCHEMATIC ASSEMBLY
2				SCHEMATIC ASSEMBLY
3				SCHEMATIC ASSEMBLY
4				SCHEMATIC ASSEMBLY
5				SCHEMATIC ASSEMBLY
6				SCHEMATIC ASSEMBLY
7				SCHEMATIC ASSEMBLY
8				SCHEMATIC ASSEMBLY
9				SCHEMATIC ASSEMBLY
10				SCHEMATIC ASSEMBLY

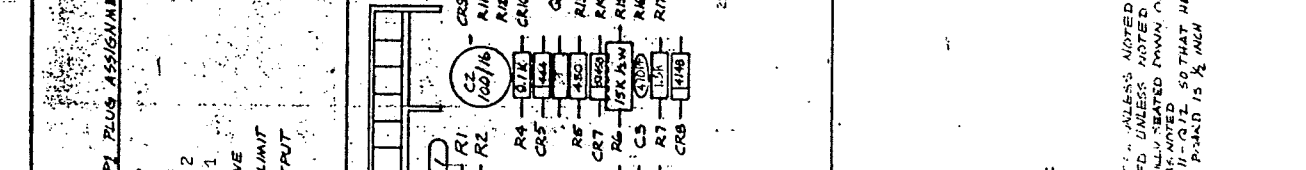
ITEM	DESCRIPTION	QTY	UNIT
1	TRANSISTOR MTC DETAIL	1	PCB
2	RESISTOR	1	PCB
3	CAPACITOR	1	PCB
4	DIODE	1	PCB
5	INDUCTOR	1	PCB
6	CONNECTOR	1	PCB
7	WIRE	1	PCB
8	SCREW	1	PCB
9	SPACER	1	PCB
10	OTHER	1	PCB



- P1 PLUG ASSIGNMENTS**
1. GND
  2. NC
  3. IEE 2
  4. IEE 1
  5. DRIVE
  6. VI LIMIT
  7. OUTPUT
  8. NC
- P2 PLUG ASSIGNMENTS**
1. +VZ LIMIT
  2. NC
  3. NC
  4. BIAS
  5. BIAS CAT
  6. DRIVE
  7. VCC 1
  8. VCC 2



- II PIN ASSIGNMENTS**
1. VI 1
  2. SUPPLY GND
  3. VAC. TEC.
  4. VAC. TEC.
  5. PROTECTION BOARD
  6. VOID
  7. INPUT
  8. INPUT GROUND
  9. NOT CONNECTED
  10. VI 1

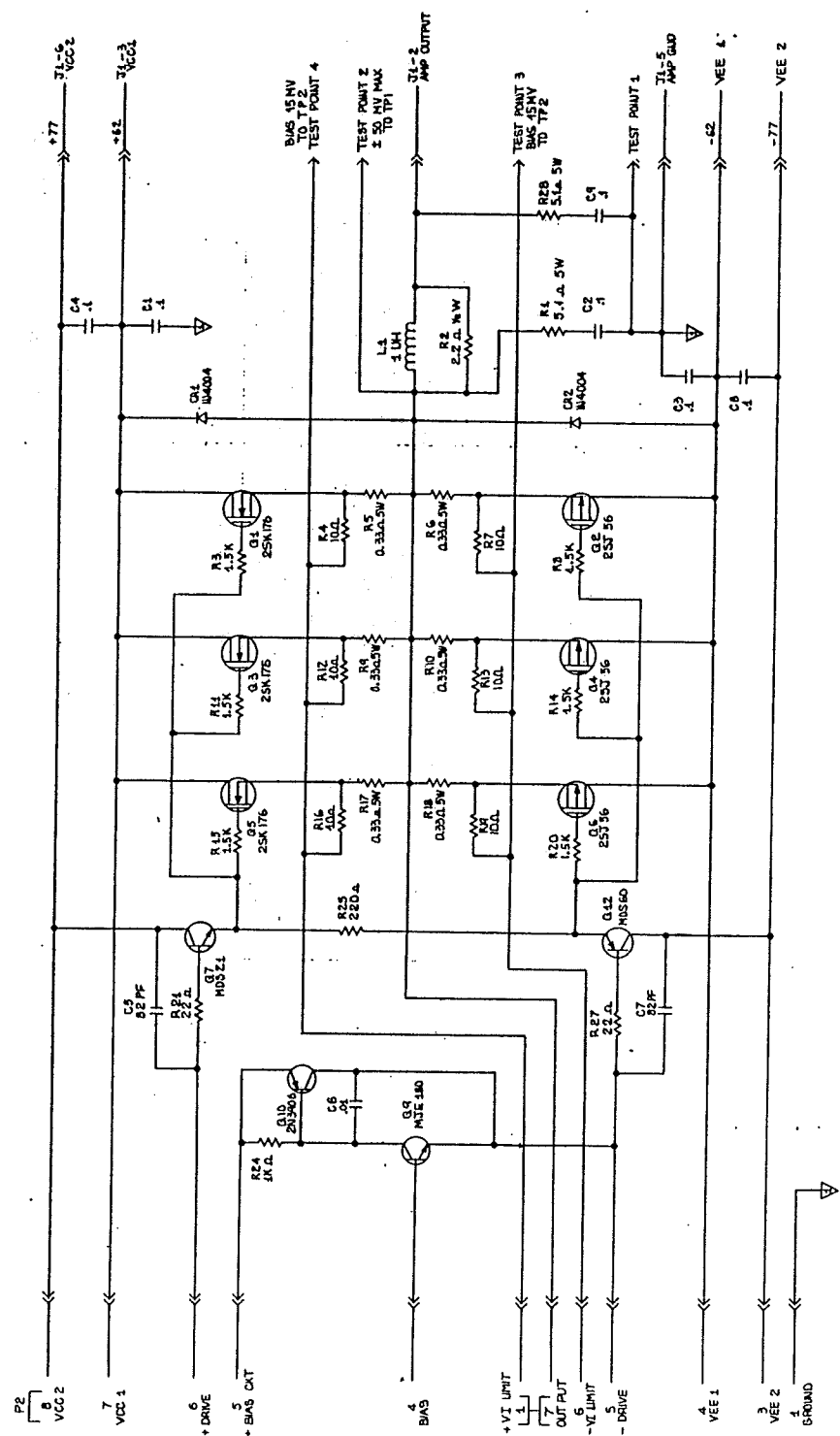


TRANSISTOR MTC DETAIL

- 1 - ALL RESISTORS IN CIRCUIT UNLESS NOTED
- 2 - ALL CAPACITORS IN CIRCUIT UNLESS NOTED
- 3 - ALL COMPONENTS TO BE FULLY SEATED DOWN ON BOARD EXCEPT AS NOTED
- 4 - ALL COMPONENTS AND Q11-Q12 SO THAT HEIGHT FROM TOP OF CASE TO P-HEAD IS 1/2 INCH

MODEL	REV. NO.	DATE

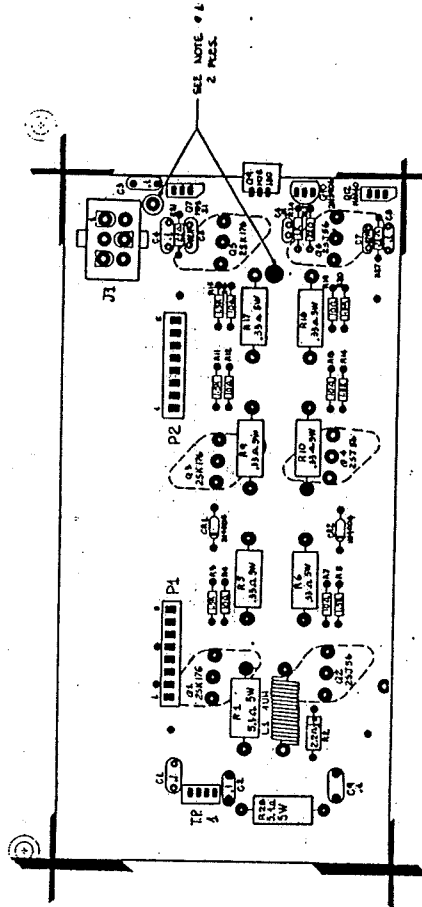
BIAMP SYSTEMS IN  
 COMPONENT ASSEMBLY  
 DRIVER PCB  
 1000 T1000 AMPLIFIED  
 REV. NO. 01 - 010-00  
 PAGE 23



- NOTES:**
1. ALL CAPACITORS IN  $\mu$ F UNLESS NOTED.
  2. ALL RESISTORS IN  $\Omega$  UNLESS NOTED.
  3. MAXIMUM DC VOLTAGE MEASUREMENT BETWEEN TEST POINTS 1 AND 2 IS 50 MV.
  4. ADJUST THE BIAS CONTROL FOR A DC VOLTAGE MEASUREMENT OF 15 MV BETWEEN TEST POINTS 2 AND 4 AS WELL AS 2 AND 3.

REV. 1.0	DATE: 10/1/78	QTY: 1000	REMOV. ON CONNECTING
<b>biamp</b> T500 OUTPUT STAGE CH-A (LEFT) & CH-B (RIGHT)		PART NO. 100-1001-02 REV. 1.0	DATE: 10/1/78 QTY: 1000 REMOV. ON CONNECTING

REV	E.C.D.	CHANGES	DATE
10 A	INX	CORRECTED C2 VALUE	2-9-88



P1 PIN ASSIGNMENT	
1.	GROUND
2.	NC
3.	VEE 2
4.	VEE 1
5.	- DRIVE
6.	- VI LIMIT
7.	OUTPUT
8.	NC

P2 PIN ASSIGNMENT	
1.	+ VI LIMIT
2.	NC
3.	NC
4.	BAS
5.	BAS CMT.
6.	+ DRIVE
7.	Vec 1
8.	Vec 2

TP. PIN ASSIGNMENT	
TP1.	AMP GROUND
TP2.	AMP OUTPUT
TP3.	-BAS
TP4.	+BAS

J1 PIN ASSIGNMENT	
1.	VEE 1
2.	AMP OUTPUT
3.	YCC 1
4.	VEE 2
5.	AMP GROUND
6.	YCC 2

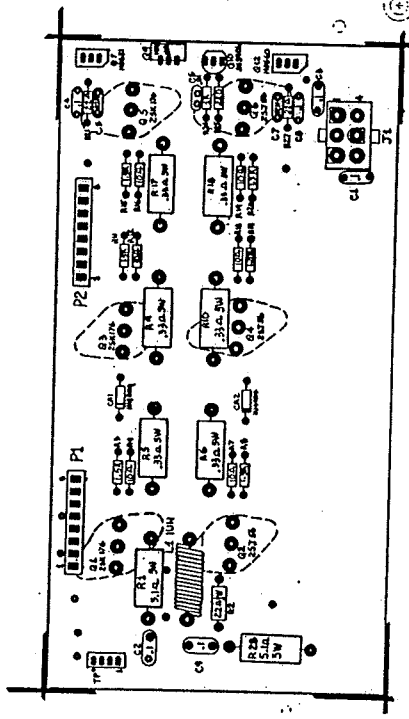
**NOTES:**

- ONE 16 AWG 3" BLACK WIRE P/N 405-0080-00 IS PLACED INTO THESE HOLES.



DATE	BY
01-16-88	BLG
01-16-88	ND/WW
PRINT TOLERANCE ACCURACY (SEE T)	

REV	ECO	CALCULATED	CHANGE	DATE
A			DIRRECTED CZ YAUJIE	2-8

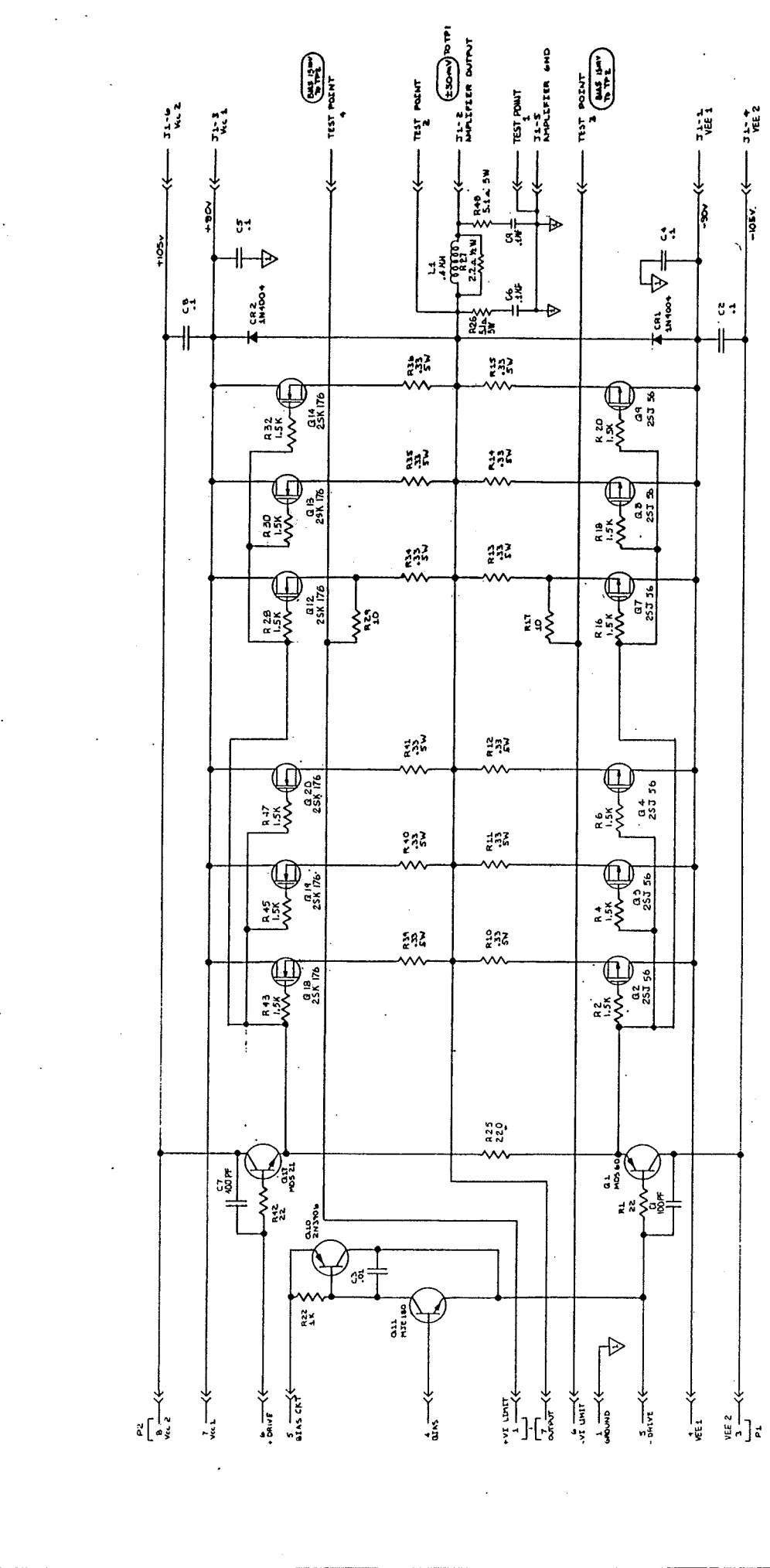


- |   |   |   |  |
|---|---|---|--|
| <b>PI PIN ASSIGNMENT</b><br>1. GROUND<br>2. NC<br>3. VEE 2<br>4. VEE 1<br>5. - DRIVE<br>6. - VI LIMIT<br>7. OUTPUT<br>8. NC | <b>P2 PIN ASSIGNMENT</b><br>1. +VI LIMIT<br>2. NC<br>3. NC<br>4. BIAS<br>5. BIAS CRKT<br>6. + DRIVE<br>7. VCC 1<br>8. VCC 2 | <b>T.P. PIN ASSIGNMENT</b><br>TP1. AMP GROUND<br>TP2. AMP OUTPUT<br>TP3. - BIAS<br>TP4. +BIAS | <b>J1 PIN ASSIGNMENT</b><br>1. VEE 1<br>2. AMP OUTPUT<br>3. VCC 1<br>4. VEE 2<br>5. AMP GROUND<br>6. VCC 2 |
|---|---|---|--|



DATE	REV	BY	CHKD	DATE
	1	YAUJIE		
BIAMP CORPORATION				





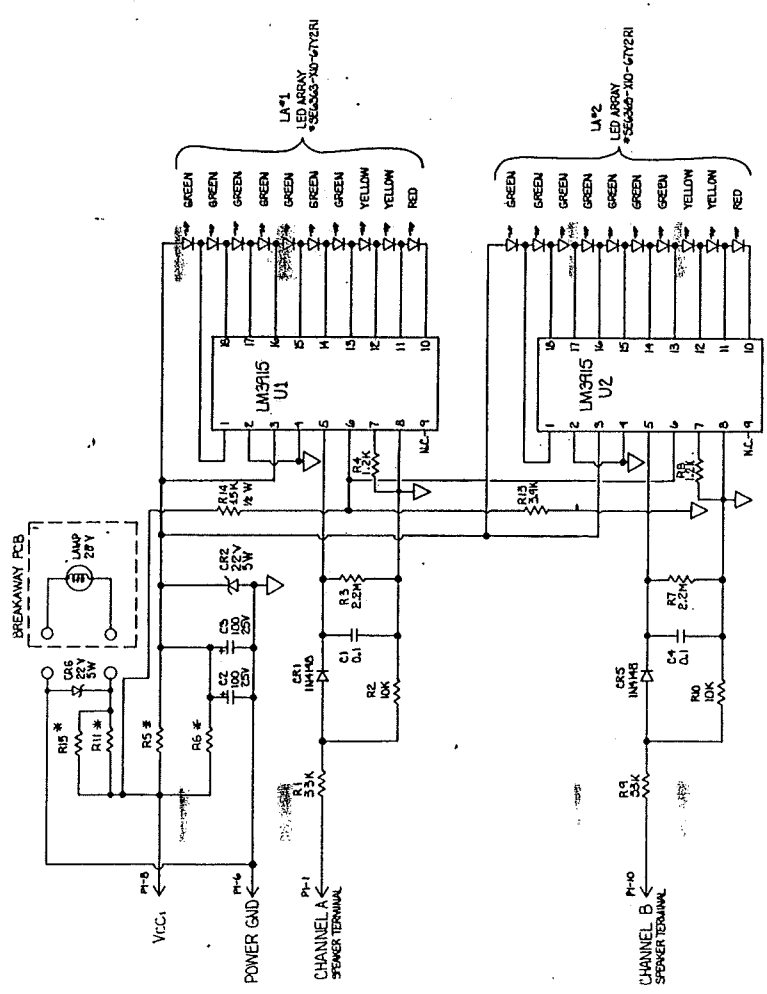
- NOTES:
1. ALL RESISTORS ARE 1/4 WATT UNLESS OTHERWISE NOTED.
  2. ALL CAPACITORS ARE STATED IN MICRO FARADS UNLESS OTHERWISE NOTED.

REV	DATE	BY	CHKD	APP'D	DESCRIPTION
1					
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A B C D E F U

ALL DIMENSIONS ARE IN MILLIMETERS UNLESS OTHERWISE SPECIFIED.  
 ALL COMPONENTS ARE TO BE MOUNTED ON THE PCB UNLESS OTHERWISE SPECIFIED.  
 ALL COMPONENTS ARE TO BE MOUNTED ON THE PCB UNLESS OTHERWISE SPECIFIED.  
 ALL COMPONENTS ARE TO BE MOUNTED ON THE PCB UNLESS OTHERWISE SPECIFIED.

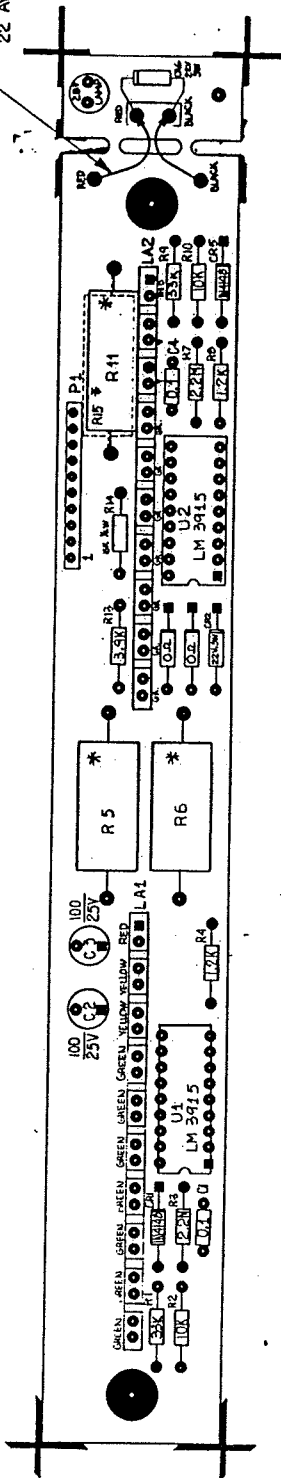


- NOTES:
1. ALL RESISTORS ARE GIVEN IN OHMS UNLESS OTHERWISE SPECIFIED.
  2. ALL CAPACITORS ARE GIVEN IN MICROFARADS UNLESS OTHERWISE SPECIFIED.
  3. DESIGNATOR NUMBERS FOR EACH COMPONENT SHALL BE ASSIGNED BY THE CIRCUIT BOARD DESIGNER WITH LAST DESIGNATOR NUMBERS LISTED IN A TABLE.

*	T 500	T1000
R5	1.5K 3W	2K 5W
R6	4.5K 3W	1.5K 5W
R11	1.5K 3W	4.5K 3W
R15	1.5K 3W	2K 5W

REV	E.C.D.	DATE	CHANGE	DATE
A			Q6GD ZENER REGULATION	2-9-87

8" TWISTED PAIR  
22 AWG.



*	T 500	T 1000
R5	1.5K 3W	2K 5W
R6	1.5K 3W	1.5K 5W
R11	1.5K 3W	4.5 K 5W
R15	1.5K 3W	2K 5W

NOTES:

1. P1 MOUNTS ON REAR OF BOARD
2. R15 MOUNTS ON REAR OF BOARD (PARALLEL TO R11)
3. MOUNT LED ARRAYS .700" ABOVE BOARD SURFACE.

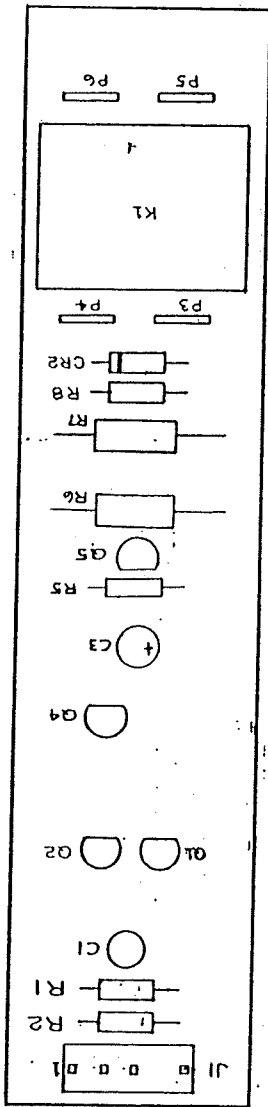


REV	DATE	BY	CHKD
ND	10-04-86		
REV	DATE	BY	CHKD



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REV.	E.D.	CHANGE	DATE	BY
B	02/83	REVISE CIRCUIT	2-83	ID
C	03/83	ADD R4	3-8-83	SR
D	02/83	REMOVE Q3, R4, R9, CR1	4-21-83	SR



**PLUG ASSIGNMENTS**

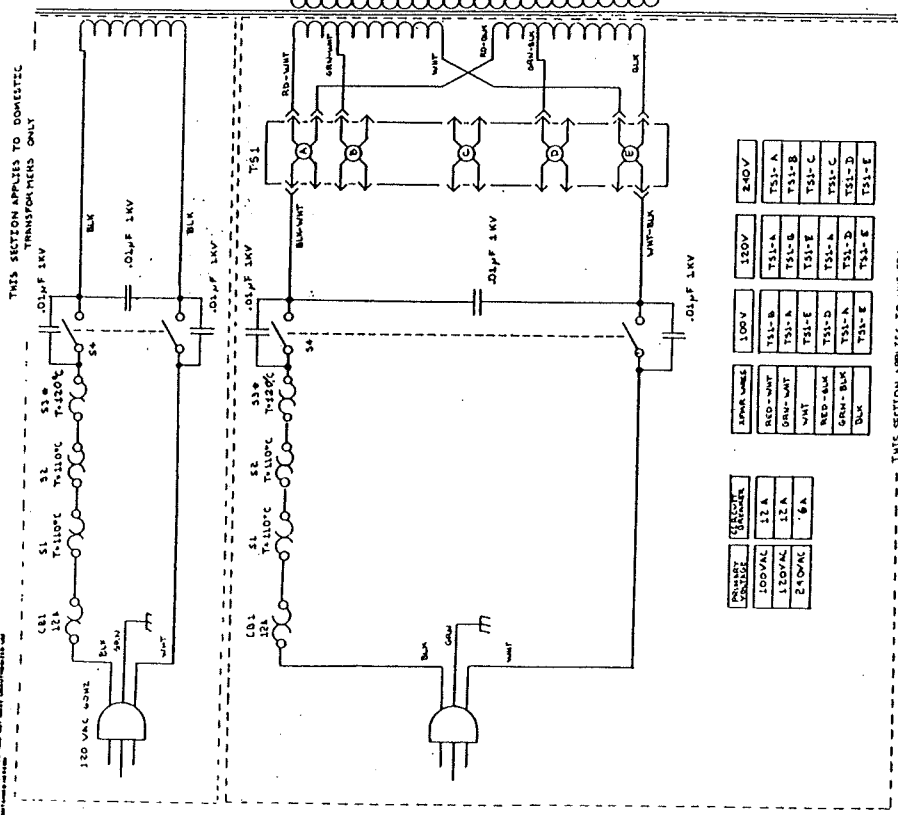
- 1. CH. A DRIVER
- 2. CH. B DRIVER
- 3. D.C. PWR SUPPLY GND
- 4. NC
- 5. PROTECT SUPPLY

REV.	E.D.	CHANGE	DATE	BY
B	02/83	REVISE CIRCUIT	2-83	ID
C	03/83	ADD R4	3-8-83	SR
D	02/83	REMOVE Q3, R4, R9, CR1	4-21-83	SR

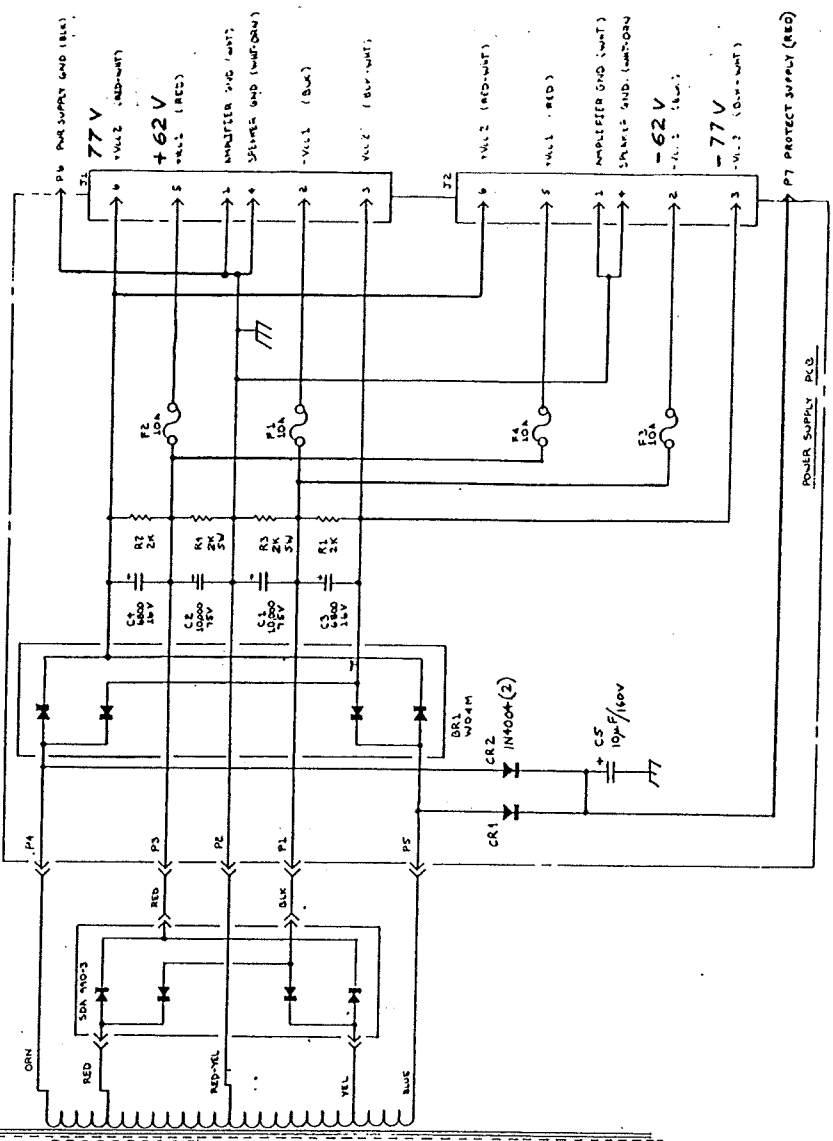
  

1100	131-0000-00	131-0000-00	131-0000-00	131-0000-00
UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES, TOLERANCES ARE DECIMALS FRACTIONS ANGLES MATERIAL	UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES, TOLERANCES ARE DECIMALS FRACTIONS ANGLES MATERIAL	UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES, TOLERANCES ARE DECIMALS FRACTIONS ANGLES MATERIAL	UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES, TOLERANCES ARE DECIMALS FRACTIONS ANGLES MATERIAL	UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES, TOLERANCES ARE DECIMALS FRACTIONS ANGLES MATERIAL
.XX ±	.XX ±	.XX ±	.XX ±	.XX ±
.XXX	.XXX	.XXX	.XXX	.XXX
MATERIAL	MATERIAL	MATERIAL	MATERIAL	MATERIAL
FINISH	FINISH	FINISH	FINISH	FINISH
MODEL	NEXT DWR.	MODEL	NEXT DWR.	MODEL
BIAMP SYSTEMS INC.	BIAMP SYSTEMS INC.	BIAMP SYSTEMS INC.	BIAMP SYSTEMS INC.	BIAMP SYSTEMS INC.
COMPONENT ASSEMBLY	COMPONENT ASSEMBLY	COMPONENT ASSEMBLY	COMPONENT ASSEMBLY	COMPONENT ASSEMBLY
PROTECTION PCB	PROTECTION PCB	PROTECTION PCB	PROTECTION PCB	PROTECTION PCB
1200-2100 AMPLIFIERS	1200-2100 AMPLIFIERS	1200-2100 AMPLIFIERS	1200-2100 AMPLIFIERS	1200-2100 AMPLIFIERS
SIZE	SIZE	SIZE	SIZE	SIZE
8317-0035-00	8317-0035-00	8317-0035-00	8317-0035-00	8317-0035-00
DWG. NO.	DWG. NO.	DWG. NO.	DWG. NO.	DWG. NO.
B	B	B	B	B

REV	DATE	BY	CHKD	DESCRIPTION
1	10/18/81	REAGERS	CHILKIS	REVISED
2	11/18/81	REAGERS	CHILKIS	REVISED
3	02/25/82	REAGERS	CHILKIS	REVISED
4	02/25/82	REAGERS	CHILKIS	REVISED



PRIMARY VOLTAGE	SECONDARY	AMPERE RATING
100VAC	12A	12A
120VAC	12A	12A
240VAC	6A	6A

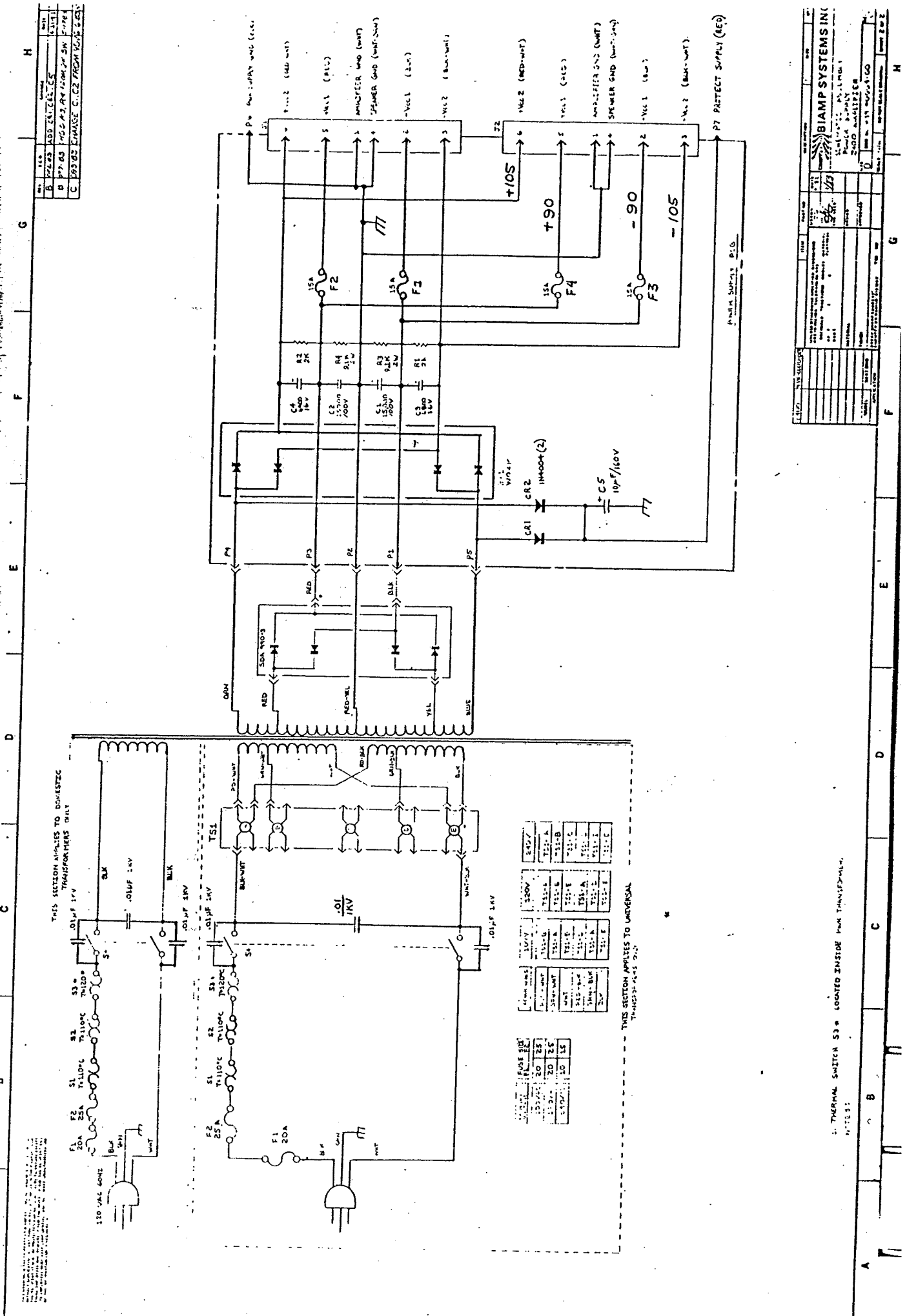


REV	DATE	BY	CHKD	DESCRIPTION
1	10/18/81	REAGERS	CHILKIS	REVISED
2	11/18/81	REAGERS	CHILKIS	REVISED
3	02/25/82	REAGERS	CHILKIS	REVISED
4	02/25/82	REAGERS	CHILKIS	REVISED

1. TRIP SW. SWITCH 53# LOCATED INSIDE PWR TRANSFORMER.

NOTES:

REV	DATE	BY	CHKD	DESCRIPTION
A	02-28-63	ADD	ADD	REVISIONS
B	07-10-63	ADD	ADD	REVISIONS
C	09-25-63	CHANGE	CHANGE	REVISIONS



THIS SECTION APPLIES TO DOMESTIC TRANSFORMERS ONLY

THIS SECTION APPLIES TO UNIVERSAL TRANSFORMERS ONLY

FUSE BLOCK	
15A	15A
20A	20A
25A	25A
30A	30A
35A	35A
40A	40A
45A	45A
50A	50A

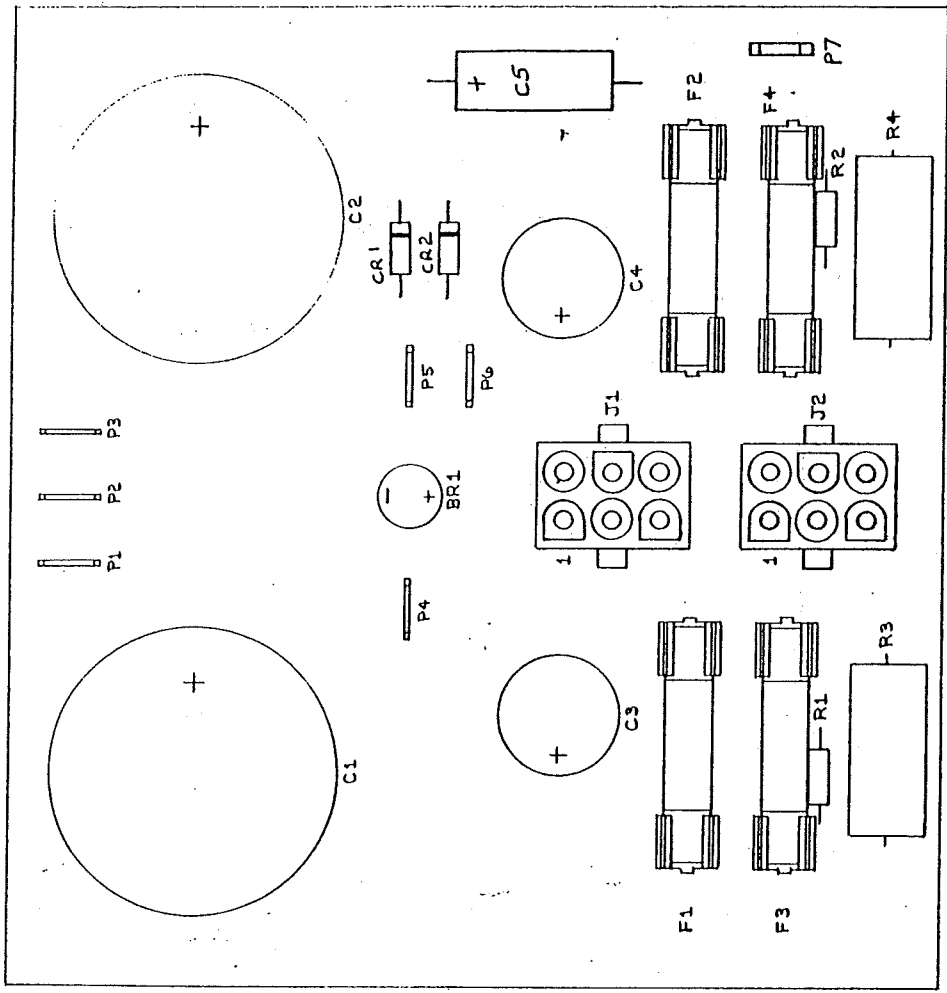
1. THERMAL SWITCH S3 IS LOCATED INSIDE MAIN TRANSFORMER. NOTE: 1

BIAMP SYSTEMS INC.  
 2400 W. 11TH AVE. SUITE 100  
 DENVER, CO 80202  
 (303) 751-1100



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REV	BY	DATE
B	ADD CRI/CRA	3-22-83



**PLUG ASSIGNMENTS:**

- P1. -Vcc 1 (BLK)
- P2. CENTER TAP (RED-YEL)
- P3. +Vcc 1 (RED)
- P4. AC INPUT Vcc 2 (ORN)
- P5. AC INPUT Vcc 2 (BLU)
- P6. PWR SUPPLY GND (BLK)
- P7. PROTECT SUPPLY (RED)

**J1 AND J2 PIN ASSIGNMENTS:**

- 1. AMPLIFIER GND (WHT)
- 2. -Vcc 1 (BLK)
- 3. -Vcc 2 (BLK-WHT)
- 4. SPEAKER GND (WHT-ORN)
- 5. +Vcc 1 (RED)
- 6. +Vcc 2 (RED-WHT)

1200	118-0004-00	ITEM	PART NO.	DATE	DESCRIPTION	SIZE	QTY.
		UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES. TOLERANCES ARE:	J.D.	7-82	BIAMP SYSTEMS INC.		
		DECIMALS FRACTIONS ANGLES MATERIAL	ADD	1-83	COMPONENT ASSEMBLY		
		.XX .XXX .XXX	ENGINEER		1200 POWER SUPPLY		
			SCHEM		1200 AMPLIFIER		
			APPROV				
			DATE	11/83			
			DRW. NO.	83B-0004-00			
			REV.	B			