



NOTES

ONLY ONE (LEFT) OF TWO IDENTICAL CHANNELS IS SHOWN. THE DESIGNATIONS *LEFT & RIGHT* REFER ONLY TO THE PHYSICAL ORIENTATION OF THE CHANNELS IN THE AMPLIFIER CHASSIS. THE TWO CHANNELS ARE INTERCONNECTED IN EITHER PARALLELED OR BRIDGED MODES ON THE BACKBOARD.

☐ SYMBOL INDICATES CONNECTIONS TO "OUTRIGGER" PC BOARDS.

ALL RESISTORS ARE 1/4 WATT EXCEPT WHERE NOTED.

ALL PARTS DRAWN IN BOLD LINES ARE TO BE MATCHED WITH THE SAME PARTS ON THE MATING CHANNEL

3u3	220P	470u/16v	22P
2K2	47K	1K/1%	33K/1%

Rev 1.0 May 94
 200K(2) in DC-0/5 circuit chg'd to 100K. Inverting input removed.
 Bias transistor chg'd to 2N5550



7B MONO POWER AMPLIFIER MAIN BOARD (7B-MB/2.51) SCHEMATIC

REV 1.0 MAY 94

FILE: 7B-MBS

