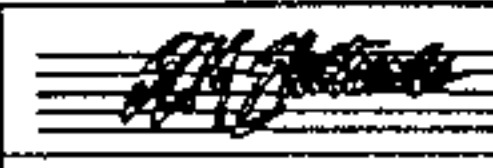


ALL CONTROLS MAX. EXCEPT CH. B LEVEL P 5 AND REV. A & B & D
 INPUT SIGNAL 10mV @ 1kHz
 LINE VOLTAGE 120VAC @ 60Hz
 ALL DC VOLTAGES WITH NO SIGNAL APPLIED

TP	CHANNEL A (CLEAN CHANNEL)			CHANNEL B (DIRTY CHANNEL)		
	AC VOLTAGE	DC VOLTAGE	WAVEFORM	AC VOLTAGE	DC VOLTAGE	WAVEFORM
12	8V p-p	-4.6V	~	1.2V p-p	-4.6V	~
13	21V p-p	171V	~	23.5V p-p	171V	~
14	20V p-p	170V	~	26V p-p	170V	~
15	—	87V	—	—	87V	—
16	6.6V p-p	-27.5V	~	10V p-p	-27.5V	~
17	—	9.7V	—	—	9.7V	—
18	7V p-p	36V	~	7V p-p	36V	~
19	10V p-p	31V	~	10V p-p	31V	~
20	40V p-p	—	~	42V p-p	—	~
21	10V p-p	31V	~	10V p-p	31V	~
22	2V p-p	-3V	~	2V p-p	-3V	~
23	2V p-p	3V	~	2V p-p	3V	~
24	8V p-p	—	~	8V p-p	—	~
25	8V p-p	—	~	8V p-p	—	~
26	7V p-p	15.4V	~	7V p-p	15.4V	~
27	7V p-p	-15.4V	~	7V p-p	-15.4V	~
28	10mV p-p	10.4V	~	10mV p-p	10.4V	~
29	10mV p-p	-9.9V	~	10mV p-p	-9.9V	~

REV. LOADED WITH 150 ohm RESISTOR
 SPKR. LOADED WITH 8 ohm RESISTOR

3				
2				
1	04/08/94	REM		ART TO REV 2. CHANGED VALUES PER E097 & 154.
REV	DATE	BY	CHK'D	DESCRIPTION
SIGNATURES:		DATE:		 THOMAS BORMAN DR. ST. LOUIS, MISSOURI 63148
DRAWN: MGA		11/07/93		
CHK'D: OK		11/08/93		PROJECT NAME:
APP'D: OK		11/08/93		VC-3112
ORIGINAL ISSUED:		11/07/93		DRAWING NAME:
PLOT DATE:		04/13/94		SCHEMATIC DRAWING
PLOT TIME:		15:33:32		DRAWING NO. 07S269-01
FILE NAME:		B901H1Z		REV. 1
SCALE:		~		SHEET: 2 OF 2