

DRAWMER

DS 201

SERVICE INFORMATION

SETTING UP PROCEDURE

Set all rotary controls anti-clockwise.

Set switches (both channels) :

Key Source :	INT
Function :	DUCK (Green l.e.d. on)
Output :	GATE

Set switches :

Stereo Link :	ON (l.e.d. on)
Power :	ON

Connect 1 kHz 0dBV signal to input of channel 1.

Link across to input channel 2.

Turn channel 1 key filter HF control fully clockwise.

Turn channel 1 threshold control clockwise until gating occurs (green l.e.d. off, red and yellow l.e.d. on).

Osc. probe to output of V.C.A. op. amp channel 1 (see Fig 1).

Osc. sens. 5 mV/div.

Adjust DC.

Bal pre-set to give 0V D.C.

Adjust zero bal. pre-set to null any signal.

REPEAT for channel 2.

Osc. probe to Output channel 1 (XLR or jack socket).

Turn F.e.t. bias pre-set (channel 1) anti-clockwise until A.C. signal appears, then clockwise only to the point where the signal disappears.

It may be necessary at this point to re-null using the Zero Bal pre-set

REPEAT for channel 2.

This completes the basic setting of the dual noise gate.

STEREO OPERATION (Stereo Link On)

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Optimum stereo matching of both channels may be obtained as follows:-

After setting up the gate to the previous procedure;

Connect each output to an input of a dual beam Osc. (sens. .5V/div timebase 1 ms/div).

Switch scope to display both channels.

Connect a 100HZ, 1 ms pulse to the EXT key input (channel 1).

Switch channel 1 Key source to EXT.

Increase signal input frequency from 1 kHz to 10 kHz.

Switch function to gate (both channels).

The scope display should now contain both gated envelopes.

Trim channel 1 F.e.t. bias pre-set until both cut-off points match as closely as possible.

DRAWMER
ELECTRONICS

DS 201 DUAL NOISE GATE
(BLOCK DIAGRAM)

POWER
SUPPLIES

Fig. 4

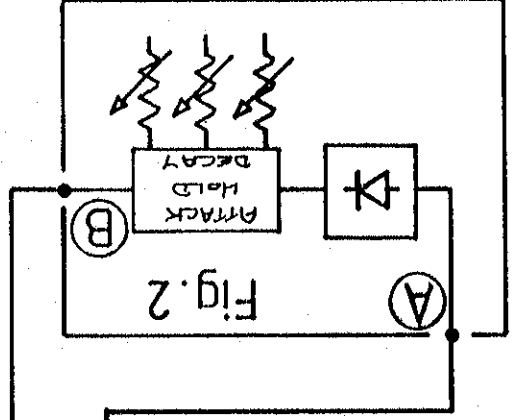


Fig. 2

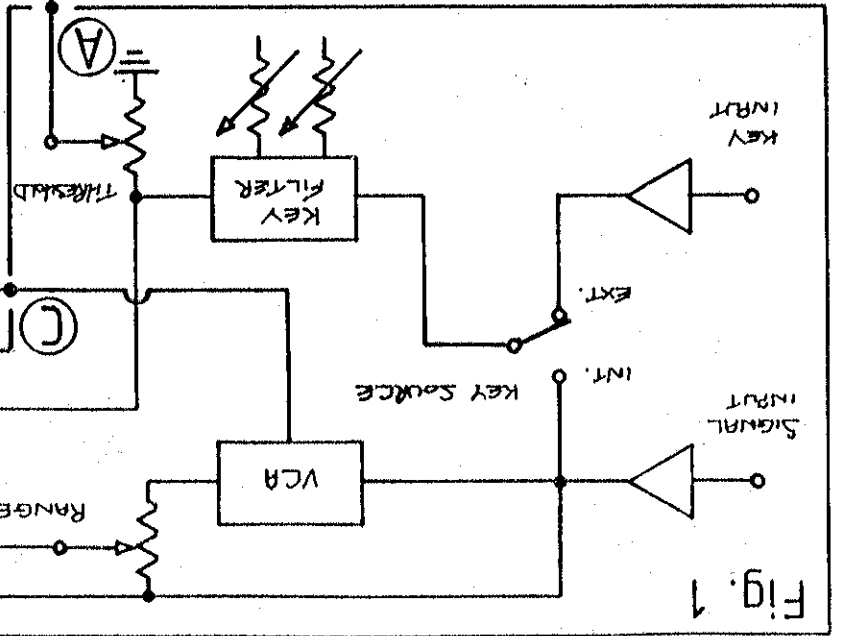


Fig. 1

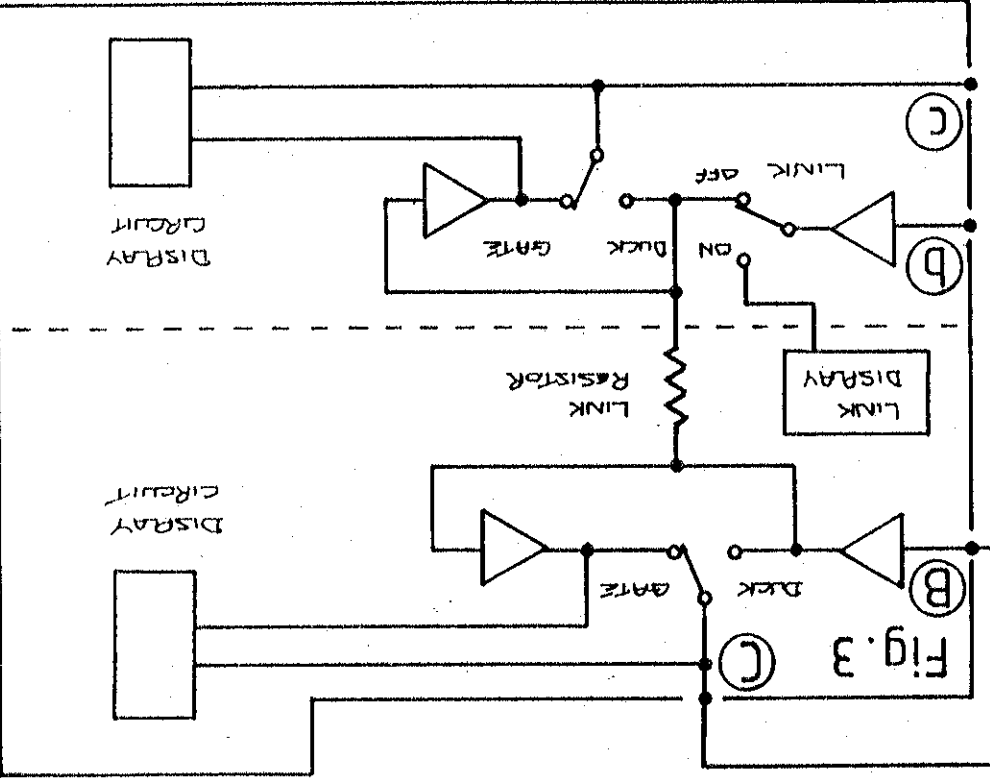


Fig. 3

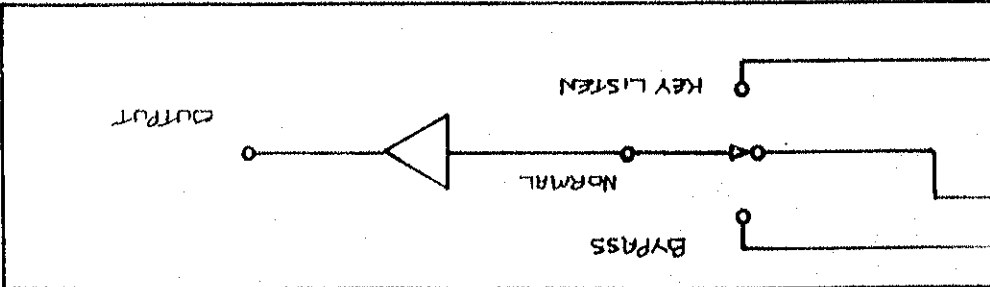
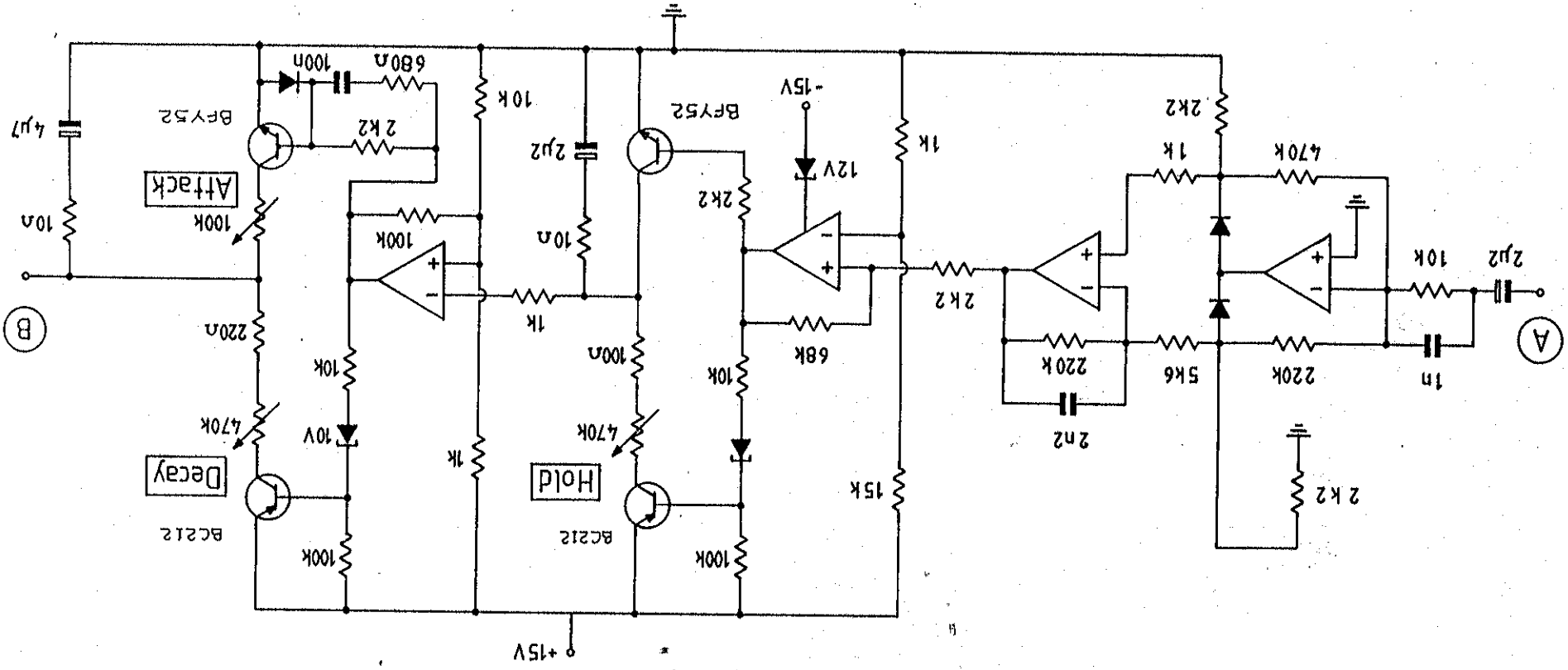


Fig. 4

DS 201 (Fig. 2) Rectifier, Attack, Hold, Decay Circuits.



All Op. Amps. TL072 or LF353

