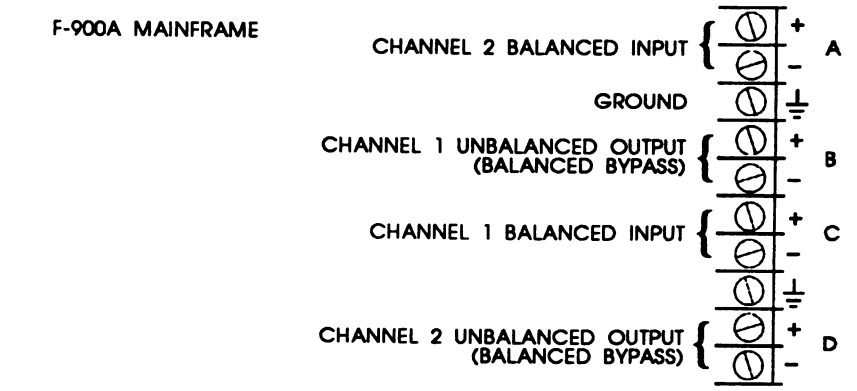
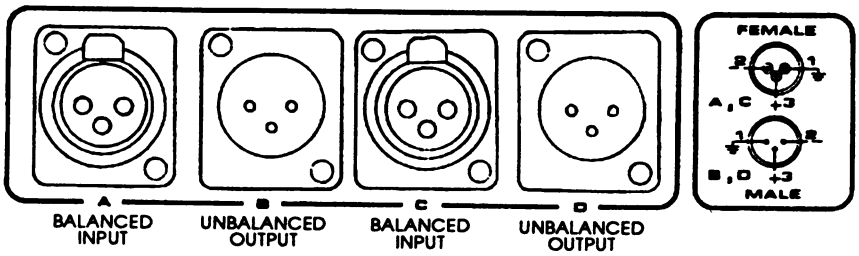
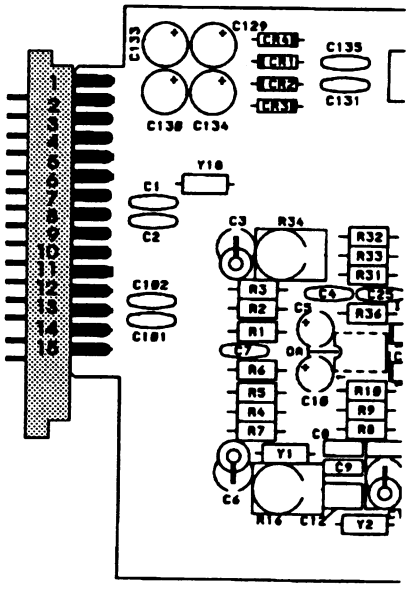
F-900A MAINFRAME




FS900 MINIFRAME

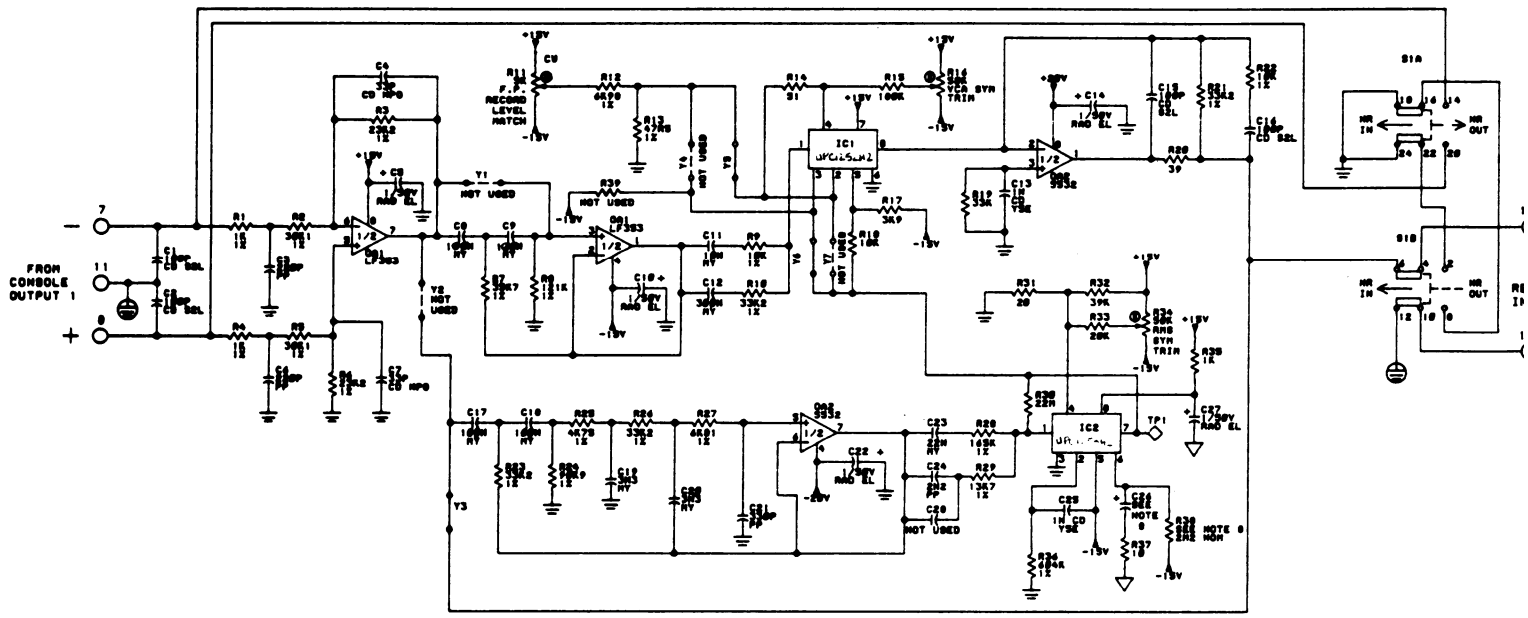
1 -24V
2 +24V
3 FRONT PANEL GROUND
4 CIRCUIT GROUND
5 +15V
6 -15V
7 C - } CHANNEL 1
8 C + } BALANCED INPUT
9 D - } CHANNEL 2
10 D + } UNBALANCED OUTPUT
11 CIRCUIT GND
12 A + } CHANNEL 2
13 A - } BALANCED INPUT
14 B + } CHANNEL 1
15 B - } UNBALANCED OUTPUT



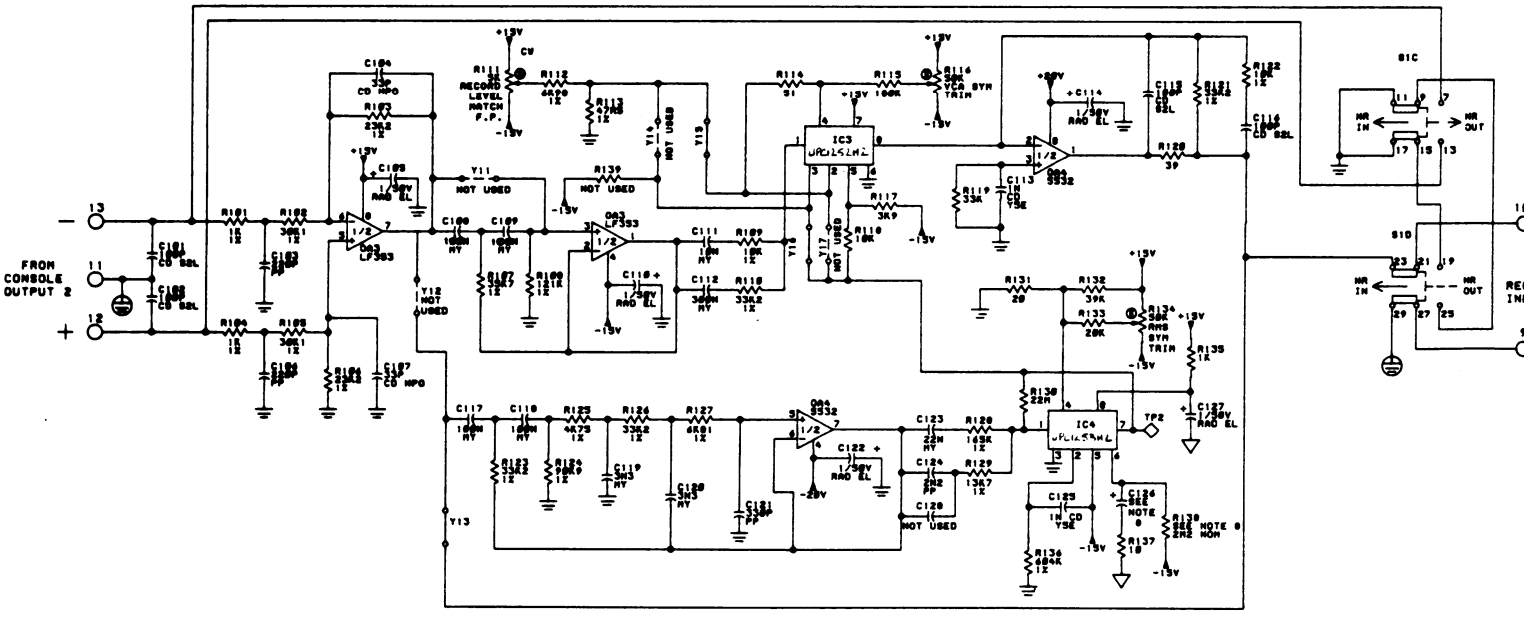

941A

Connections: F-900A Mainframe, FS900 Miniframe, 941A PC Fingers

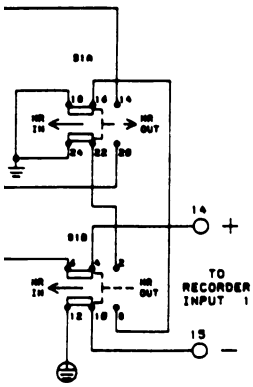
CHANNEL 1



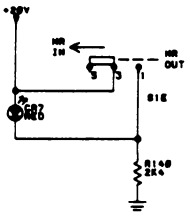

CHANNEL 2

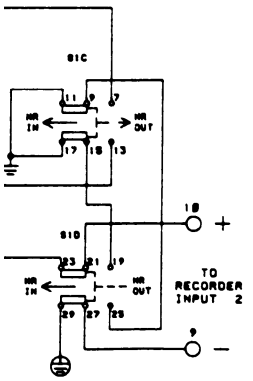



NOTES:
UNLESS OTHERWISE
1. ALL RESISTORS
2. ALL CAPACITOR
3. ALL MYLAR CAP
4. ALL NPO AND S
5. ALL Y5E CERAM
6. ALL POLYPROPY
7. ALL RADIAL EL
8. SELECT A 1% R
SELECTION PRO
SELECT RESIST
PROCEDURE
9. ▽ DENOTES GR
RETURN TO

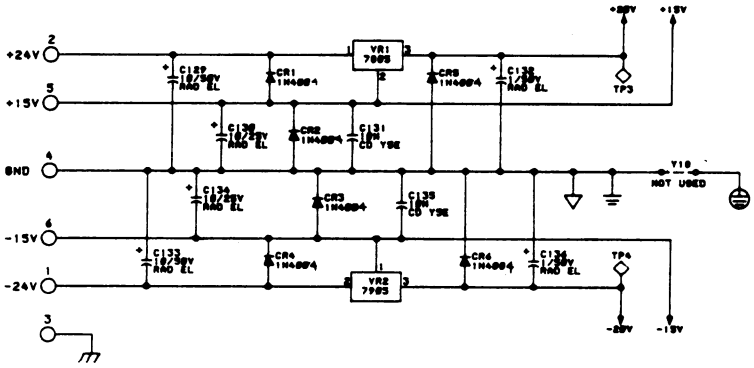


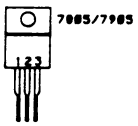

CHANNEL 1




CHANNEL 2




NOTES:
UNLESS OTHERWISE SPECIFIED:

1. ALL RESISTORS ARE EXPRESSED IN OHMS AND ARE 1/4W, 5%.
2. ALL CAPACITORS ARE EXPRESSED IN MICROFARADS.
3. ALL MYLAR CAPACITORS ARE +/-5%.
4. ALL NPO AND S2L CERAMIC DISC CAPACITORS ARE +/-5%.
5. ALL Y5E CERAMIC DISC CAPACITORS ARE +/-10%.
6. ALL POLYPROPYLENE CAPACITORS ARE +/-2.5%.
7. ALL RADIAL ELECTROLYTIC CAPACITORS ARE +/-20% AND ARE LOW LEAKAGE.
8. SELECT A 10 MFD ELECTROLYTIC CAPACTOR (C26 AND C126) WITH SELECTION PROCEDURE [illegible] GROUP A OR B.
   SELECT RESISTOR (R38 AND R138) WITH SELECTION PROCEDURE [illegible] GROUP A OR B.
9. ▽ DENOTES GROUNDS ARE TIED CLOSELY TOGETHER AND RETURN TO CENTRAL GROUND SEPARATELY.