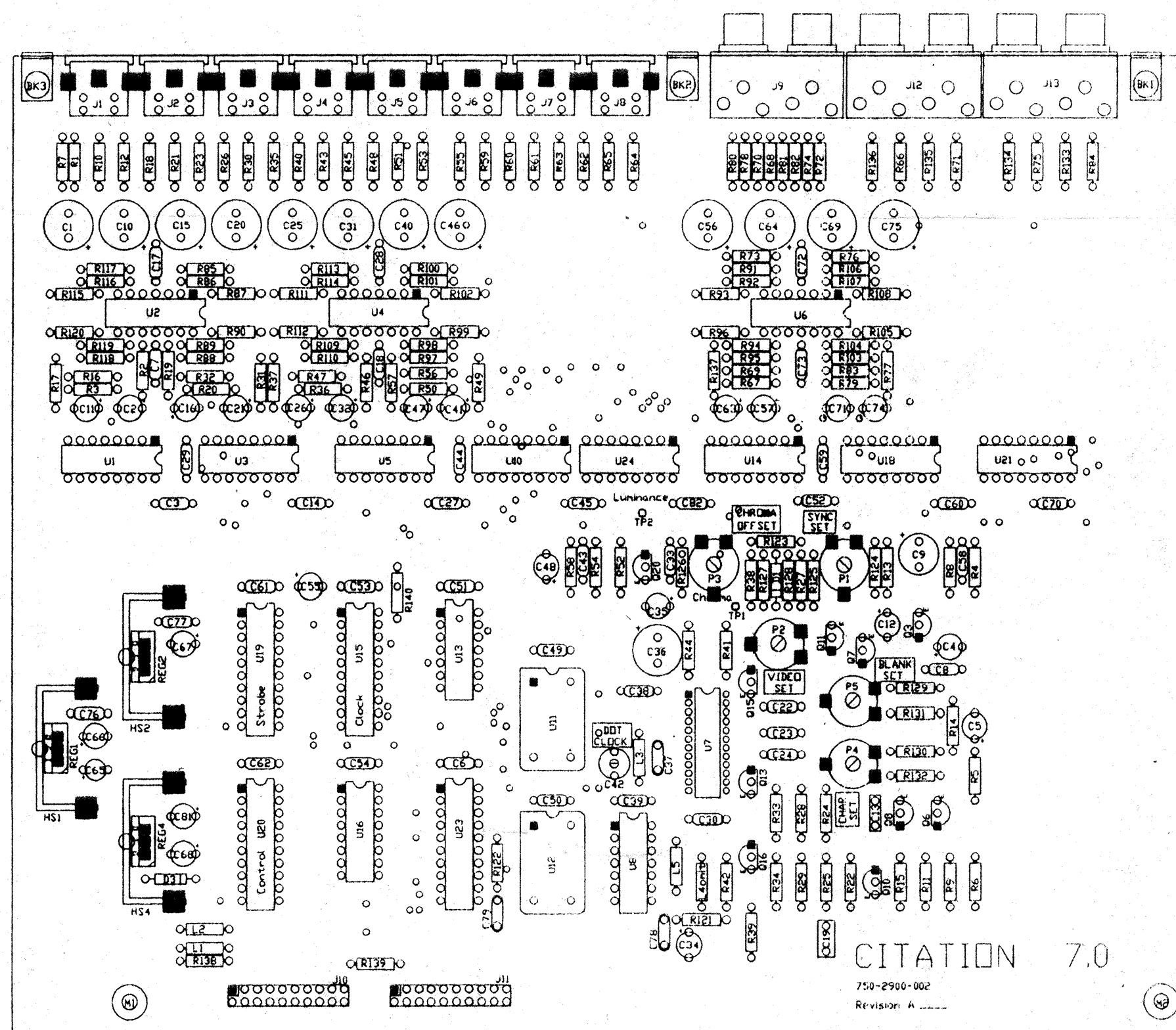
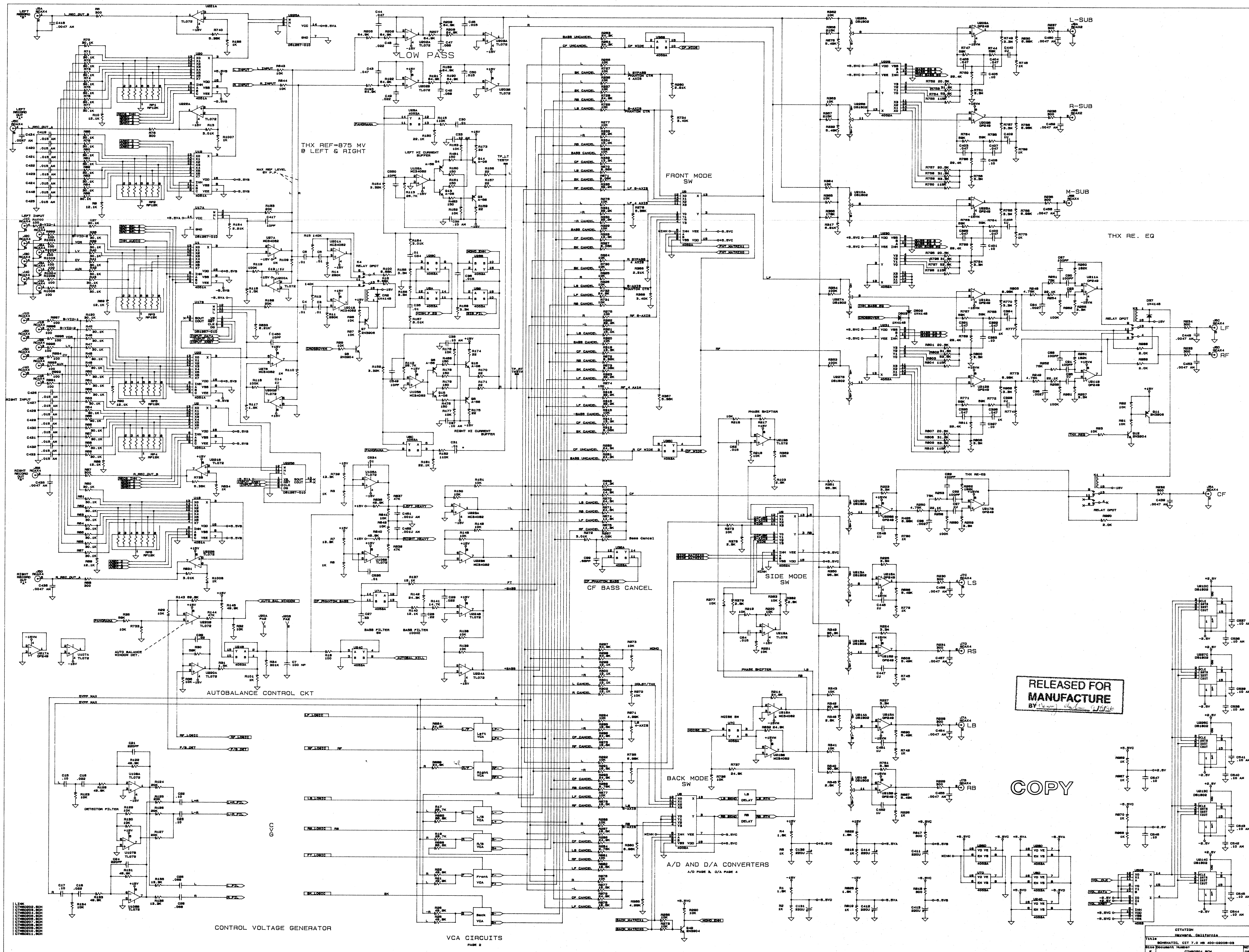


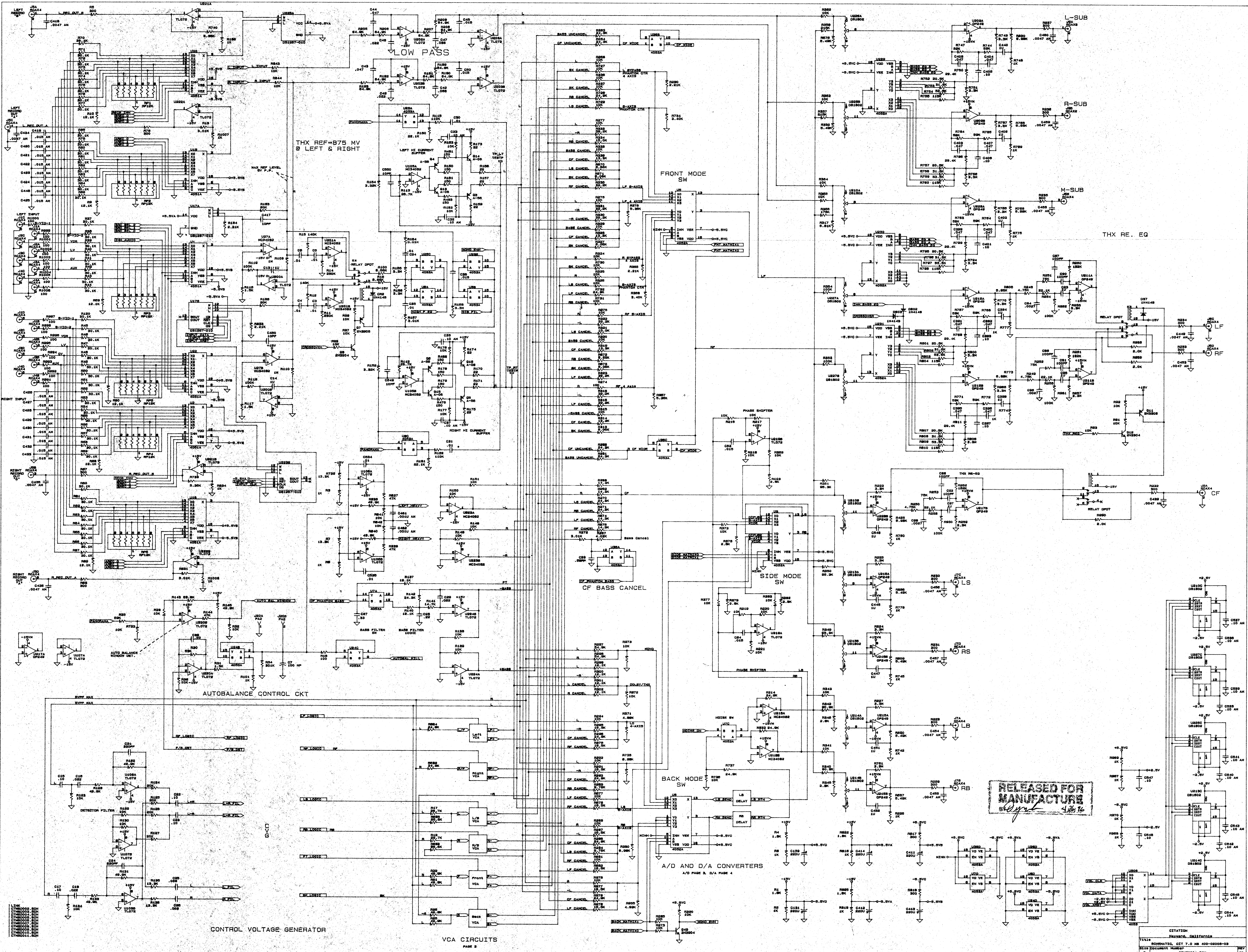
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REV	DESCRIPTION OF CHANGE OR PREVIOUS STATE	DRAWN	DATE
A2	ORIGINAL	DC	04/18/95



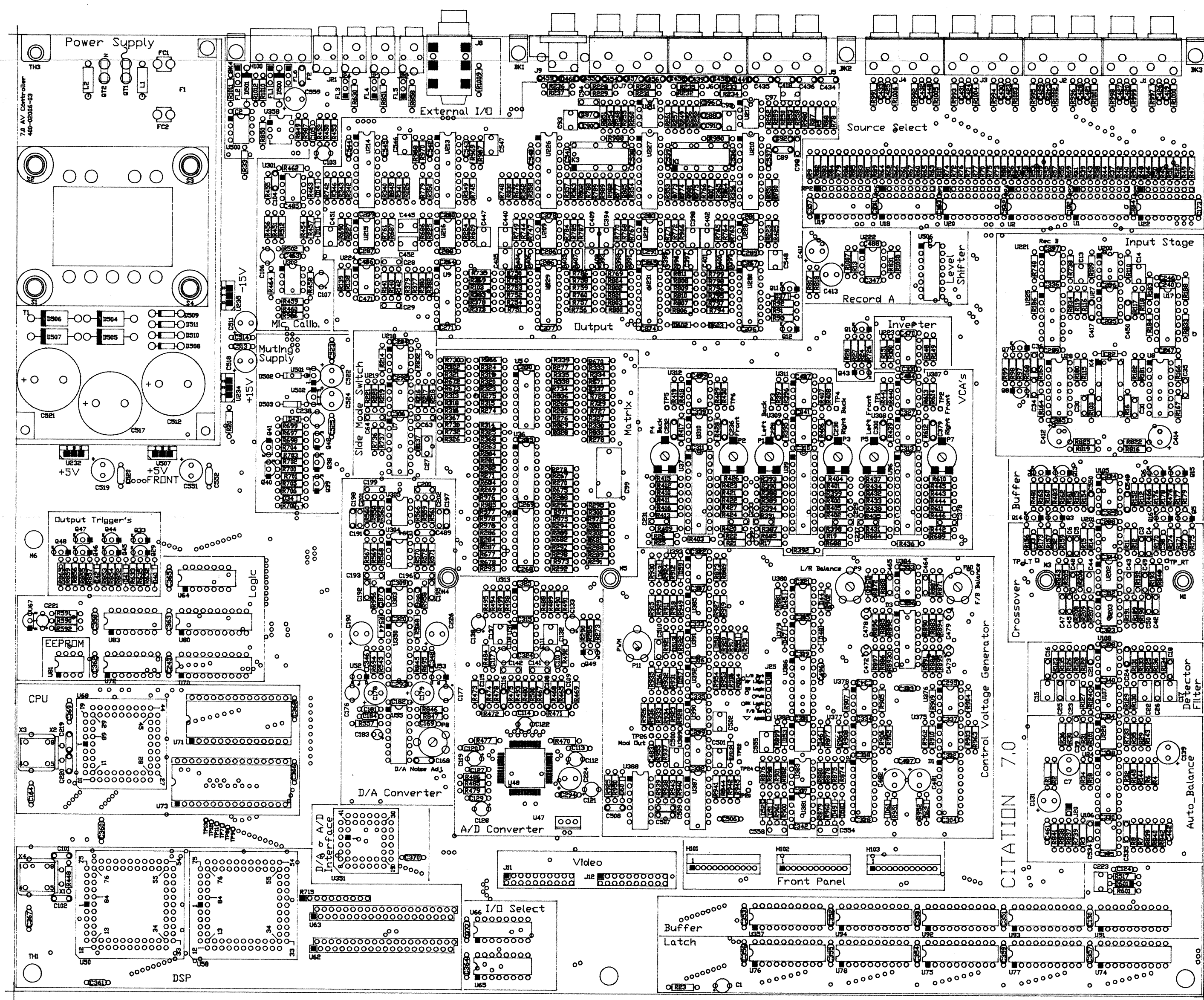
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MANUFACTURE**
9/18/95

SIGNATURE		DATE		CITATION 26046 EDEN LANDING ROAD, HAYWARD, CA 94545	
DRAWN	D.J.CLARK	04/18/95			
DESIGN CHK	<i>X. Miller</i>	<i>9-19-95</i>		CITATION 7.0 VIDEO PCB PART PLACEMENT	
PROJ MGR	<i>D. Clark</i>	<i>9-19-95</i>			
C70VBPA1.SKD				DWG NUMBER	REV A2
SHEET 1 OF 1					





REVISIONS			
REV	DESCRIPTION OF CHANGE OR PREVIOUS STATE	DRAWN	DATE
C1	DIODE REVERSED	DJC	10/17/95
C2	SOFTWARE CHANGE	DJC	02/28/96
C3	CHANGE RESISTOR VALUE IN R670, AND R672	DJC	02/29/96
03	PCB CIRCUIT REVISION	DJC	04/09/96
04	PCB CIRCUIT REVISION ECO 0191	DJC	09/19/96

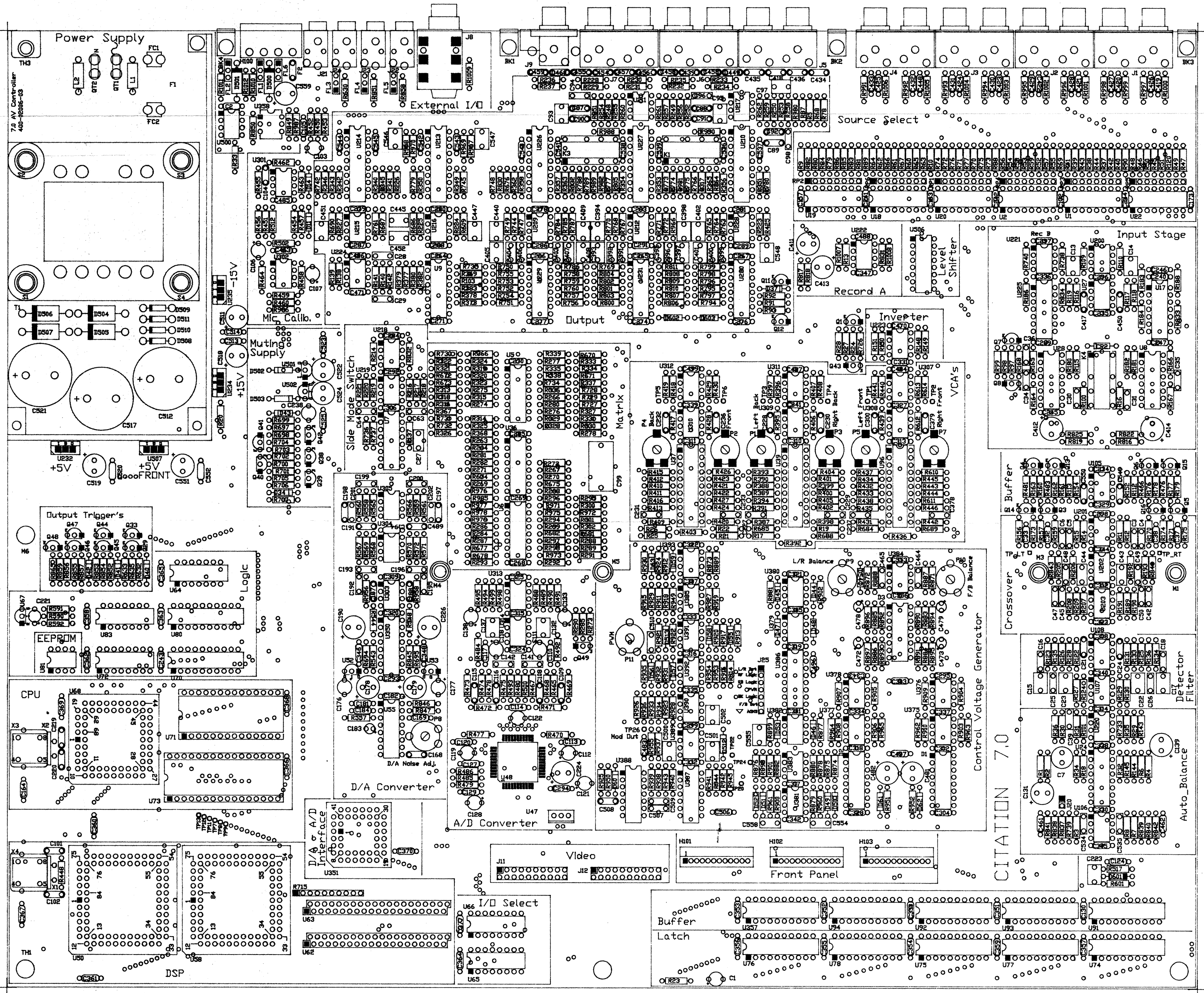


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RELEASED FOR
MANUFACTURE
BY J.L.

SIGNATURE		DATE		CITATION 26046 EDEN LANDING ROAD, HAYWARD, CA 94545 PART PLACEMENT CIT 7.0 MAIN BD.	
DRAWN	D.J. CLARK	09/19/96			
DESIGN	CHK				
PROJ	MGR				
DWG NUMBER				400-02006	REV
C70MBPD2.DWG				04	
SCALE: NOT TO SCALE				SHEET 1 OF 1	

REVISIONS			
REV	DESCRIPTION OF CHANGE OR PREVIOUS STATE	DRAWN	DATE
C1	DIODE REVERSED	DJC	10/17/95
C2	SOFTWARE CHANGE	DJC	02/28/96
C3	CHANGE RESISTOR VALUE IN R670, AND R672	DJC	02/29/96
03	PCB CIRCUIT REVISION	DJC	04/09/96



RELEASED FOR
MANUFACTURE

SIGNATURE		DATE		CITATION	
				26046 EDEN LANDING ROAD, HAYWARD, CA 94545	
DRAWN	D.J.CLARK	04/09/96		PART PLACEMENT CIT 7.0 MAIN BD.	
DESIGN CHK	<i>[Signature]</i>	<i>4/09/96</i>			
PROJ MGR	<i>[Signature]</i>	<i>5-15-96</i>			
C70MBPD0.DWG				DWG NUMBER	REV
				400-02006	03
				SCALE: NOT TO SCALE	
				SHEET 1 OF 1	

REVISIONS			
REV.	DESCRIPTION OF CHANGE OR PREVIOUS STATE	DRAWN	DATE
00	ORIGINAL	DJC	7/21/97
01	ADDED NOTE 7A	DJC	7/24/97
02	COMPONENT VALUE, AND NOTE 15 SHEET 2	DJC	12/17/97
03	CITATION 5.0 ONLY: ROTATION OF P104	DJC	06/04/98

04 CITATION 5.0 ONLY DISTANCE OF SPACING FOR R206/R207 CHANGED FROM 1/4" TO 1/2"

NOTES:
UNLESS OTHERWISE SPECIFIED:

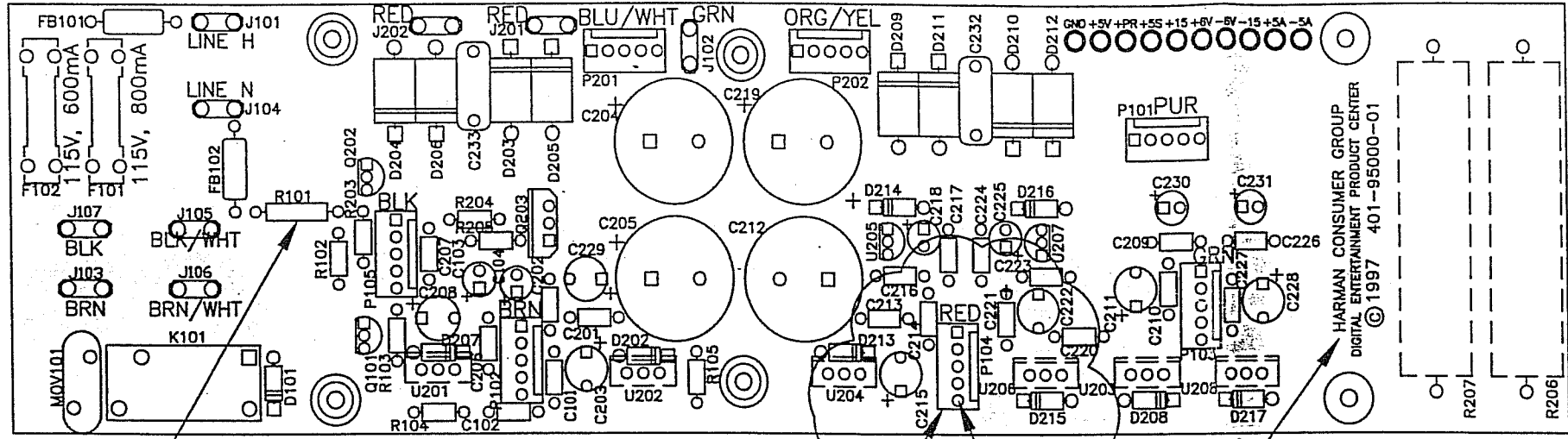
- SQUARE PADS ON THRU HOLE PARTS (ie: CONNECTORS, DIPS, SIPS, LEDS,) DENOTE PIN 1.
- ALL BOARDS REQUIRE A COMPLETE AND THOROUGH VISUAL INSPECTION.
- ALL BOARDS MUST BE BARE BOARD TESTED.
- XX CIRCLES REFER TO ITEM NUMBERS ON THE PARTS LIST.
- XX SQUARES REFER TO THE NOTES.
- ASSEMBLE AND SOLDER PER ANSI/IPC-A-610B.

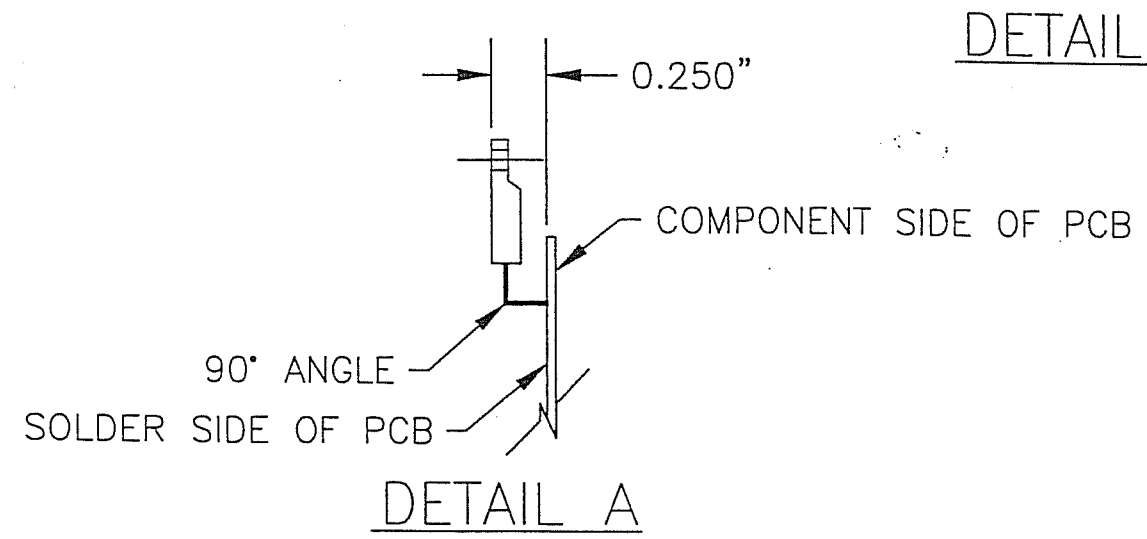
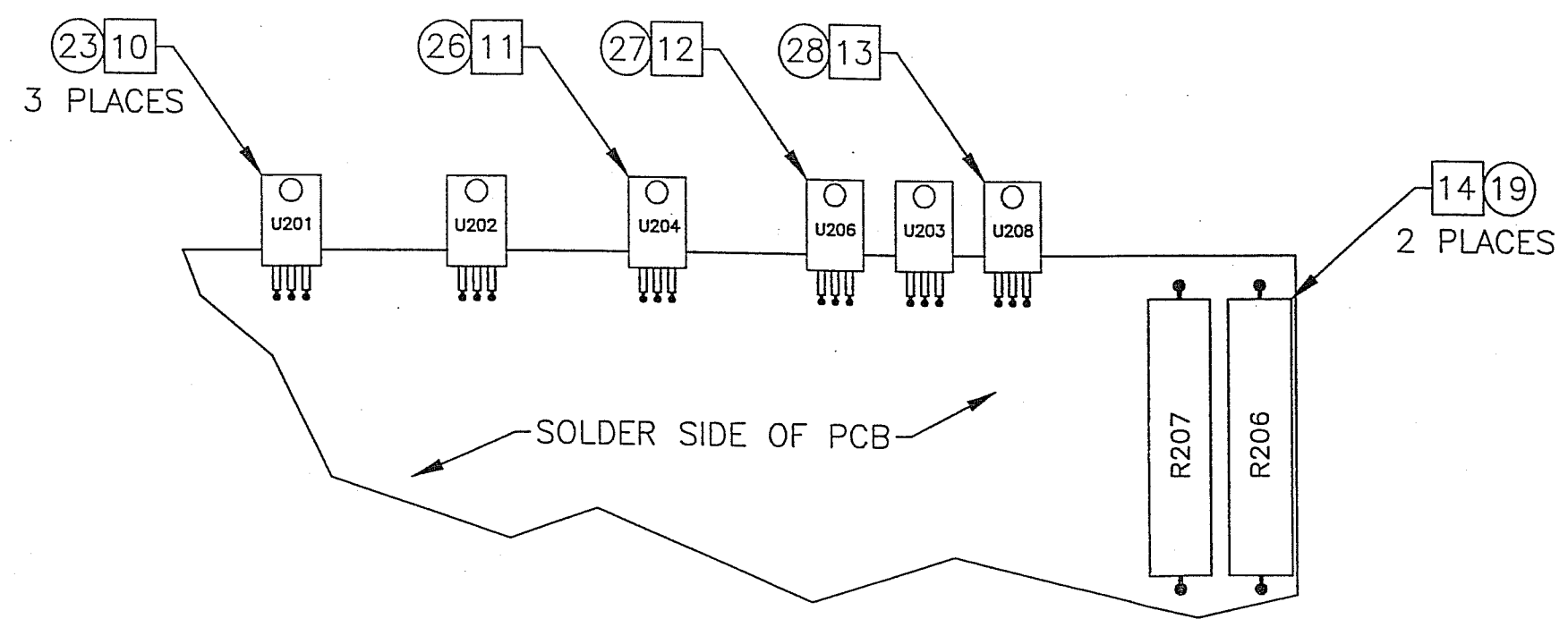
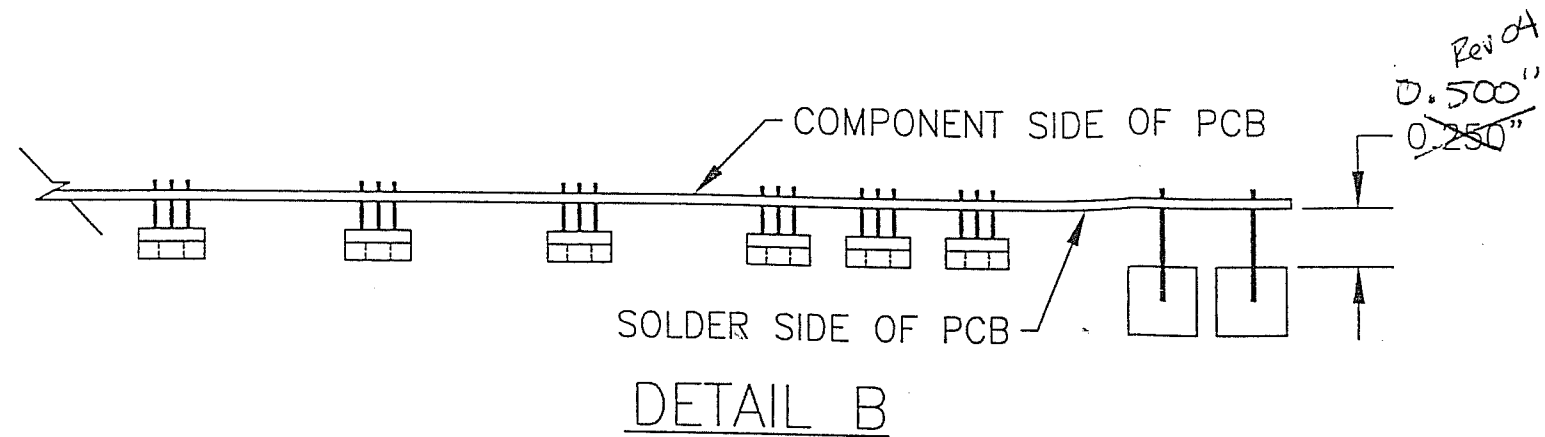
PRE WAVE:

- KEEP U201, U202, U204, U206, U203, U208, R207, R206, PLUS ALL MOUNTING HOLES FREE AND CLEAR OF SOLDER.
- OMIT 4.3M, 5%, 1/2W, RESISTOR, IN R101; IF BUILDING PCB'S FOR SIGNATURE 2.0 230V UNITS ONLY.
OMIT 4.3M, 5%, 1/2W, RESISTOR, IN R101; IF BUILDING PCB'S FOR CITATION 5.0 120V, AND 230V UNITS.

POST WAVE:

(CONTINUED ON SHEET 2)





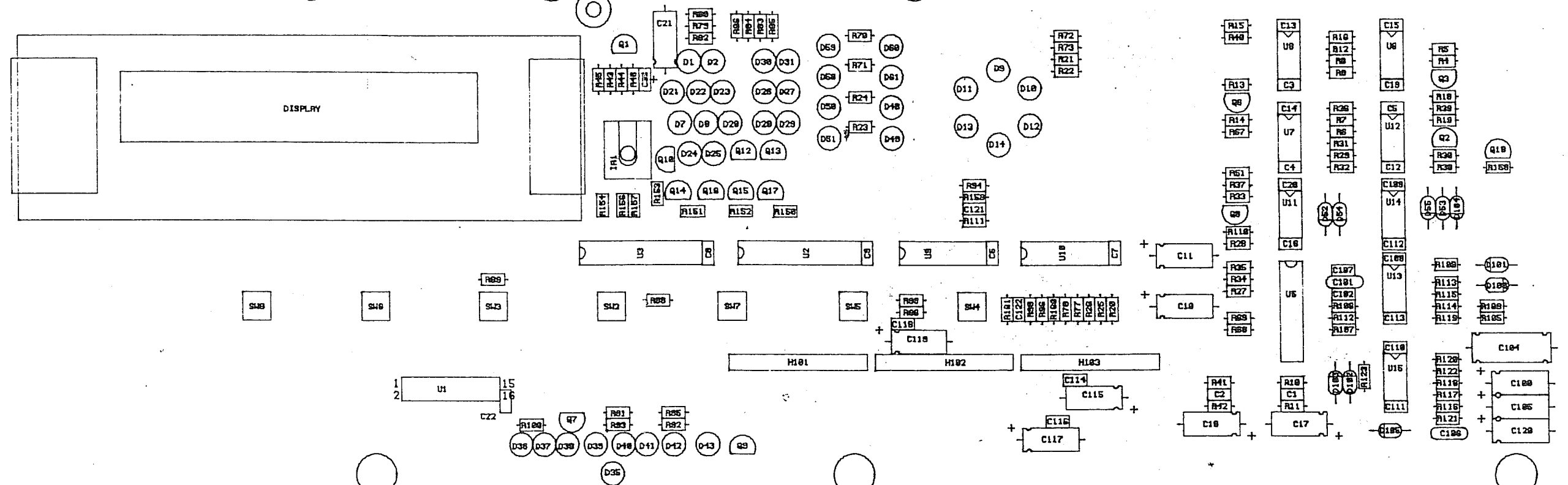
NOTES: (CONTINUED FROM SHEET 1)

POST WAVE:

9. AFTER WAVE SOLDER; ALL COMPONENTS ARE TO BE FLUSH, AND PERPENDICULAR TO PCB. (ie: RELAY, CONNECTORS, FUSE CLIPS, AND FAST-ON'S)
10. THREE, 7805 VOLTAGE REGULATORS, ARE TO BE BENT AT A 90° ANGLE. (SEE DETAIL A). INSTALL 7805 VOLTAGE REGULATORS IN U201, U202, U203, ON SOLDER SIDE OF PCB (SEE DETAIL C). BE SURE TO KEEP BACK SIDE OF REGULATORS, PARALLEL, AND A HEIGHT OF 0.250" FROM SOLDER SIDE OF PCB, AS SHOWN IN DETAIL A.
11. ONE, 7815 VOLTAGE REGULATOR, IS TO BE BENT AT A 90° ANGLE. (SEE DETAIL A). INSTALL 7815 VOLTAGE REGULATOR IN U204, ON SOLDER SIDE OF PCB (SEE DETAIL C). BE SURE TO KEEP BACK SIDE OF REGULATOR, PARALLEL, AND A HEIGHT OF 0.250" FROM THE SOLDER SIDE OF PCB, AS SHOWN IN DETAIL A.
12. ONE, 7915 VOLTAGE REGULATOR, IS TO BE BENT AT A 90° ANGLE. (SEE DETAIL A). INSTALL 7915 VOLTAGE REGULATOR IN U206, ON SOLDER SIDE OF PCB (SEE DETAIL C). BE SURE TO KEEP BACK SIDE OF REGULATOR, PARALLEL, AND A HEIGHT OF 0.250" FROM THE SOLDER SIDE OF PCB, AS SHOWN IN DETAIL A.
13. ONE, 7905 VOLTAGE REGULATOR, IS TO BE BENT AT A 90° ANGLE. (SEE DETAIL A). INSTALL 7905 VOLTAGE REGULATOR IN U208 ON SOLDER SIDE OF PCB (SEE DETAIL C). BE SURE TO KEEP BACK SIDE OF REGULATOR, PARALLEL, AND A HEIGHT OF 0.250" FROM THE SOLDER SIDE OF PCB, AS SHOWN IN DETAIL A.
14. INSTALL TWO, 33 OHM, 5%, WIREWOUND, 10W, RESISTORS IN R206, AND R207 ON SOLDER SIDE OF PCB. BE SURE TO KEEP BODY OF RESISTORS ~~0.250"~~ 0.500" (Rev 04) OFF PCB. (SEE DETAILS B AND C)
15. THIS ASSEMBLY DRAWING TO BE USED IN COMPLIANCE WITH PART LIST 061-95000-02, AND BARE BOARD 401-95000-01 ONLY.

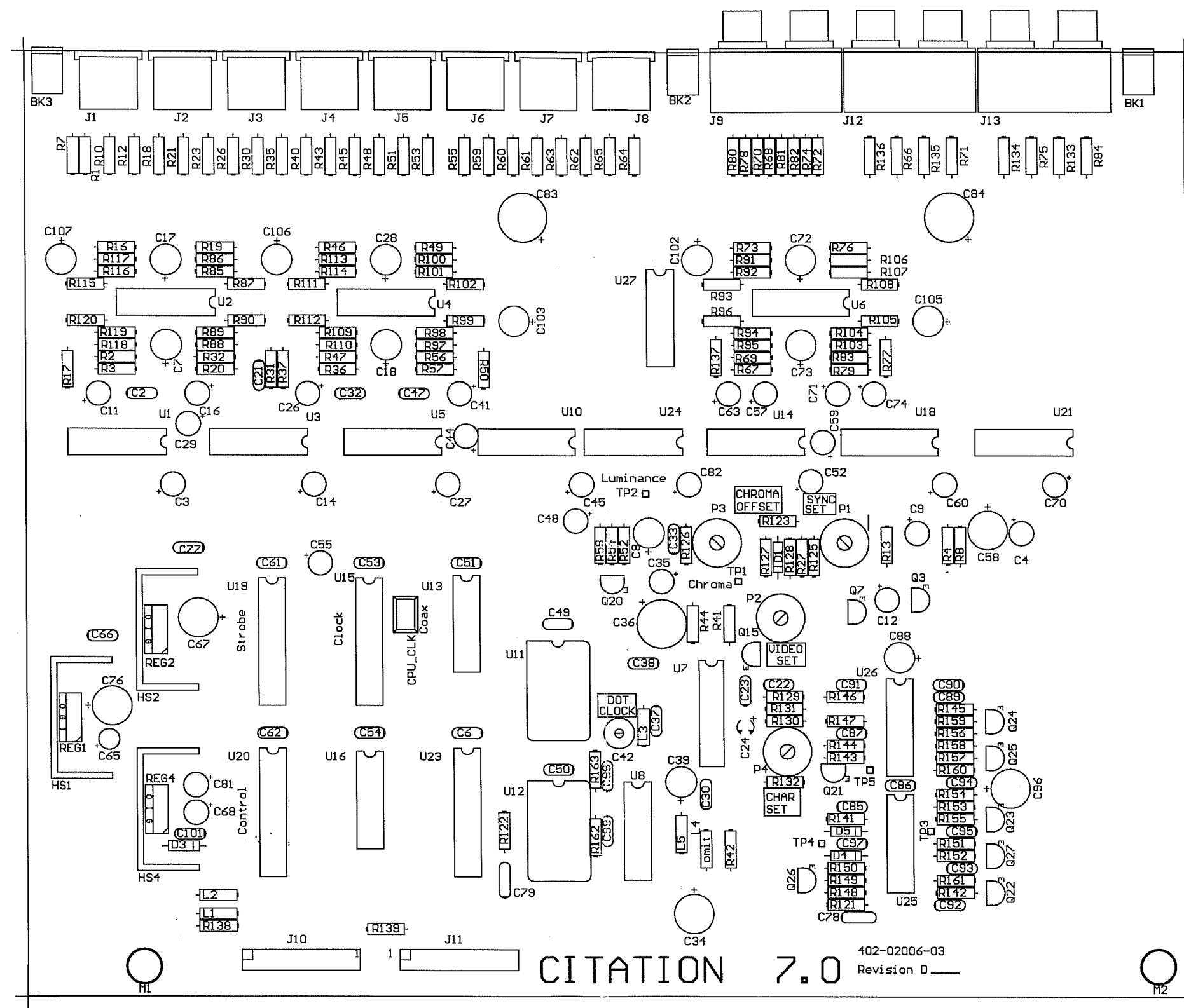
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SIGNATURE		DATE	harman consumer group digital entertainment product center 1308 Borregas Ave., Sunnyvale, CA 94089-1001	
DRAWN	DJ CLARK	7/21/97		
DESIGN CHK				
PROJ. MGR				
CSPWA203.DWG			DWG. NUMBER	REV.
			041-95000	04
			SCALE: NOT TO SCALE	SHEET: 2 OF

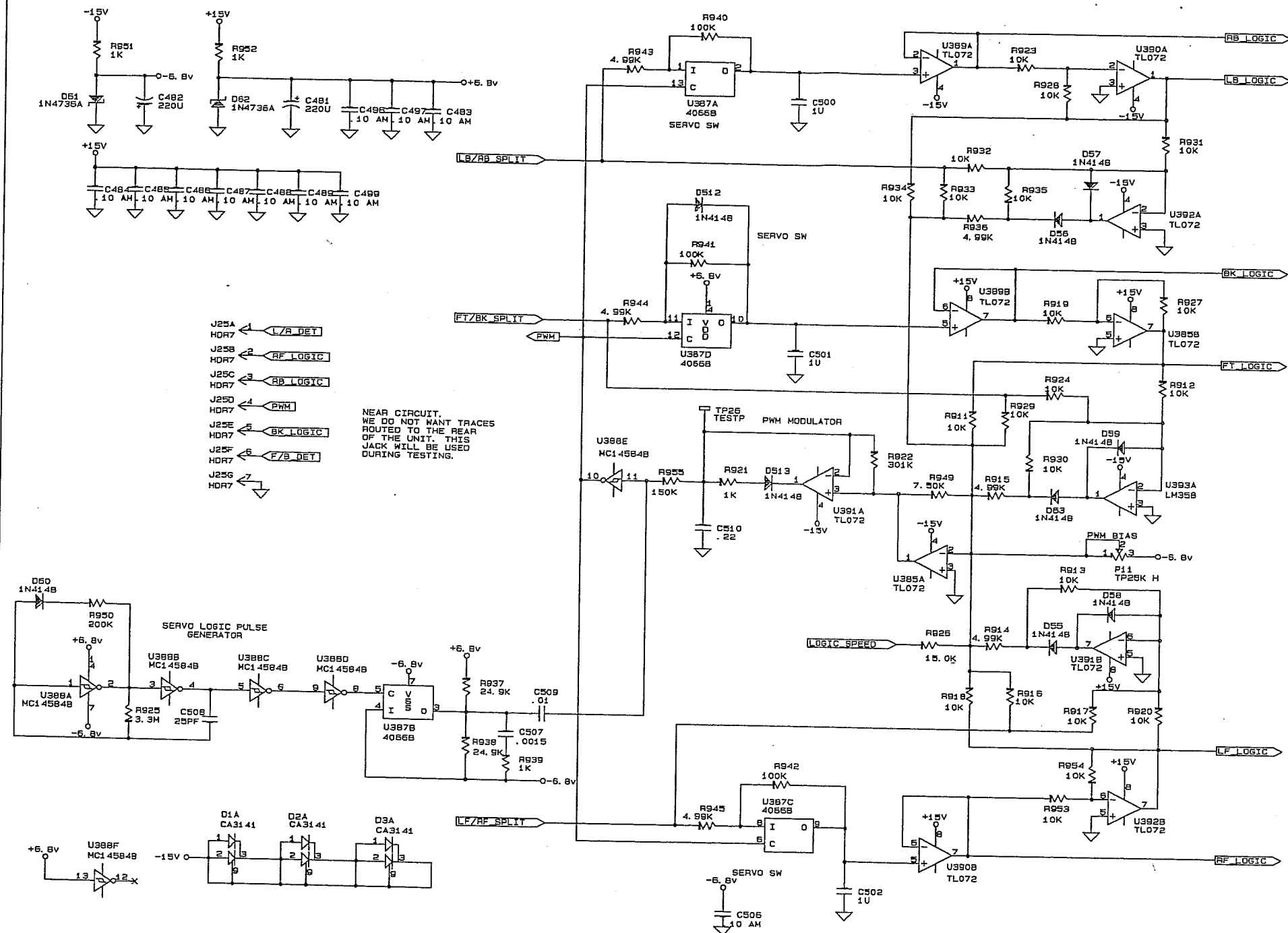


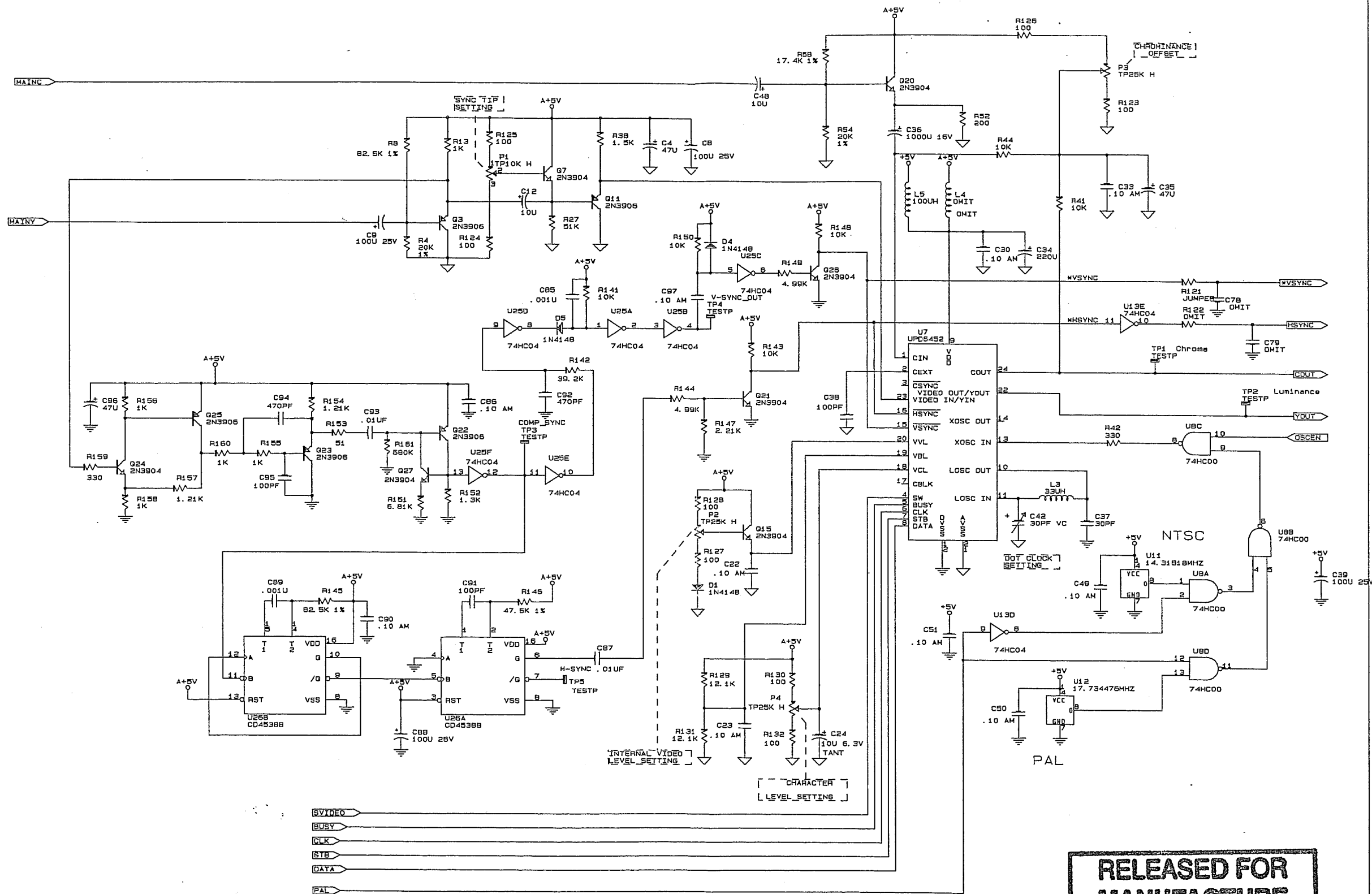
RELEASED FOR
MANUFACTURE
12/19/95

CITATION 26046 EDEN LANDING RD. HAYWARD, CA. 94545 (510) 293-0183	LAYER: SILKSCREEN
CITATION 7.0 FRONT PANEL PART PLACEMENT	
750-2900-000 REV D0	
DATE: MAY 31, 1995	
DRAWN BY: LAURIE MITCHELL	



RELEASED FOR
MANUFACTURE
BY *[Signature]* 8/15/97



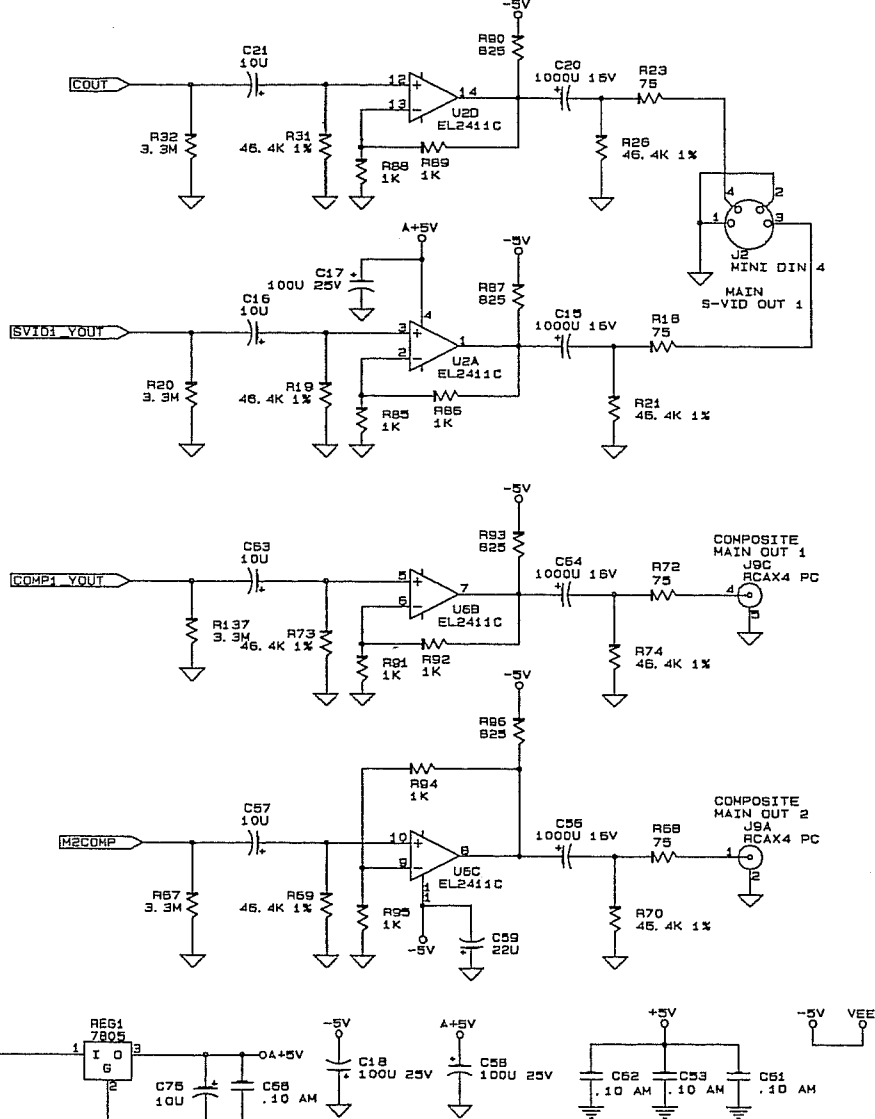
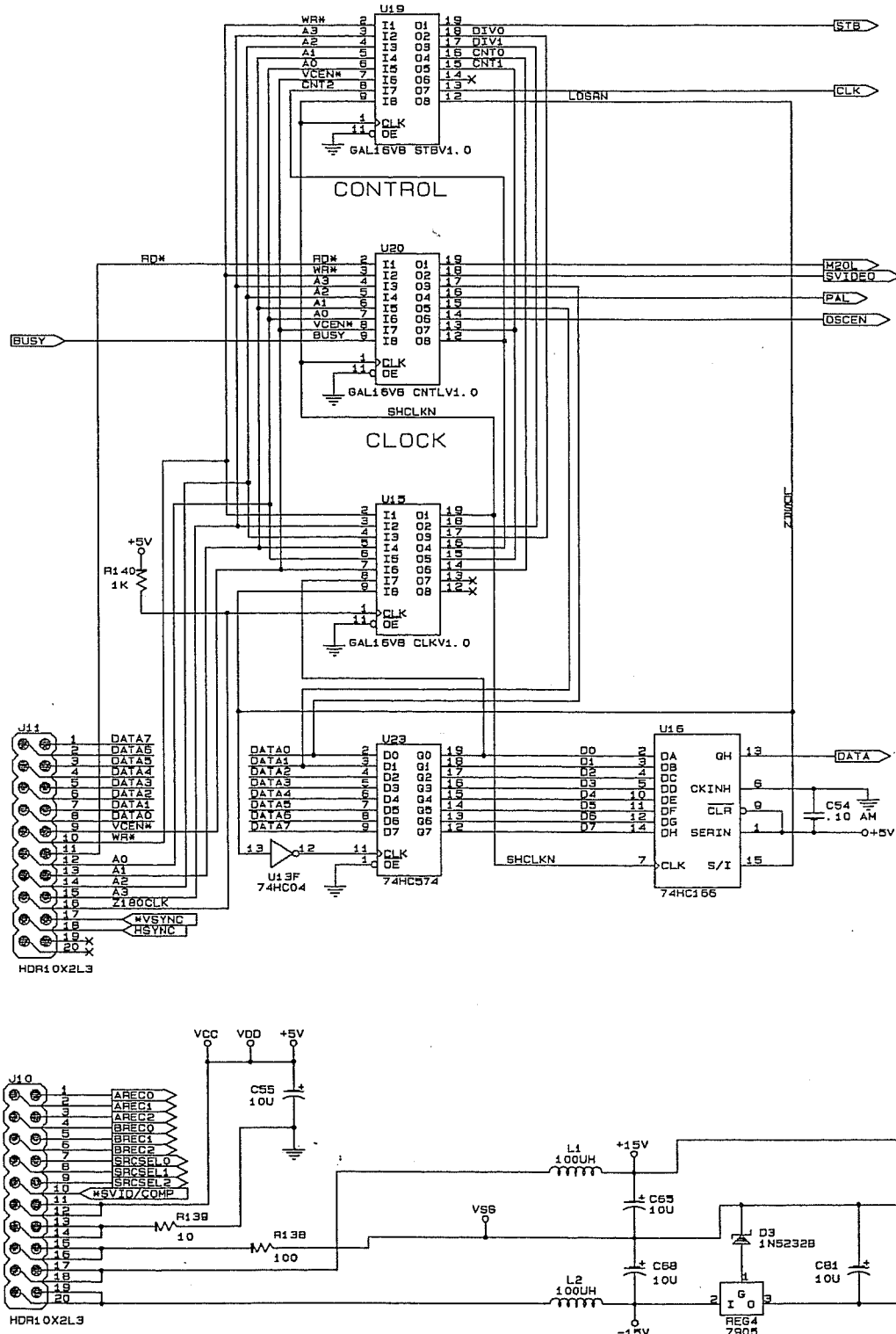


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MANUFACTURE
BY

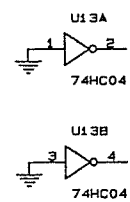
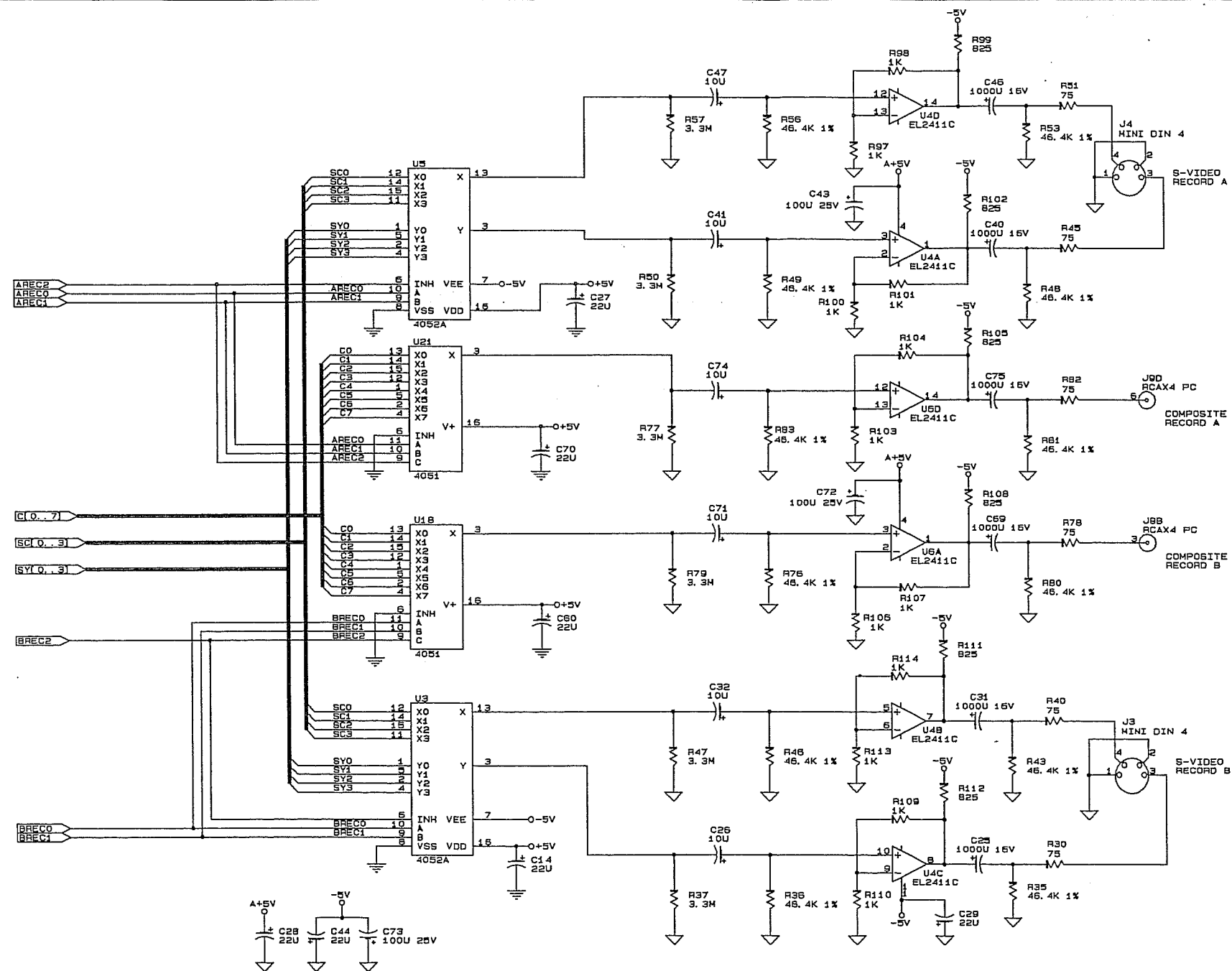
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Size	Document Number
C	C70VBB01.SCH
Date:	May 24, 1996 Sheet 1 of 4

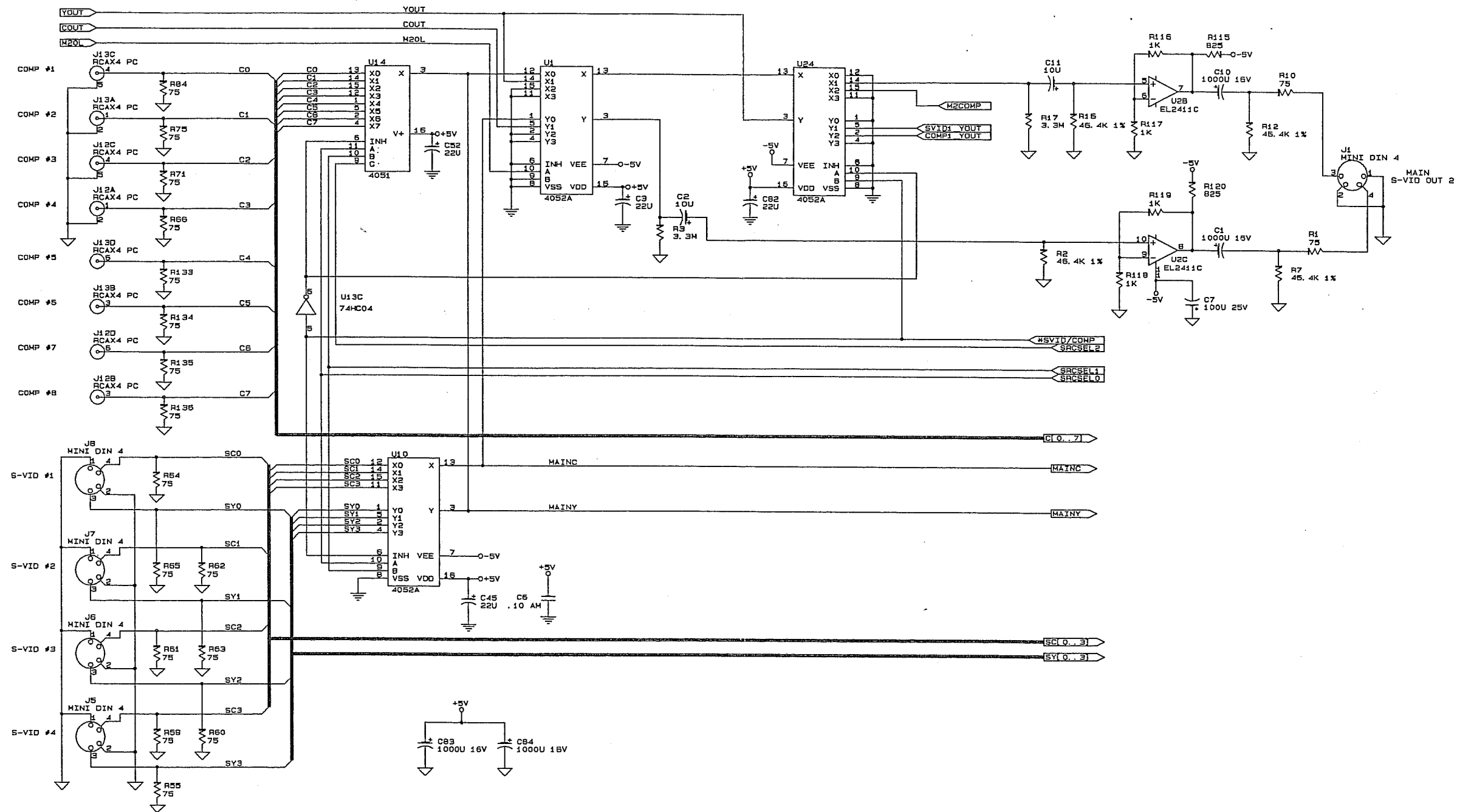
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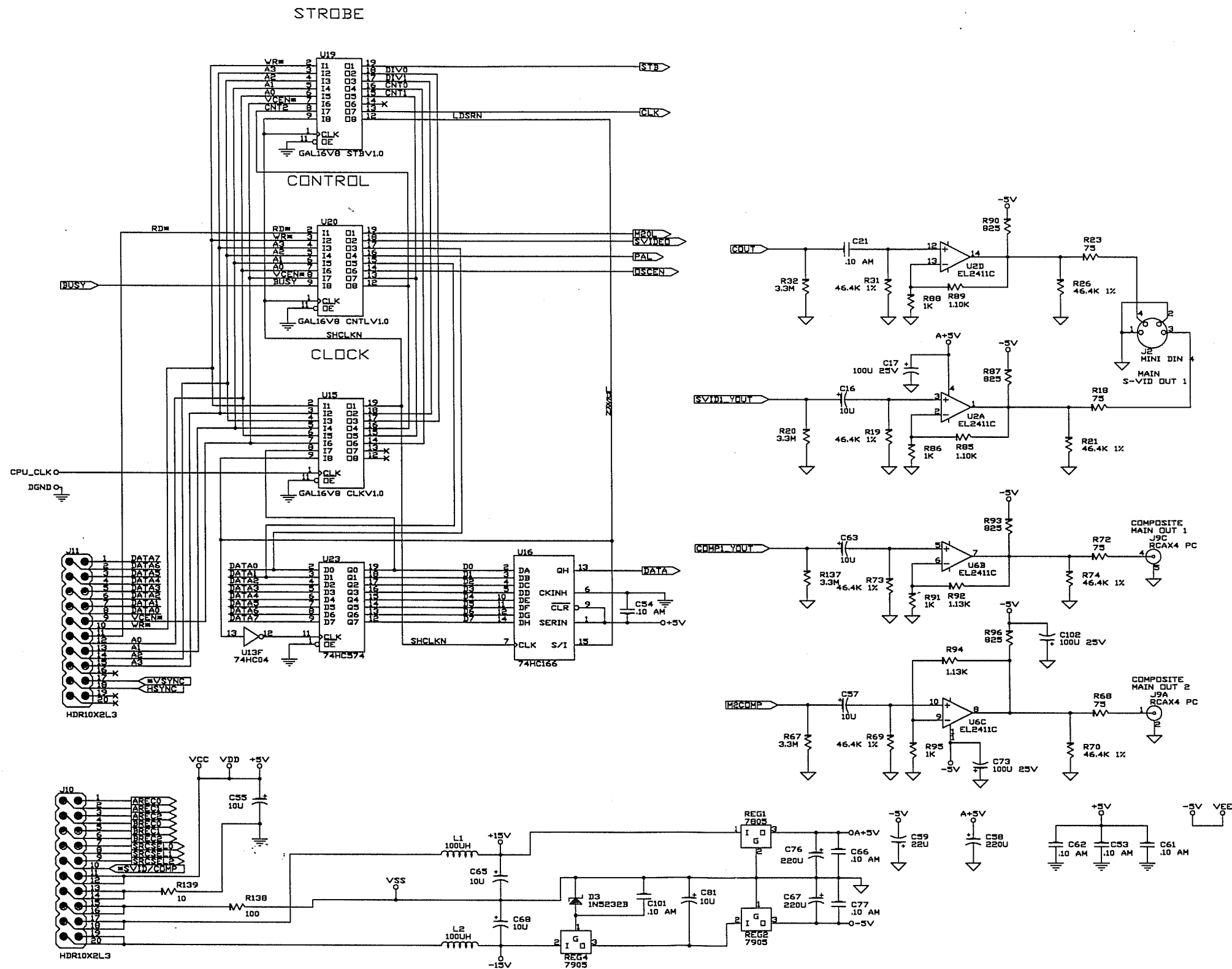
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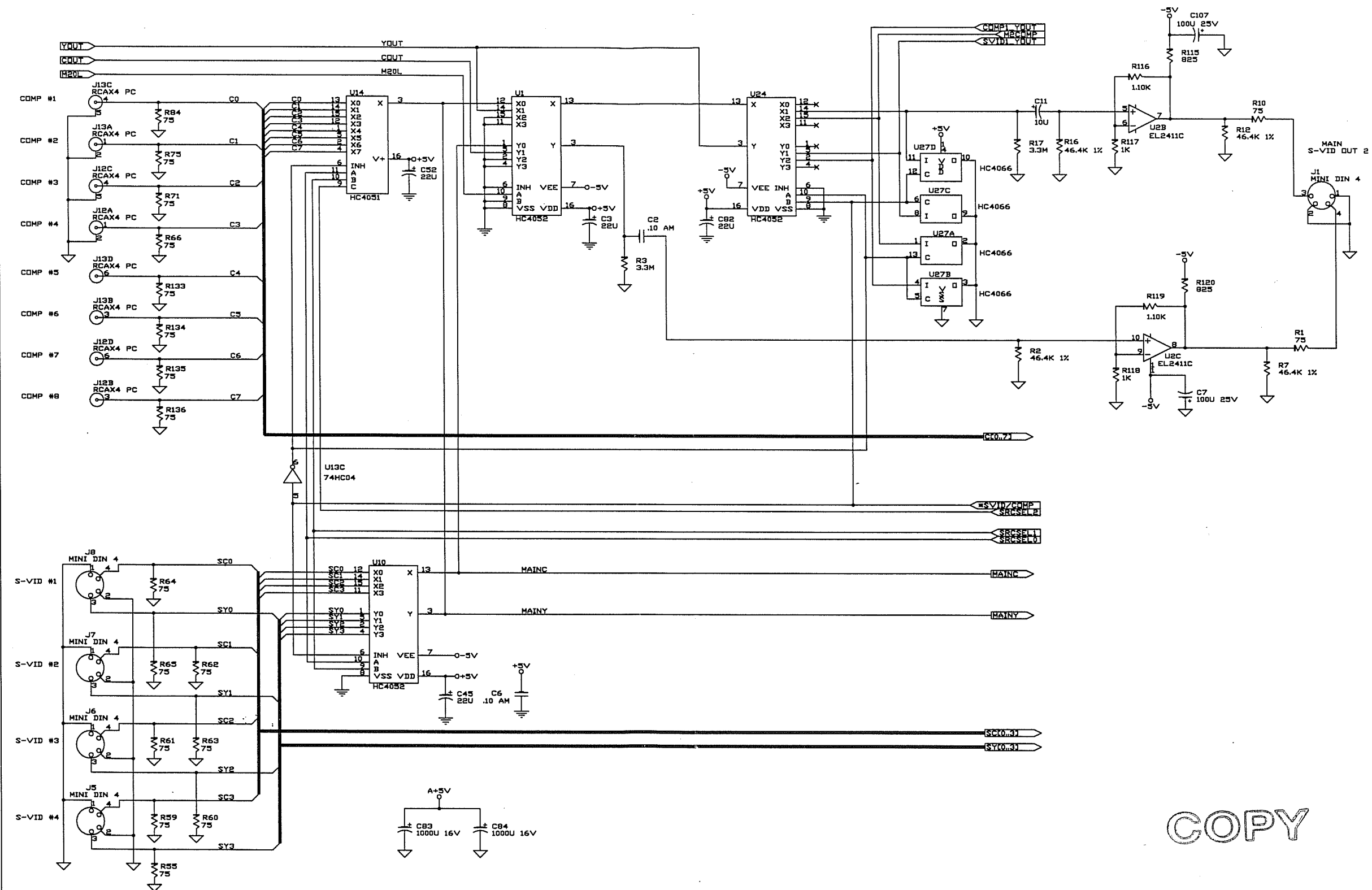






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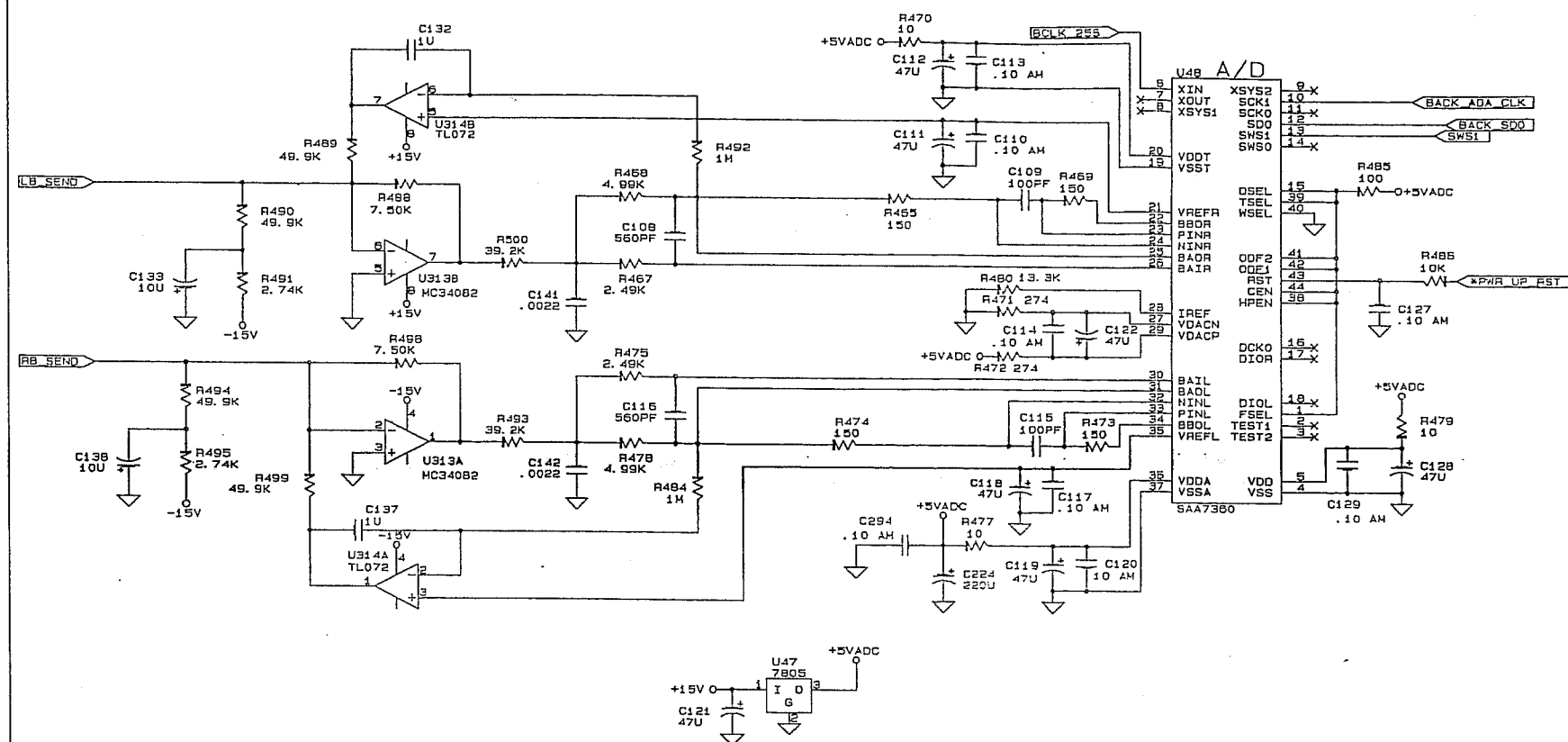


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Date	August 6, 1997	Sheet 4 of 4

ALIAS FILTERS

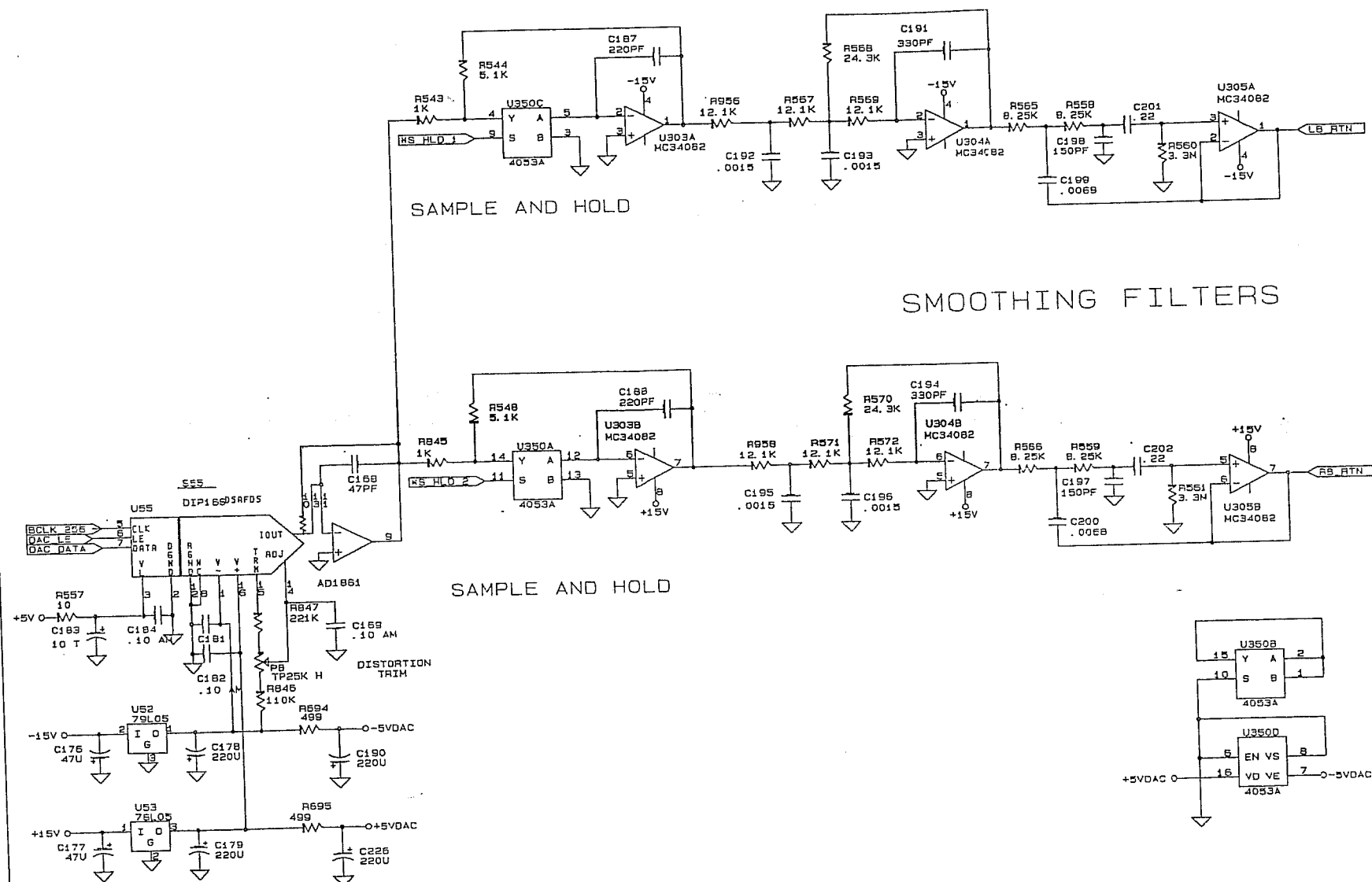
ANALOG TO DIGITAL CONVERTER



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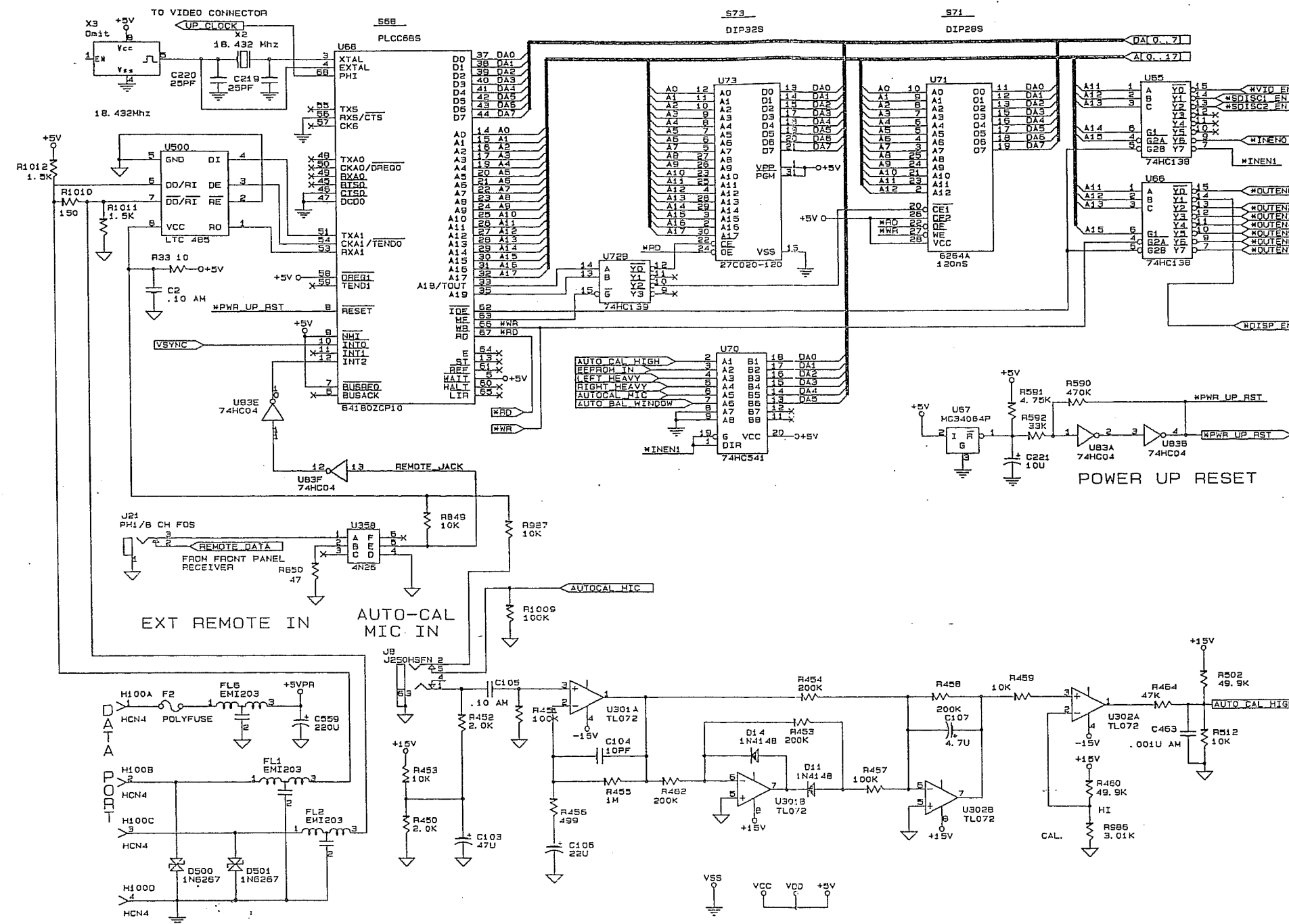
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Size	Document Number
C	C7HBD003.6CH
Date:	April 26, 1995 Sheet 3 of 11

DIGITAL TO ANALOG CONVERTER



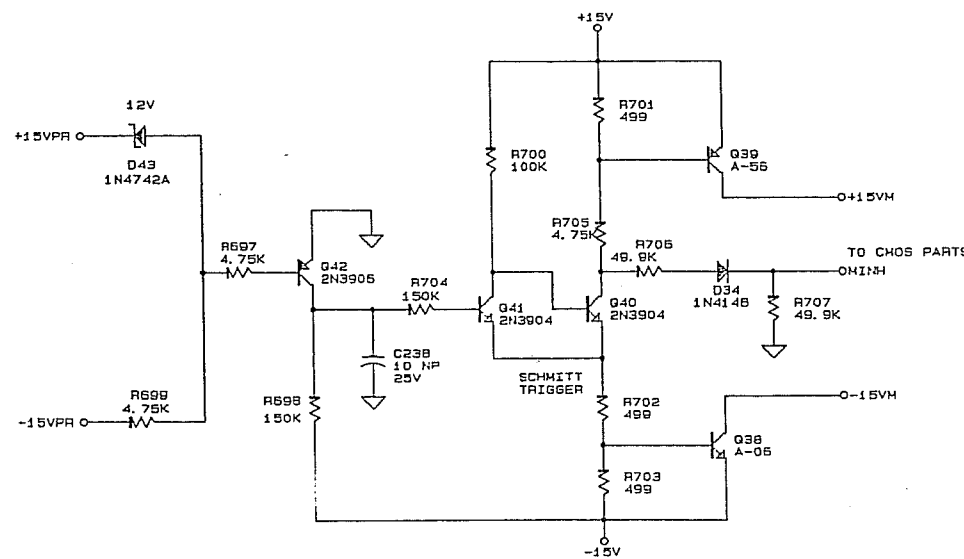
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Date:	April 26, 1995	Sheet 4 of 11

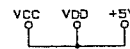
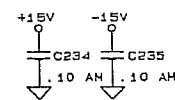
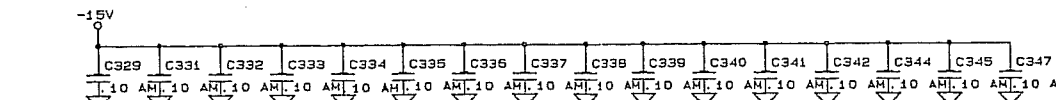
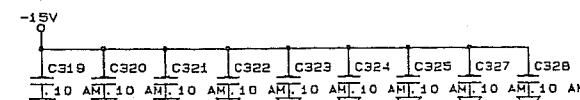
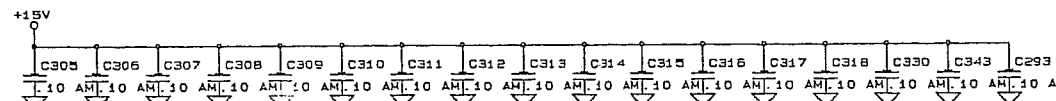
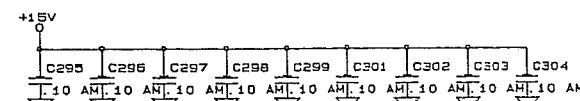
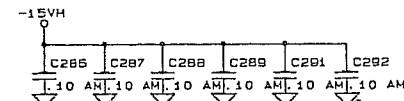
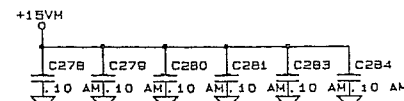
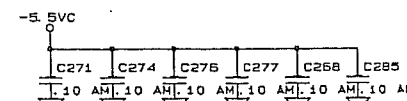
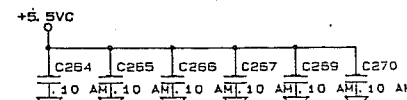
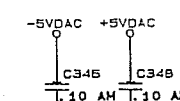
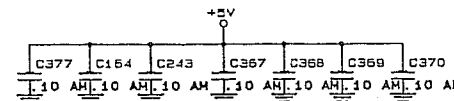
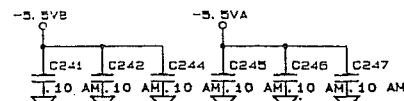
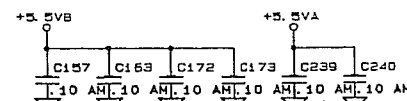
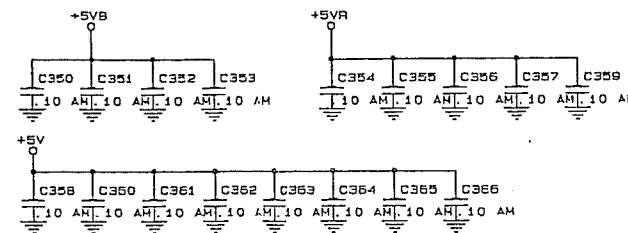


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Date: April 25, 1995	Sheet 5 of 11

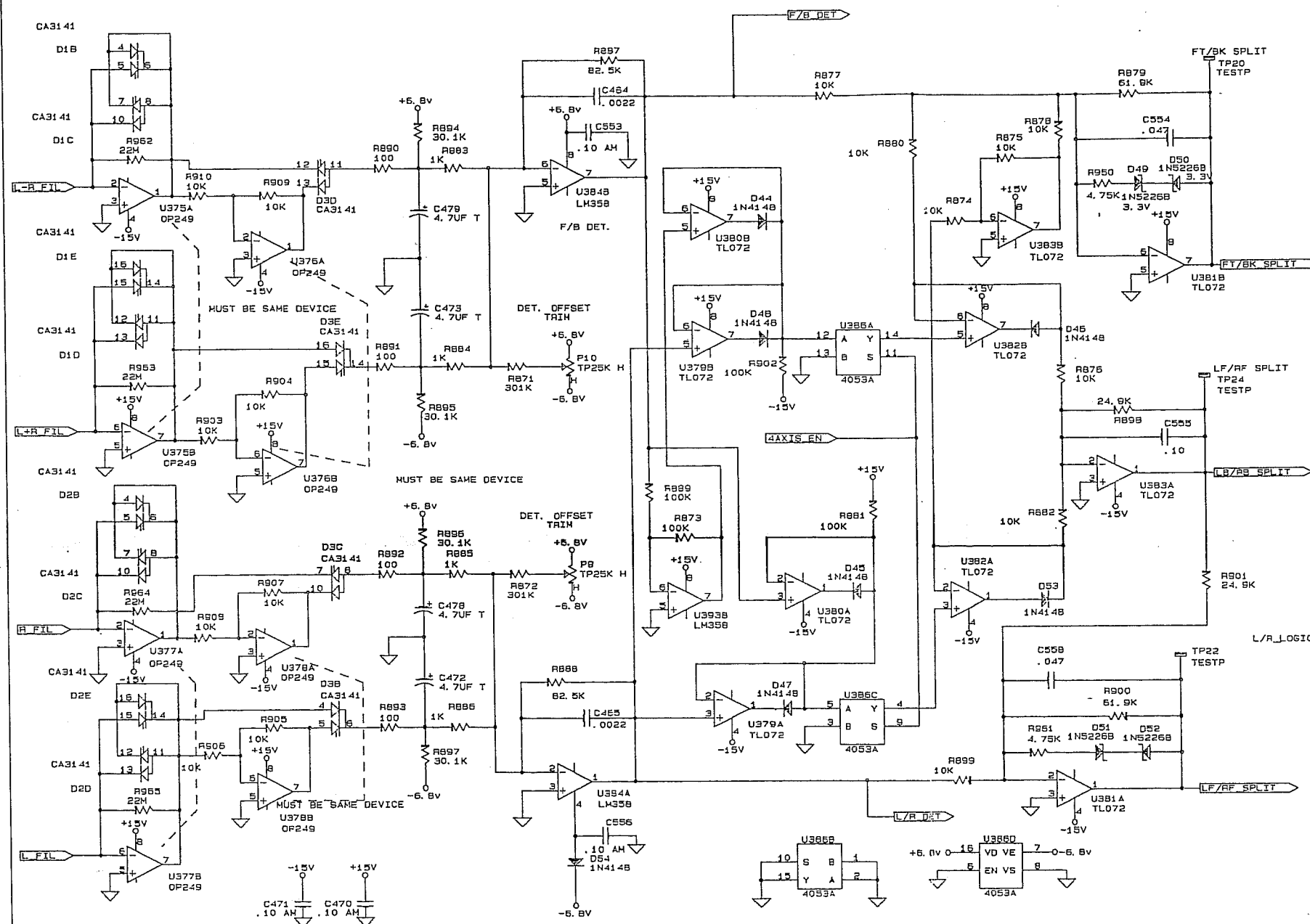


MUTING CIRCUIT



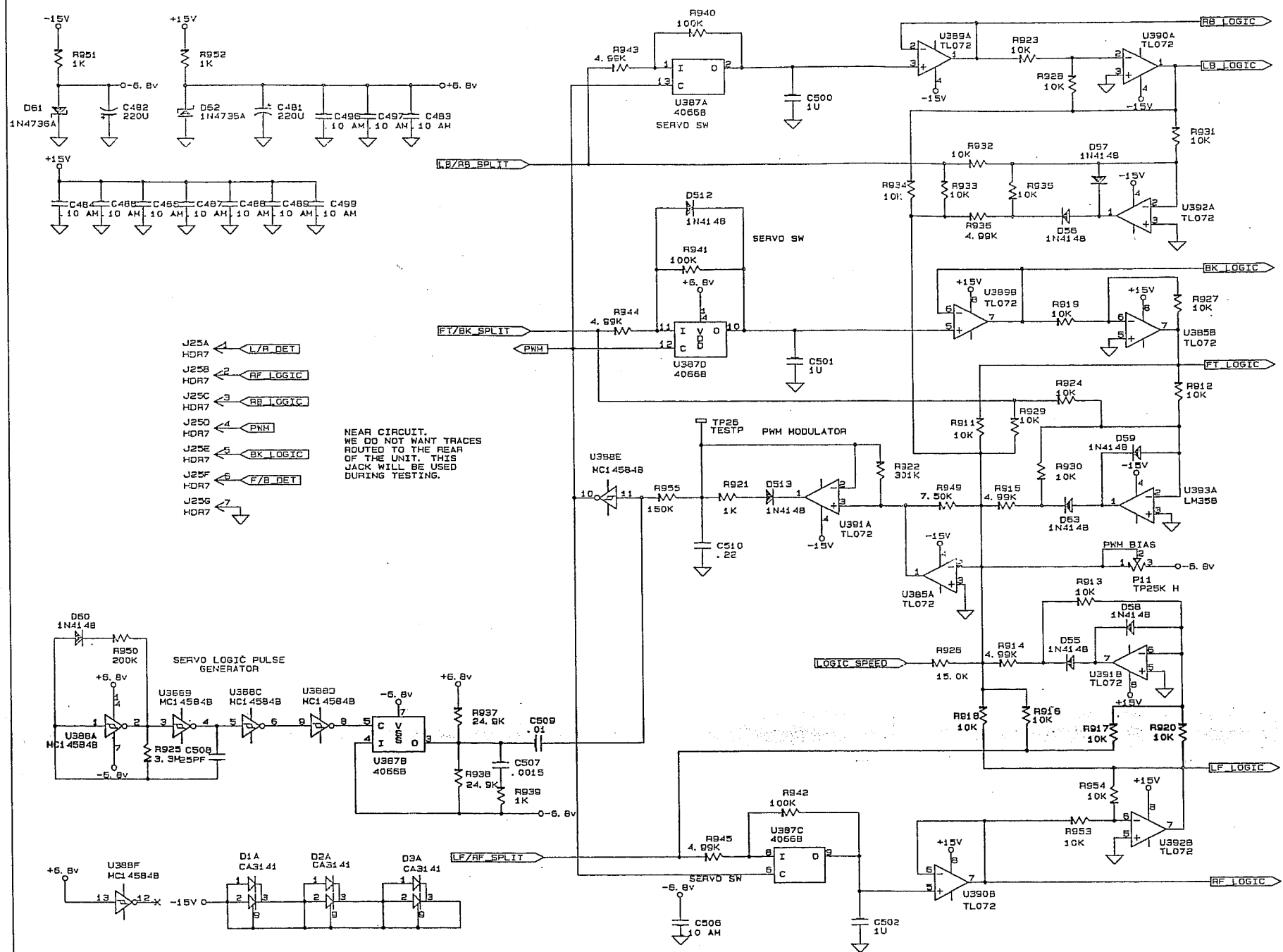
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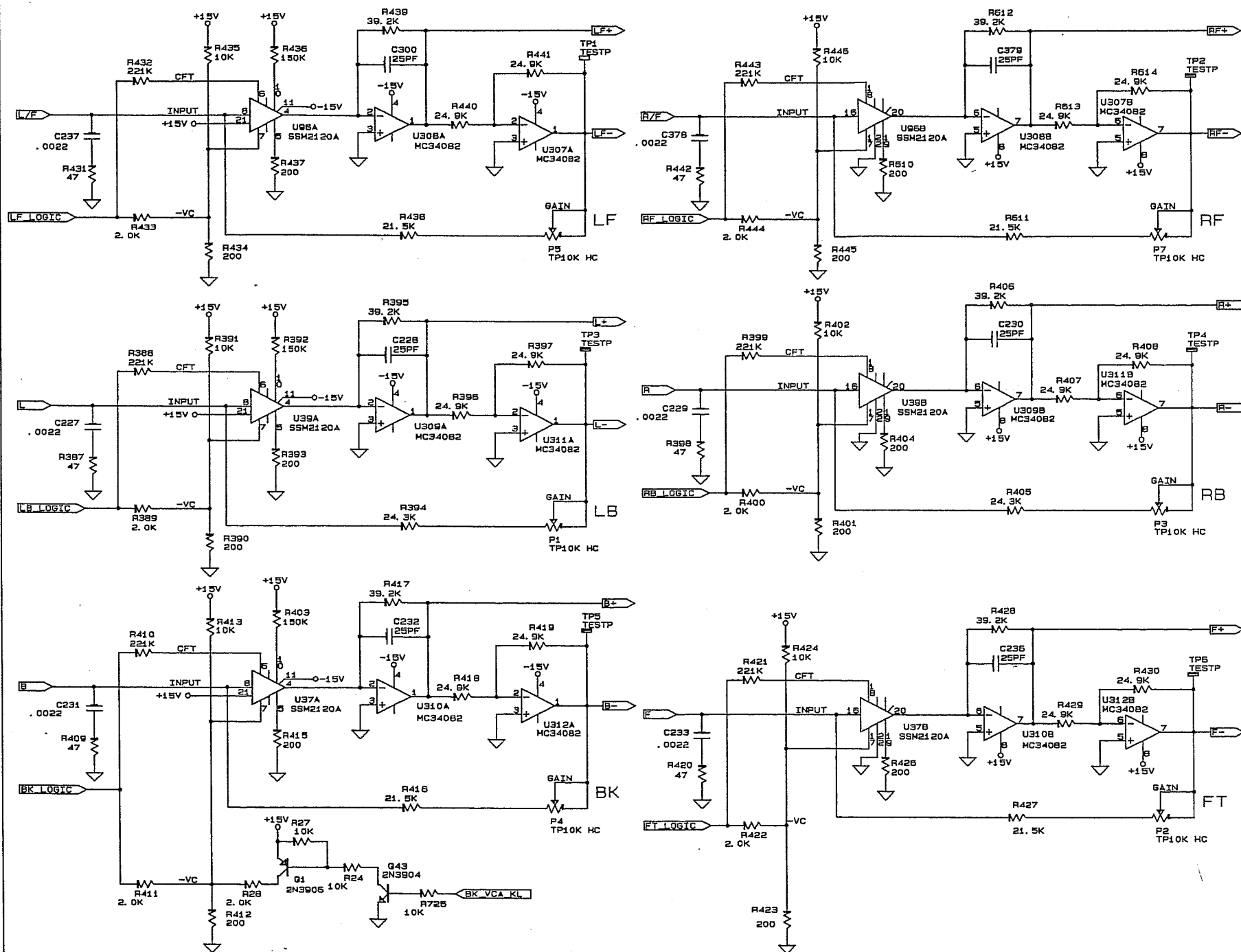
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Date: April 26, 1955 Sheet 10 of 11			



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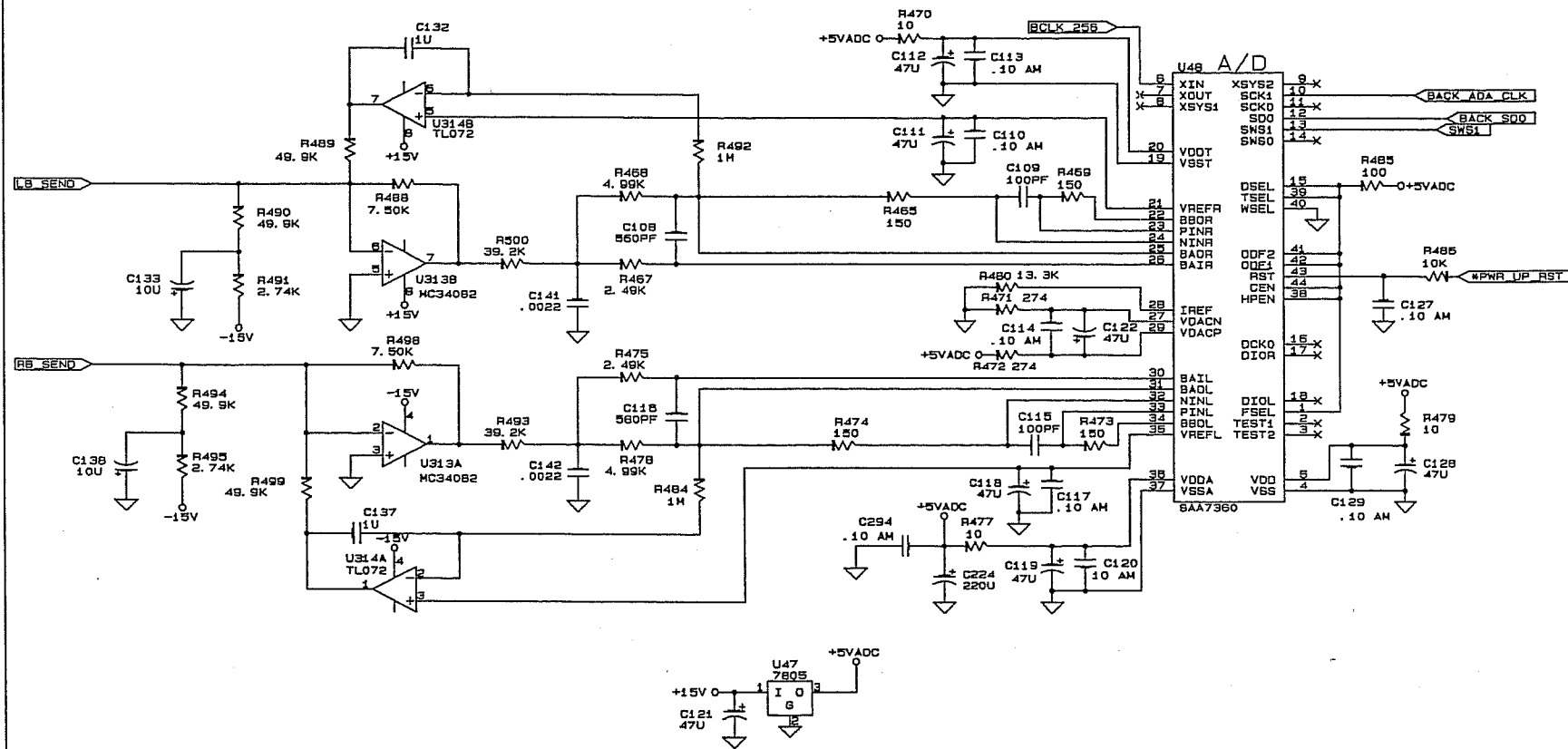


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ALIAS FILTERS

ANALOG TO DIGITAL CONVERTER



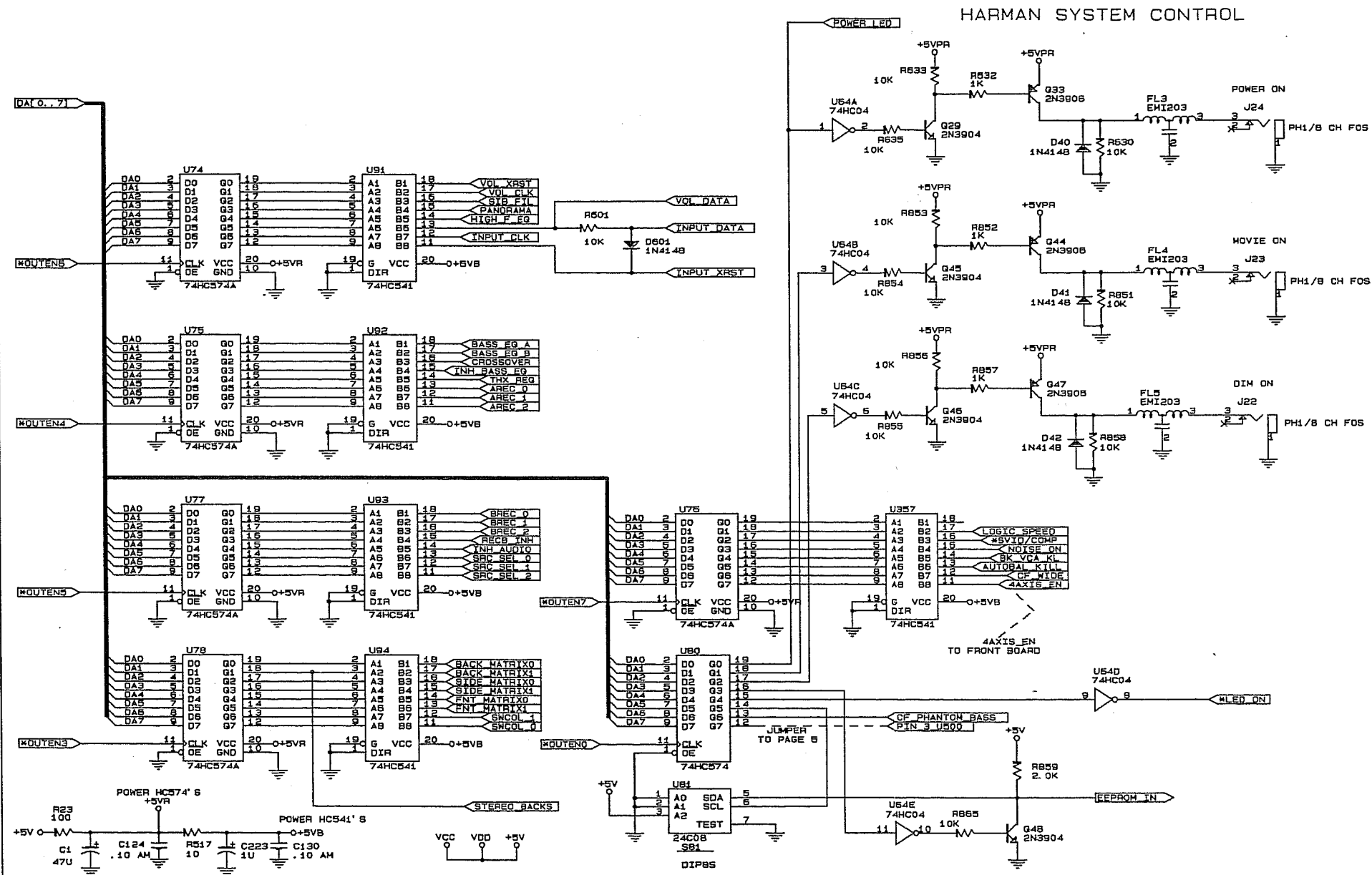
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Page:	3 of 11

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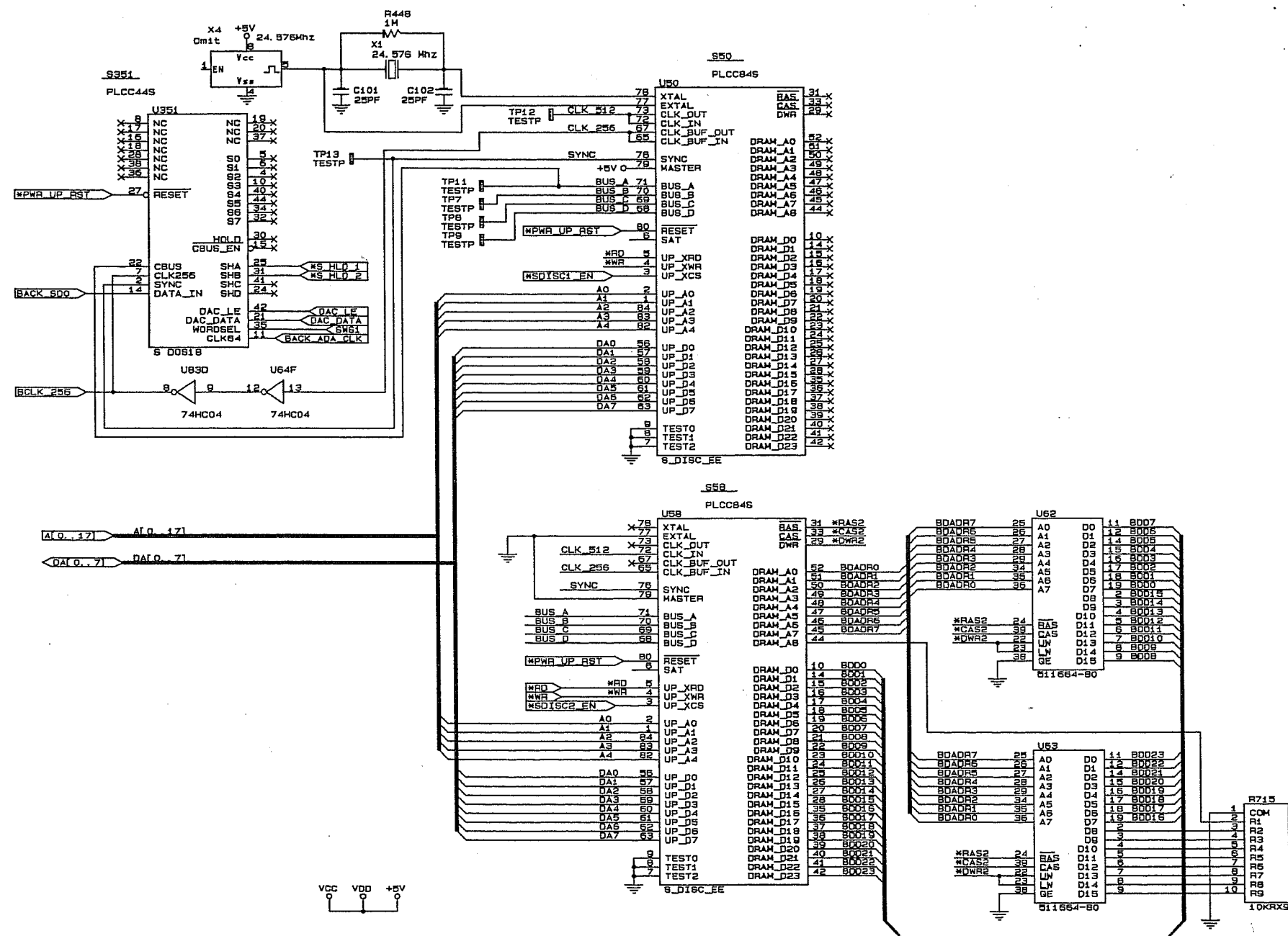
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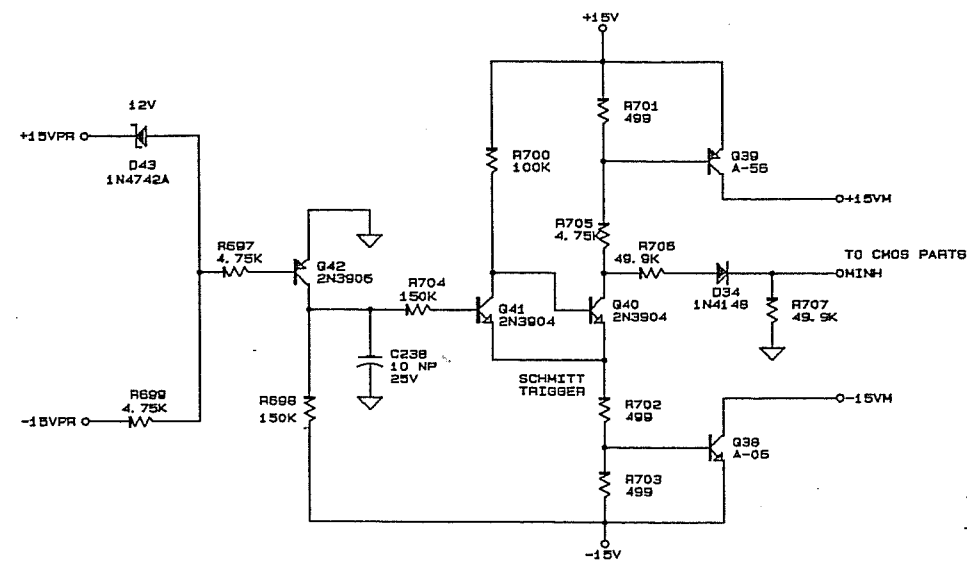


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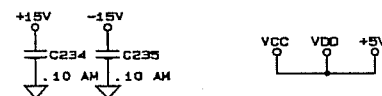
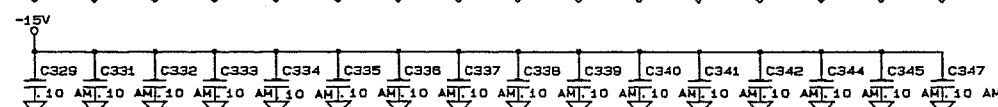
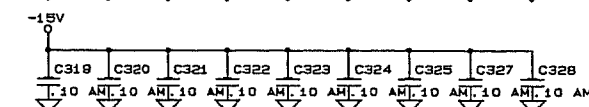
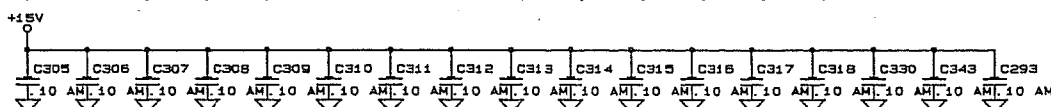
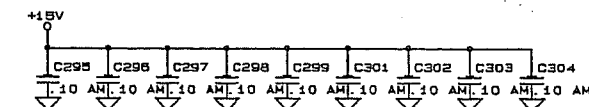
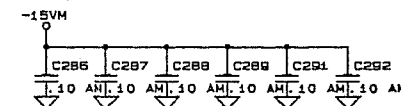
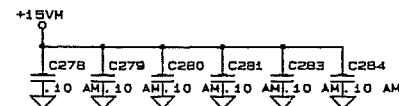
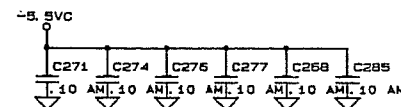
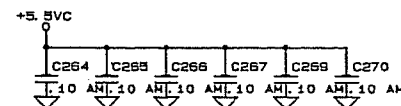
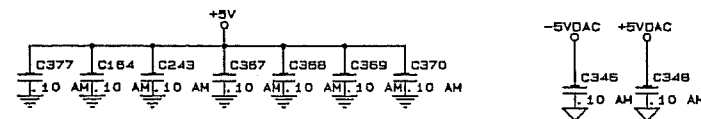
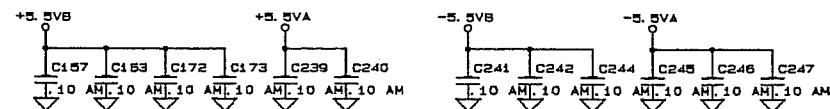
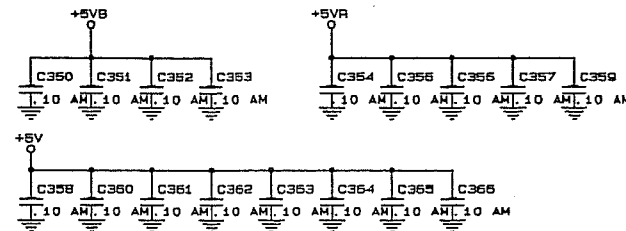
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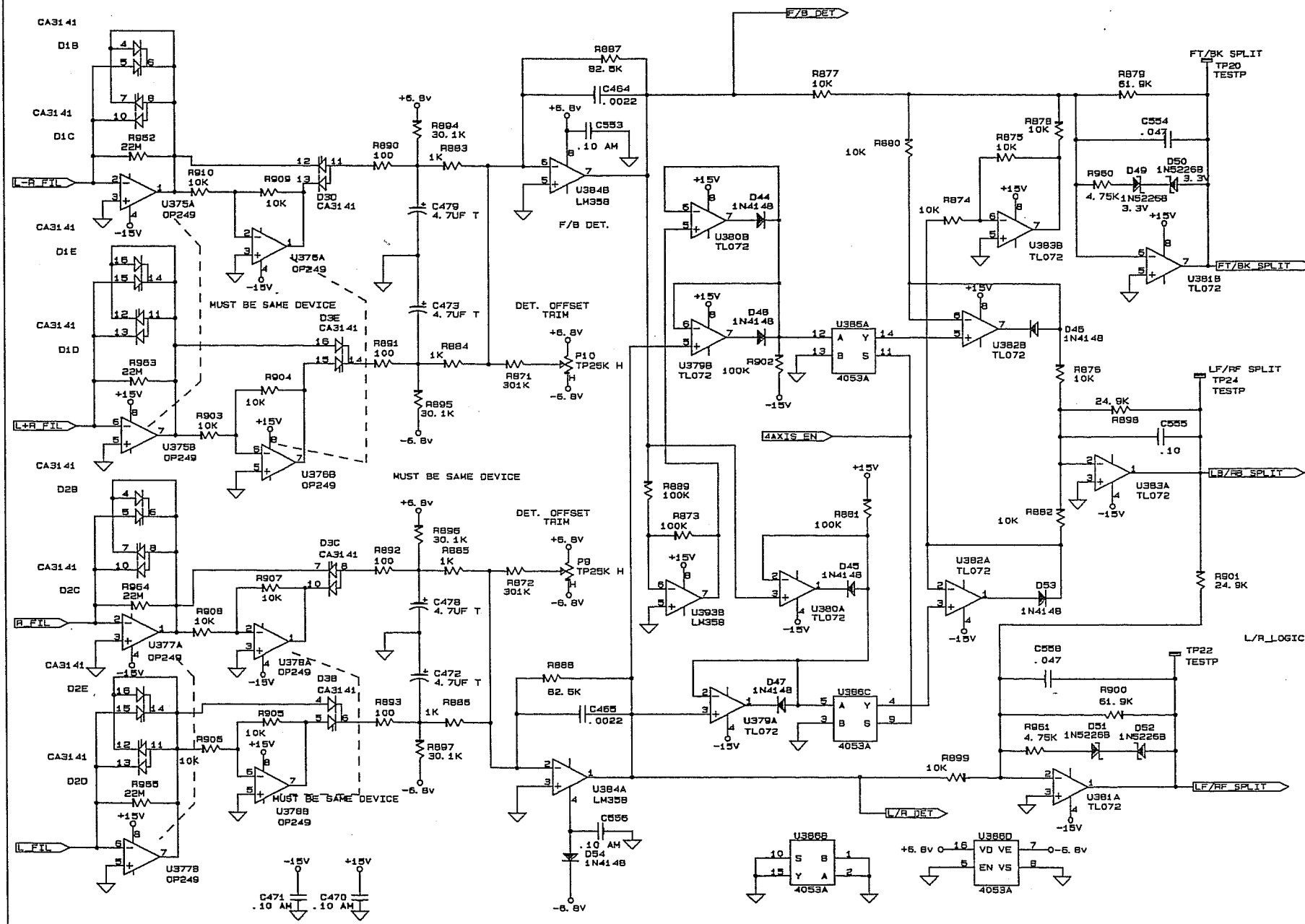
MUTING CIRCUIT



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Date: September 5, 1995 Sheet 8 of 11		

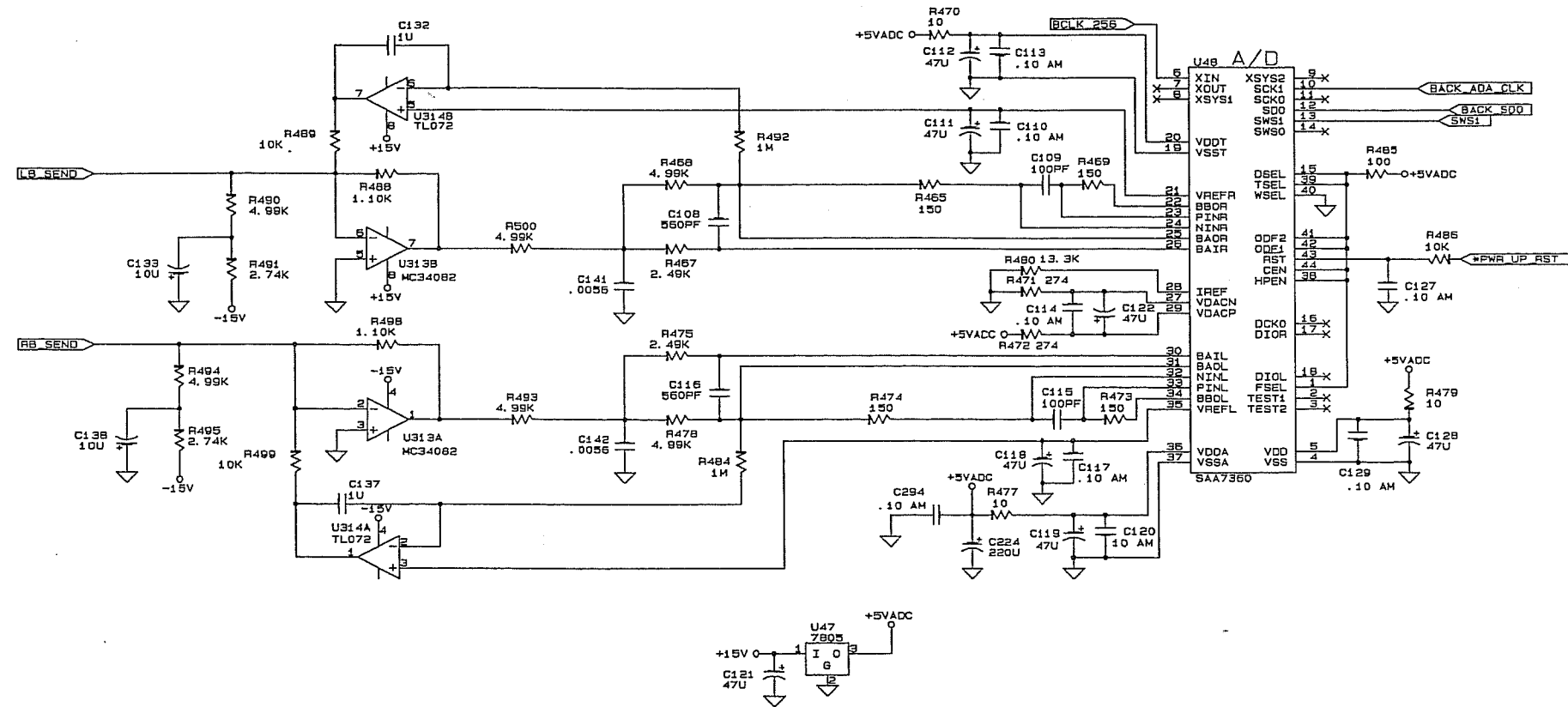




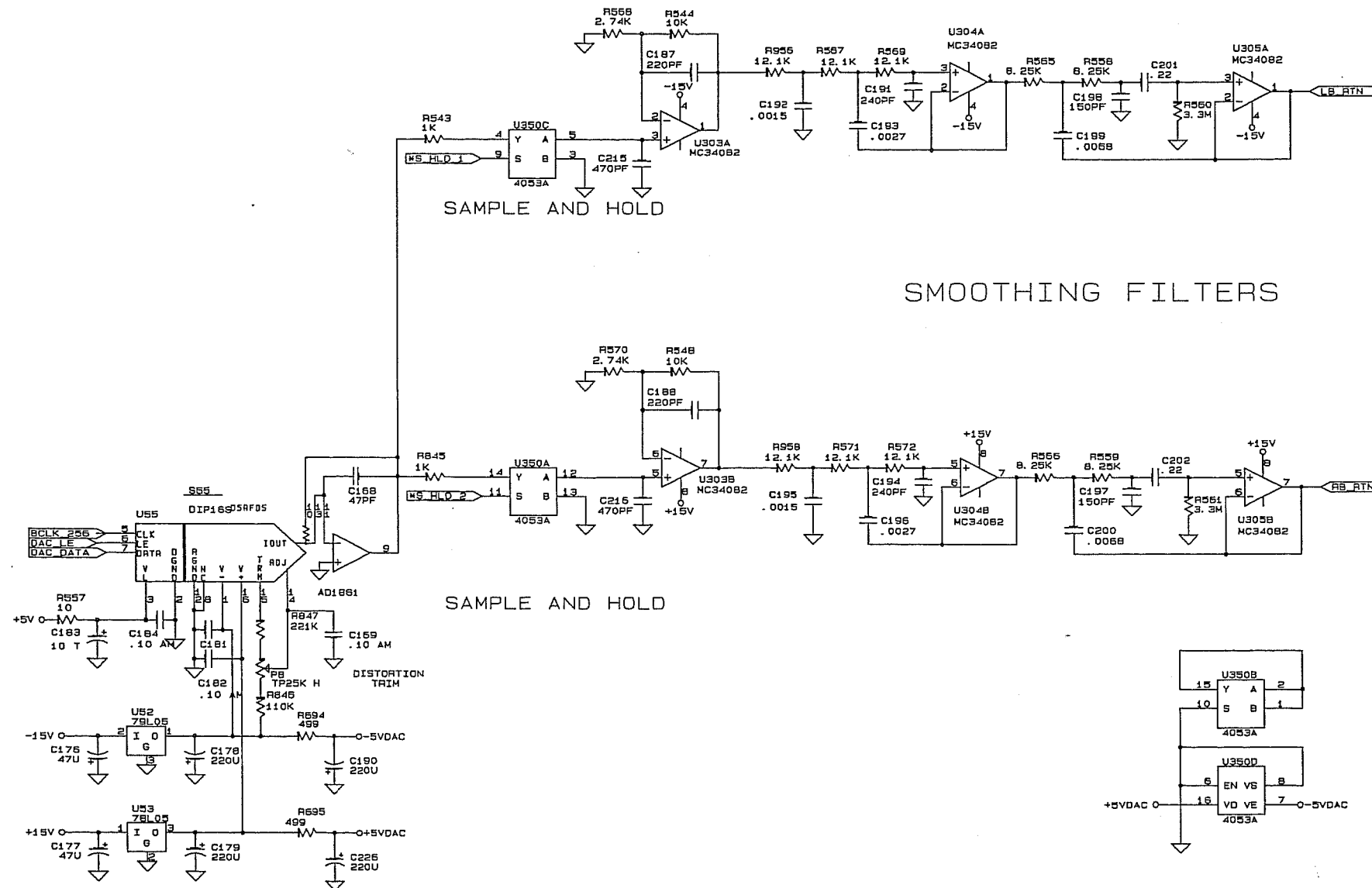
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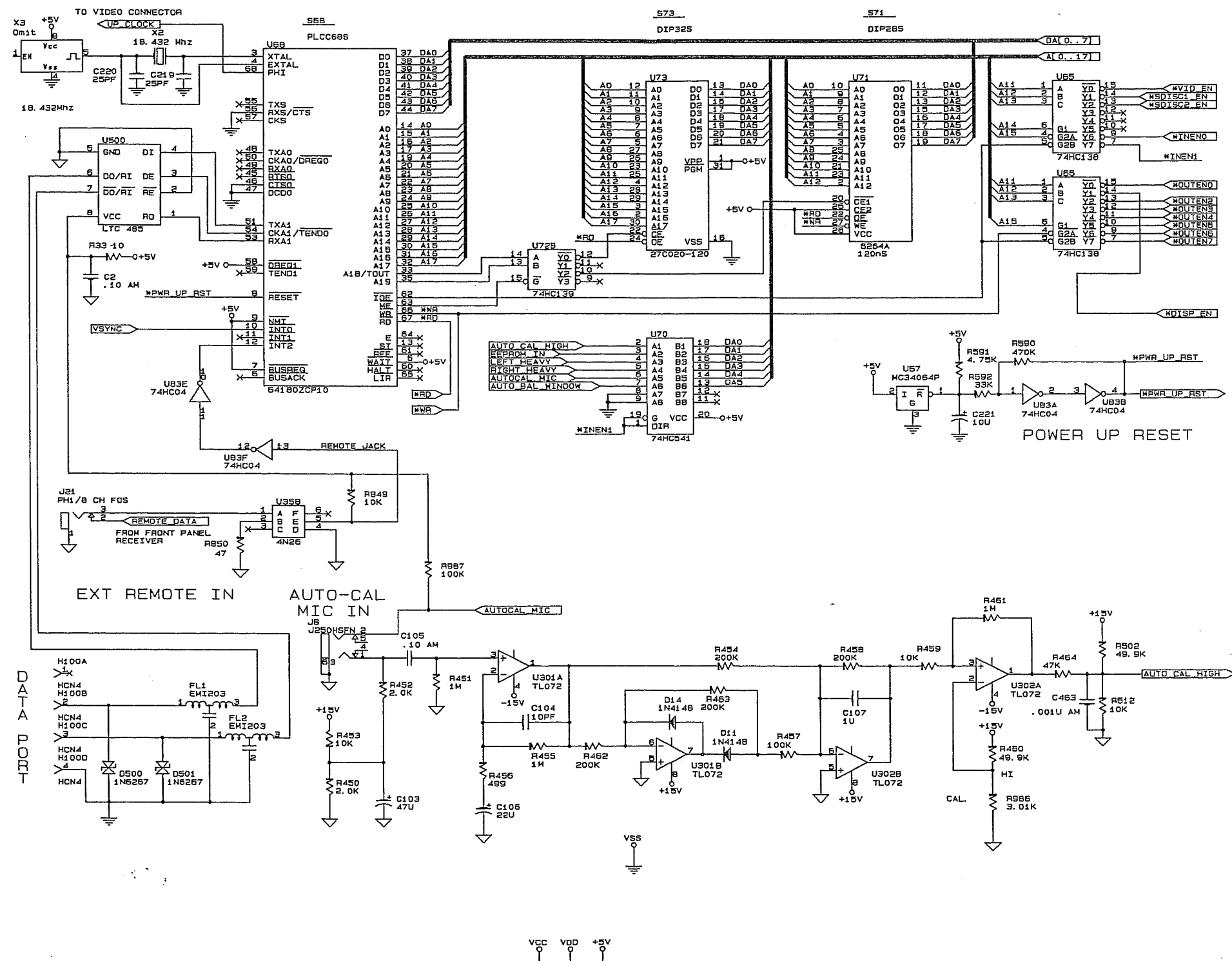
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Title			
CITATION 7.0 CVG DET/SPLITTER 400-02006-03			
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Date: September 5, 1995 Sheet			10 of 11

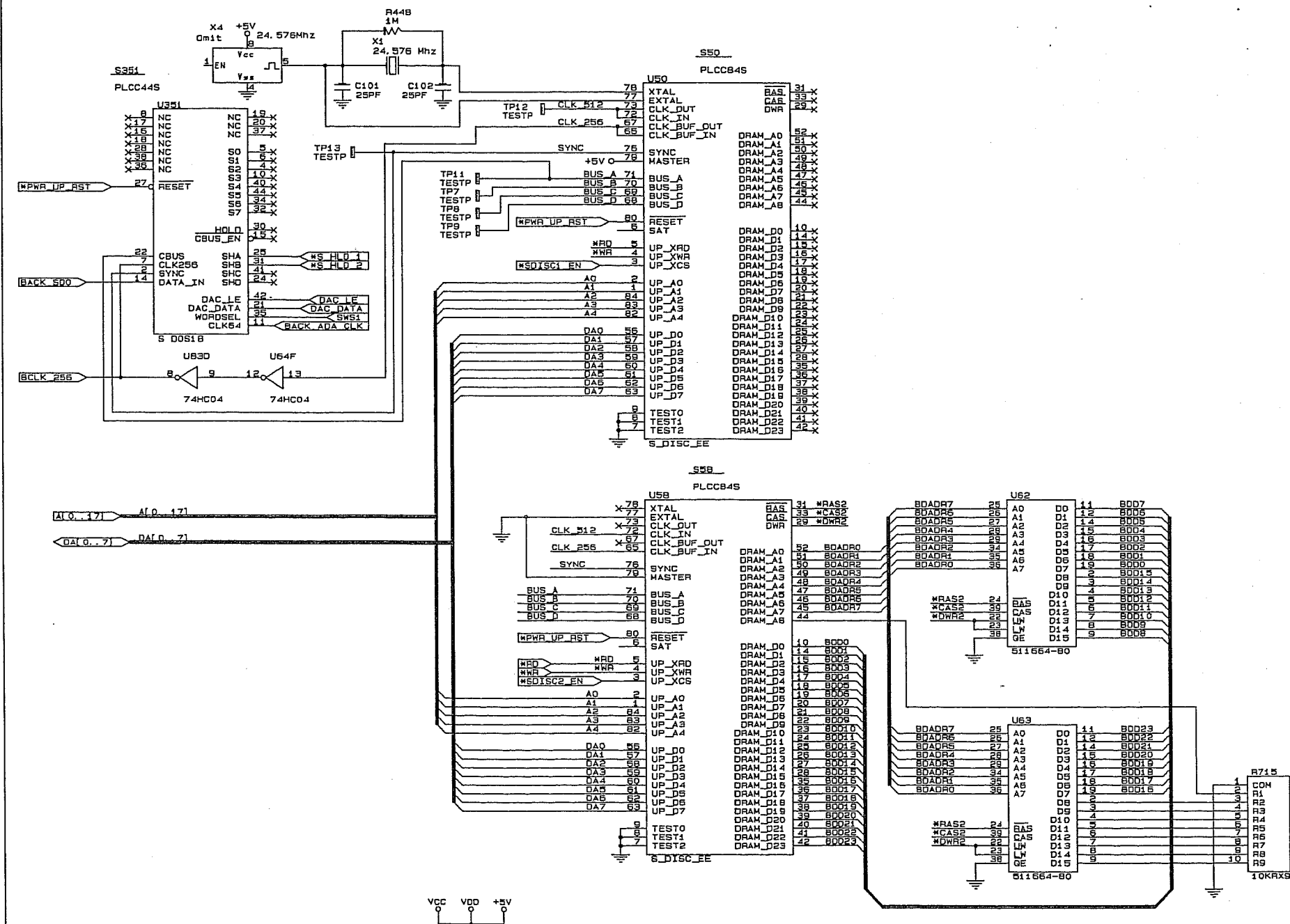
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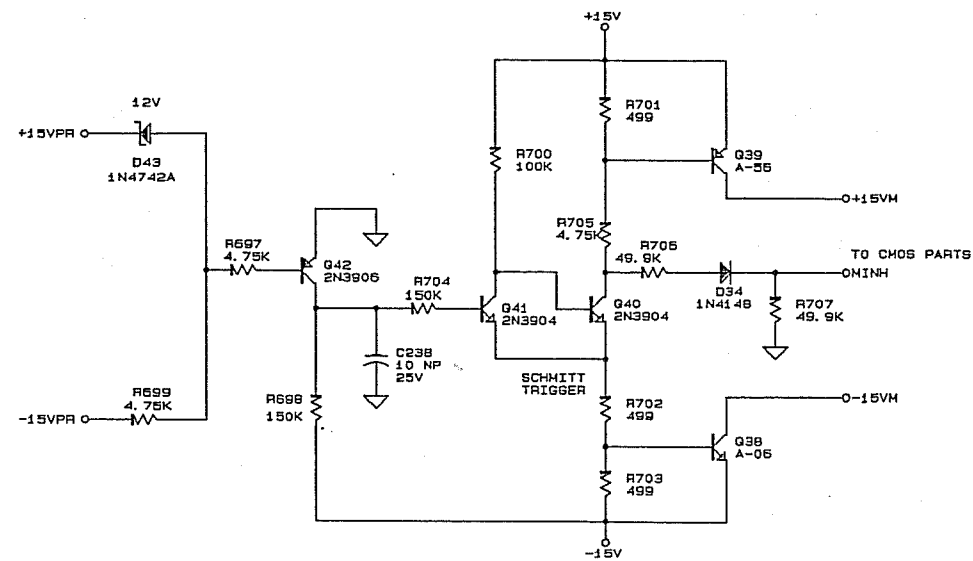


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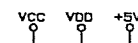
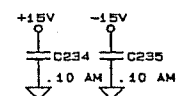
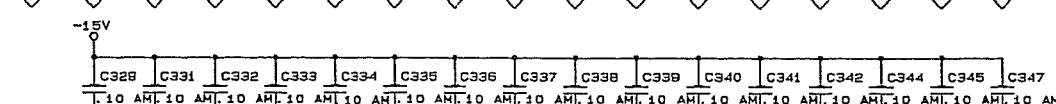
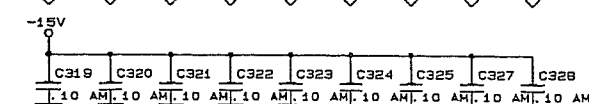
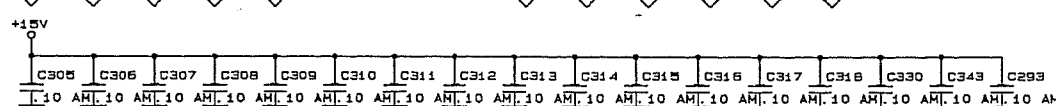
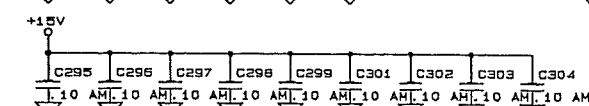
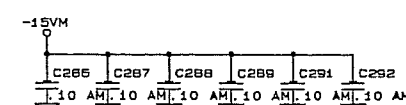
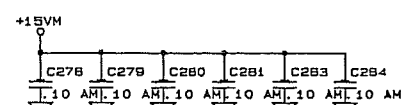
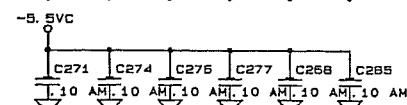
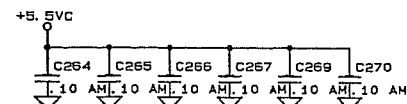
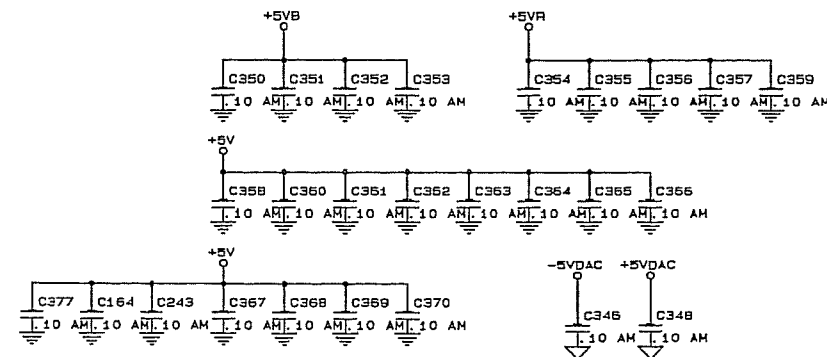
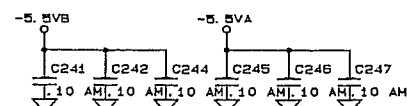
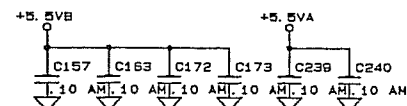


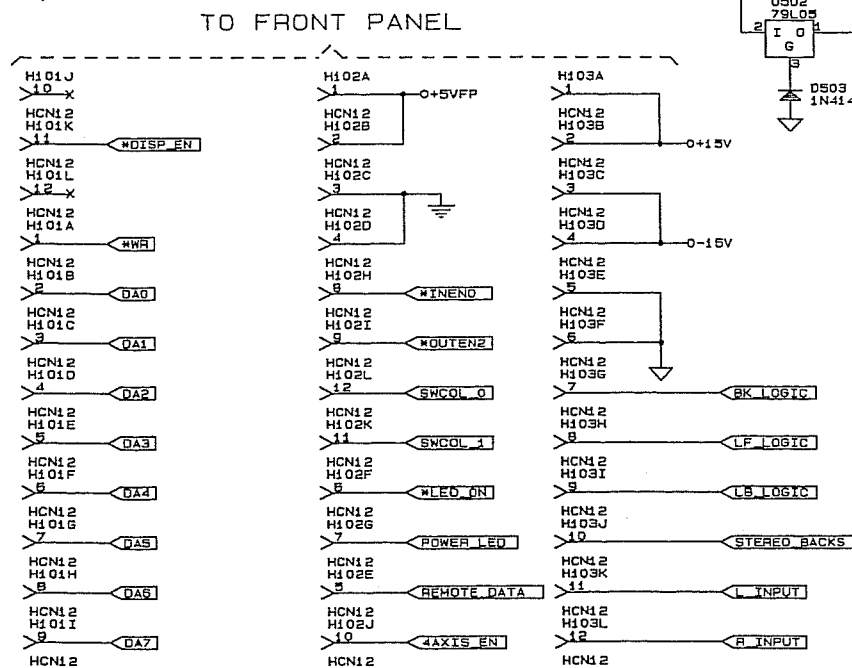
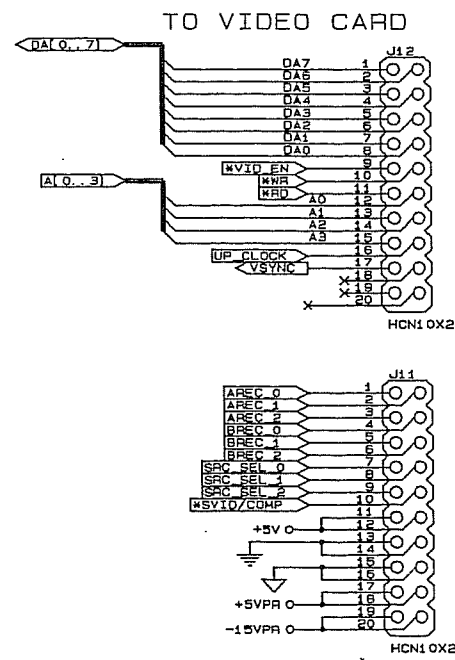
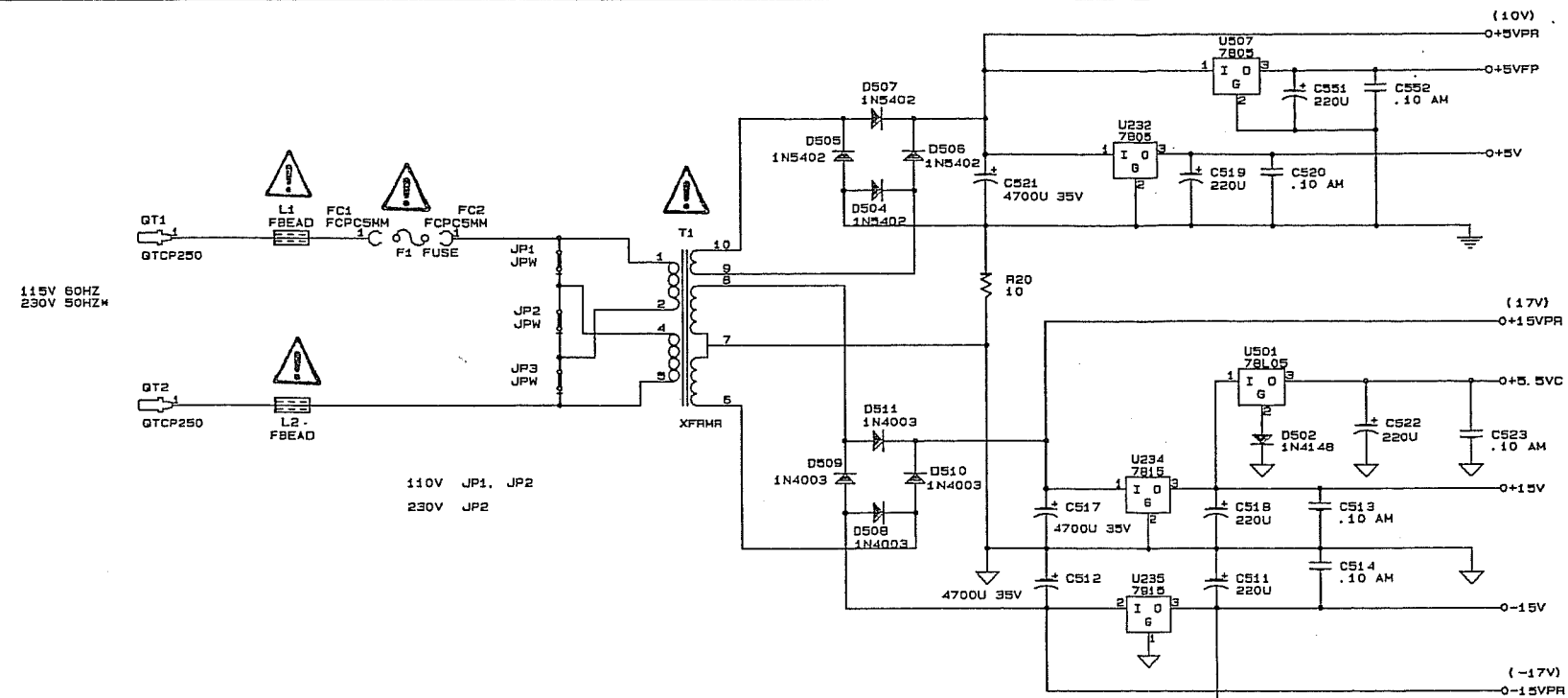


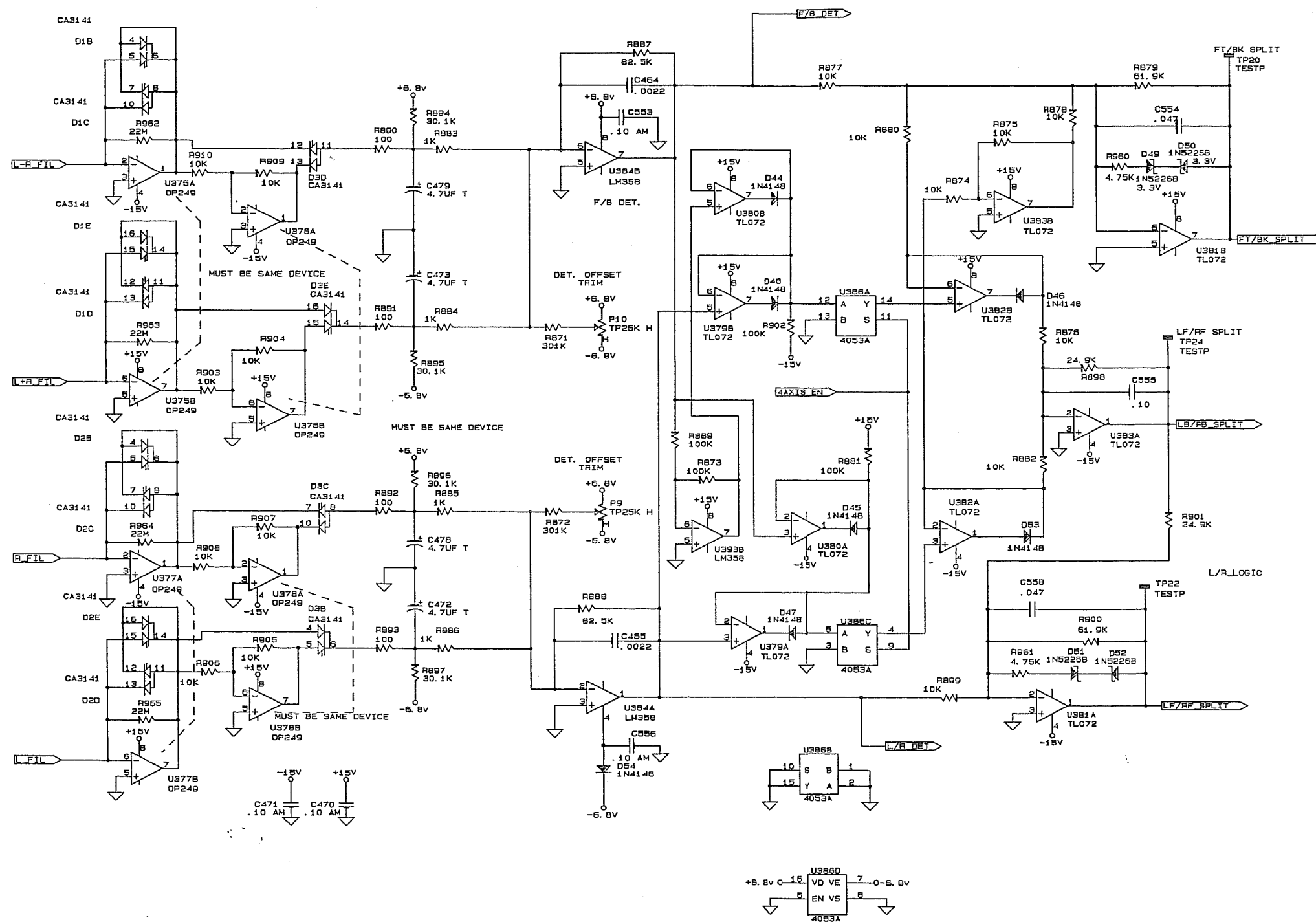




MUTING CIRCUIT



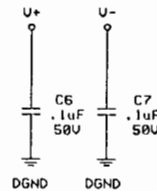
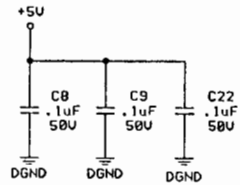
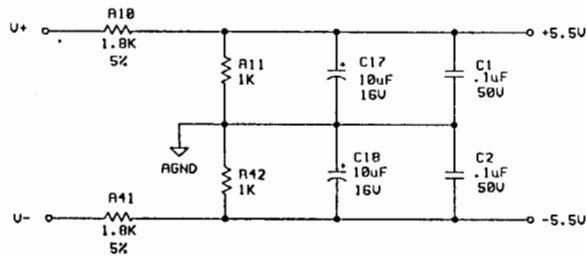




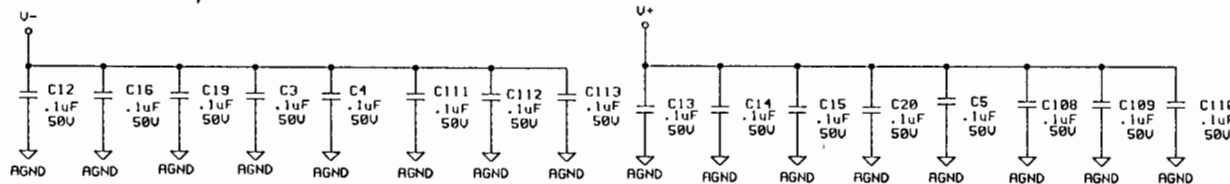
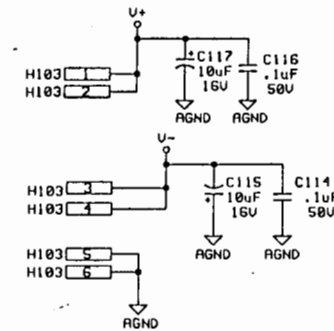
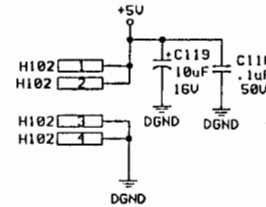
IC CHART								
TYPE	VOLTAGE/PIN NO.							REFERENCE DESIGNATOR
	U-	U+	+5V	AGND	DGND	+5.5V	-5.5V	
1053				8		16	7	U5
LM358	4	8						U6, U7, U8, U11, U12
74HC244			20		10			U2
74HC574			20		10			U3
LF353	4	8						U13, U14, U15
LM339	12	3						U9, U10

NOTES:

1. FOR CAPACITOR TYPE SEE PARTS LIST.
2. ALL RESISTORS ARE 1/8W, 5% CARBON FILM, UNLESS OTHERWISE SPECIFIED.



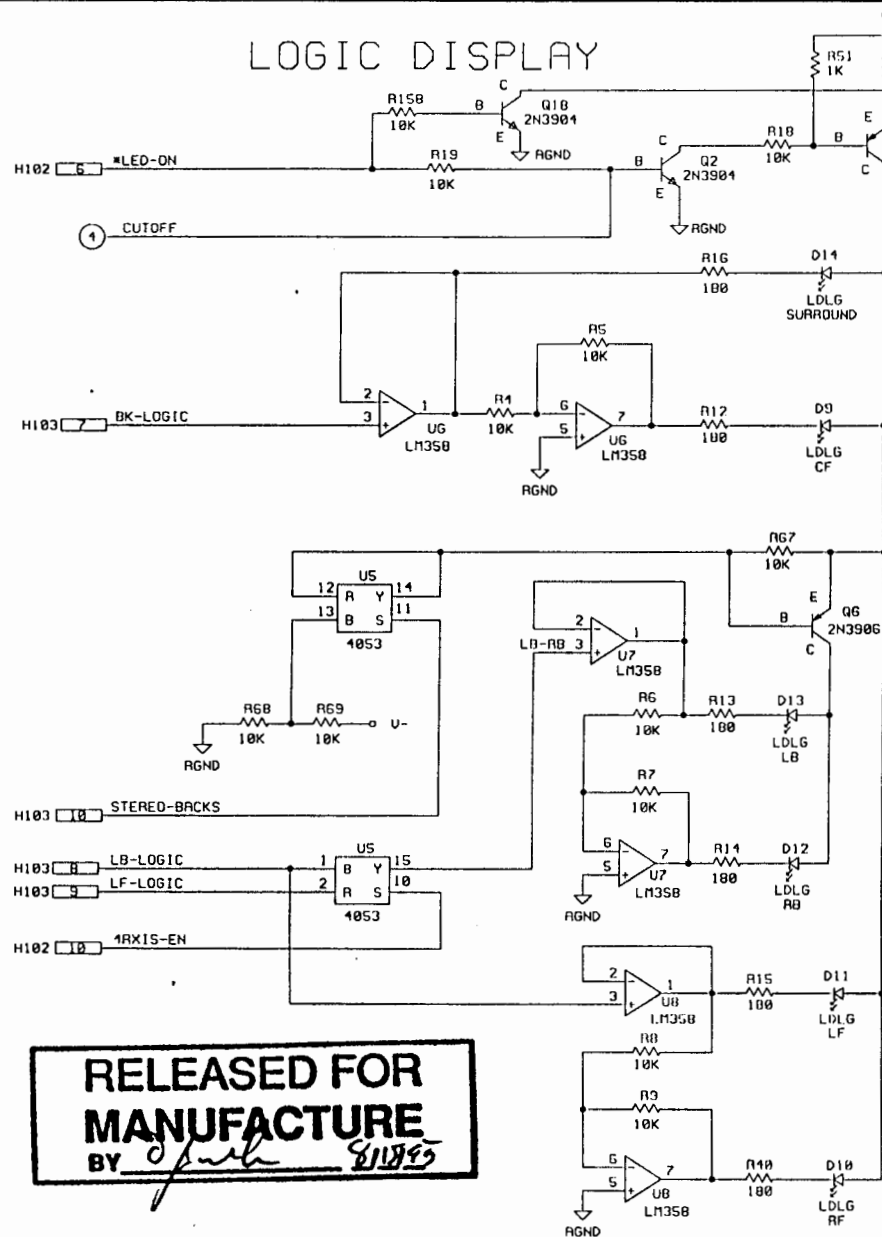
H101 10x
H101 12x



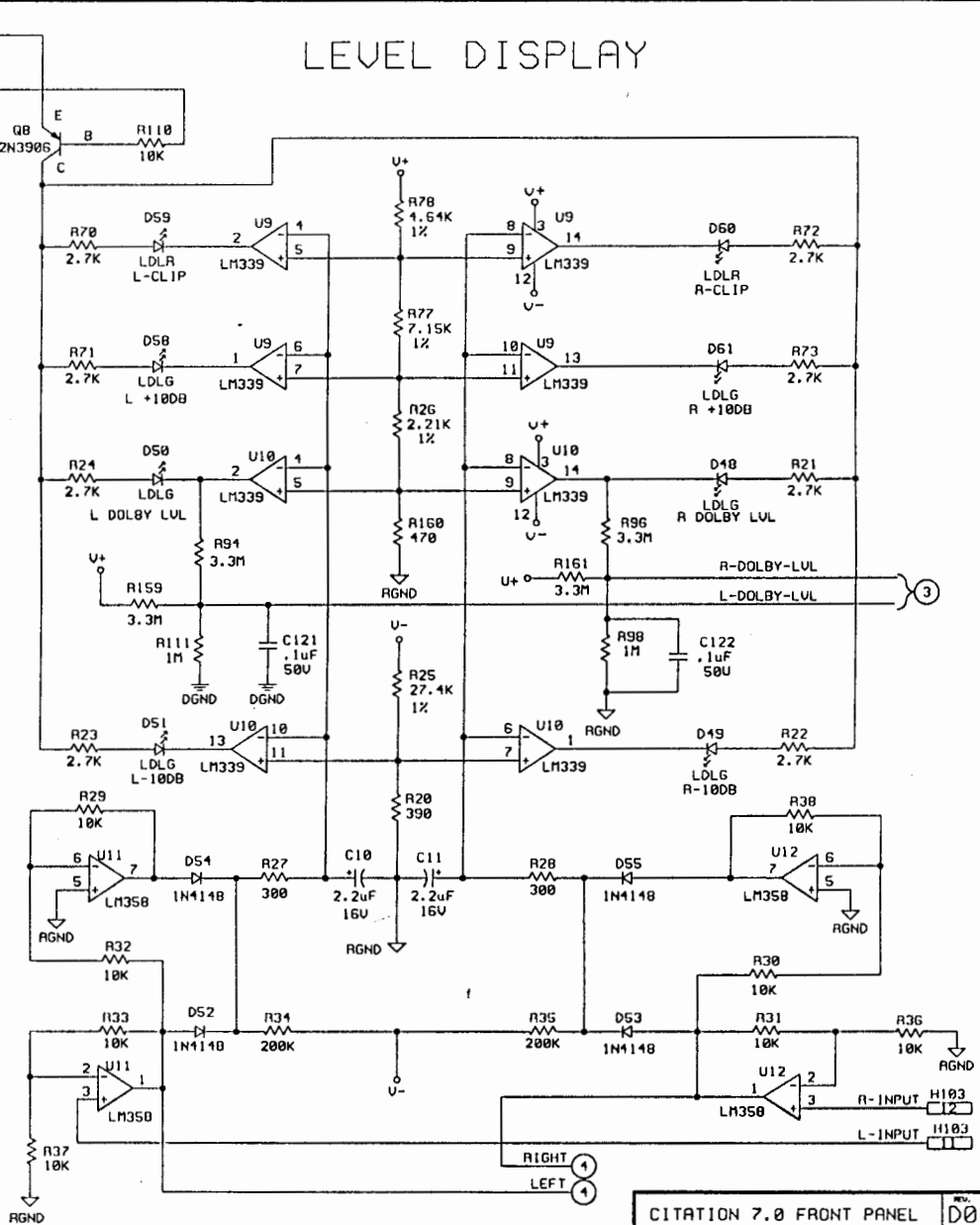
RELEASED FOR
MANUFACTURE

SIGNATURE		DATE	AUDIOACCESS 26046 EDEN LANDING ROAD HAYWARD, CA. 94545 CITATION 7.0 FRONT PANEL
DRAWN	MILLE	11-2-94	
DAFTG CHK			
DESIGN CHK			
C70FPA0.SCH			DWG. NO.
			REV. D0
			SHEET 1 OF 4

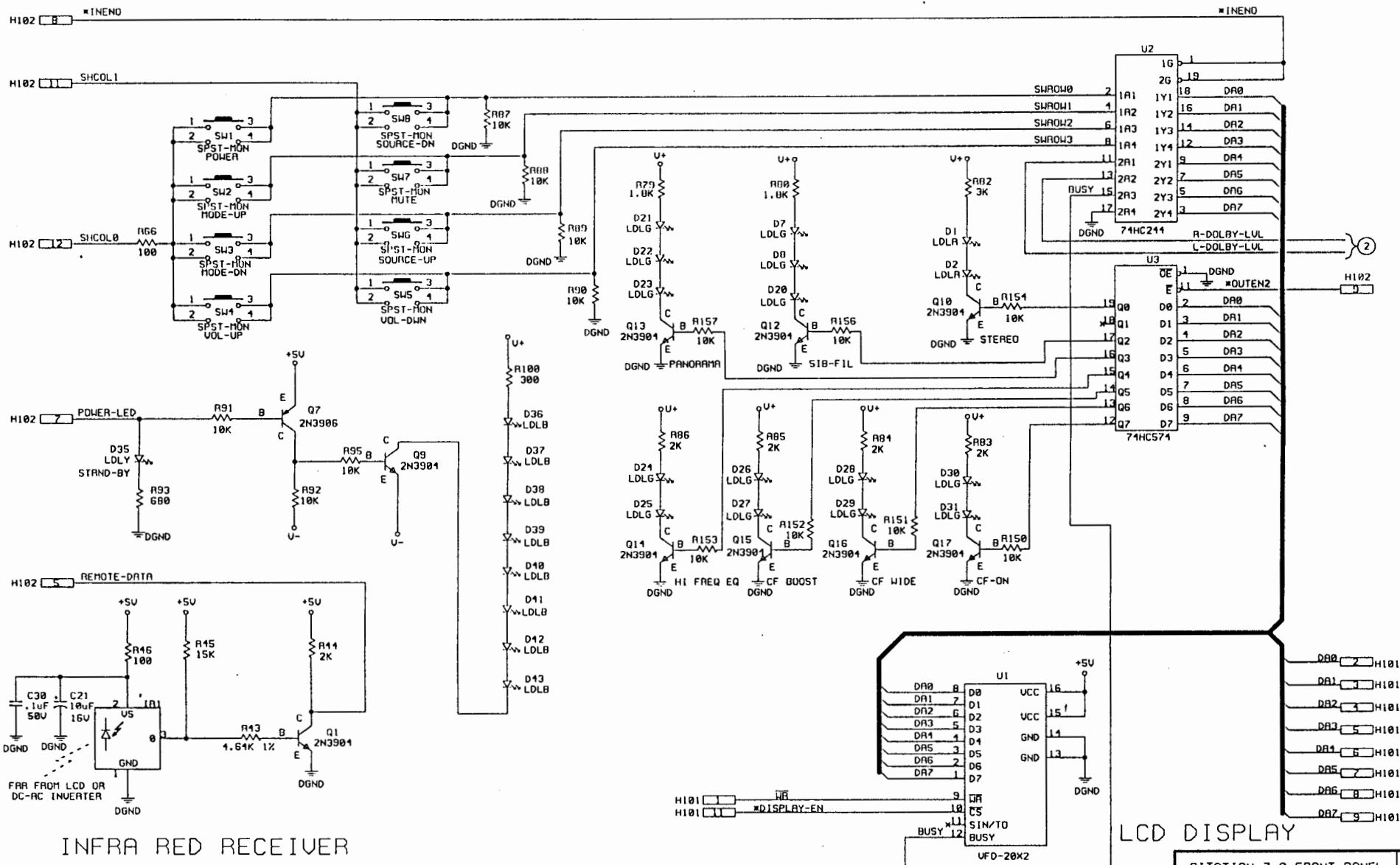
LOGIC DISPLAY



LEVEL DISPLAY



RELEASED FOR
MANUFACTURE
BY *of* *8/18/92*





BY Deane 8/1/99