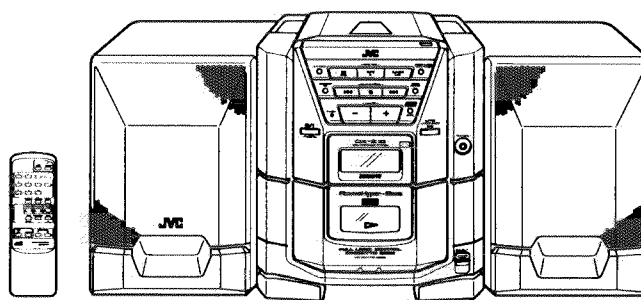


JVC

SERVICE MANUAL

CD PORTABLE COMPONENT SYSTEM

DX-E10BK B/E/EN/G



COMPACT
disc
DIGITAL AUDIO

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■ Description of Error Display/Indication

In addition to the normal functions, the service test modes have been incorporated in this model for confirmation and adjustment as described below:

1. Setting of the Service Test Mode

◆ Setting Method

After the power supply has been turned on, it is possible to set this model to the service test mode from the normal function conditions:

When the three **POWER**, **PLAY MODE** and **STOP** buttons on the are pressed at the same time, this model will be set to the test mode. When this model has been set to the test mode, all of the LCDs will be turned on.

◆ Release of setting

When this model has been set to the test mode, press the three **POWER**, **PLAY MODE** and **STOP** buttons at the same time, set this model to the backup mode or reset this model. Then, the test mode will be released.

2. Self-Diagnosis Function of CD Servo

When any error has occurred, either of the following error codes will be displayed depending on the contents of the error:

- 1) Err 1: Failure to turn on the reset switch (Internal peripheral feeding time-out)
- 2) Err 2: Failure to turn off the reset switch (Outer peripheral feeding time-out)
- 3) Err 3: Focusing failure
- 4) Err 4: TOC reading failure
- 5) Err 5: Failure to read the Q code continuously for 3 seconds
- 6) Err 6: Failure to locate the beginning of music when selecting a music
- 7) Err 7: Spindle servo failure

5 Wiring Connections

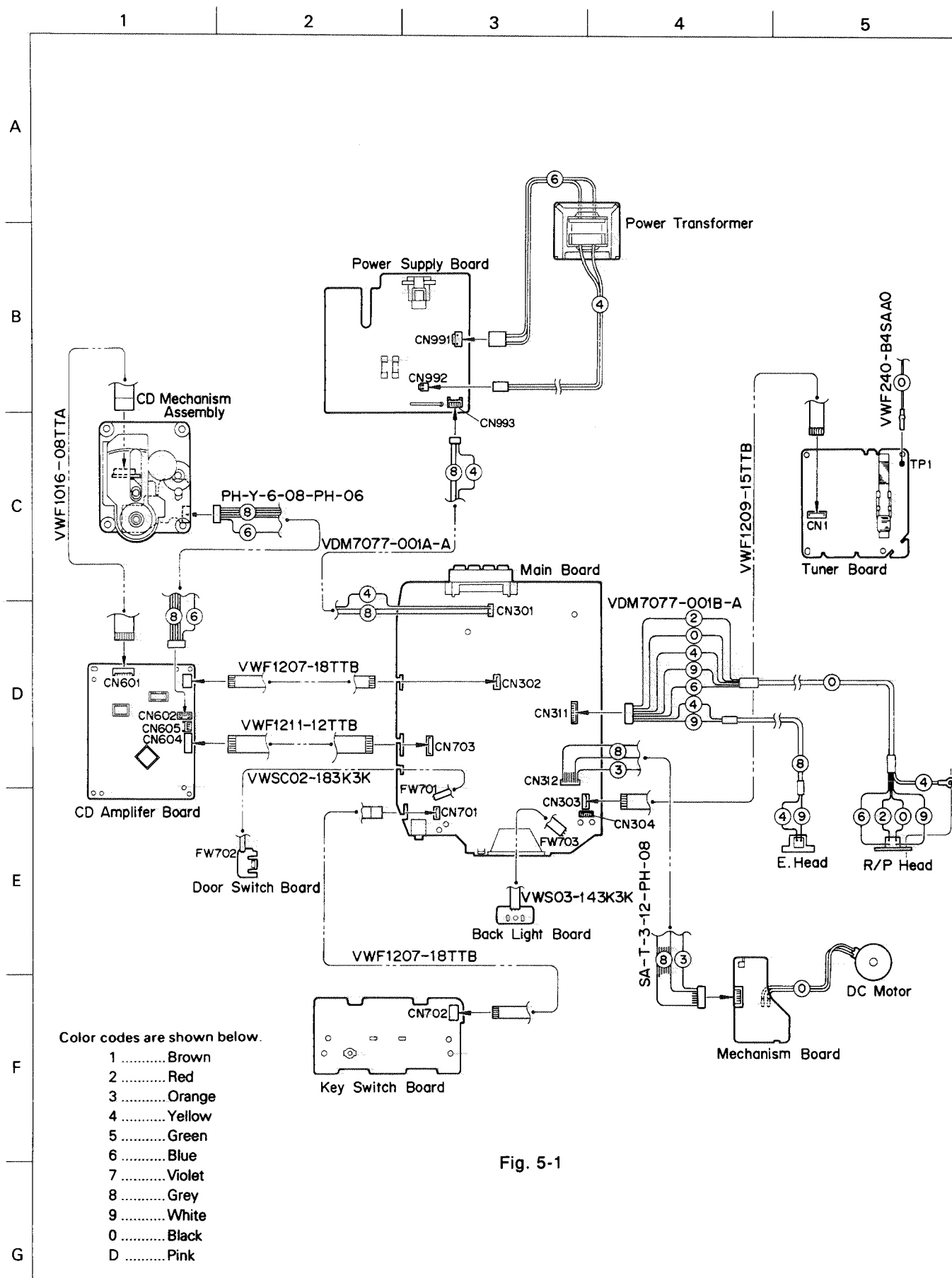
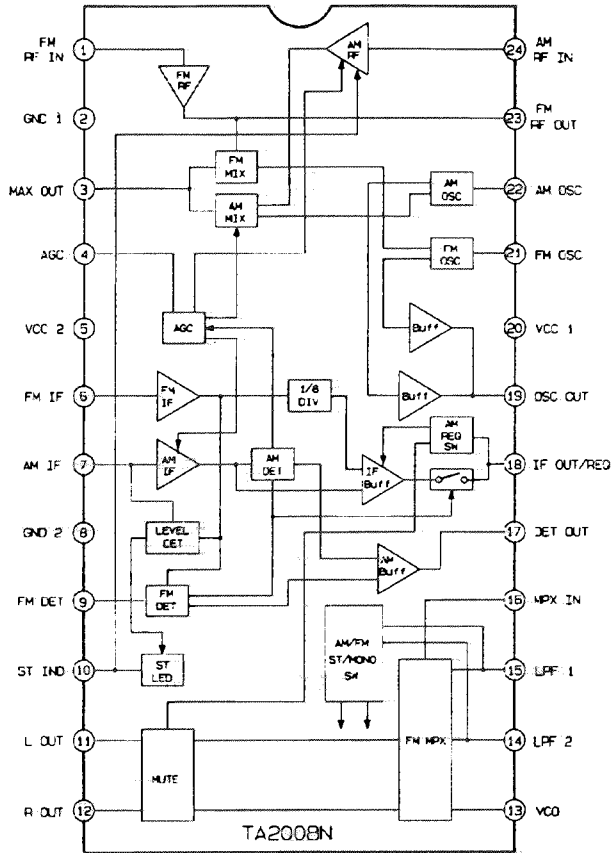
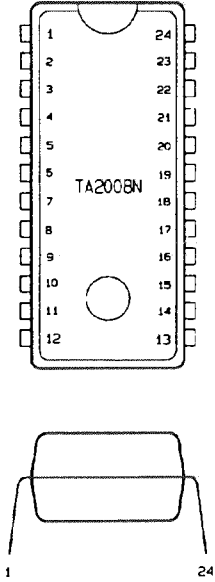


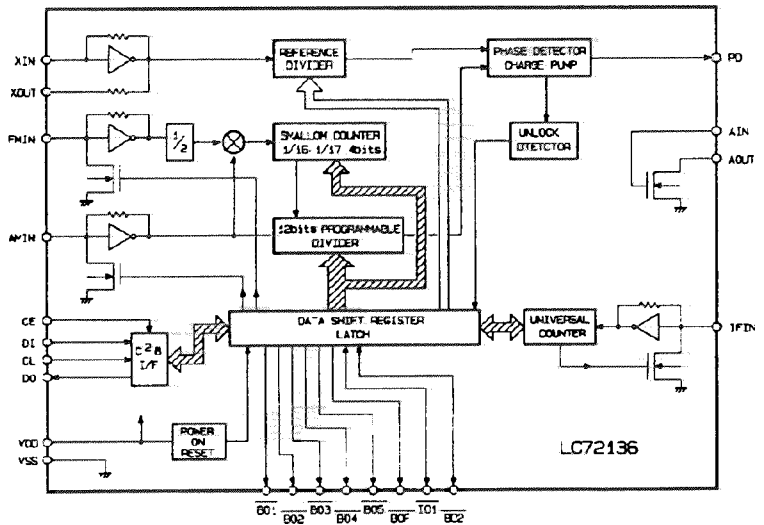
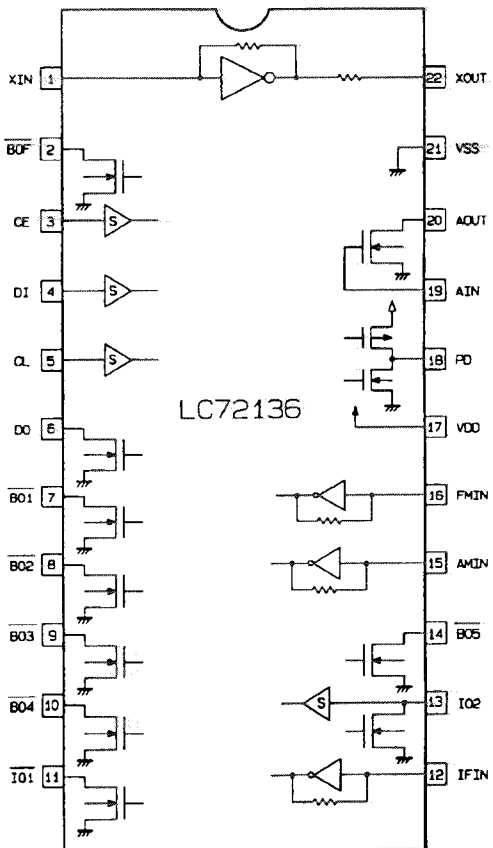
Fig. 5-1

6 Block diagram

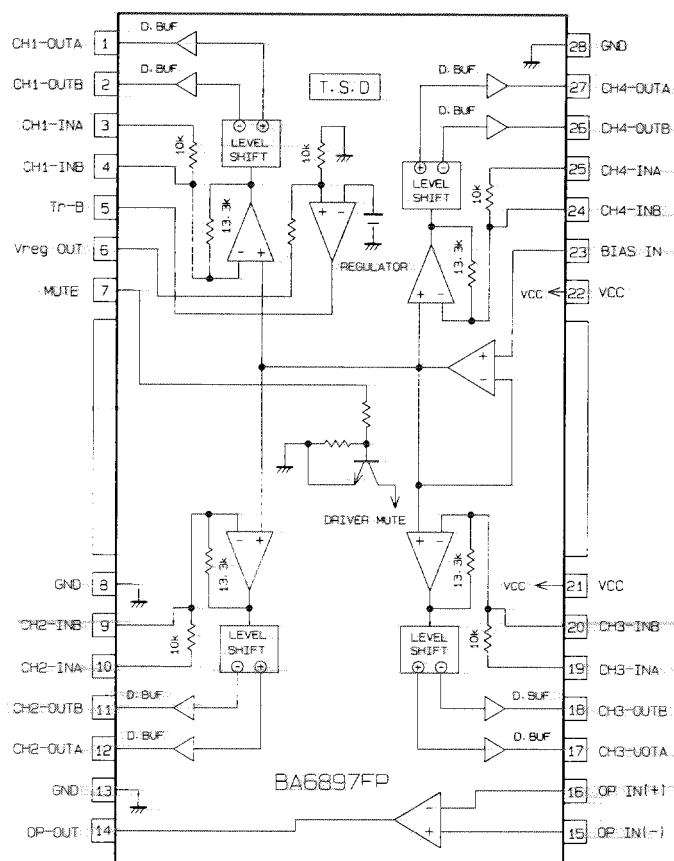
◆ IC2 (TA2008AN)



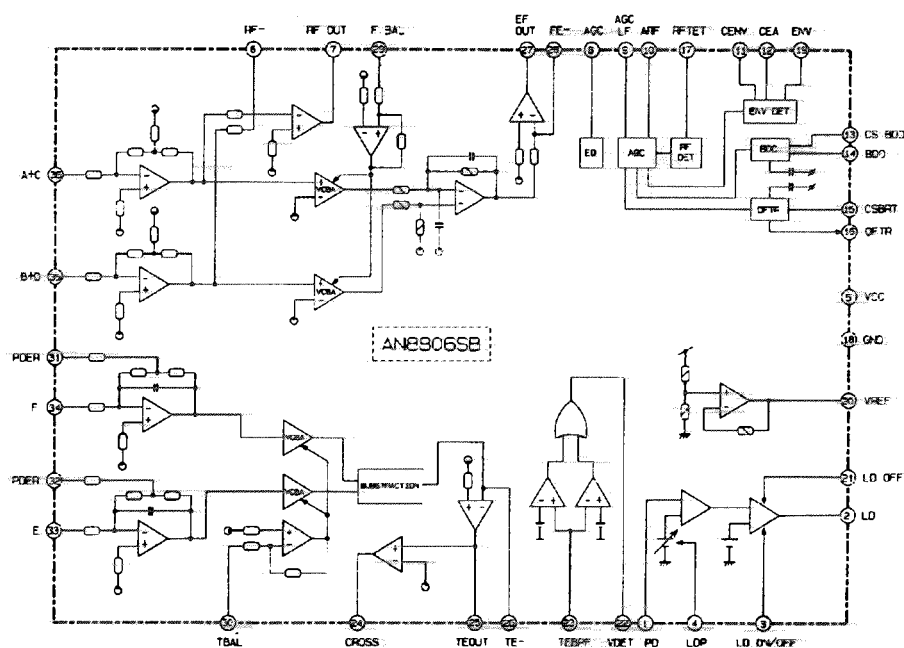
◆ IC3 (LC72136N) PAL



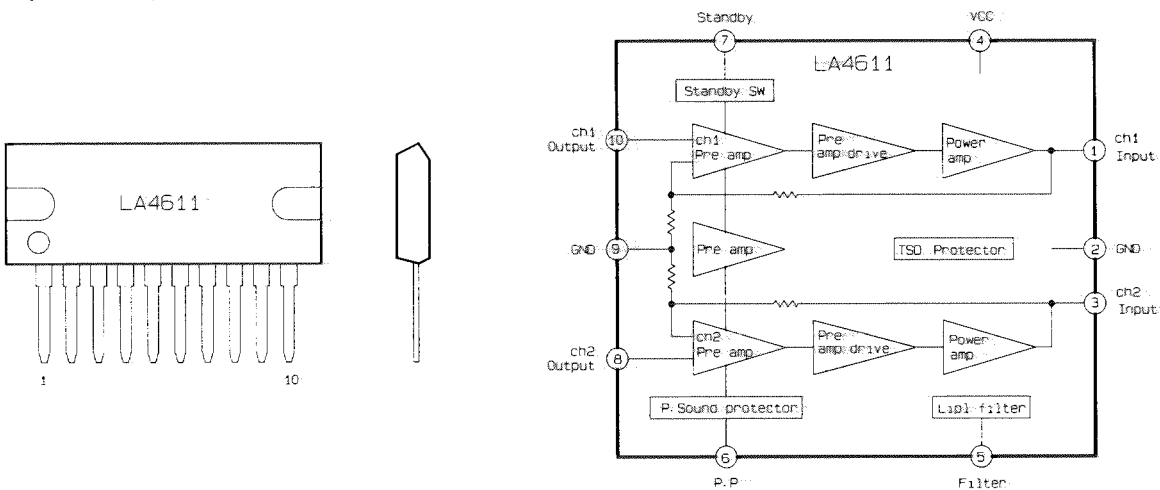
◆ IC301 (LA4611) POWER Amp.



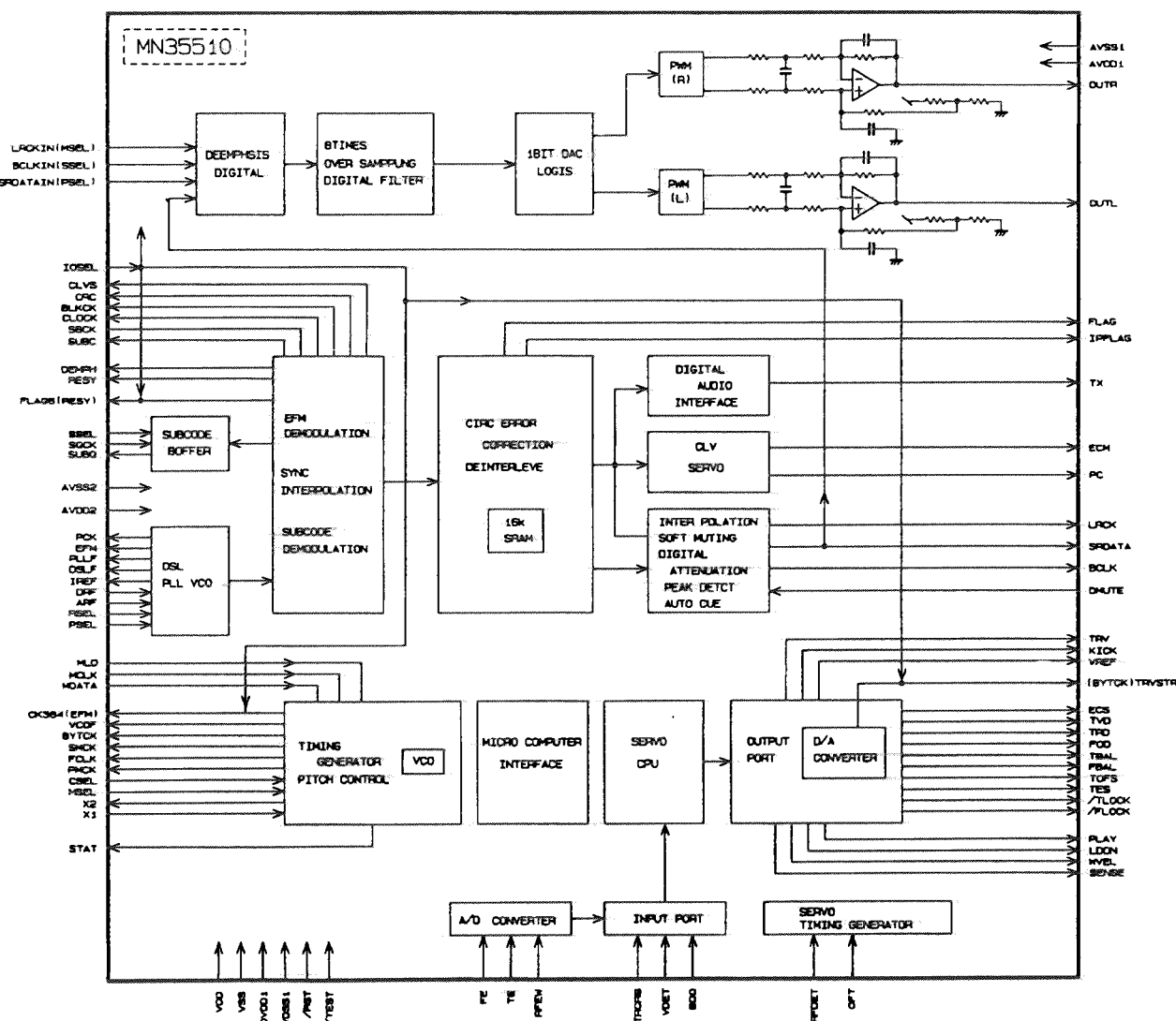
◆ IC601 (AN8906SB) RF & SERVO



◆ IC602 (BA6897FP) BTL DRIVER



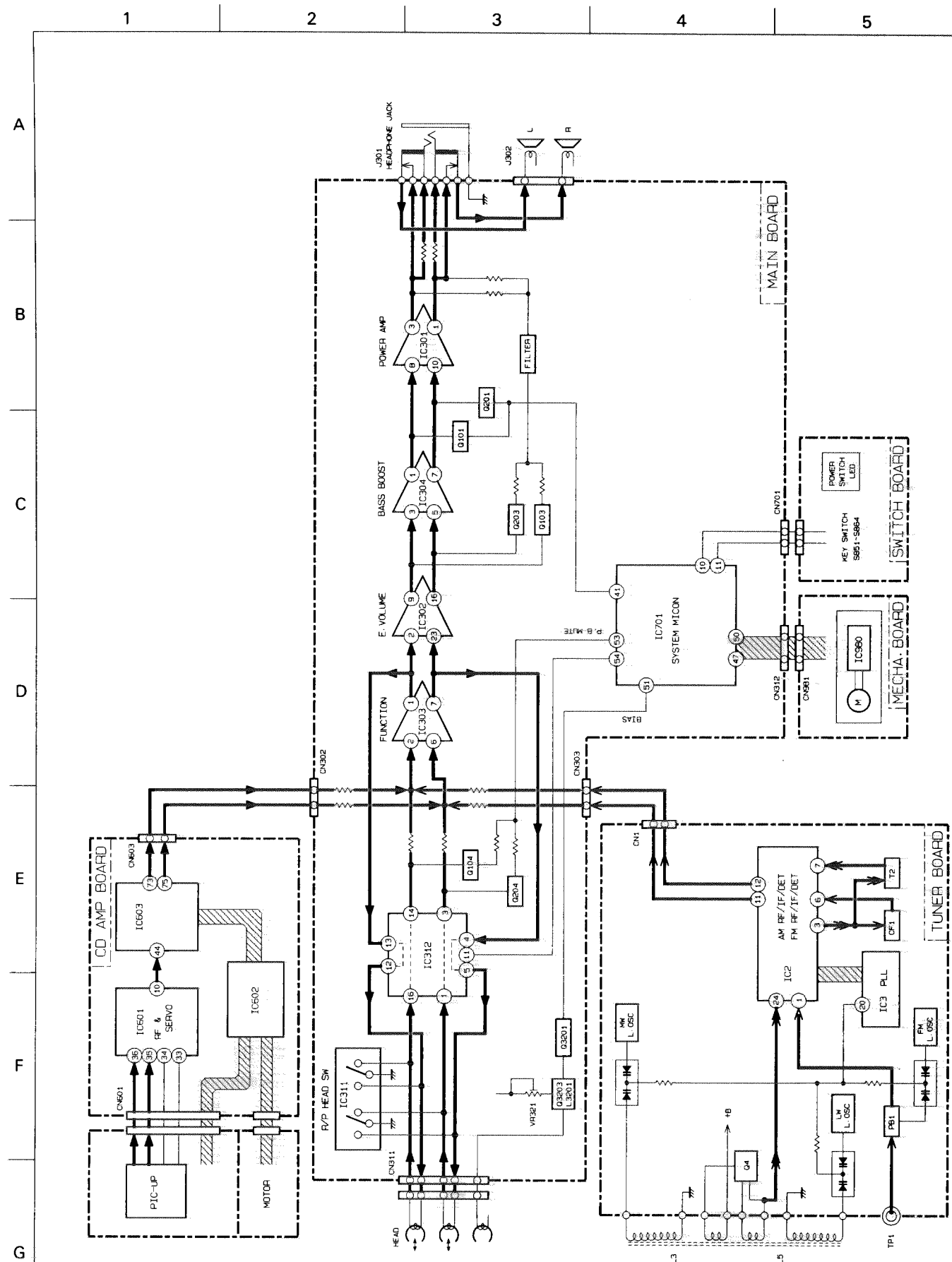
◆ IC603 (MN35510) DIGITAL SERVO



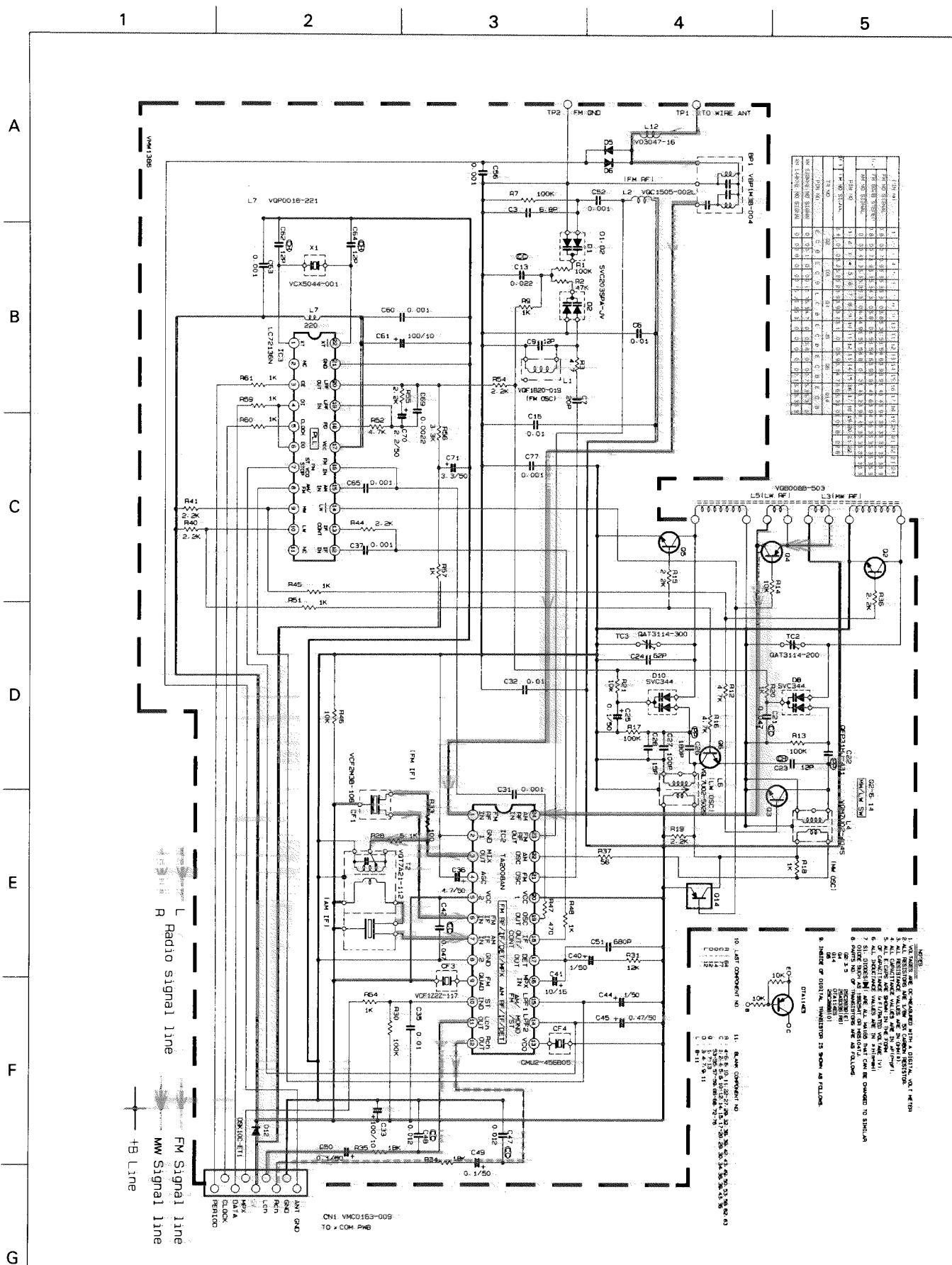
IC701 (MN171603J8X) System microprocessor pin function

No	NAME	I/O	Discriptions	No	NAME	I/O	Discriptions
1	REST	I	REST SW	43	TUST	O	TUNER STROVE
2	NC	I	—	44	MPX	O	TUNER STEREO
3	AVSS	—	A.GND	45	NC		—
4	VER	I	VERSION DETECT	46	REC		REC DETECT
5	SAFETY		SW8V SAFETY DETECT	47	REEL PALS		REEL PALS
6	CD+B		CD+B SAFETY DETECT	48	FAST	O	FAST
7	CLOSE		CD DOOR DETECT	49	F/R		FWD/REW CONTROL
8	NC	I	—	50	M ON		MOTOR SW
9	BUP		BUCK UP DETECT	51	RECB		RECB
10	KEY1		KEY1	52	R/P	O	REC/PLAY CONTROL
11	KEY2		KEY2	53	PBMUTE		PBMUTE
12	AVDD	—	AVDD	54	RECMUTE		RECMUTE
13	VLC1	—	LCD BIAS(VDD>VLC1>VLC2>VLC3>VSS)	55	+BCTL		+B CONTROL
14	VLC2	—		56	F.CD	O	FUNCTION CD
15	VLC3	—		57	F.TU		FUNCTION TUNER
16	COM3	O	LCD COMMON	58	VOL		VOLUME (PWM)
17	COM2	O	•	59	BASS		BASS (PWM)
18	COM1	O	•	60	TRE	O	TREBLE (PWM)
19	COM0	O	•	61	MLD		MICON COMMAND LOAD
20	SEG0	O	LCD SEGMENT	62	XRST		CD LSI RESET
21	SEG1	O	•	63	BEAT2		BEAT2
22	SEG2	O	•	64	AC/DC	I	AC/DC DETECT
23	SEG3	O	•	65	NC		—
24	SEG4	O	•	66	STAT		STATUS
25	SEG5	O	•	67	NC		—
26	SEG6	O	•	68	RESET	I	RESET
27	SEG7	O	•	69	NC	I	—
28	SEG8	O	•	70	NC	O	—
29	SEG9	O	•	71	VSS	—	D.GND
30	SEG10	O	•	72	OSC2	O	OSC2
31	SEG11	O	•	73	OSC1	I	OSC1
32	SEG12	O	•	74	VDD	—	VDD
33	SEG13	O	•	75	BEAT1	O	BEAT1
34	SEG14	O	•	76	SQCK	O	SUBCODE Q CLOCK
35	SEG15	O	•	77	SUBQ	I	SUBCODE Q DATA
36	SEG16	O	•	78	MCLK	O	MICON COMMAND CLOCK
37	SEG17		•	79	MDATA	O	MICON COMMAND DATA
38	SEG18		•	80	SCK	O	SERIAL CLOCK
39	NC			81	S1	I	SERIAL DATA IN
40	POUT	O	POWER CONTROL	82	S0	O	SERIAL DATA OUT
41	SMUTE		SYSTEM MUTE	83	PIN	I	POWER KEY
42	AHB		ACTIVE HYPER BASS	84	REM	I	REMOCON IN

■ Signal diagram



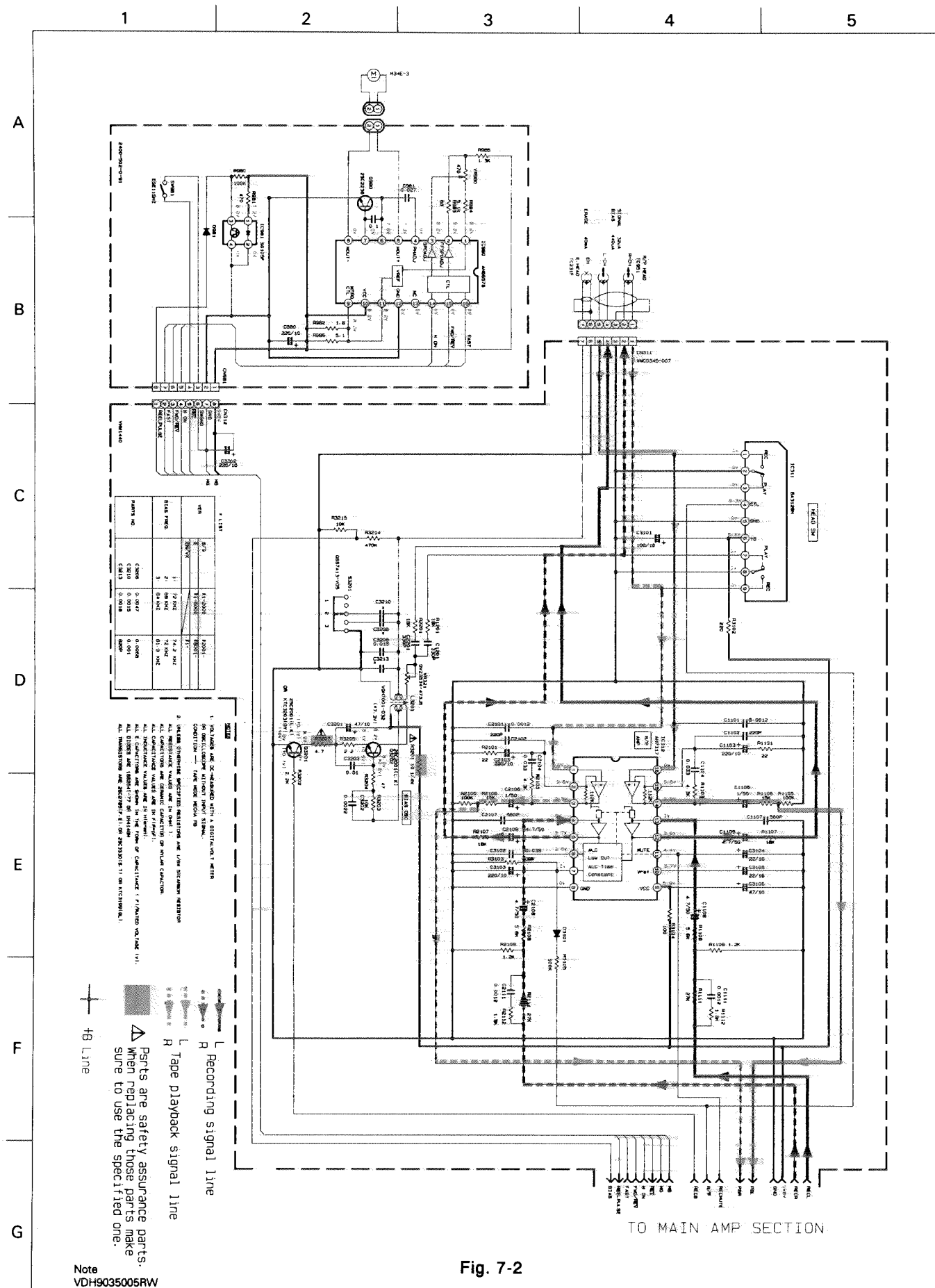
7 Standard schematic diagram ■ Tuner circuit



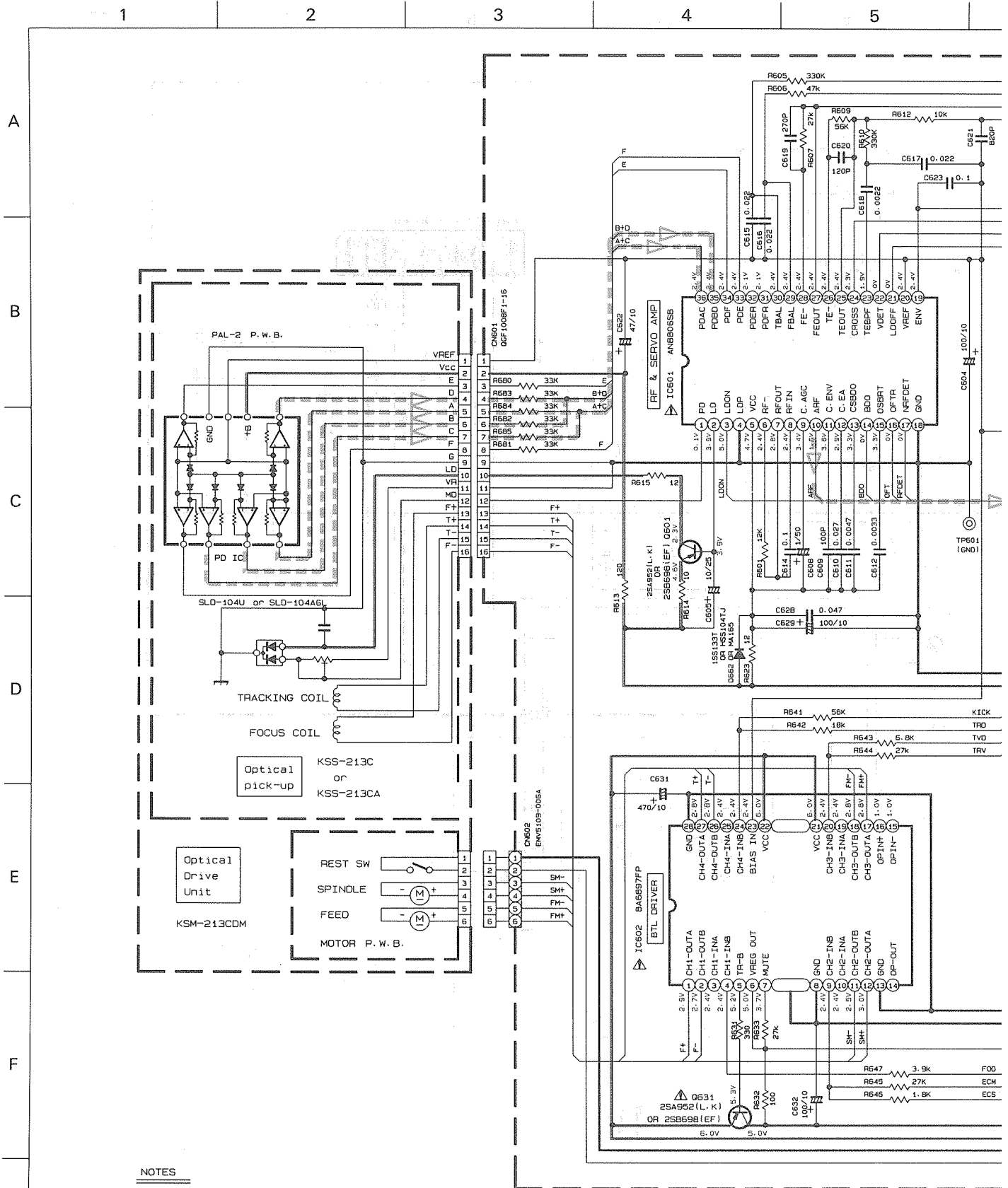
Note
VDH9035005TW

Fig. 7-1

■ Cassette amplifier circuit



CD amplifier circuit



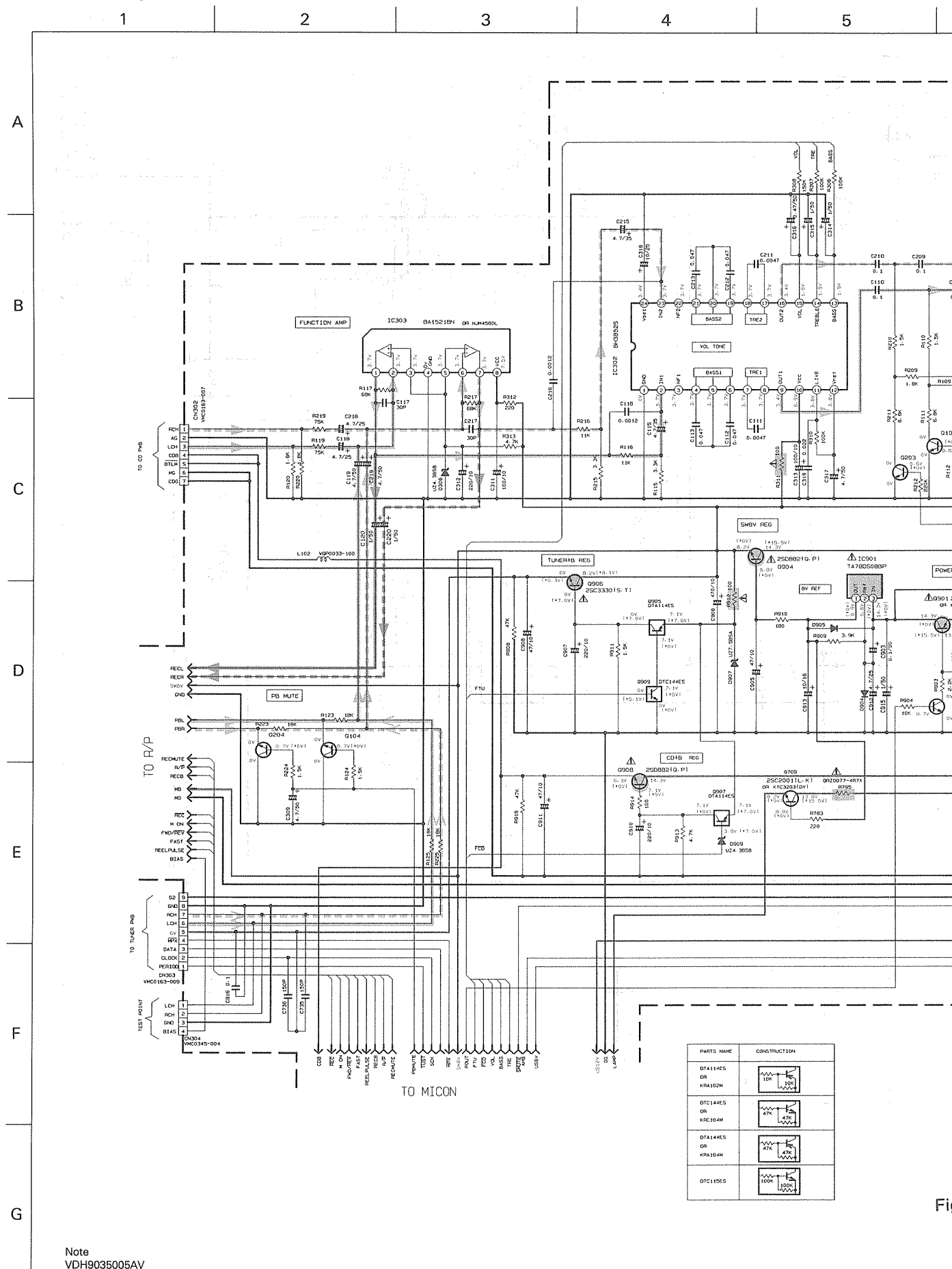
- NOTES**
- VOLTAGES ARE DC-MEASURED WITH A DIGITAL VOLT METER WITH PLAY MODE.
 - UNLESS OTHERWISE SPECIFIED, RESISTORS ARE 1/6W ±5% CARBON RESISTOR. ALL RESISTANCE VALUES ARE IN OHM(Ω).
 - ALL CAPACITORS ARE CERAMIC CAPACITOR, OR MYLAR CAPACITOR. ALL CAPACITANCE VALUES ARE IN pF(P=pF).
 - ALL E-CAPACITORS ARE SHOWN IN THE FORM OF CAPACITANCE (pF)/RATED VOLTAGE (V).

Note
VDH1035001CW

Fig. 7-3



■ Power amplifier circuit



Fig

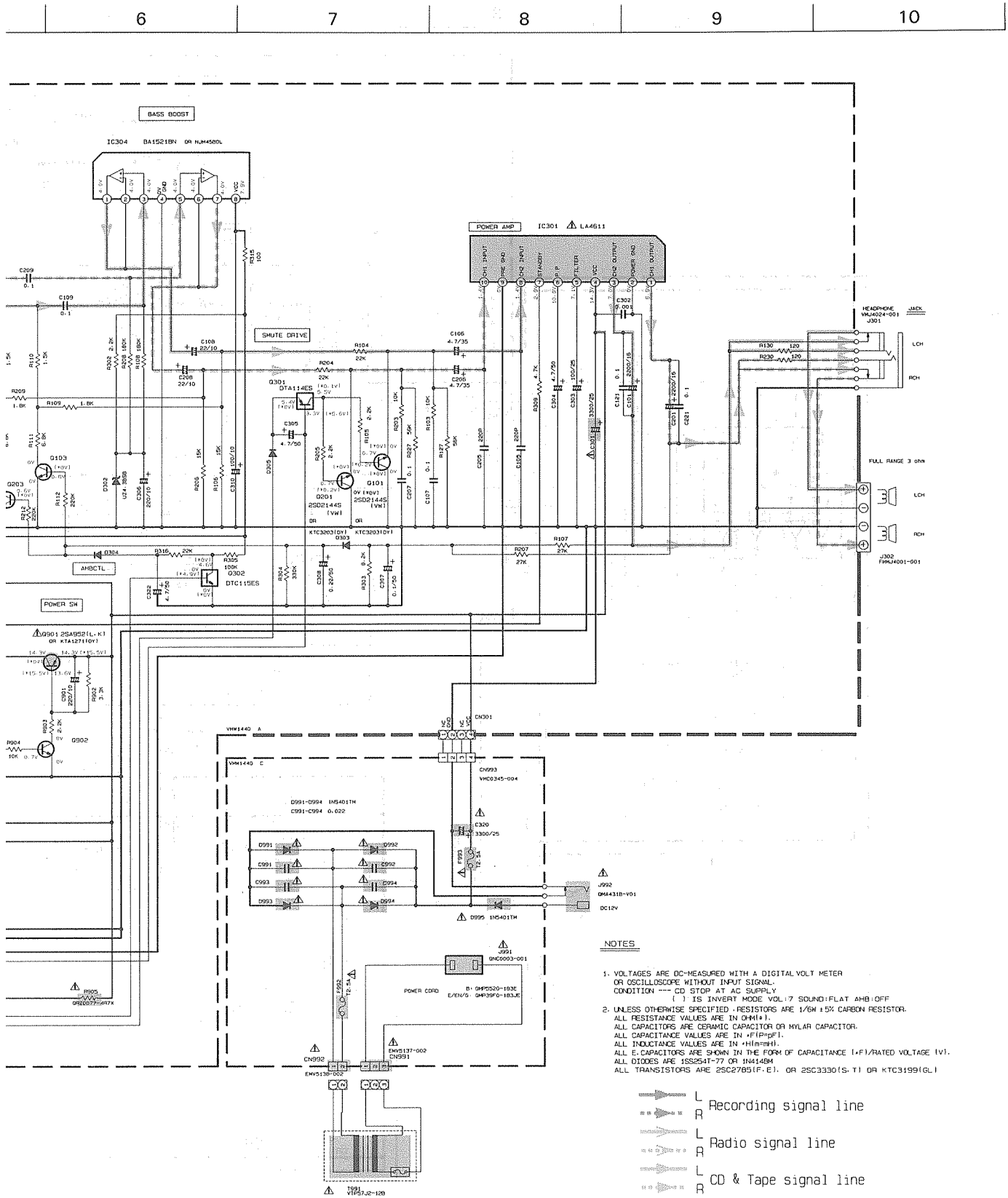


Fig. 7-4



Note
VDH9035005SV

Fig. 7-5

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