CIRCUIT DIAGRAM

1. POWER(SMPS) CIRCUIT DIAGRAM

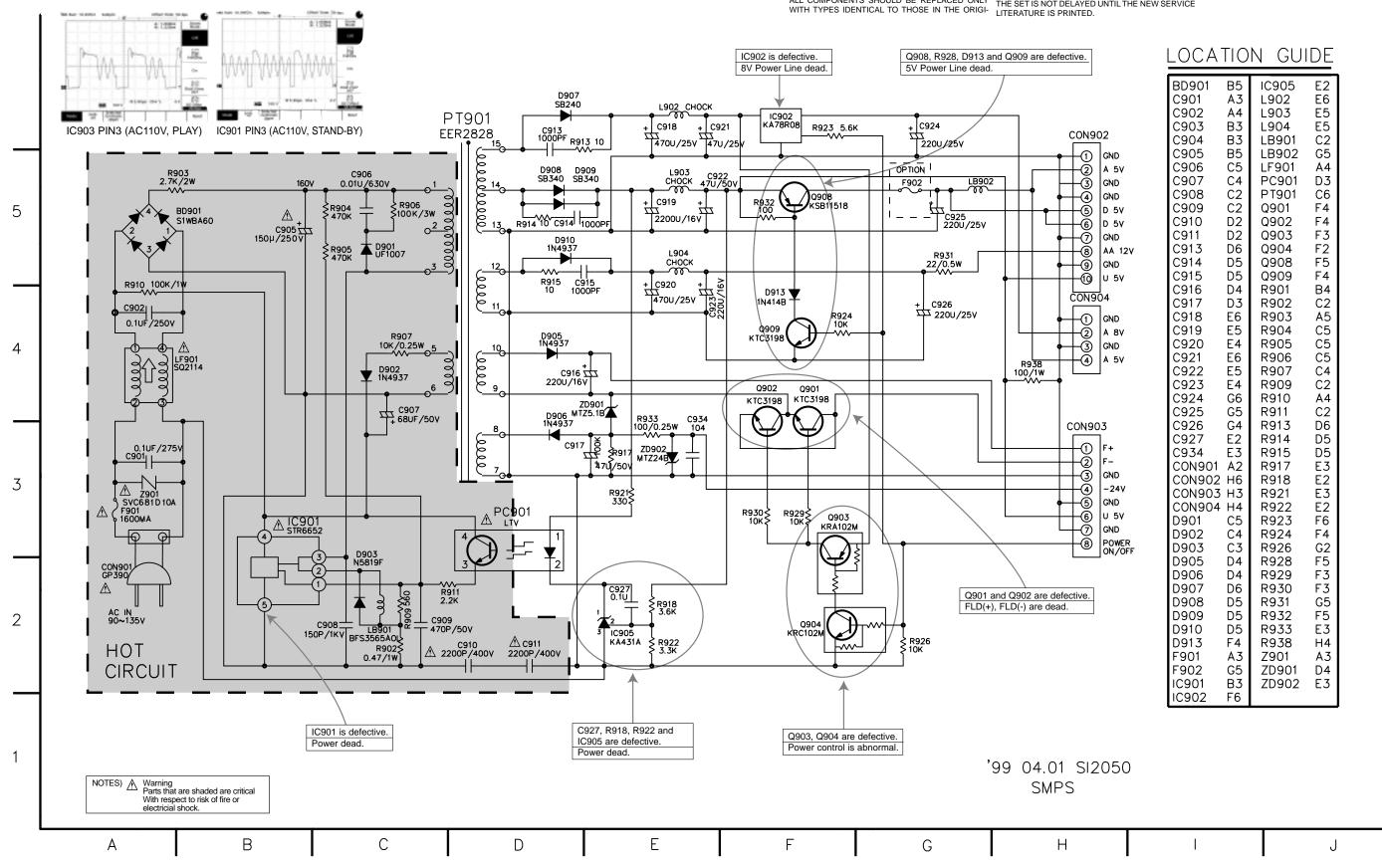
IMPORTANT SAFETY NOTICE

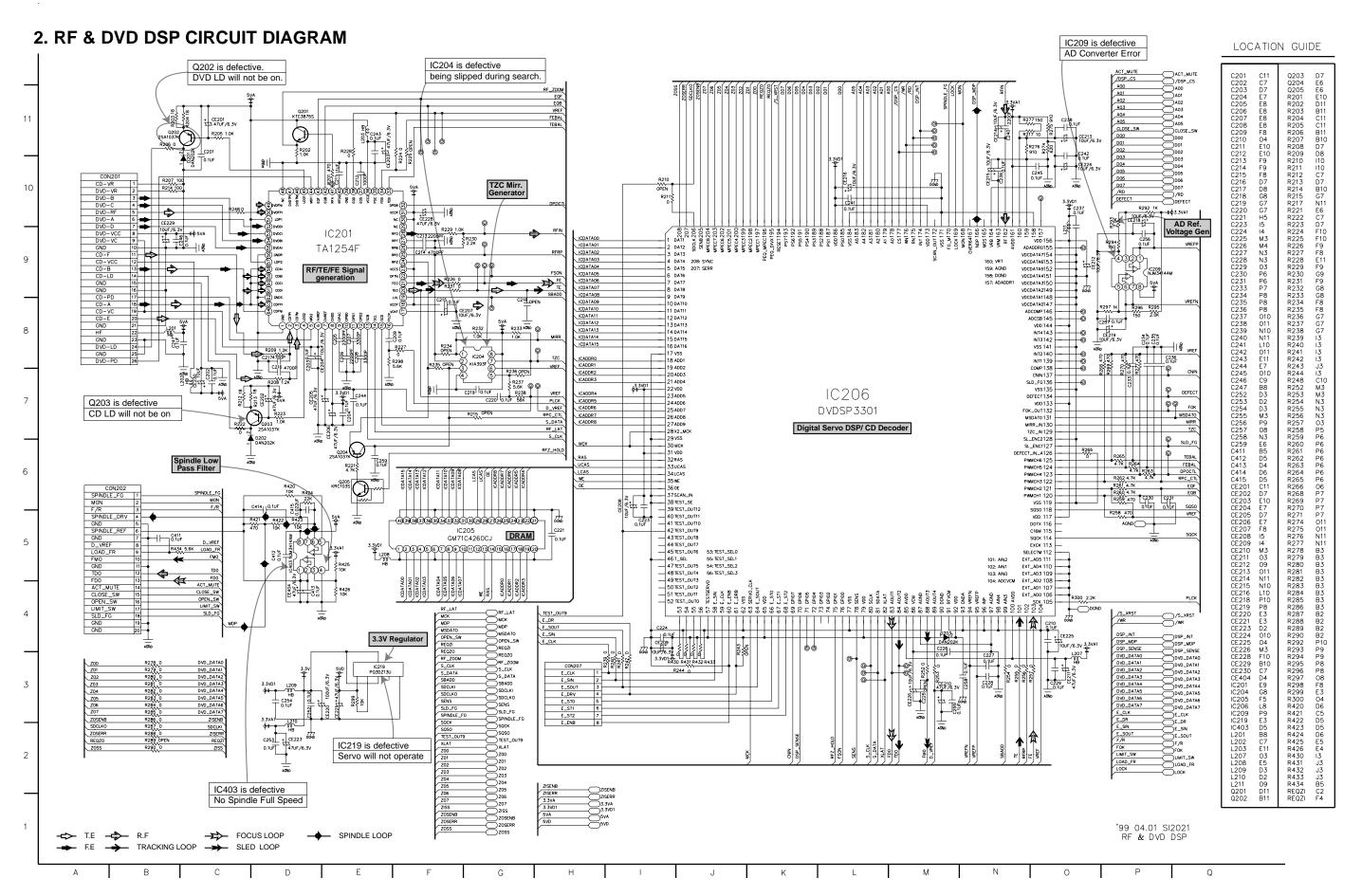
WHEN SERVICING THIS CHASSIS, UNDER NO CIR-CUMSTANCES SHOULD THE ORIGINAL DESIGN BE MODIFIED OR ALTETED WITHOUT PERMISSION

NAL CIRCUIT SPECIAL COMPONTS ARE SHADED ON NOTE: THE SCHEMATIC FOR EASY IDENTIFICATION. 1. Sha

THIS CIRCUIT DIAGRAM MAY OCCASIONALLY DIF-FER FROM THE ACTUAL CIRCUIT USED. THIS WAY, IMPLEMENTATION OF THE LATEST SAFETY AND MODIFIED OR ALIELED WITHOUT FERMISSION IMPLEMENTATION OF THE LATEST ONLY
FROM THE ZENITH ELECTRONICS CORPORATION PERFORMANCE IMPROVEMENT CHANGES INTO ALL COMPONENTS SHOULD BE REPLACED ONLY
THE SET IS NOT DELAYED UNTIL THE NEW SERVICE

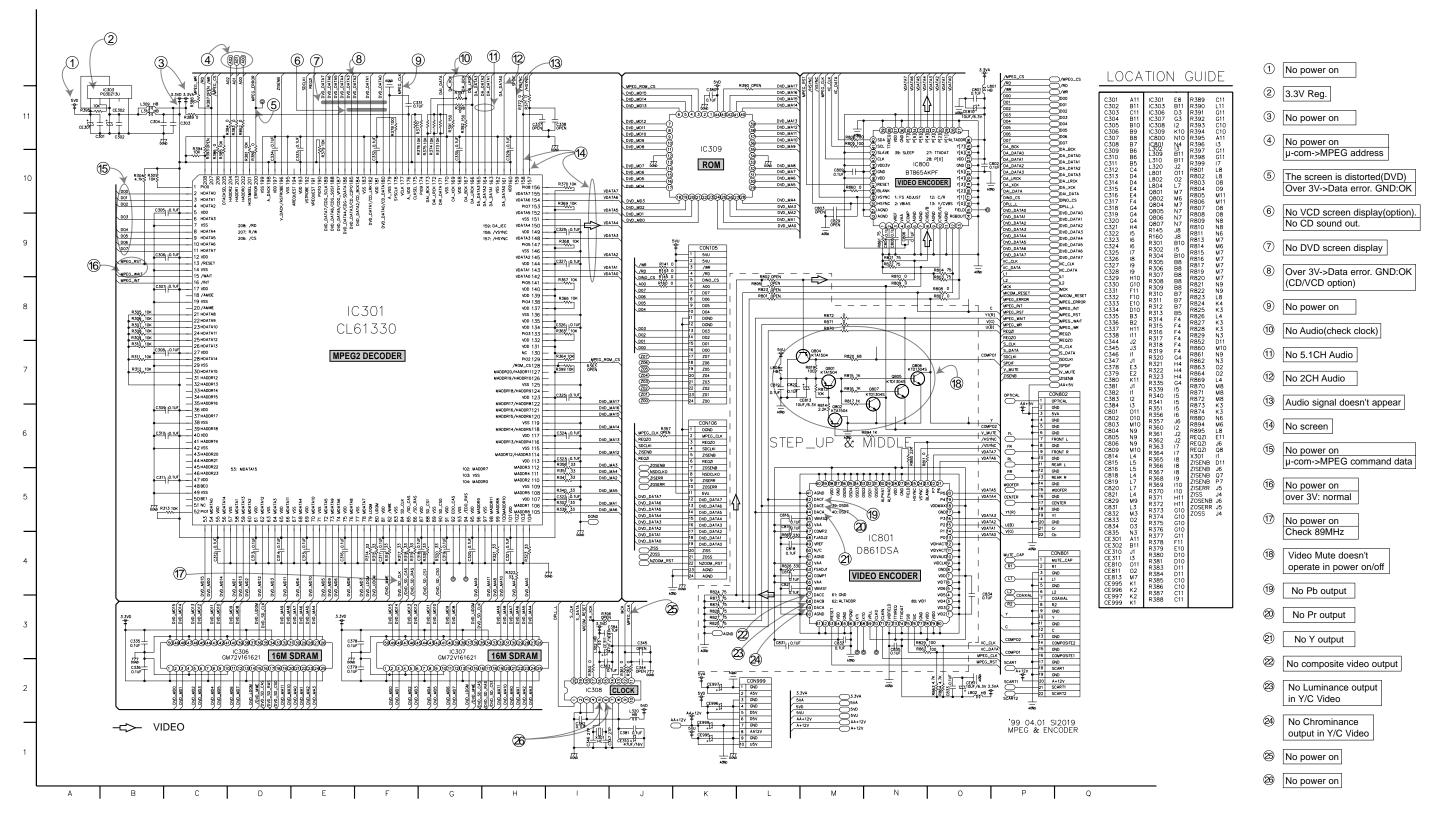
- 1. Shaded(■) parts are critical for safety. Replace only with specified part number.
- 2. Voltages are DC-measured with a digital voltmeter



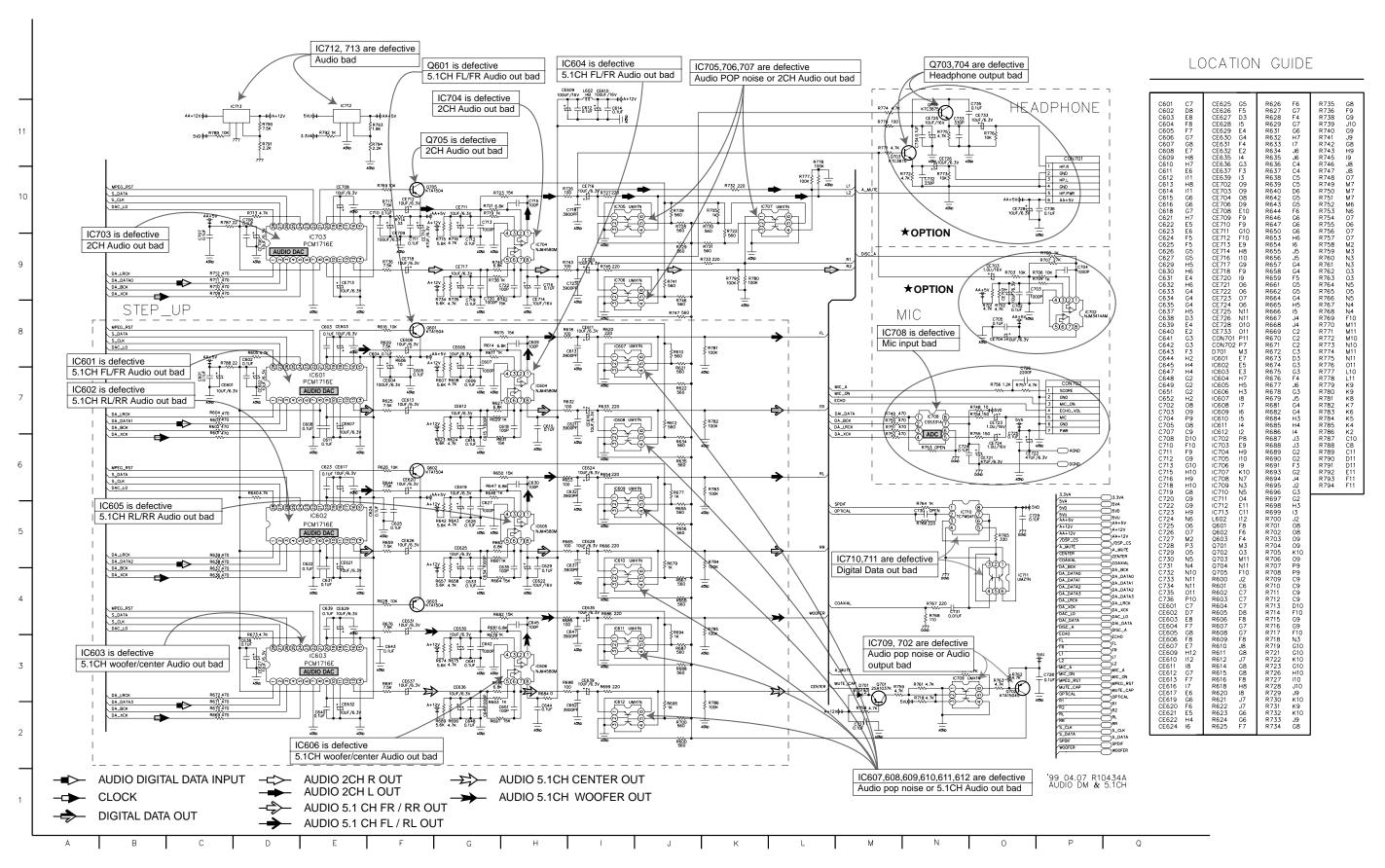


3. DRIVE CIRCUIT DIAGRAM LOCATION GUIDE CN400 35802 CD-VR 8 DVD-VR DVD-VR E7 E7 G6 F7 C403 C407 DVD-B 5V GND DVD-B DVD-C DVD-C C410 DVD-RF DVD-RF C411 DVD-A DVD-A C412 DVD-D DVD-D H3 D2 E3 C416 C417 DVD-VCC DVD-VCC DVD-VC CN402 GF102 DVD-VC IC400 BA6859AFP +9V R430 1.0 C451 VV 47M/16V C420 CD-VR GND1 G3 F4 CD-VR GND C435 DVD-VR DVD-VR CD-F C436 7 DVD-B CD-VCC CD-VCC C437 C438 G6 G4 G3 DVD-C CD-B DVD-C CD-B DVD-RF CD-LD DVD-RF CD-LD C439 3 N.C -(4) A2 DVD-A GND2 DVD-A GND C440 DVD-D DVD-D FO-5 N.C 6 N.C –7 A1 G6 G7 F3 C450 FG (24) T_ACT DVD-VCC DVD-VCC TR-SPINDLE_DRV C450 47M/16V C451 DVD-VC T_ACT+ DVD-VC TR+ C452 F_ACT+ GND1 SPINDLE C437 + T GND FO+ C453 G4 CD-F GND3 20 GND -21 CD-P -22 CD-A -23 CD-V CD-F J8 C8 B7 CN400 CD-VCC SPINDLE_REF CD-PD CD-VCC CD-PD CN401 6 CD-B CD-A CD-B FR (19) N. C (19) SB (17) N. C (15) N. C (15) F/R CD-A CN402 CD-LD CD-VC CD-LD CD-VC CN40.3 GND2 CD-E C410 0.1UF GND CD-E CN404 -(11)H2+ -(12)H2--(13)H3+ GND3 GND4 J3 J2 H7 D6 GND GND CN405 CD-PD CD-PD . W+ CN406 CD-A GND5 GND GND1 GND1 CD-VC DVD-LD CD-VC DVD-LD CD-E GND4 GND6 CD-E GND GND2 DVD-PD GND DVD-PD GND2 HF 22 23 24 25 26 GND3 GND3 5 GND5 GND D6 H6 D5 DVD-LD CN403 GF102 DVD-LD GND4 GND6 1 LIMIT_SW+ 2 LIMIT_SW-GND GND4 DVD-PD 2 LIMI 3 H1N 4 W5 W+ 6 V7 V+ 8 U9 U+ 10 H1N 11 U 12 V 13 W DVD-PD GND5 H1N-ZD401 X ZD402 MTZ10B X MTZ10B GND5 D5 H5 GND6 GND6 D5 F7 CN404 GF102 IC400 SPINDLE_FG SPINDLE_FG C436 0.1UF IC401 MON IC401 BA5983FP MOM MOM 4 F/R F/R MON H6 C2 H4 SPINDLE_DRY SPINDLE_DRY н1Ņ+ 0401 C453777 47M/16V C400 0.1UF OPEN R401 2) PINI(+) 3) OPINI(-) R442 20K (5) OPIN2(+) 12 10K (5) OPIN2(-) 14 OPOUT1 15 OPIN2(-) 16 OPIN2(-) R403 1.0K SPINDLE_REF OPIN4(+)(27)-SPINDLE_REF R402 OPIN4(-)(26) OPEN R403 R401 4.7K —~~~ OPOUT4 25 OPIN3(+)(24) D_VREF Ē3 H5 R404 LOAD_FR R405 OPIN3(+)429 OPIN3(-)(23) OPOUT3(22) R444 8.2K R408 R409 1.0K 10K C416 0.1uF R402 10K FM0 R417 120 1 GND 2 ANODE 3 COLLECTOR 4 SL_M-5 SL_M+ CN405 GF102 FMO R407 C420 R441 20K 7 OPOUT2 OPEN G3 G3 E4 G4 R408 TDO TDO MOTOR DRIVE R409 3 FD0 FD0 R410 8 GND 9 STBY1 (10) PowVcc1 (11) V02(-) (12) V02(+) (13) V01(-) (14) V01(+) ACT_MUTE R411 STBY2(20 C439 0.1UF PowVcc2(9) + C435 CLOSE_SW R412 E2 R415 10K OPEN_SW R414 F_ACT-F_ACT-T_ACT-V03(-)(18)-V03(+)(17)-V04(-)(16)-V04(+)(15)-LIMIT_SW R415 D2 D2 I3 CN406 SLD_FG R416 R419 4,7K —-VVV----1 LOAD_M-2 LOAD_M+ OPEN R417 T_ACT+ C2 C2 G7 G7 F3 OPEN R418 R412 10K R419 R430 R431 2 R441 R442 R442 F3 R443 G3 R444 G3 R450 D2 SW400 D2 ZD401 C5 ZD402 C5 SPINDLE → FOCUS TRACKING → SLIED '99 04.01 SI2048 DOOR DRIVE В С D G

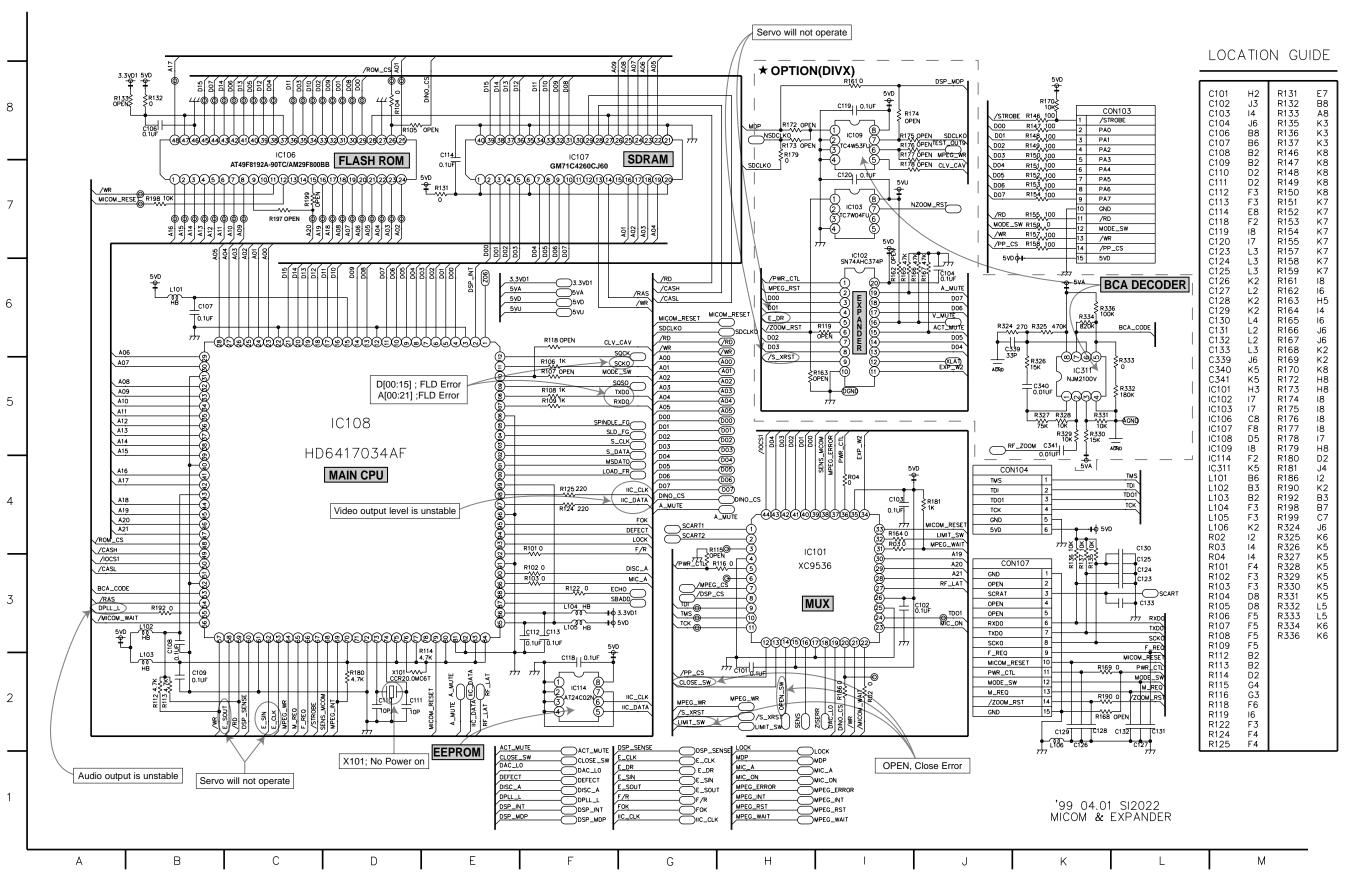
4. MPEG & ENCODER CIRCUIT DIAGRAM



5. AUDIO CIRCUIT DIAGRAM



6. μ-COM/EXPANDER CIRCUIT DIAGRAM

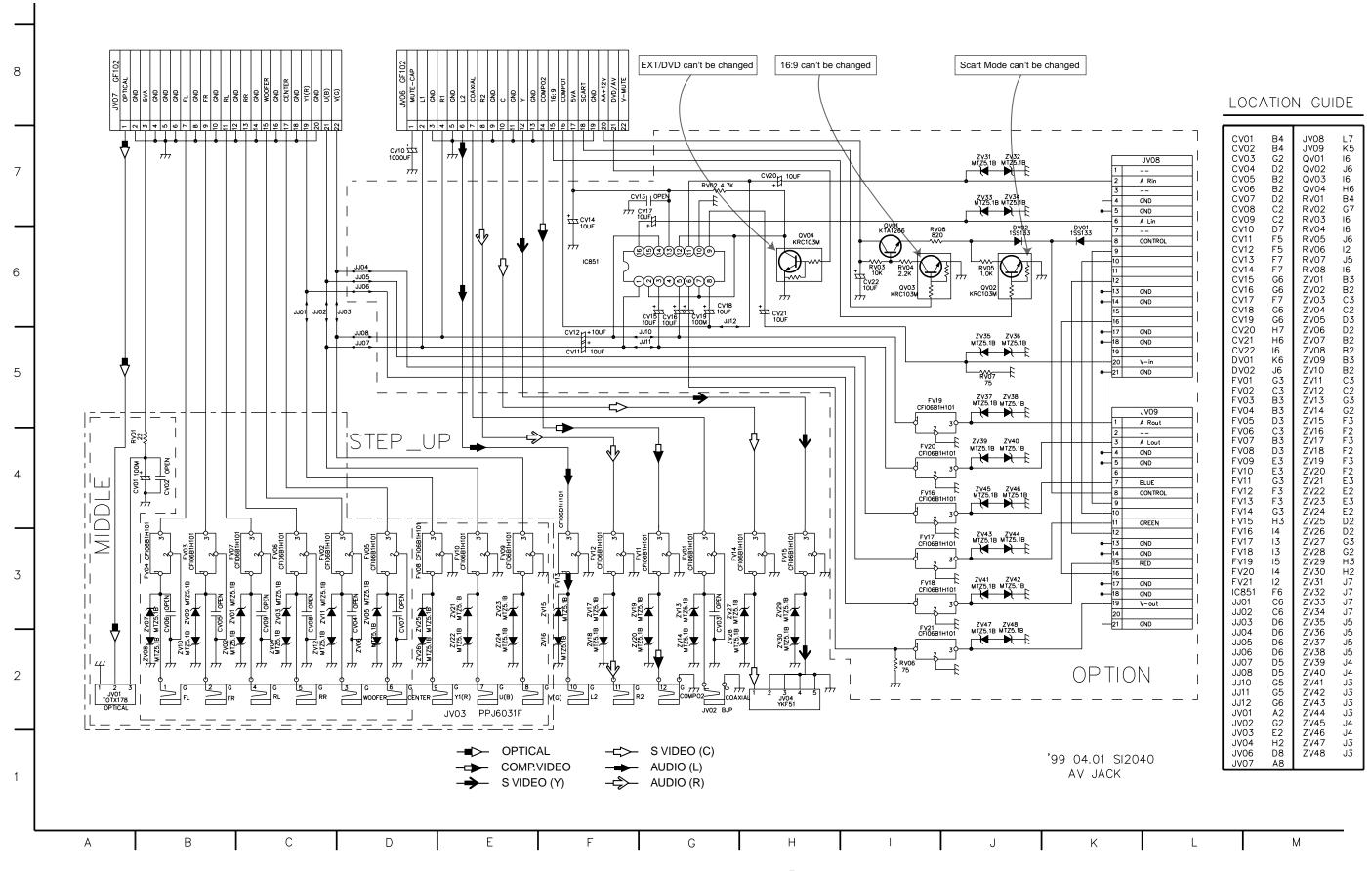


7. FRONT(TIMER) & KEY CIRCUIT DIAGRAM SW509 is defective DIG501 is defective Direction Key is defect LOCATION GUIDE FLD is distorted 8 4 Direction S/W C500 C501 C502 C503 C504 C505 C506 C507 C508 C509 C510 C512 C513 CN500 CN503 R521 J6 R522 I6 R523 E8 R524 E8 R526 E6 R527 E6 R527 E6 R528 F6 R531 F5 R540 D6 RC501 H3 SW502 H3 SW503 G3 SW504 G3 SW504 G3 SW505 G3 SW505 G3 SW506 F3 SW507 J2 SW509 B7 **ENTER** BASIC SW510 SKHV10910B STEP-UP NΡ G1 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 1975 | 7 G3 8 G4 G3 1 GND 2 GND 9 G5 GND F5 G2 K3 G2 C7 C7 B5 3P37 4PWM1 5TEST1 G6 DOWN G7 12 NX JSW4 CN504 ŊX CN505 CN506 CN507 JSW3 JS500 SRGPVJ D504 1SS133 D504 NTSC PAL S31 (58 D503 RGB -- S30 (57 14 NX 15 NX 7 JSW2 03 1SS133 8 LEFT 9 JSW1 10 UP 02 1SS133 0501 1SS133 0500 1SS13 D502 5.1ch -- S29(56 ŊX D500 D501 D502 6 X501 R528 220 10 CF1 C504 12 YDD C503 33P D501 DIVX -- S28(55 SW510 SW511 G8 D7 D7 ŊX S26(53) ΝX D503 SW512 20 P19 -21 P18 -22 P17 C503 33P SW513 D7 SW514 E7 X501 F6 D504 P18 P18 SFUTTLE -(14)P81/AN1 -(15)P82/AN2 IC500 S24(51 D505 D506 H2 I2 K8 C513 47M P17 P16 P16 S22(49 16)P83/AN3 C508 OPEN C505 OPEN R531 NOR NODE SW 10K LC86P6132 23 P16 DIG501 ZOOM_RST_H8 23 P16 24 P15 25 P14 26 P13 27 P12 28 P11 29 P10 30 P9 31 P8 P15 17)P84/AN4 IC500 IC501 JS500 LED501 G5 E5 B6 H1 ZOOM_RST C4 P14 (18)P85/AN5 CN507 FRONT CONTROL P13 19)P86/AN6 FRC 20)P87/AN7 25:S0/T0 P12 OPEN RESET P10 21)P70/INTO 26:S1/T1 Q500 R500 R501 H2 G3 F8 E5 H3 G3 F3 I2 F8 E8 H3 F4 I2 F8 F8 P11 M_REQ P10 P11 P12 P12 F_RXD F_RXD JS500 is defective R502 R503 R504 F_TXD 32 P7 33 P6 F_TXD SCAN +/- wll not operate -(25)(26)(27)(28)(29)(30)(31)(32)(33)(34)(35)(36)(37)(38)(39)(40)-C507 10M/16V F_CLK RC501 F_REQ F_REQ R505 M_RESET R506 R507 R508 M_RESET PWR_CTL PWR_CTL MODE_SW MODE_SW M_REQ R508 R509 R510 R512 R513 R514 M_REQ Remocon ZOOM_RST RE500 is defective ZOOM_RST Receiver R/C receiver Error IC501 is defective Reset Error R516 R517 CN503 F+ R518 2 F-F8 E8 3 GND27 R520 4 -27V 5 GNDU 6 5VU 7 GNDU 8 PWR_CT PWR_CTL C500 55 220M/6.3V KEY_IN KEY_IN 57 57 GND GND OPEN/CLOSE LED_IN CN500 **POWER** '99 04.26 R10437A LED501 DVD-2200N,D/2250N,D/2300N Q500 is defective LED will not operate В С D Ε G Н

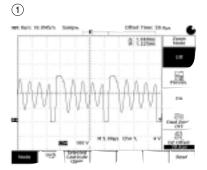
3-32

3-33

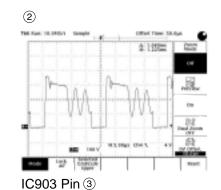
8. JACK CIRCUIT DIAGRAM



WAVEFORM (POWER)

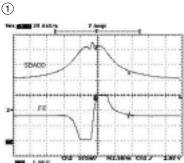


IC901 Pin ③ STAND-BY(AC110V)

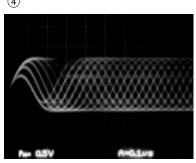


PLAY(AC110V)

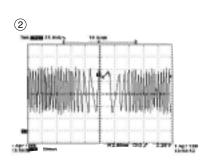
(RF/SERVO)



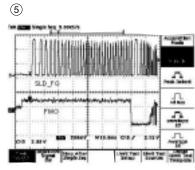
IC201 Pin ②, Focus Error IC201 Pin ③, SBADD



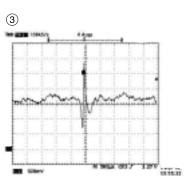
IC201 Pin 28, RF



IC201 Pin
Tracking Error

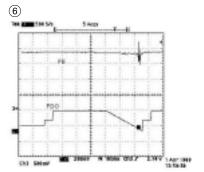


IC206 Pin ®, SLED Drive(FMO) IC202 Pin ®, SLED FG



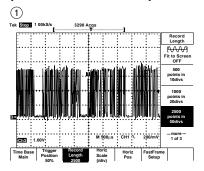
IC201 Pin

VBR TRACKING Error

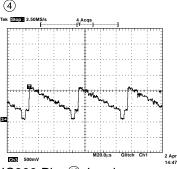


IC201 Pin ②, Focus Error(in Focus Search) IC206 Pin ⑧ Focus Drive(FDO)

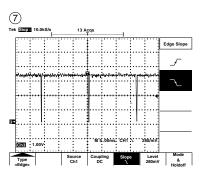
(VIDEO ENCODER)



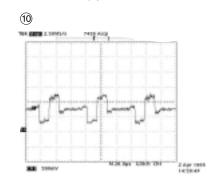
IC800 Pin 28 ~ 35, MPEG Data IC801 Pin 13 ~ 16, 19 ~ 22, MPEG Data



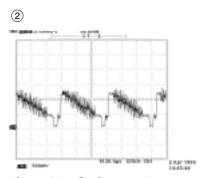
IC800 Pin ③, Luminance IC801 Pin ⑤, Luminance



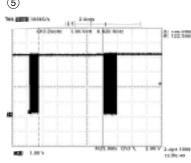
IC800 Pin @, Vertical SYNC IC801 Pin @, Vertical SYNC



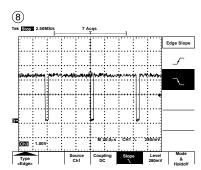
IC801 Pin @ Component Pr



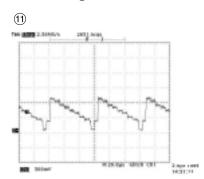
IC800 Pin (0), Composite IC801 Pin (5), Composite



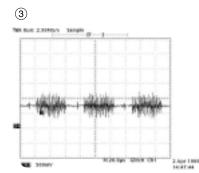
IC800 Pin 40, 41 SDA/SCL IC801 Pin 75, 76 SDA/SCL



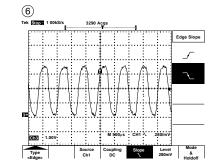
IC800 Pin (50) Horizontal SYNC IC801 Pin (50) Horizontal SYNC



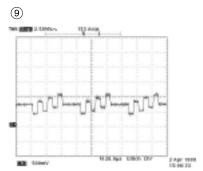
IC801 Pin 4 Component Y



IC800 Pin 12, Chrominance IC801 Pin (38), Chrominance



IC800 Pin ③, MPEG Clock(27MHz) IC801 Pin ⑦, MPEG Clock(27MHz)



IC801 Pin @ Component Pb