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SECTION 1

SUMMARY

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NEW FUNCTIONS OF DVD-RECORDER

• SUMMARY OF PRODUCT

- RECORDING FUNCTION OF DVD-RW AND DVD-R SPECIFICATIONS
 - DVD-RW: VIDEO MODE AND VR MODE RECORD AVAILABLE
 - DVD-R :VIDEO MODE RECORD AVAILABLE
- DIGITAL DUBBING FUNCTION OF DV CAMCORDER BY USING DV TERMINAL (IEEE1394)
 - RECORD, PLAY, FF/REW FUNCTION BY REMOTE CONTROL OF DV CAMCORDER
- DVD PROGRESSIVE PLAY RESPONSE
- VARIOUS FUNCTION RESPONSE OF DVD RECORDING (DISC NAVIGATION AND CONVENIENT PLAY, EDIT FUNCTION)
- OUTSIDE INPUT AND TV RECORDING AVAILABLE
 - RECORDING SCREEN QUALITY :VR(HQ, SQ, LQ), VIDEO(HQ, SQ)
- TV RESERVE RECORDING FUNCTION (AUTO MODE SETTING AVAILABLE FOR RECORDING IN ACCORDANCE WITH THE REMAINING DISC SPACE IN RESERVATION)

SUMMARIZED EXPLANATION OF MAIN FUNCTION

- DVD RECORDING FUNCTION(VR MODE RECORD / VIDEO MODE RECORD)
- 1) VR MODE RECORD : MANUAL MODE RECORDING IN ACCORDANCE WITH VARIOUS EDITING FUNCTION, REMAINING DISC SPACE AND PROGRAM TIME DVD-RW DISC RECORDED IN THE VR MODE CAN BE PLAYED WITH A DVD PLAYER CORRESPONDING TO THE DVD-RW THERE IS ALSO A PLAYER TO BE PLAYED THROUGH FINALIZING. FOR THE DVD-RW, RECORDING AND EDITING IS AVAILABLE AT THE SAME DEVICE EVEN AFTER FINALIZING.
 - 2) VIDEO MODE RECORDING : THERE IS NO EDITING FUNCTION SUCH AS VR MODE RECORDING BUT VIDEO MODE RECORDING IS PLAYED IN A GAME DEVICE (FOR EXAMPLE, "PLAY STATION 2") WITH PC, DVD PLAY FUNCTION CORRESPONDING TO DVD PLAYER, CAR DVD, DVD-ROM. TO PLAY IN ANOTHER DEVICE, FINALIZING IS REQUIRED. RECORDING, EDITING AND ERASING IS NOT POSSIBLE AFTER FINALIZING. HOWEVER, RECORDING IS ALLOWED AT THE DVD-RW DISC IF ERASING THE TITLE FINALLY RECORDED AFTER FINALIZING.
 - 3) RECORDING MODE INITIALIZATION (A KIND OF FORMATTING): BEGINS INITIALIZATION AFTER SELECTING RECORDING MODE AS VR OR VIDEO MODE BY USING INITIALIZATION FUNCTION OF THE DISC SETTING MENU. INITIALIZES DEFAULT AS VR MODE FOR DVD-RW. RECORDS IT AS VIDEO MODE WITHOUT INITIALIZATION FOR VIDEO MODE.
 - 4) FINALIZE: BEGINS FINALIZE AT THE DISC SETTING MENU DURING STOP.

PRODUCT SAFETY SERVICING GUIDELINES FOR VIDEO PRODUCTS

IMPORTANT SAFETY NOTICE

This manual was prepared for use only by properly trained audio-video service technicians.

When servicing this product, under no circumstances should the original design be modified or altered without permission from LG Electronics Corporation. All components should be replaced only with types identical to those in the original circuit and their physical location, wiring and lead dress must conform to original layout upon completion of repairs.

Special components are also used to prevent x-radiation, shock and fire hazard. These components are indicated by the letter "x" included in their component designators and are required to maintain safe performance. No deviations are allowed without prior approval by LG Electronics Corporation.

Circuit diagrams may occasionally differ from the actual circuit used. This way, implementation of the latest safety and performance improvement changes into the set is not delayed until the new service literature is printed.

CAUTION: Do not attempt to modify this product in any way. Never perform customized installations without manufacturer's approval. Unauthorized modifications will not only void the warranty, but may lead to property damage or user injury.

Service work should be performed only after you are thoroughly familiar with these safety checks and servicing guidelines.

GRAPHIC SYMBOLS



The exclamation point within an equilateral triangle is intended to alert the service personnel to important safety information in the service literature.



The lightning flash with arrowhead symbol within an equilateral triangle is intended to alert the service personnel to the presence of noninsulated "dangerous voltage" that may be of sufficient magnitude to constitute a risk of electric shock.



The pictorial representation of a fuse and its rating within an equilateral triangle is intended to convey to the service personnel the following fuse replacement caution notice:

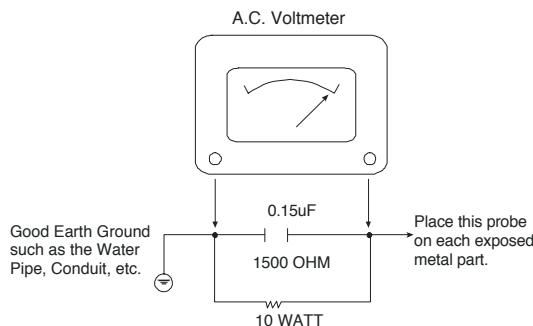
CAUTION: FOR CONTINUED PROTECTION AGAINST RISK OF FIRE, REPLACE ALL FUSES WITH THE SAME TYPE AND RATING AS MARKED NEAR EACH FUSE.

SERVICE INFORMATION

While servicing, use an isolation transformer for protection from AC line shock. After the original service problem has been corrected, make a check of the following:

FIRE AND SHOCK HAZARD

1. Be sure that all components are positioned to avoid a possibility of adjacent component shorts. This is especially important on items trans-ported to and from the repair shop.
2. Verify that all protective devices such as insulators, barriers, covers, shields, strain reliefs, power supply cords, and other hardware have been reinstalled per the original design. Be sure that the safety purpose of the polarized line plug has not been defeated.
3. Soldering must be inspected to discover possible cold solder joints, solder splashes, or sharp solder points. Be certain to remove all loose foreign particles.
4. Check for physical evidence of damage or deterioration to parts and components, for frayed leads or damaged insulation (including the AC cord), and replace if necessary.
5. No lead or component should touch a high current device or a resistor rated at 1 watt or more. Lead tension around protruding metal surfaces must be avoided.
6. After reassembly of the set, always perform an AC leakage test on all exposed metallic parts of the cabinet (the channel selector knobs, antenna terminals, handle and screws) to be sure that set is safe to operate without danger of electrical shock. DO NOT USE A LINE ISOLATION TRANSFORMER DURING THIS TEST. Use an AC voltmeter having 5000 ohms per volt or more sensitivity in the following manner: Connect a 1500 ohm, 10 watt resistor, paralleled by a .15 mfd 150V AC type capacitor between a known good earth ground water pipe, conduit, etc.) and the exposed metallic parts, one at a time. Measure the AC voltage across the combination of 1500 ohm resistor and .15 mfd capacitor. Reverse the AC plug by using a non-polarized adaptor and repeat AC voltage measurements for each exposed metallic part. Voltage measured must not exceed 0.75 volts RMS. This corresponds to 0.5 milliamp AC. Any value exceeding this limit constitutes a potential shock hazard and must be corrected immediately.



TIPS ON PROPER INSTALLATION

1. Never install any receiver in a closed-in recess, cubbyhole, or closely fitting shelf space over, or close to, a heat duct, or in the path of heated air flow.
2. Avoid conditions of high humidity such as: outdoor patio installations where dew is a factor, near steam radiators where steam leakage is a factor, etc.
3. Avoid placement where draperies may obstruct venting. The customer should also avoid the use of decorative scarves or other coverings that might obstruct ventilation.
4. Wall- and shelf-mounted installations using a commercial mounting kit must follow the factory-approved mounting instructions. A product mounted to a shelf or platform must retain its original feet (or the equivalent thickness in spacers) to provide adequate air flow across the bottom. Bolts or screws used for fasteners must not touch any parts or wiring. Perform leakage tests on customized installations.
5. Caution customers against mounting a product on a sloping shelf or in a tilted position, unless the receiver is properly secured.
6. A product on a roll-about cart should be stable in its mounting to the cart. Caution the customer on the hazards of trying to roll a cart with small casters across thresholds or deep pile carpets.
7. Caution customers against using extension cords. Explain that a forest of extensions, sprouting from a single outlet, can lead to disastrous consequences to home and family.

SERVICING PRECAUTIONS

CAUTION : Before servicing the DVD Recorder covered by this service data and its supplements and addends, read and follow the SAFETY PRECAUTIONS. NOTE : if unforeseen circumstances create conflict between the following servicing precautions and any of the safety precautions in this publication, always follow the safety precautions.

Remembers Safety First:

General Servicing Precautions

1. Always unplug the DVD Recorder AC power cord from the AC power source before:
 - (1) Removing or reinstalling any component, circuit board, module, or any other assembly.
 - (2) Disconnection or reconnecting any internal electrical plug or other electrical connection.
 - (3) Connecting a test substitute in parallel with an electrolytic capacitor.
- Caution :** A wrong part substitution or incorrect polarity installation of electrolytic capacitors may result in an explosion hazard.
2. Do not spray chemicals on or near this DVD Recorder or any of its assemblies.
3. Unless specified otherwise in this service data, clean electrical contacts by applying an appropriate contact cleaning solution to the contacts with a pipe cleaner, cotton-tipped swab, or comparable soft applicator.
Unless specified otherwise in this service data, lubrication of contacts is not required.
4. Do not defeat any plug/socket B+ voltage interlocks with which instruments covered by this service manual might be equipped.
5. Do not apply AC power to this DVD Recorder and/or any of its electrical assemblies unless all solid-state device heat sinks are correctly installed.
6. Always connect test instrument ground lead to the appropriate ground before connecting the test instrument positive lead. Always remove the test instrument ground lead last.

Insulation Checking Procedure

Disconnect the attachment plug from the AC outlet and turn the power on. Connect an insulation resistance meter(500V) to the blades of the attachment plug. The insulation resistance between each blade of the attachment plug and accessible conductive parts (Note 1) should be more than 1M-ohm.

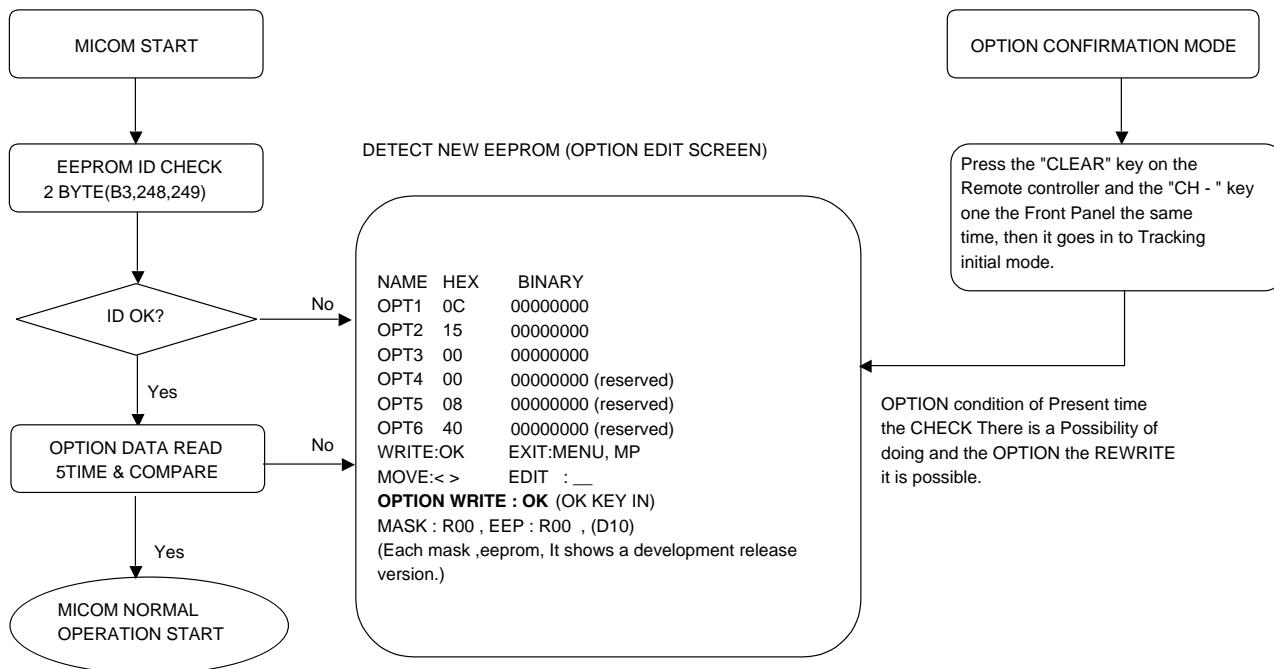
Note 1 : Accessible Conductive Parts including Metal panels, Input terminals, Earphone jacks, etc.

Electrostatically Sensitive (ES) Devices

Some semiconductor (solid state) devices can be damaged easily by static electricity. Such components commonly are called Electrostatically Sensitive (ES) Devices. Examples of typical ES devices are integrated circuits and some field effect transistors and semiconductor chip components. The following techniques should be used to help reduce the incidence of component damage caused by static electricity.

1. Immediately before handling any semiconductor component or semiconductor-equipped assembly, drain off any electrostatic charge on your body by touching a known earth ground. Alternatively, obtain and wear a commercially available discharging wrist strap device, which should be removed for potential shock reasons prior to applying power to the unit under test.
2. After removing an electrical assembly equipped with ES devices, place the assembly on a conductive surface such as aluminum foil, to prevent electrostatic charge buildup or exposure of the assembly.
3. Use only a grounded-tip soldering iron to solder or unsolder ES devices.
4. Use only an anti-static solder removal device. Some solder removal devices not classified as "anti-static" can generate electrical charges sufficient to damage ES devices.
5. Do not use freon-propelled chemicals. These can generate electrical charge sufficient to damage ES devices.
6. Do not remove a replacement ES device from its protective package until immediately before you are ready to install it. (Most replacement ES devices are packaged with leads electrically shorted together by conductive foam, aluminum foil, or comparable conductive material).
7. Immediately before removing the protective material from the leads of a replacement ES device, touch the protective material to the chassis or circuit assembly into which the device will be installed.
- Caution :** Be sure no power is applied to the chassis or circuit, and observe all other safety precautions.
8. Minimize bodily motions when handling unpackaged replacement ES devices. (Normally harmless motion such as the brushing together of your clothes fabric or the lifting of your foot from a carpeted floor can generate static electricity sufficient to damage an ES device.)

SERVICE INFORMATION FOR EEPROM IC SETTING



*** EEPROM INITIAL ***

- SETUP is displayed in the field if pressing the Front ch- & ch+ Key with the Remocon number "clear" key pressed in the status of powering on.
- AUTO SEARCH is done since the initial screen of ACMS is serviced if powering on.

SPECIFICATIONS

• GENERAL

Power requirements	AC 200-240V, 50/60 Hz
Power consumption	44W
Dimensions (approx.)	430 X 92 X 382.5 mm (16.9 x 3.6 x 15 inches) (w x h x d)
Mass (approx.)	6.4 kg (14.1 lbs)
Operating temperature	5°C to 35°C (41°F to 95°F)
Operating humidity	5 % to 90 %
Television system	PAL B/G colour system
Recording format	PAL

• RECORDING

Recording format	DVD VideoRecording, DVD-VIDEO
Recordable discs	DVD-ReRecordable, DVD-Recordable
Recordable time	Approx. 1 hour (HQ mode), 2 hours (SQ mode), 4 hours (LQ mode)
Video recording format	
Sampling frequency	27MHz
Compression format	MPEG 2
Audio recording format	
Sampling frequency	48kHz
Compression format	Dolby Digital

• DVD SPECIFICATIONS

Laser system	Semiconductor laser
Frequency response	DVD (PCM 48 kHz): 8 Hz to 22 kHz, CD: 8 Hz to 20 kHz
Signal-to-noise ratio	More than 100 dB
Harmonic distortion	Less than 0.008%
Dynamic range	More than 95 dB

• INPUTS

AERIAL IN	Aerial input, 75 ohms
VIDEO IN	1.0 Vp-p 75 ohms, sync negative, RCA jack x 2 / SCART
AUDIO IN	0 dBm more than 47 kohms, RCA jack (L, R) x 2 / SCART
DV IN	4 pin (i.LINK/IEEE 1394 standard)

• OUTPUTS

VIDEO OUT	1 Vp-p 75 Ω, sync negative, RCA jack x 1
S-VIDEO OUT	(Y) 1.0 V (p-p), 75 Ω, negative sync, Mini DIN 4-pin x 1
COMPONENT VIDEO OUT	(C) 0.3 V (p-p) 75 Ω
Audio output (digital audio)	(Y) 1.0 V (p-p), 75 Ω, negative sync, RCA jack x 1
Audio output (optical audio)	(Pb)/(Pr) 0.7 V (p-p), 75 Ω, RCA jack x 2
Audio output (analog audio)	0.5 V (p-p), 75 Ω, RCA jack x 1
	5 V (p-p), 75 Ω, Optical connector x 1
	2.0 Vrms (1 KHz, 0 dB), 600 Ω, RCA jack (L, R) x 1 / SCART

• ACCESSORY:

Video cable	1	Audio cable	1
RF Coaxial Cable.....	1	Blank DVD-R disc	2
Remote control	1	Batteries	2

SECTION 2

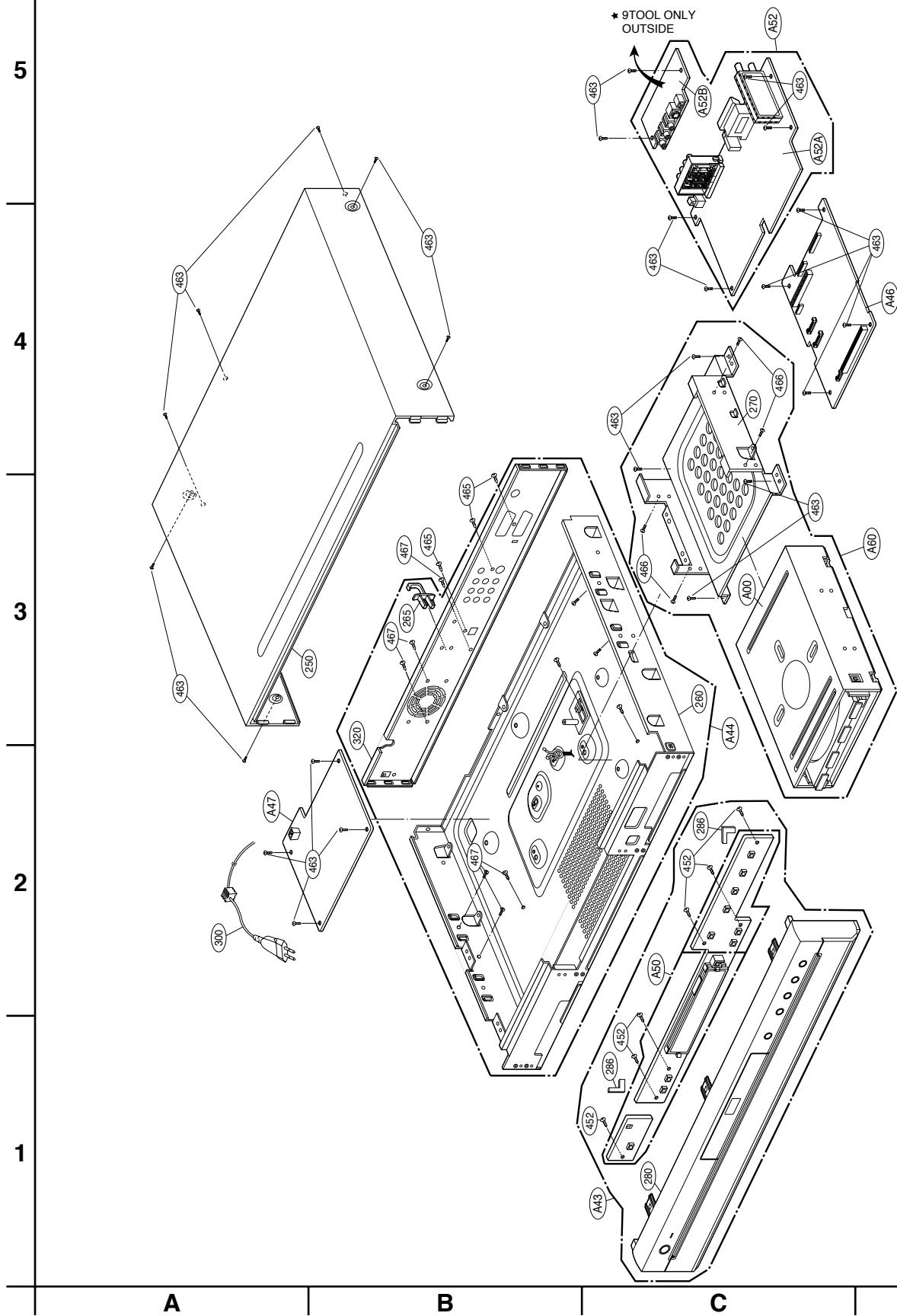
CABINET & MAIN CHASSIS

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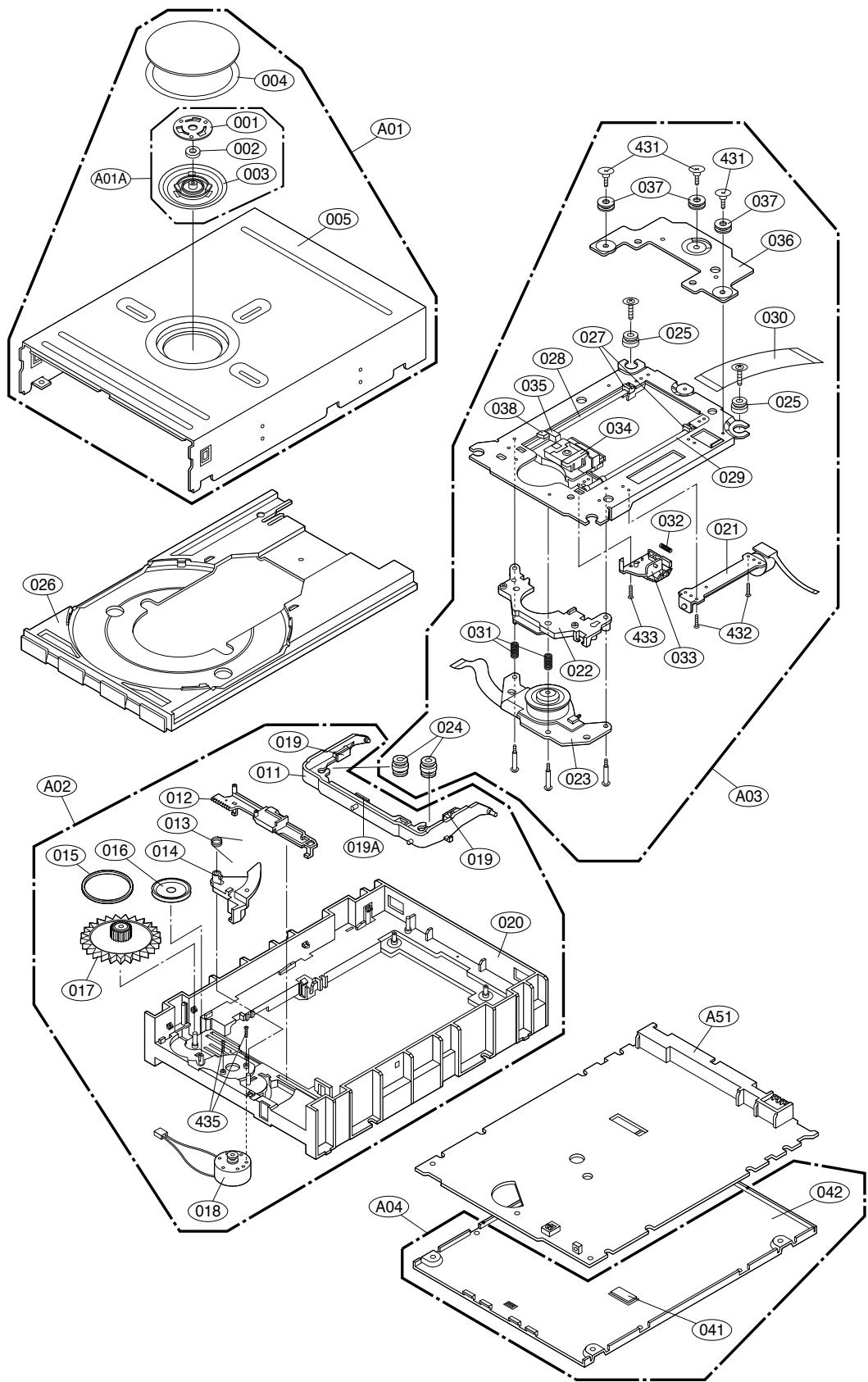
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EXPLODED VIEWS

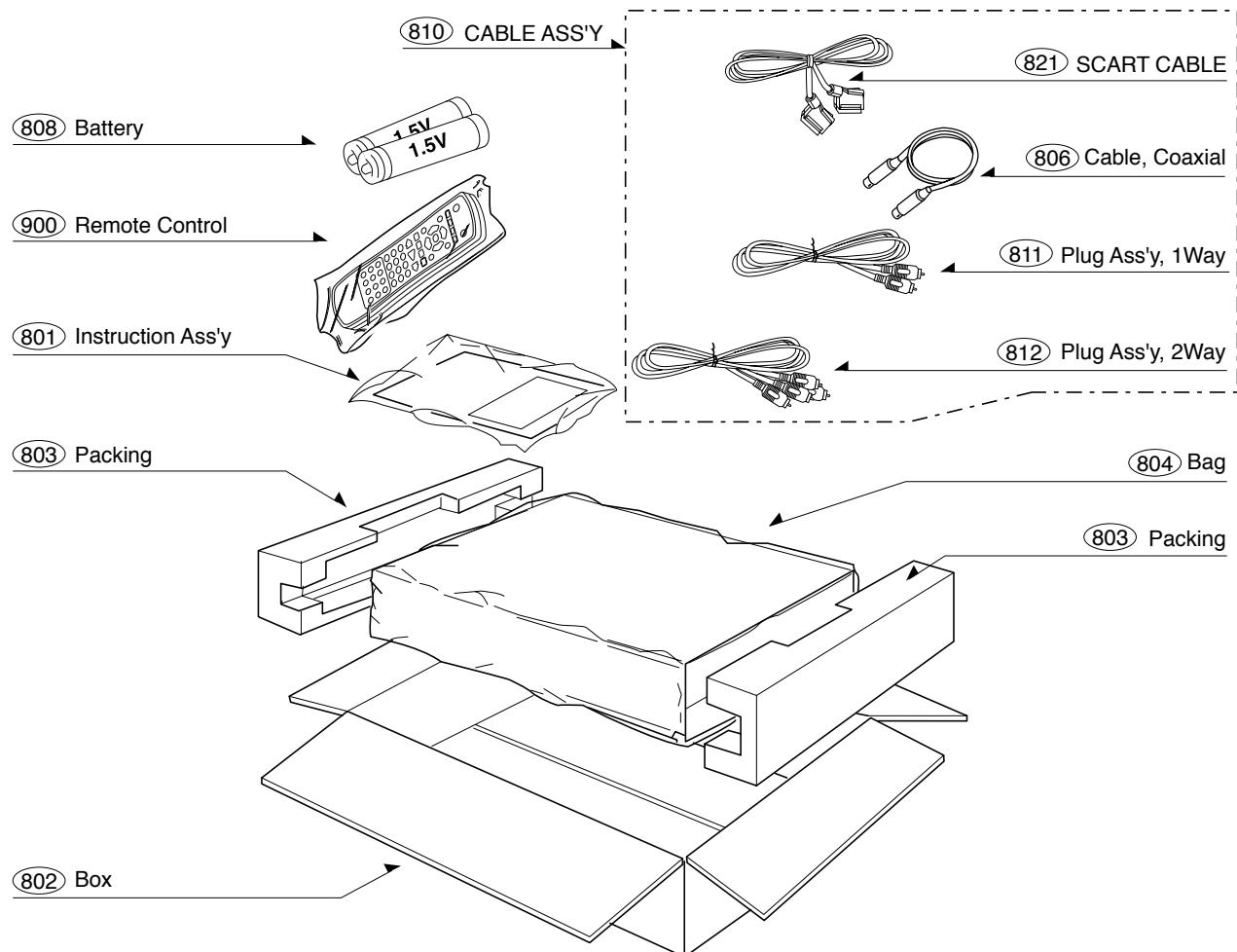
1. Cabinet and Main Frame Section



2. DECK MECHANISM SECTION(RL-01A)



3. Packing Accessory Section



SECTION 3

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RL-01A LOADER PART

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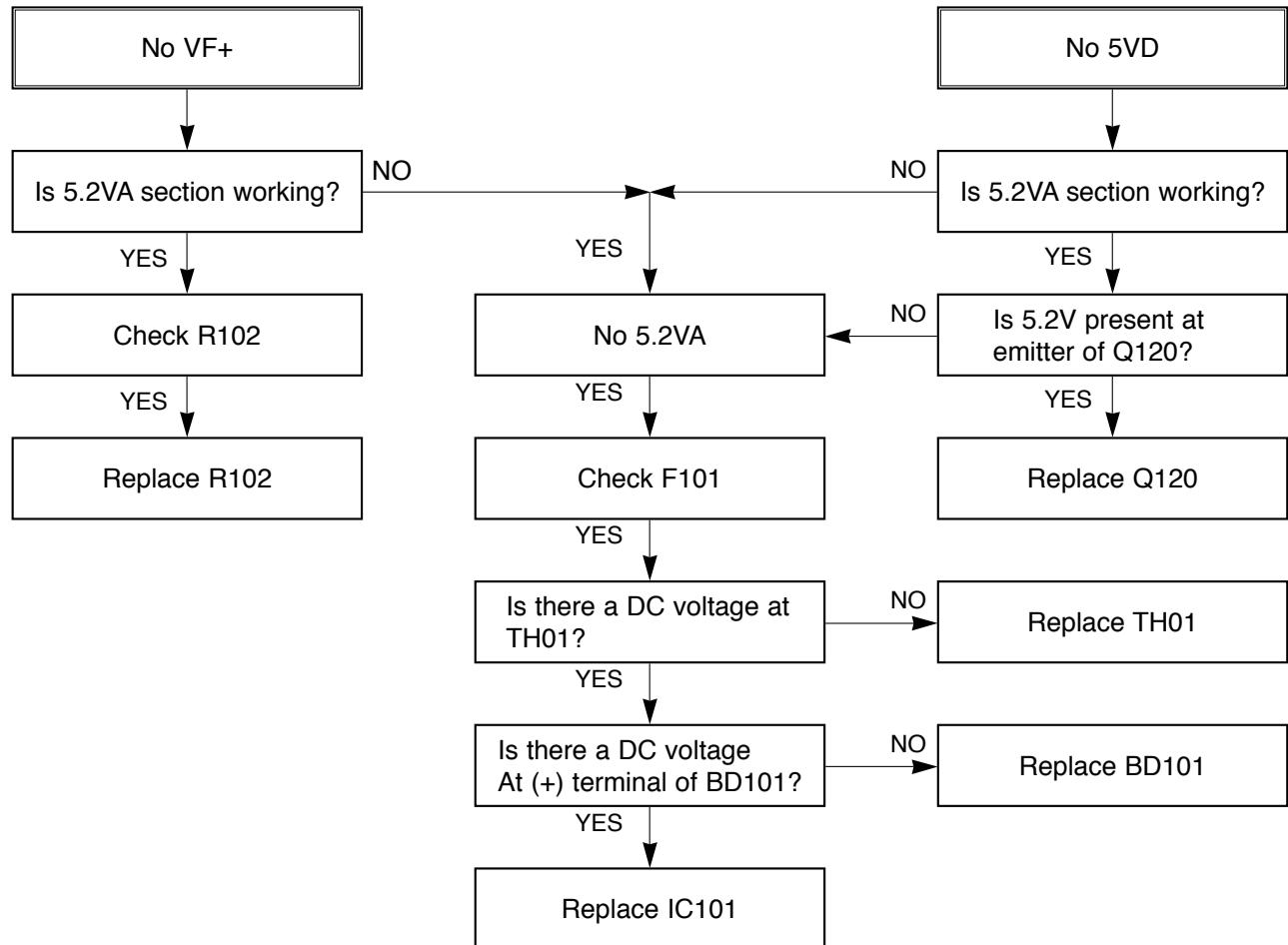
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VDR PART

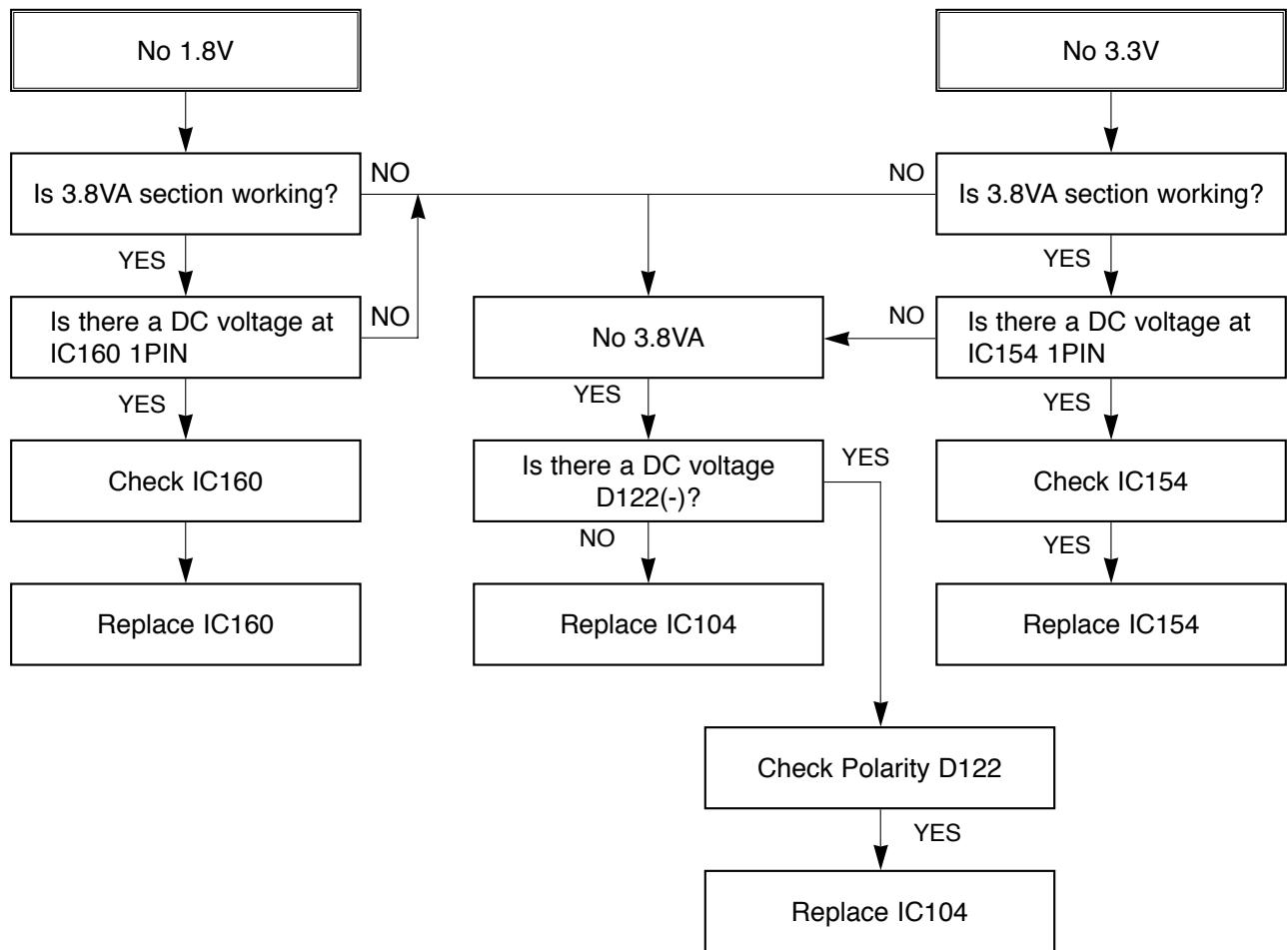
ELECTRICAL TROUBLESHOOTING GUIDE

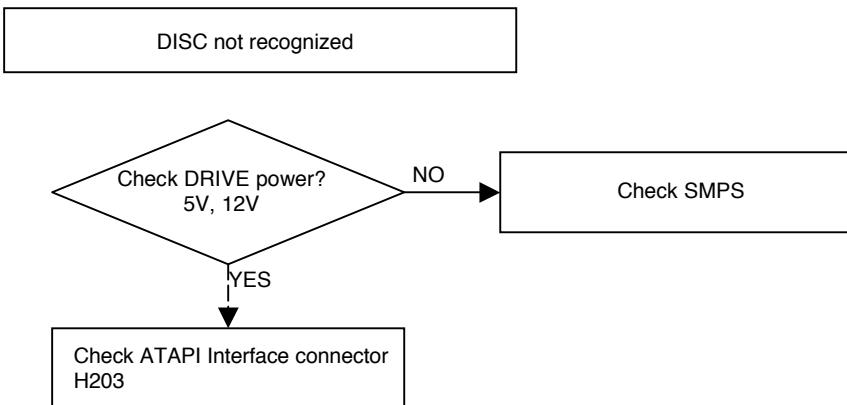
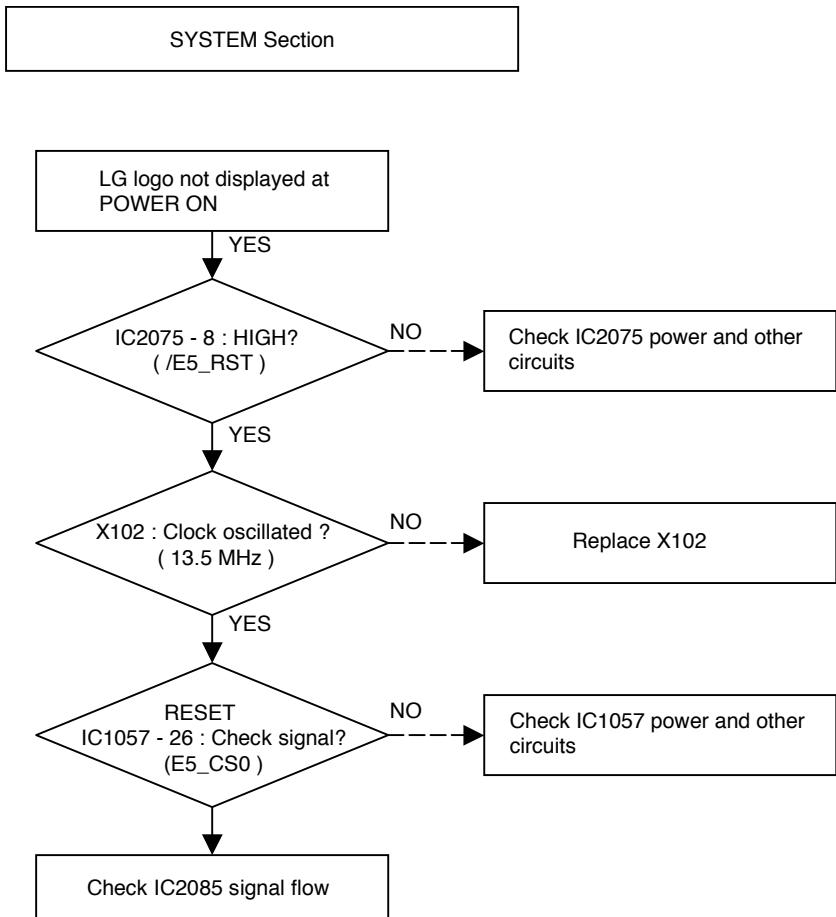
Power Section

(Power (SMPS) Circuit (Part 1))

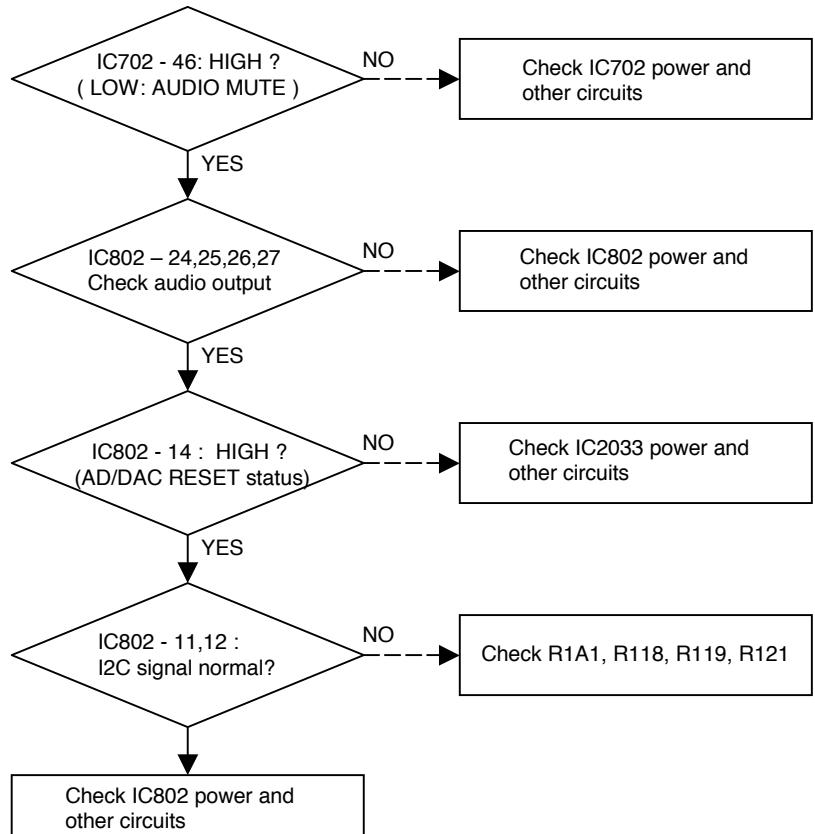


(Power (SMPS) Circuit (Part 2))

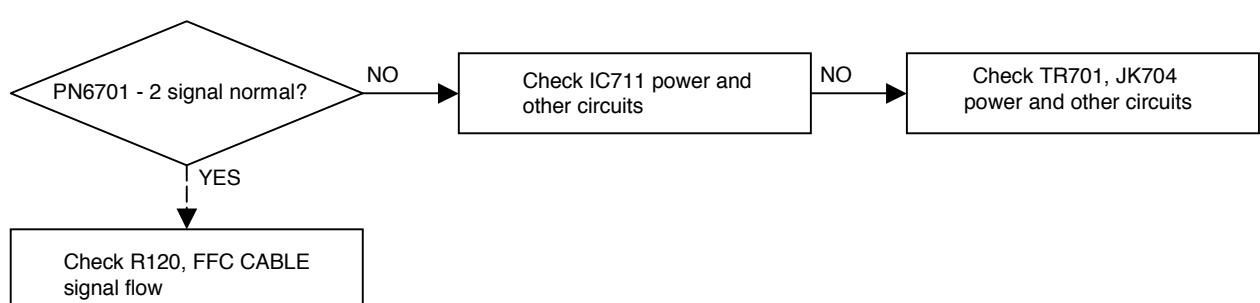


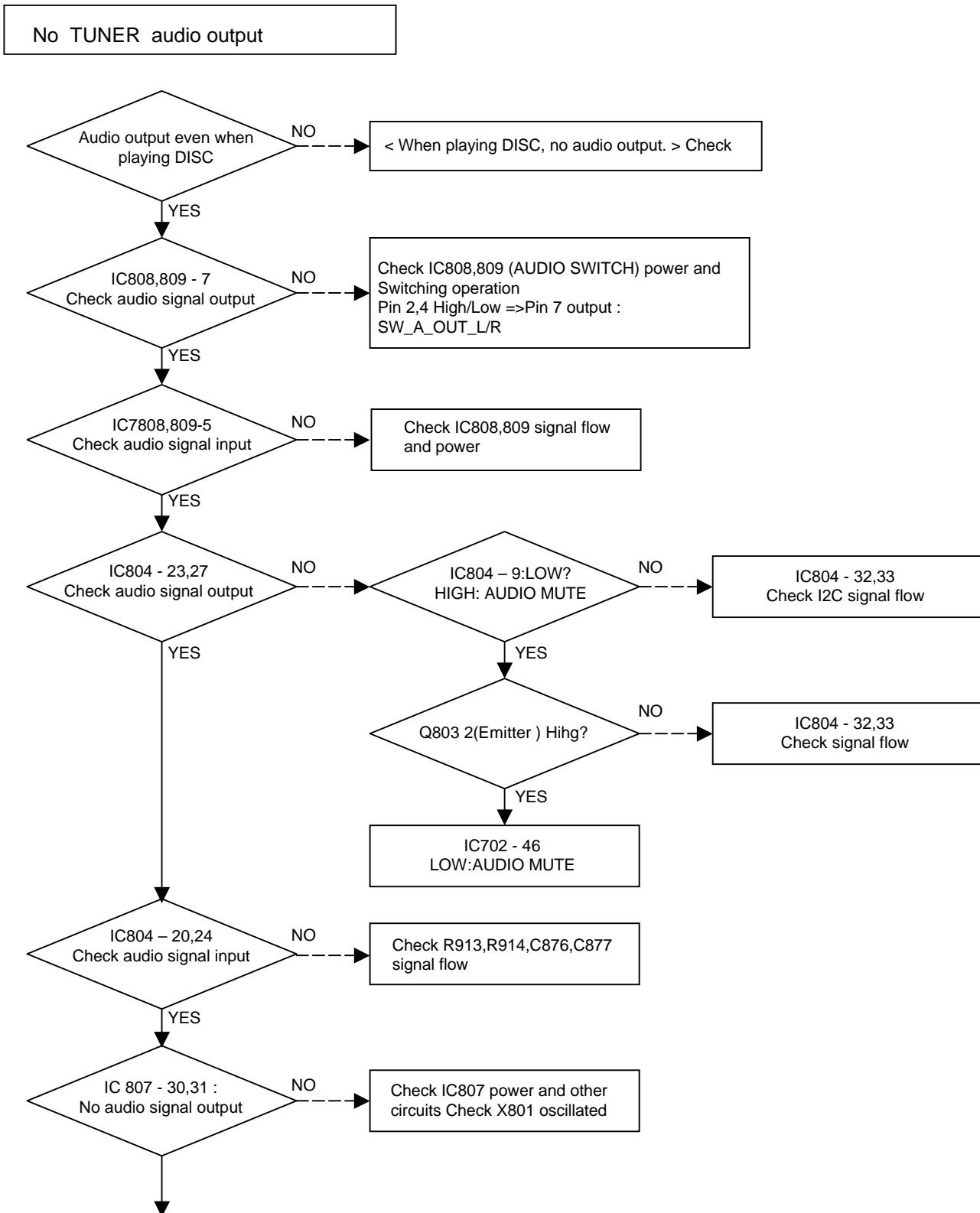


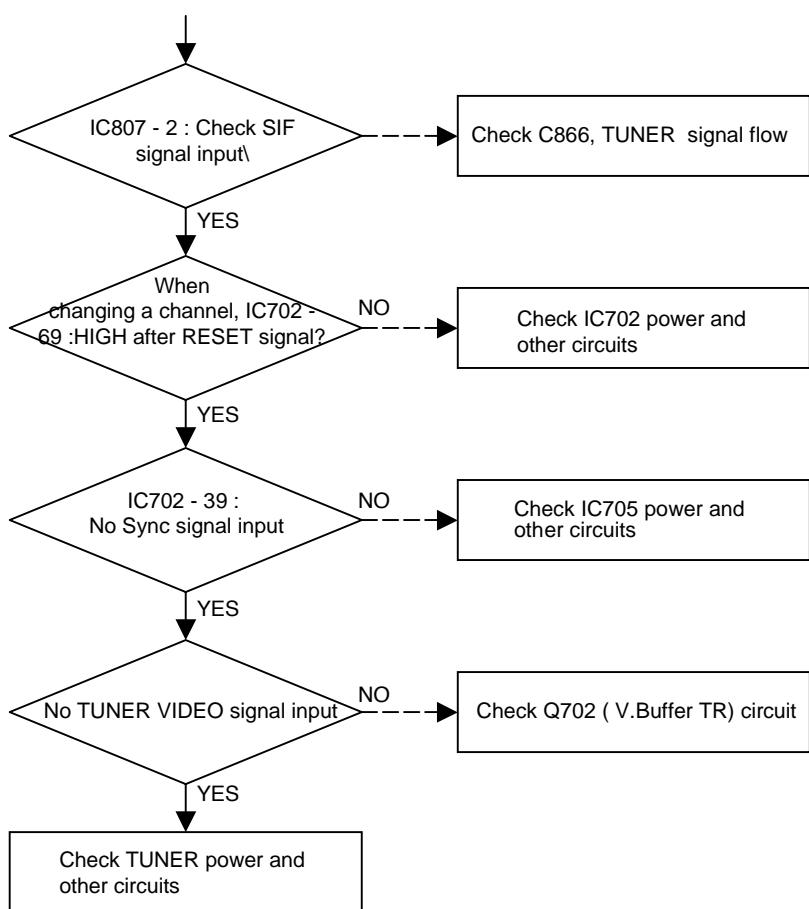
When playing DISC, no audio output

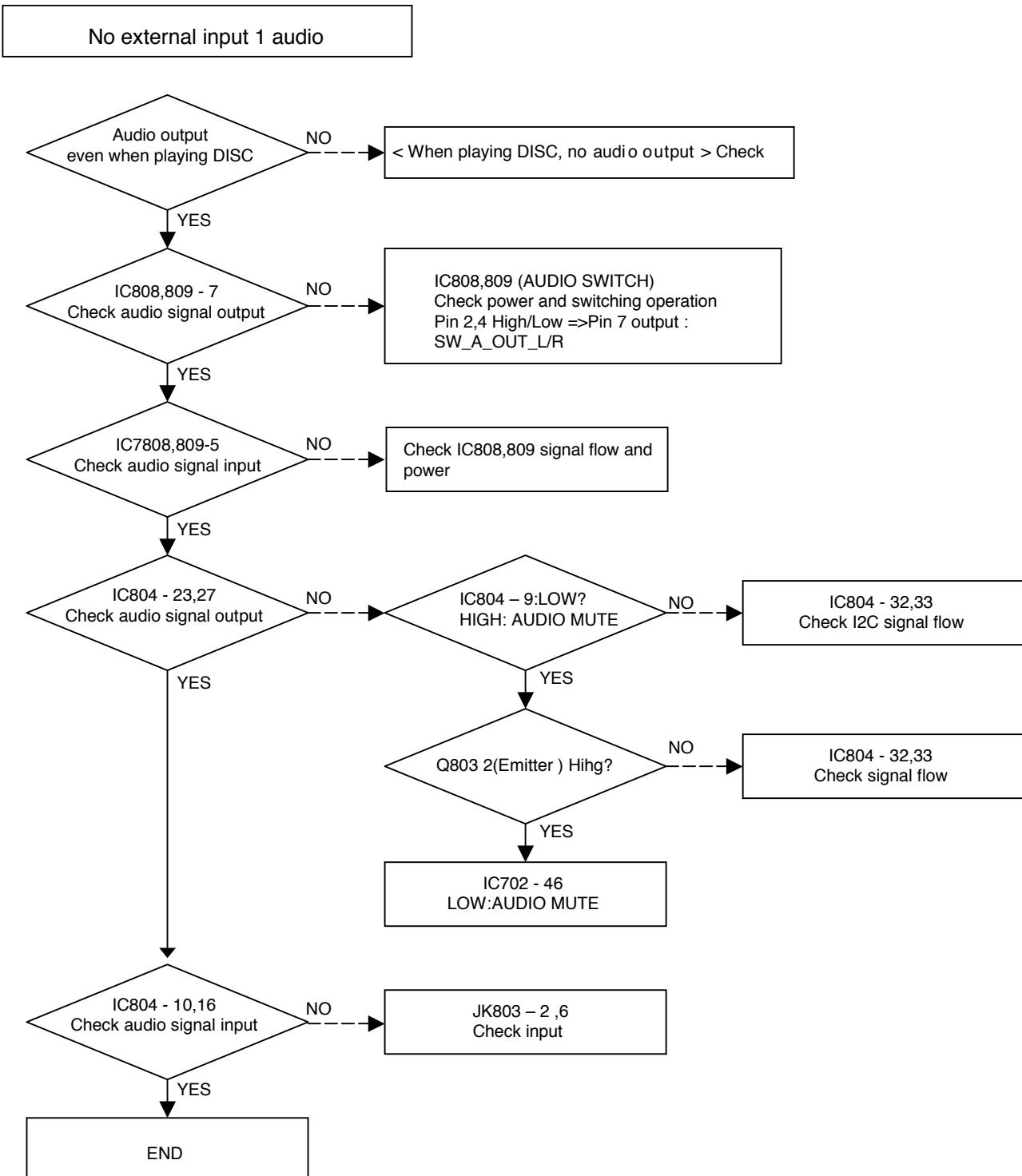


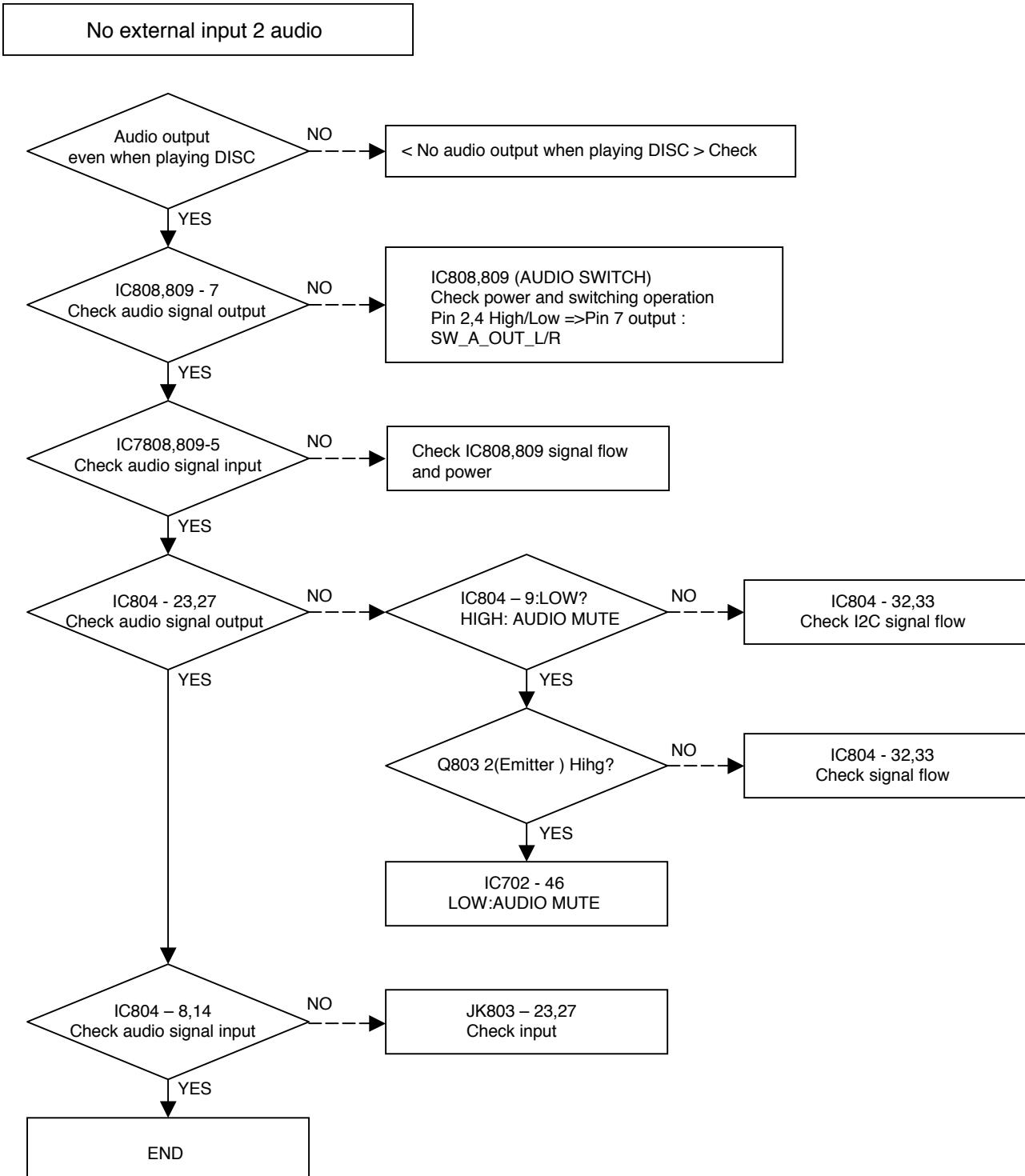
No OPTICAL / DIGITAL output

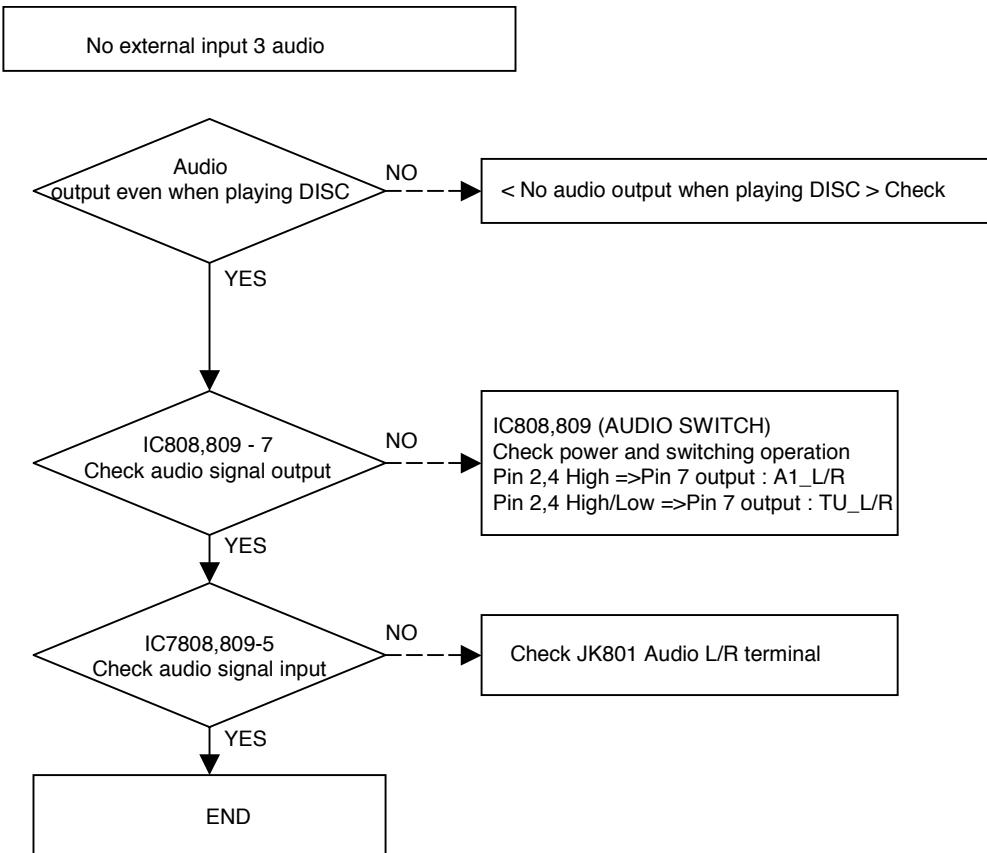


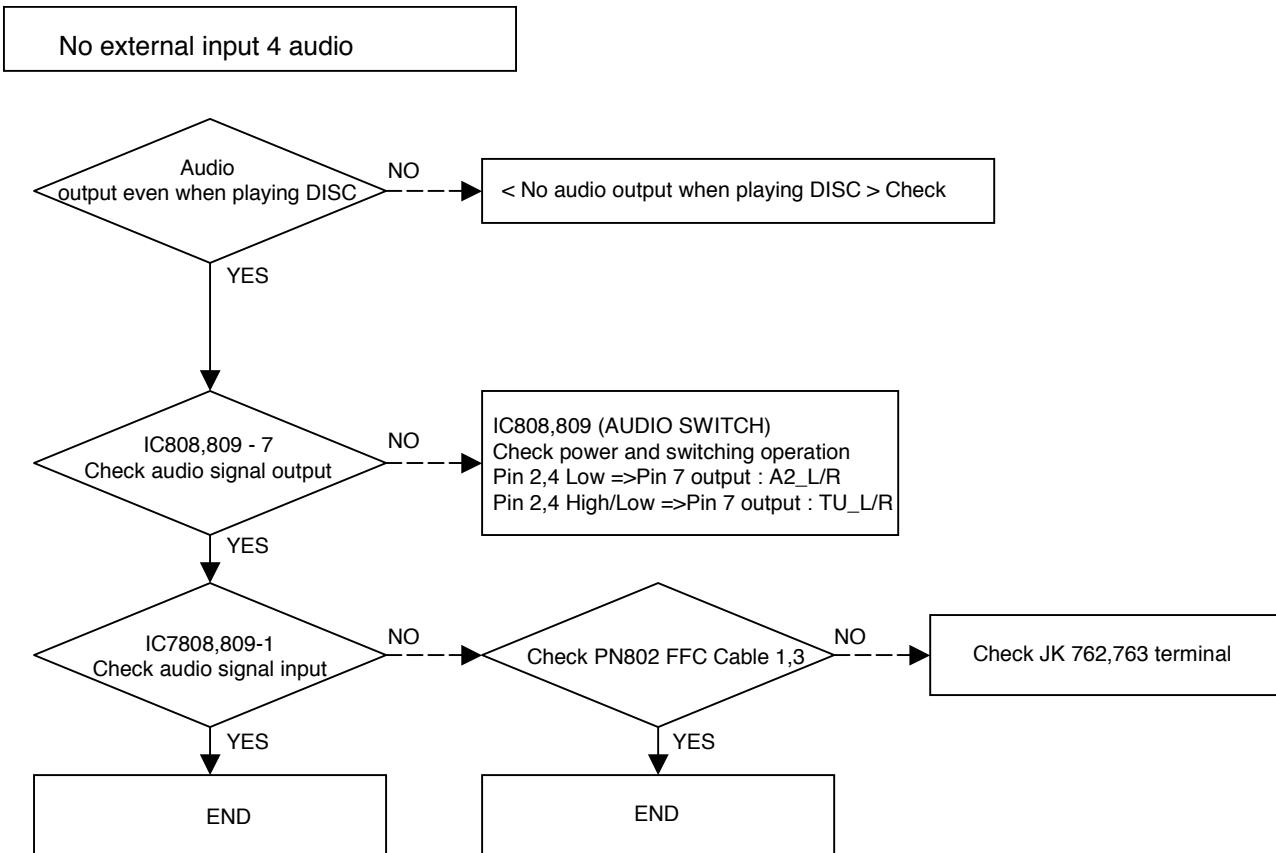


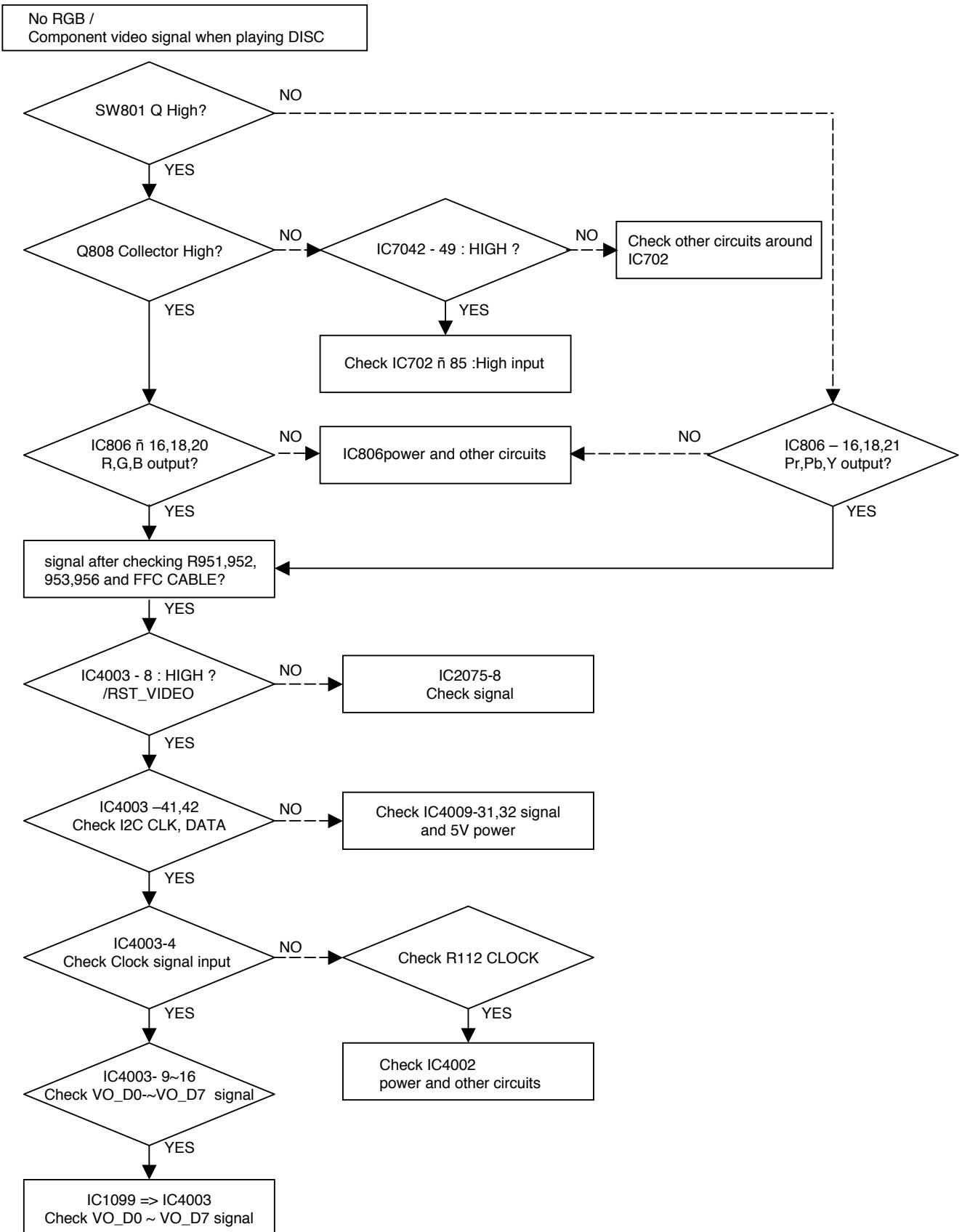


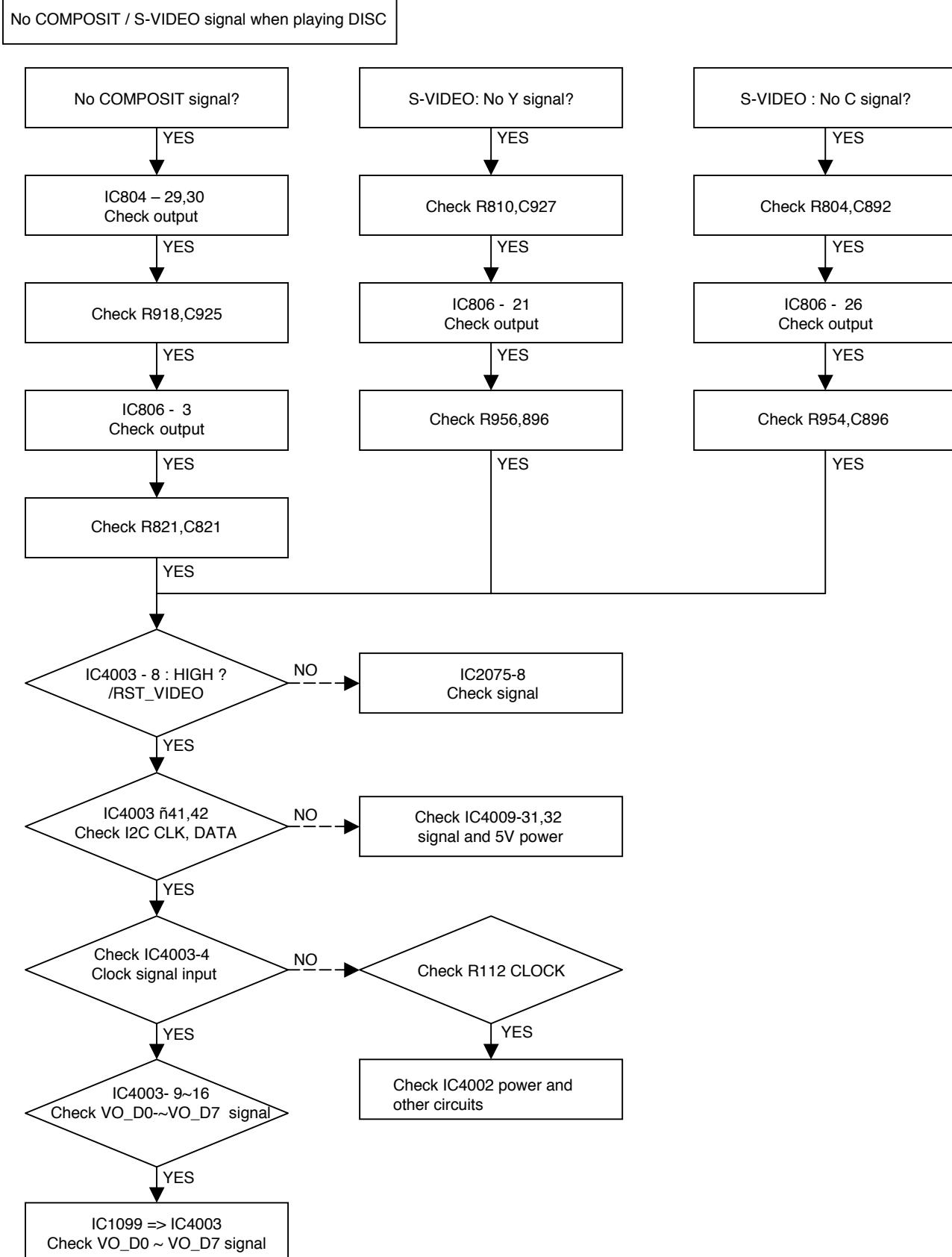


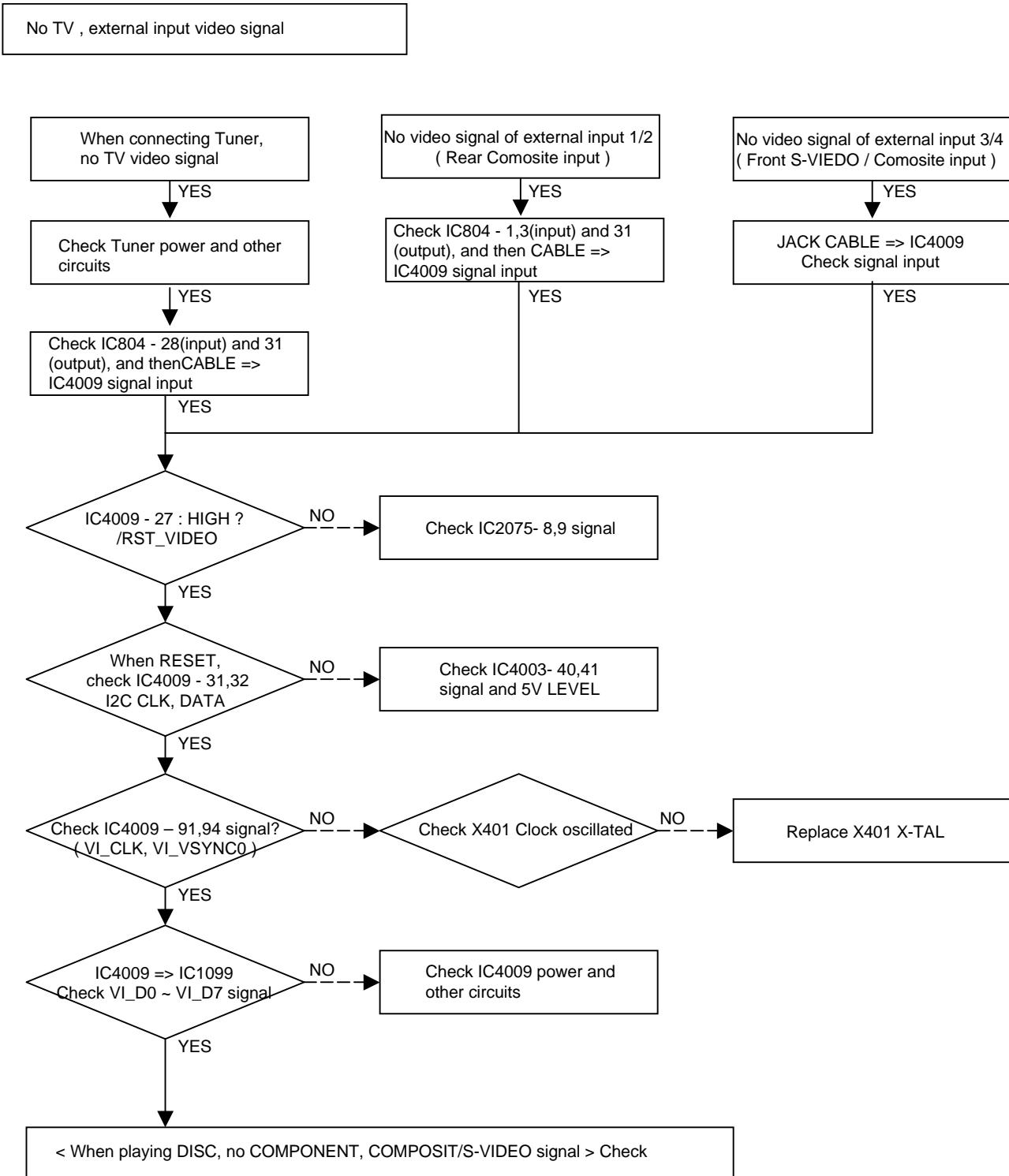


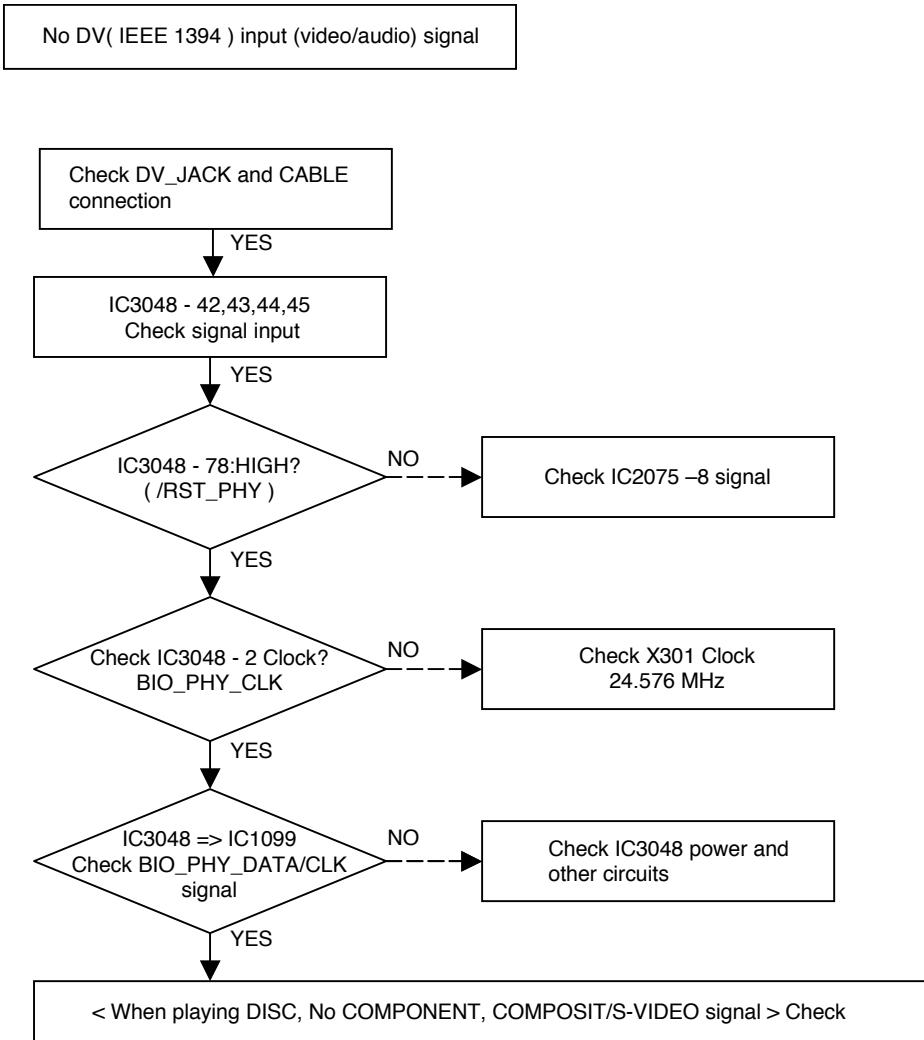






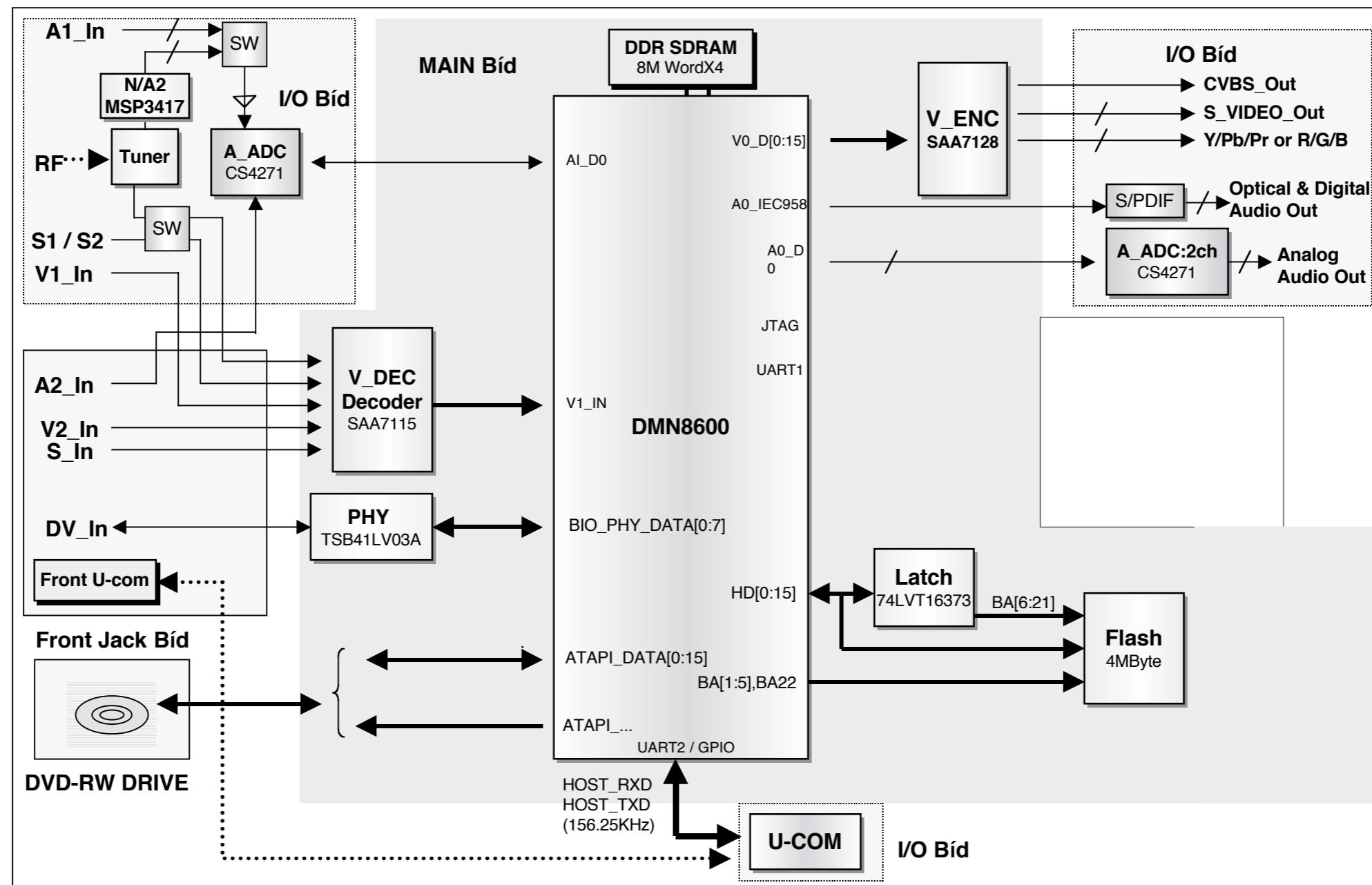




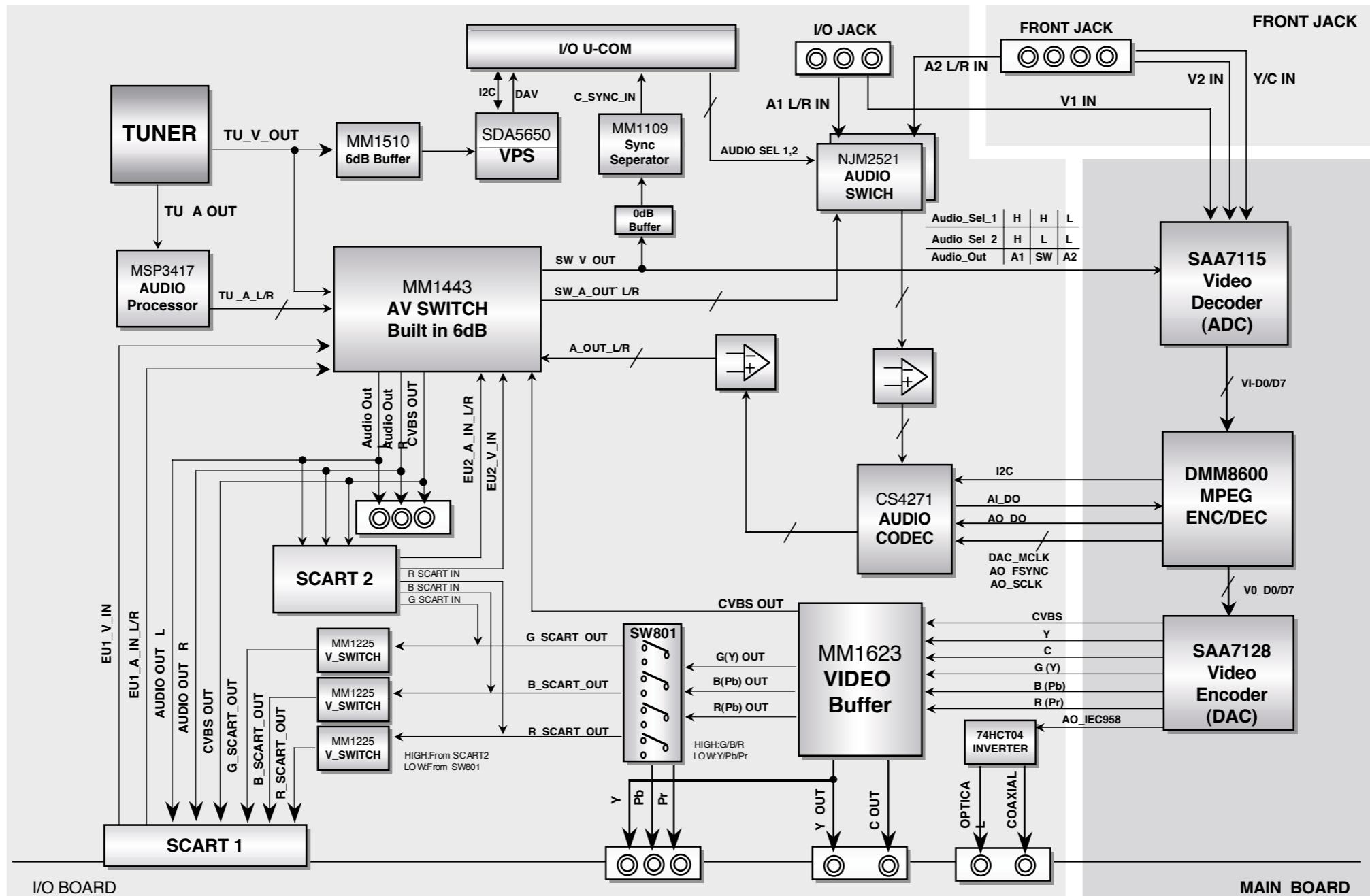


BLOCK DIAGRAMS

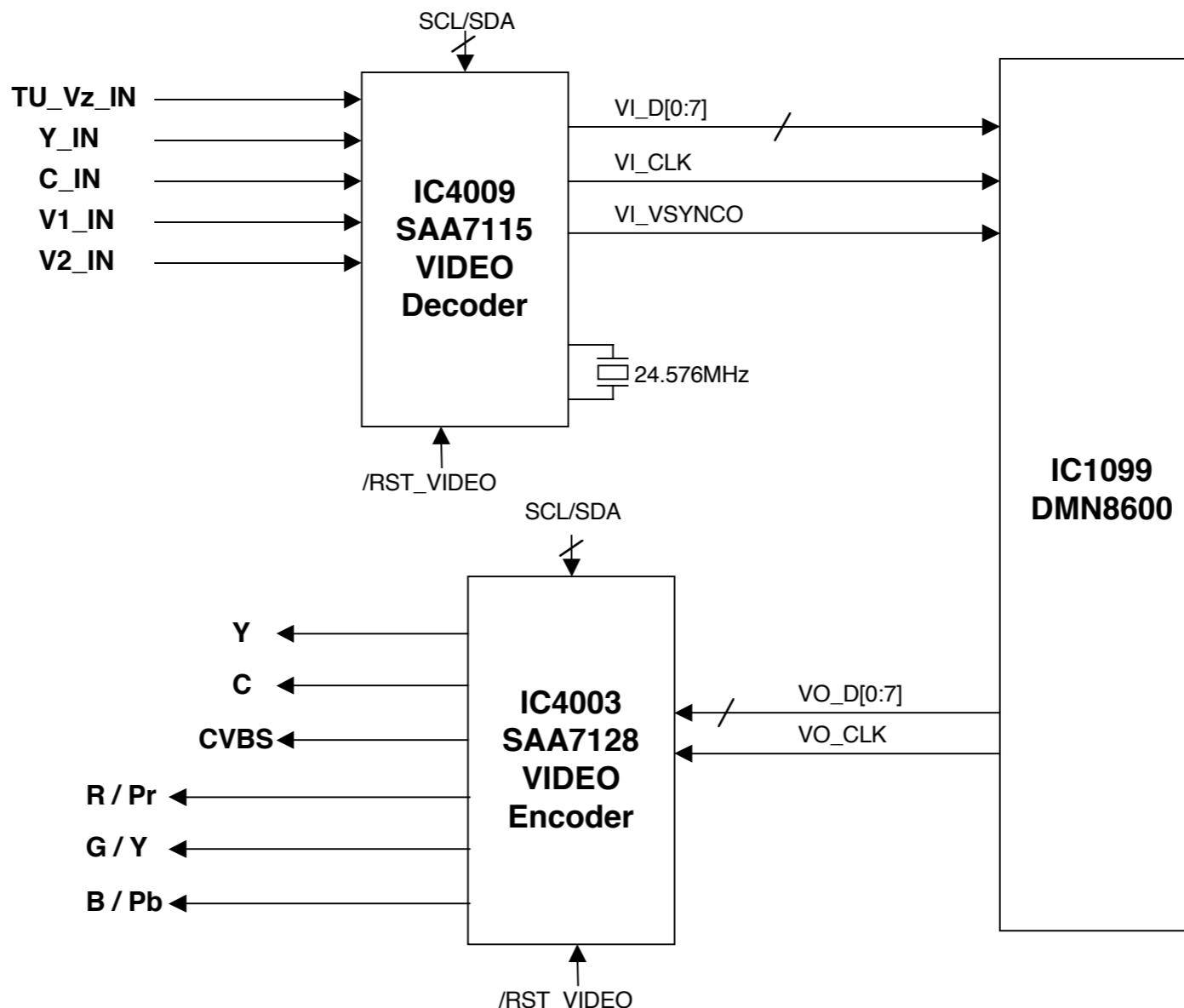
1. LSI Overall Block Diagram



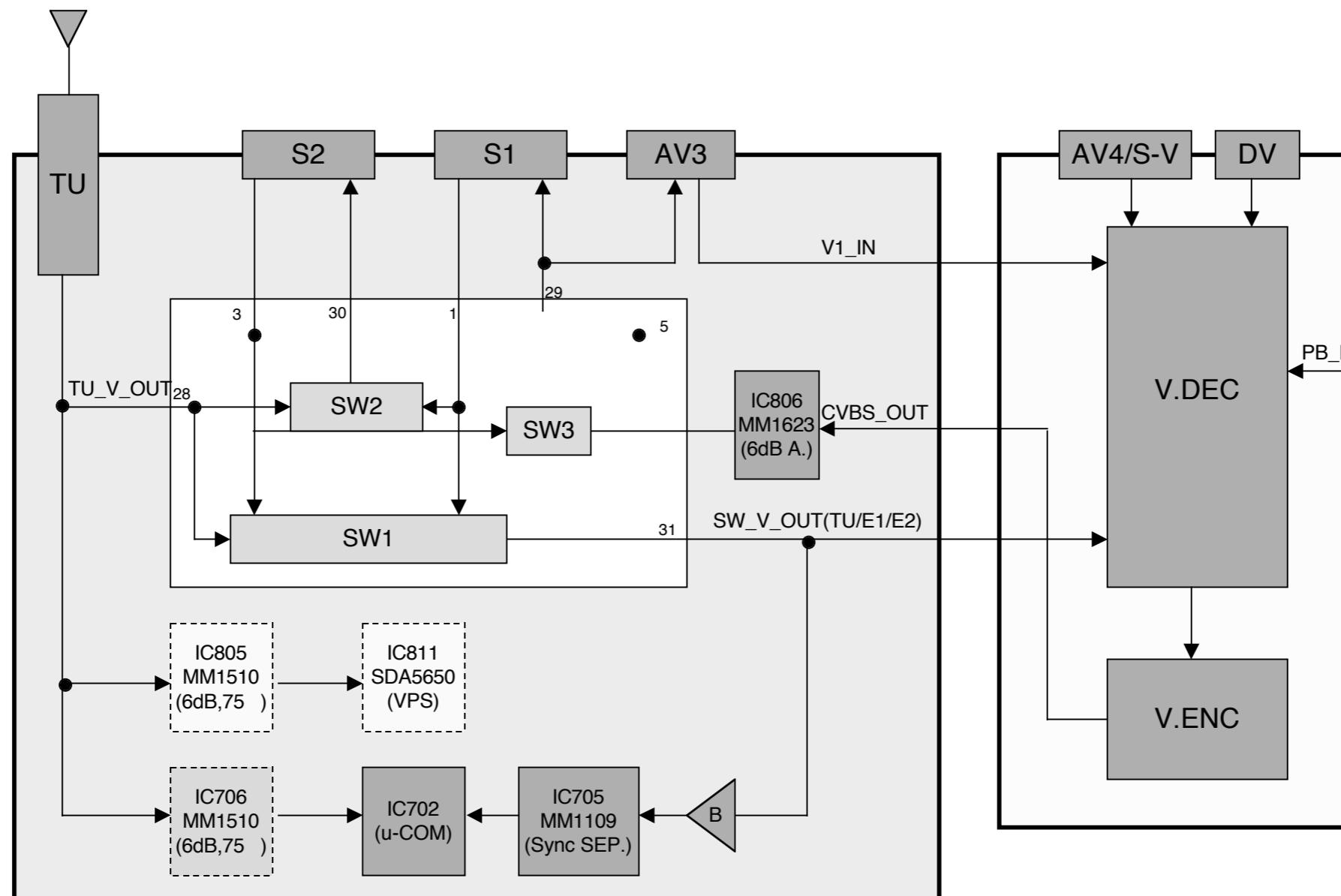
2. In/Out Block Diagram



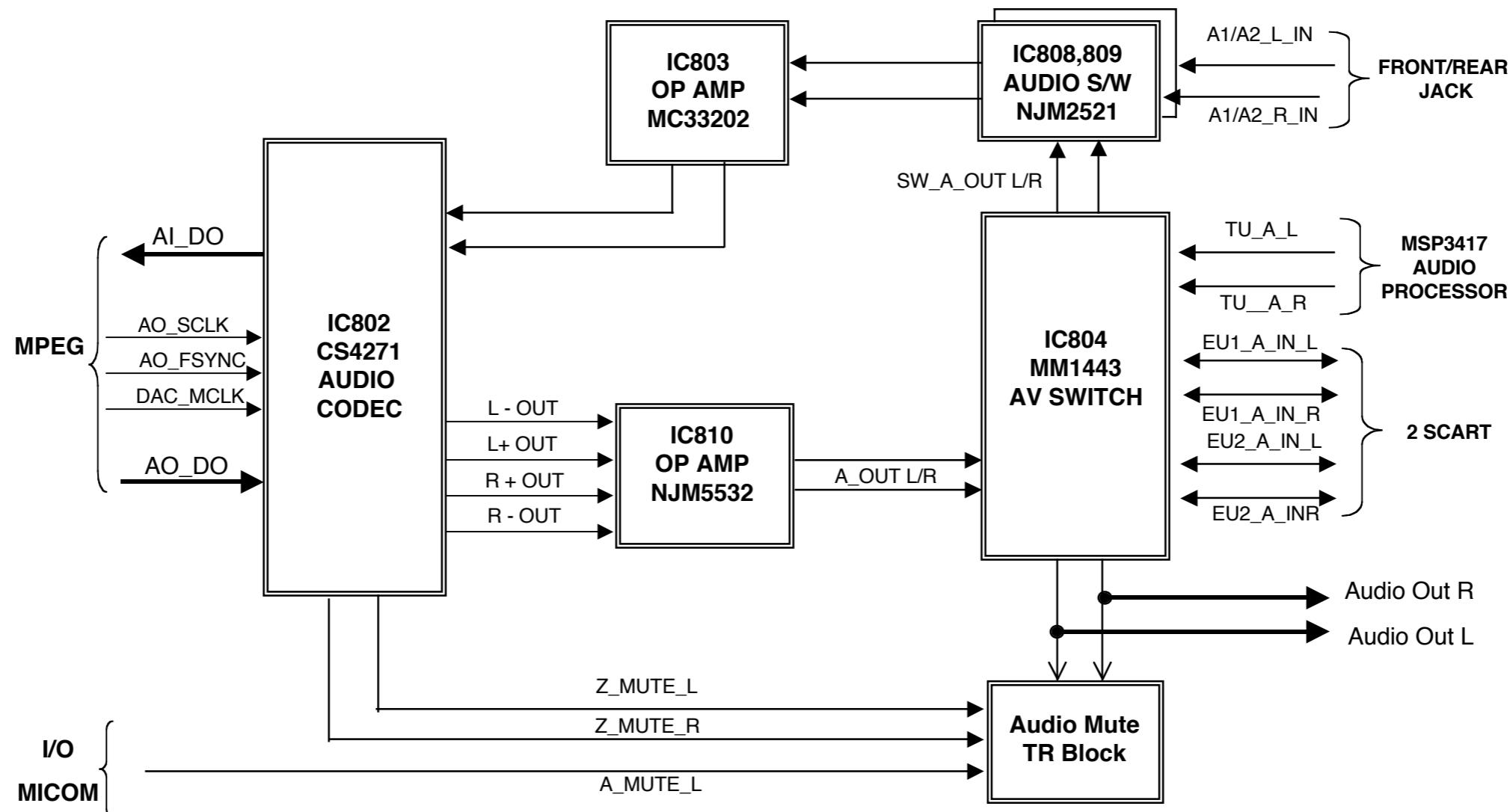
3. Video In/Out Block Diagram



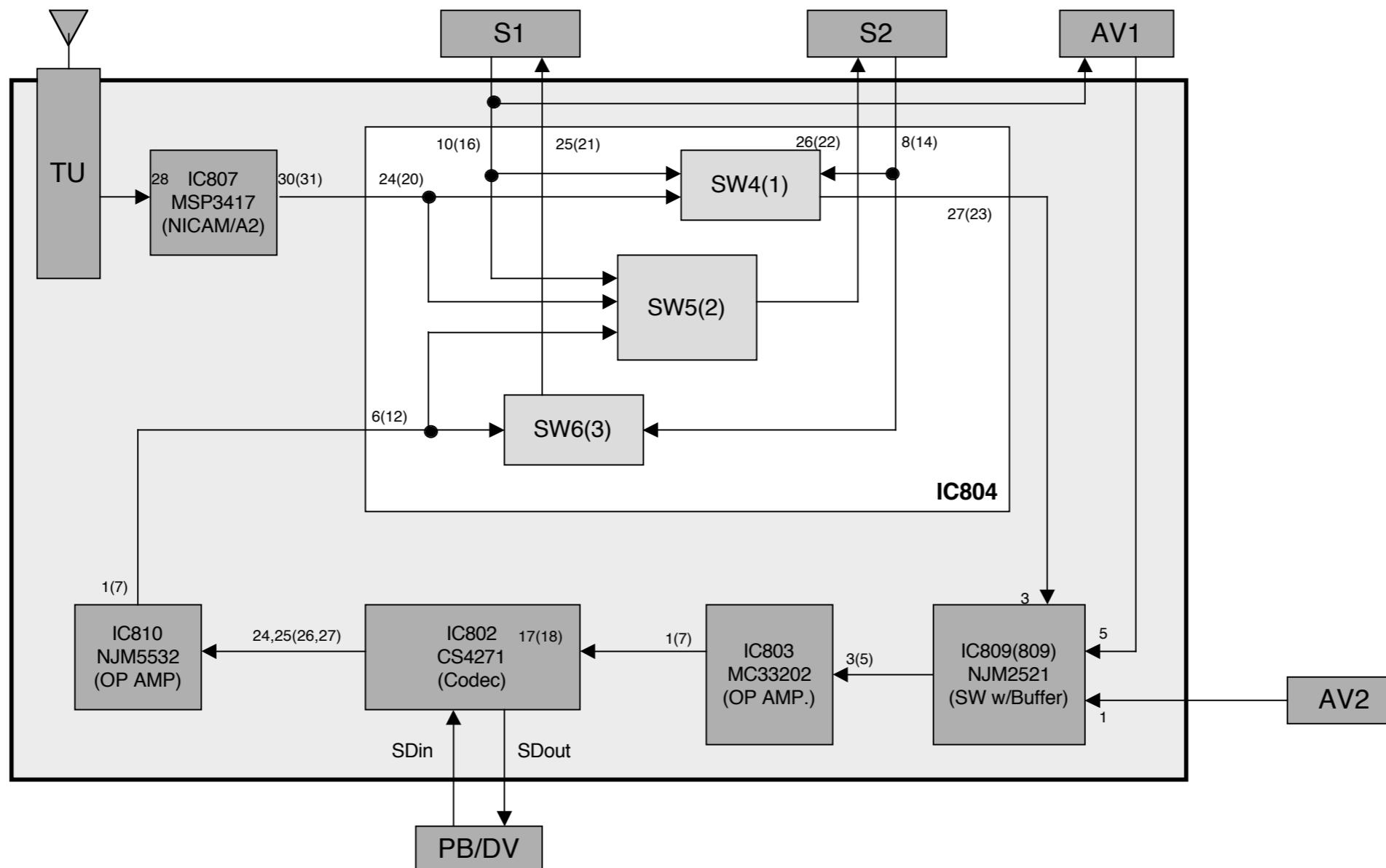
4. Video SW Path Block Diagram



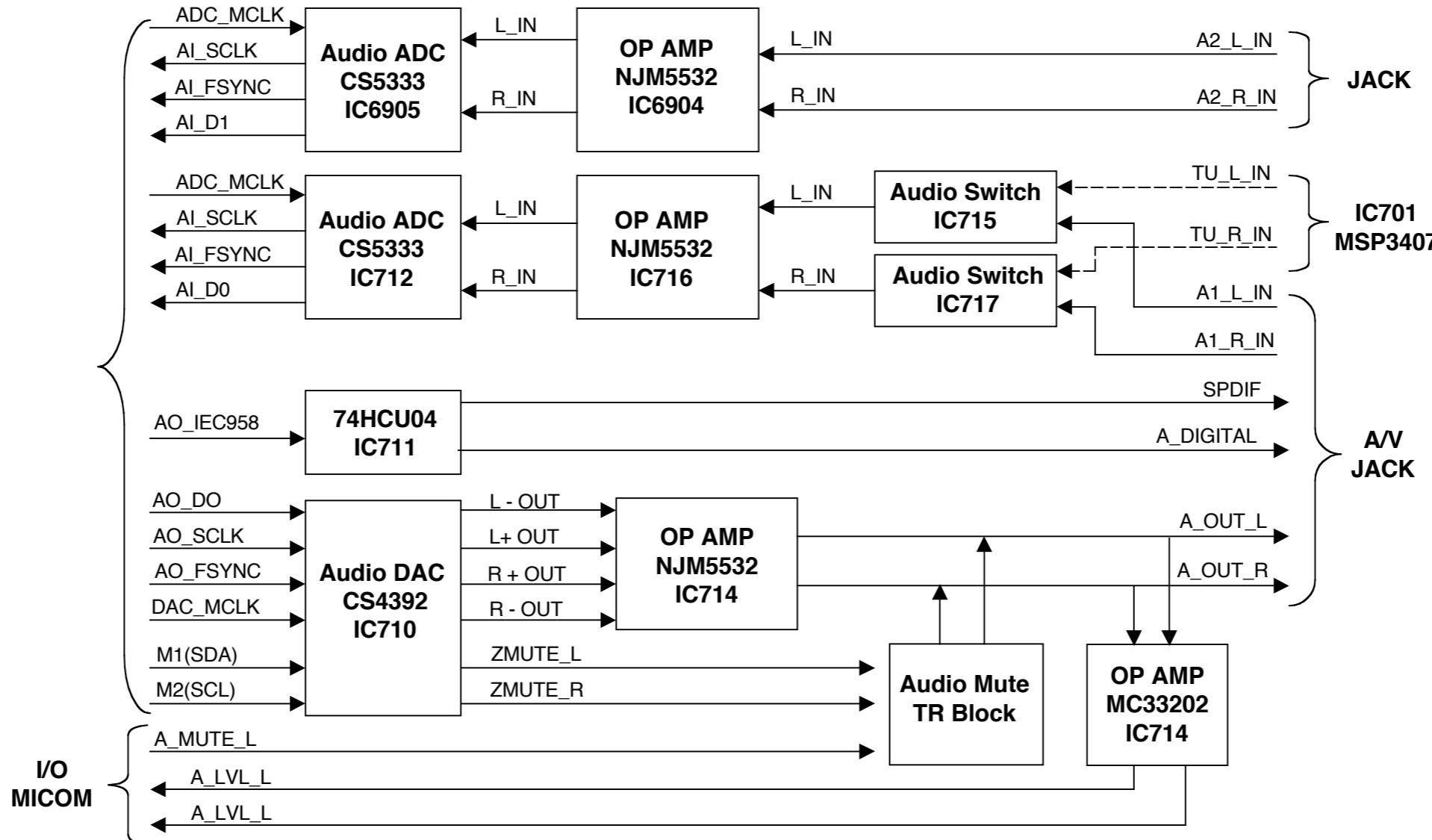
5. Audio Block Diagram



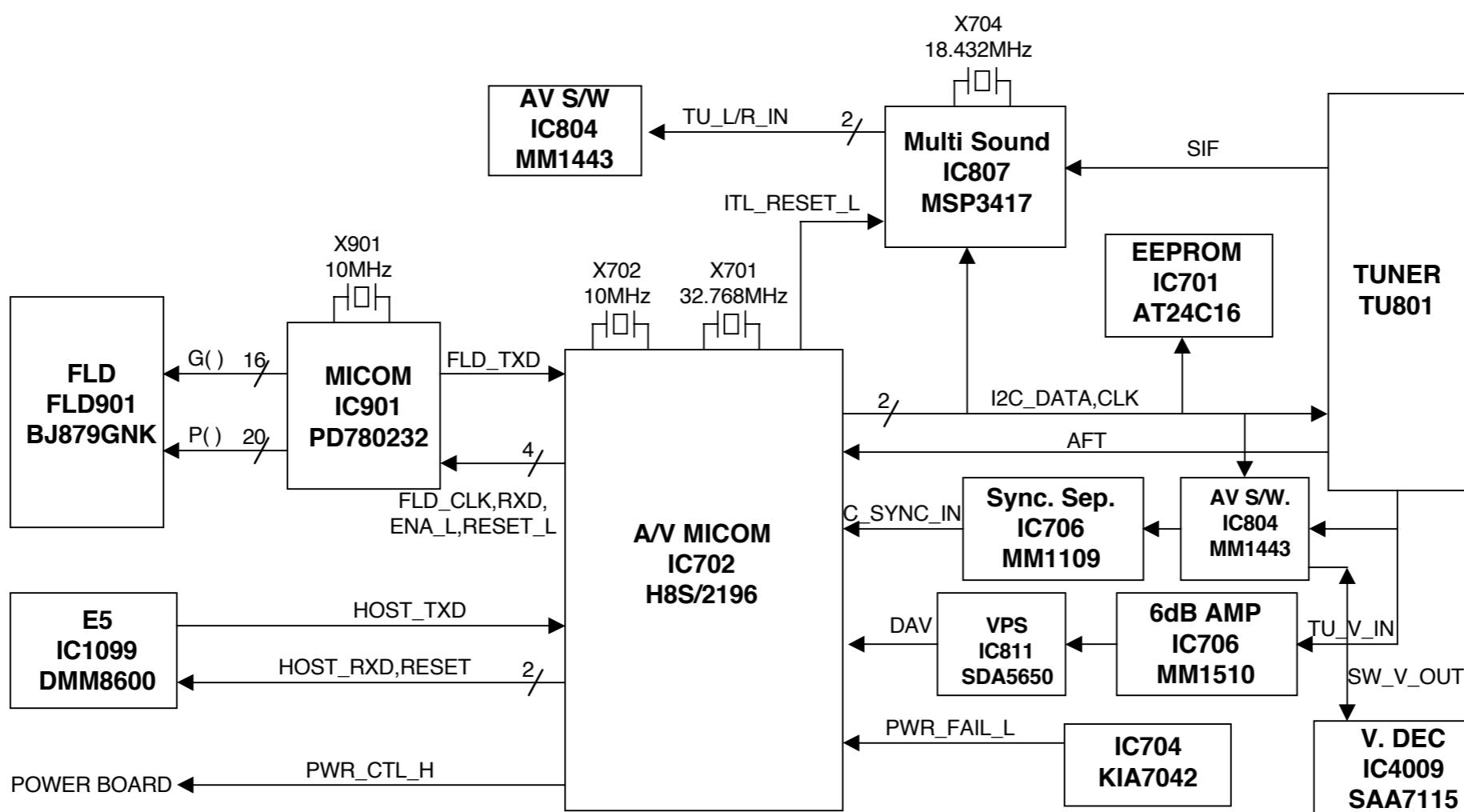
6. Audio SW Path Block Diagram



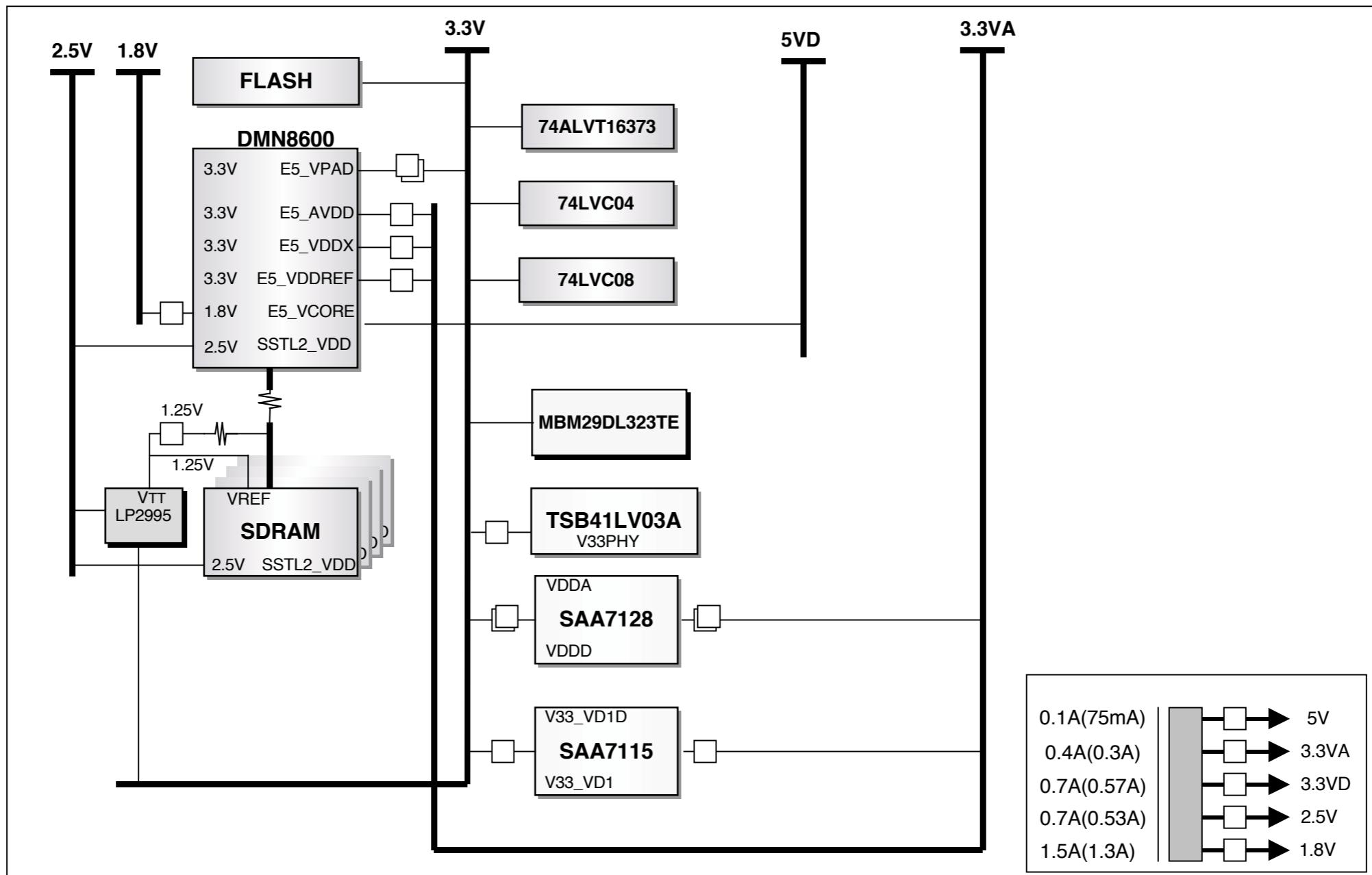
7. Audio In/Out Block Diagram



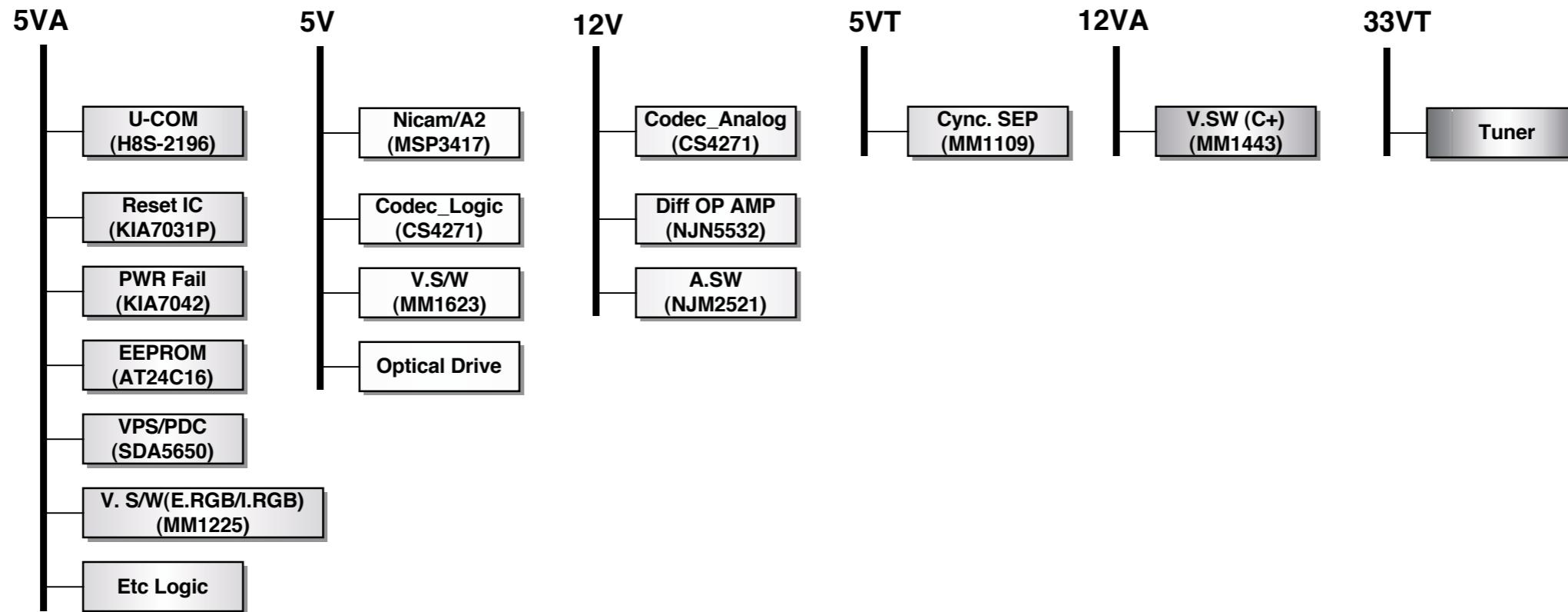
8. FLD/μ-COM/Tuner Block Diagram



9. Power : Main Board Block Diagram



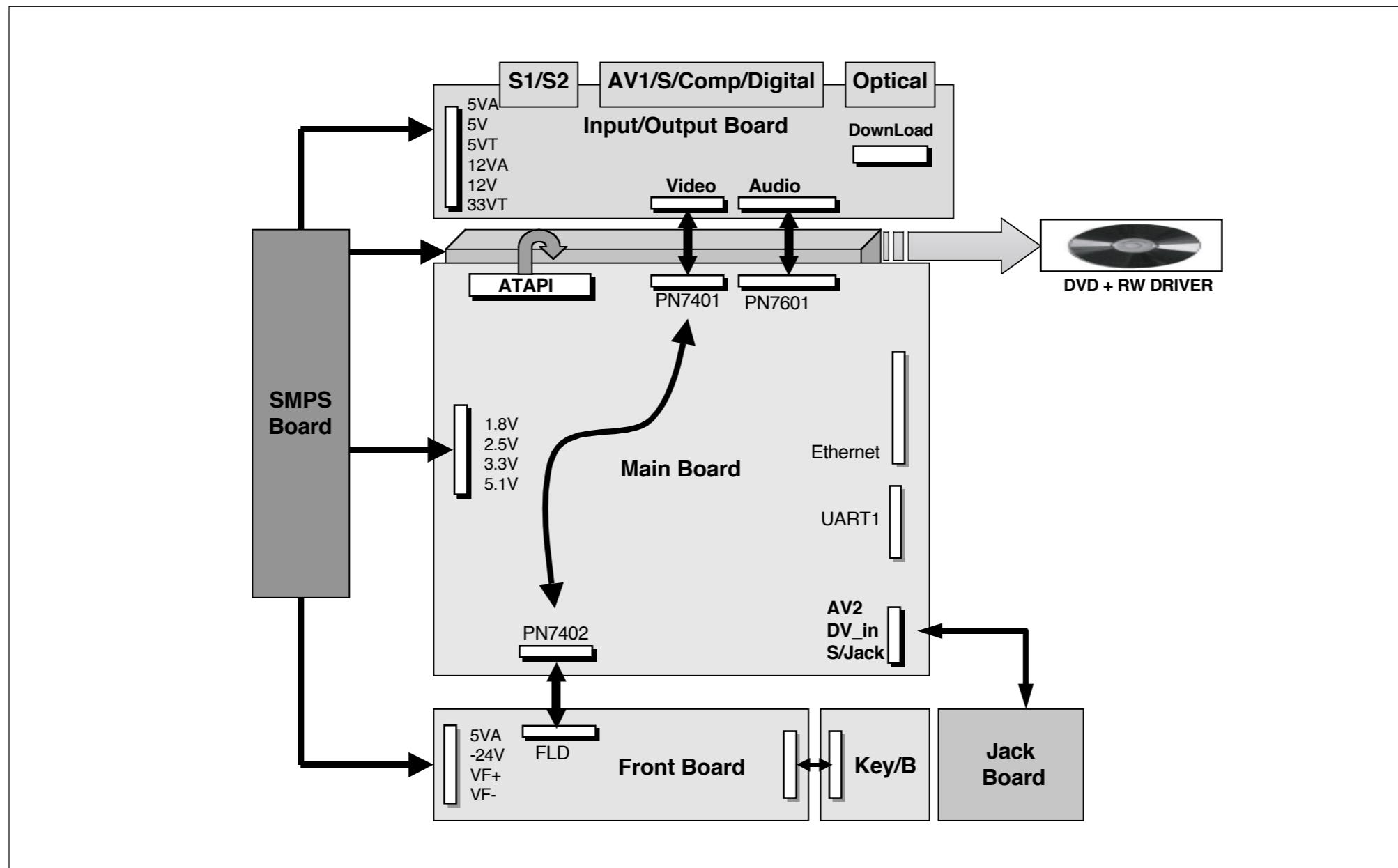
10. Power : I/O Board Block Diagram



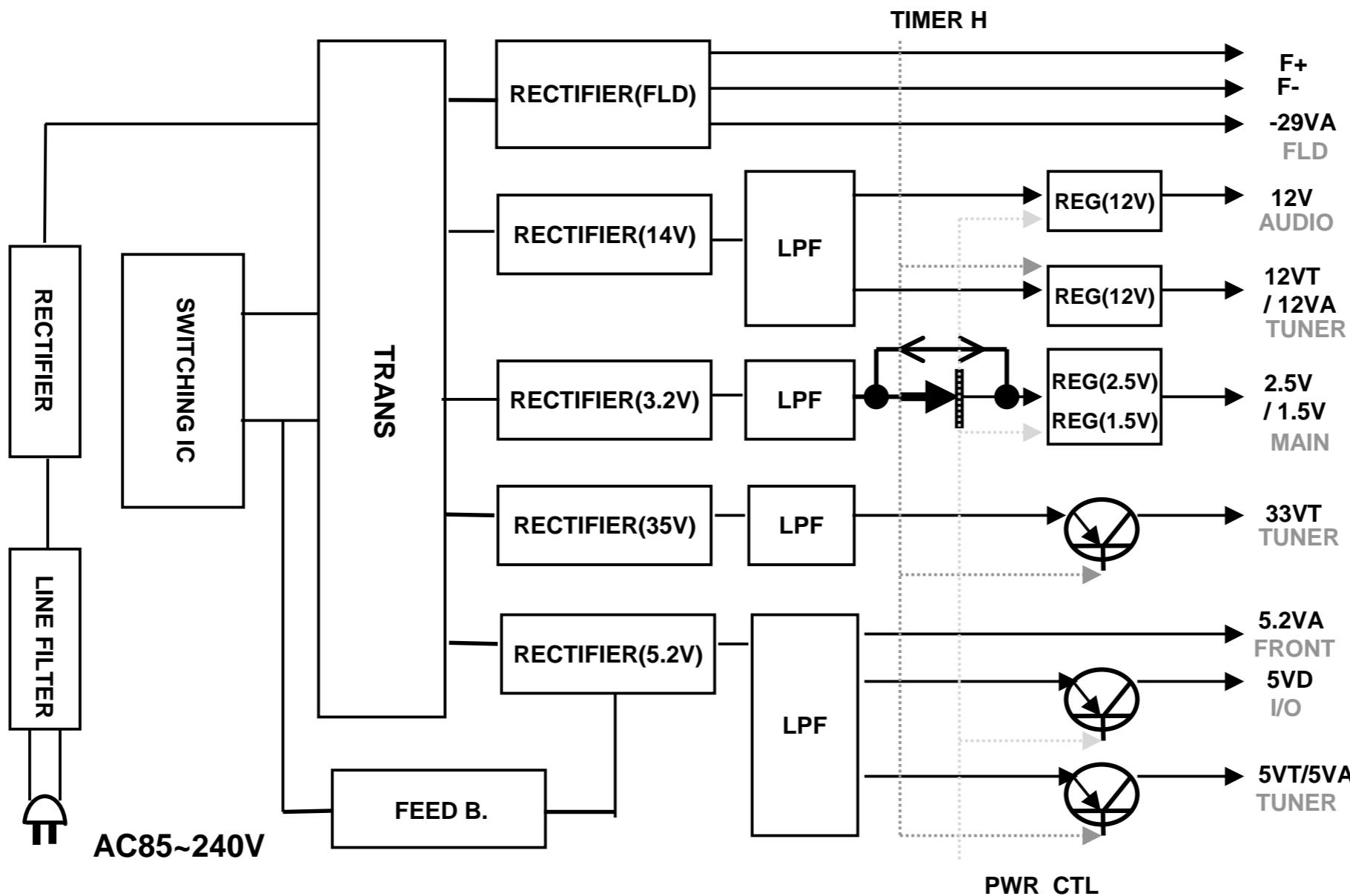
I/O Ucom PWR CTL SIGNAL

PWR_CTL_H	5V, 12V CONTROL
TIMER_H	5VT, 33VT

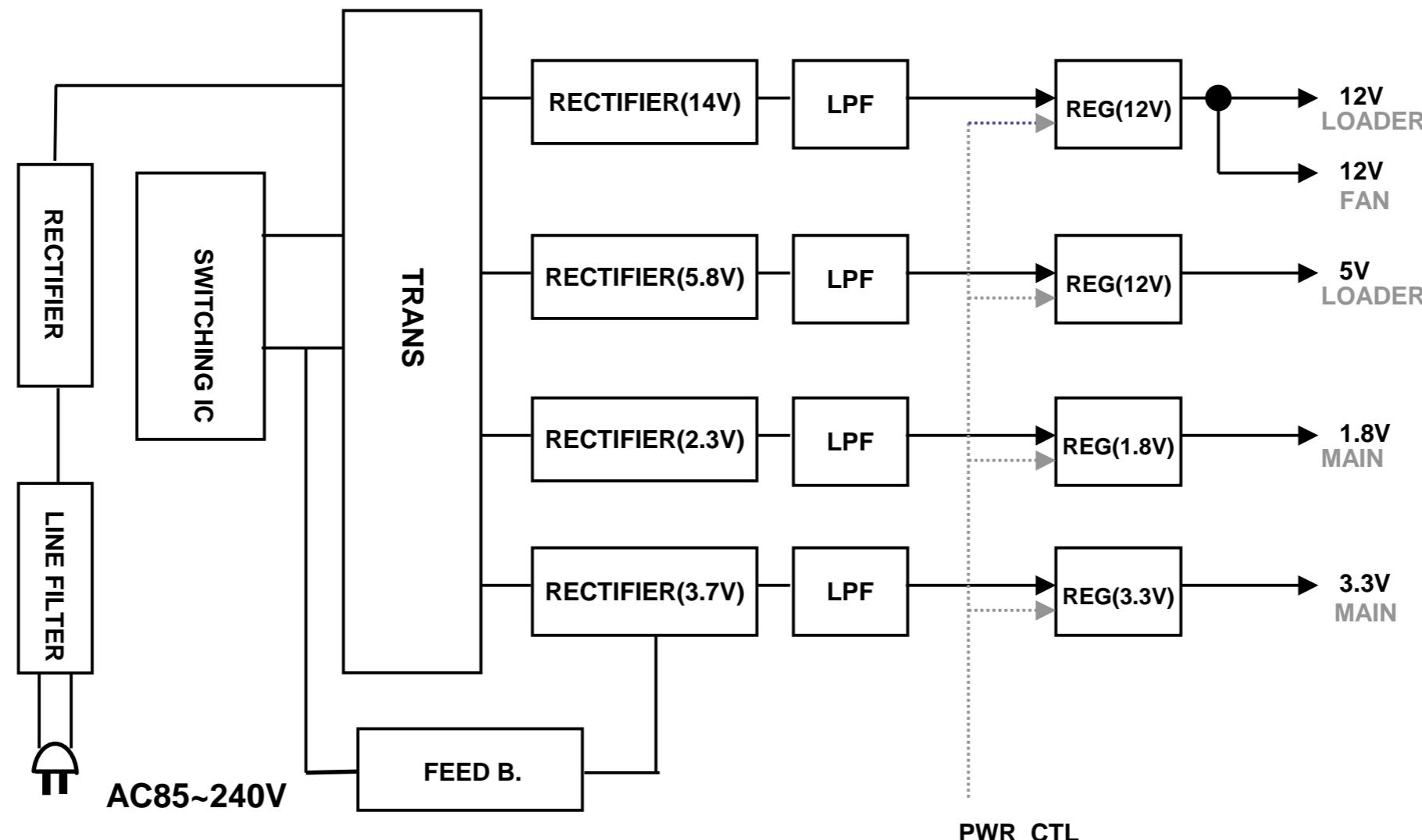
11. Power : Layout Connection Block Diagram



12. SMPS Block Diagram (PART 1)

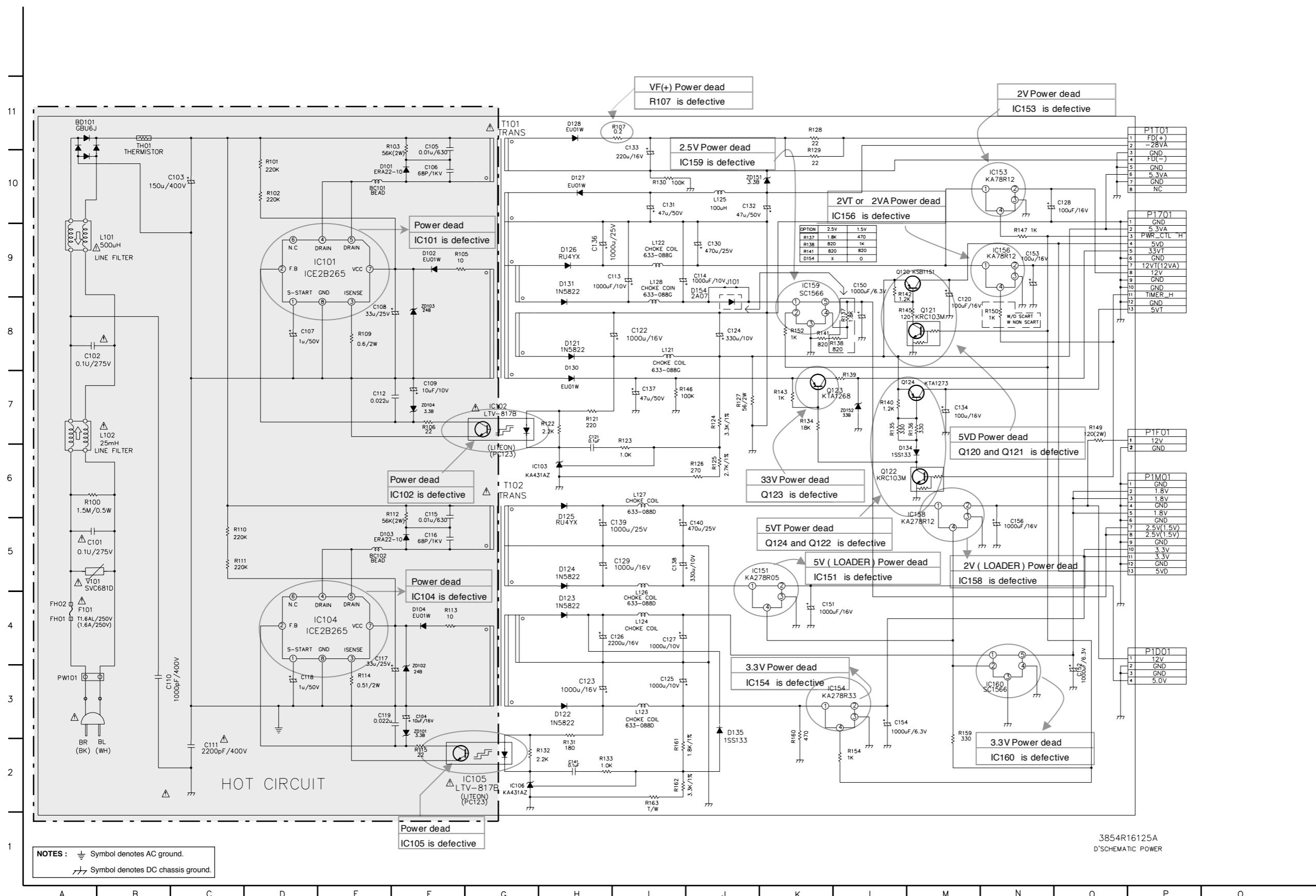


13. SMPS Block Diagram (PART 2)

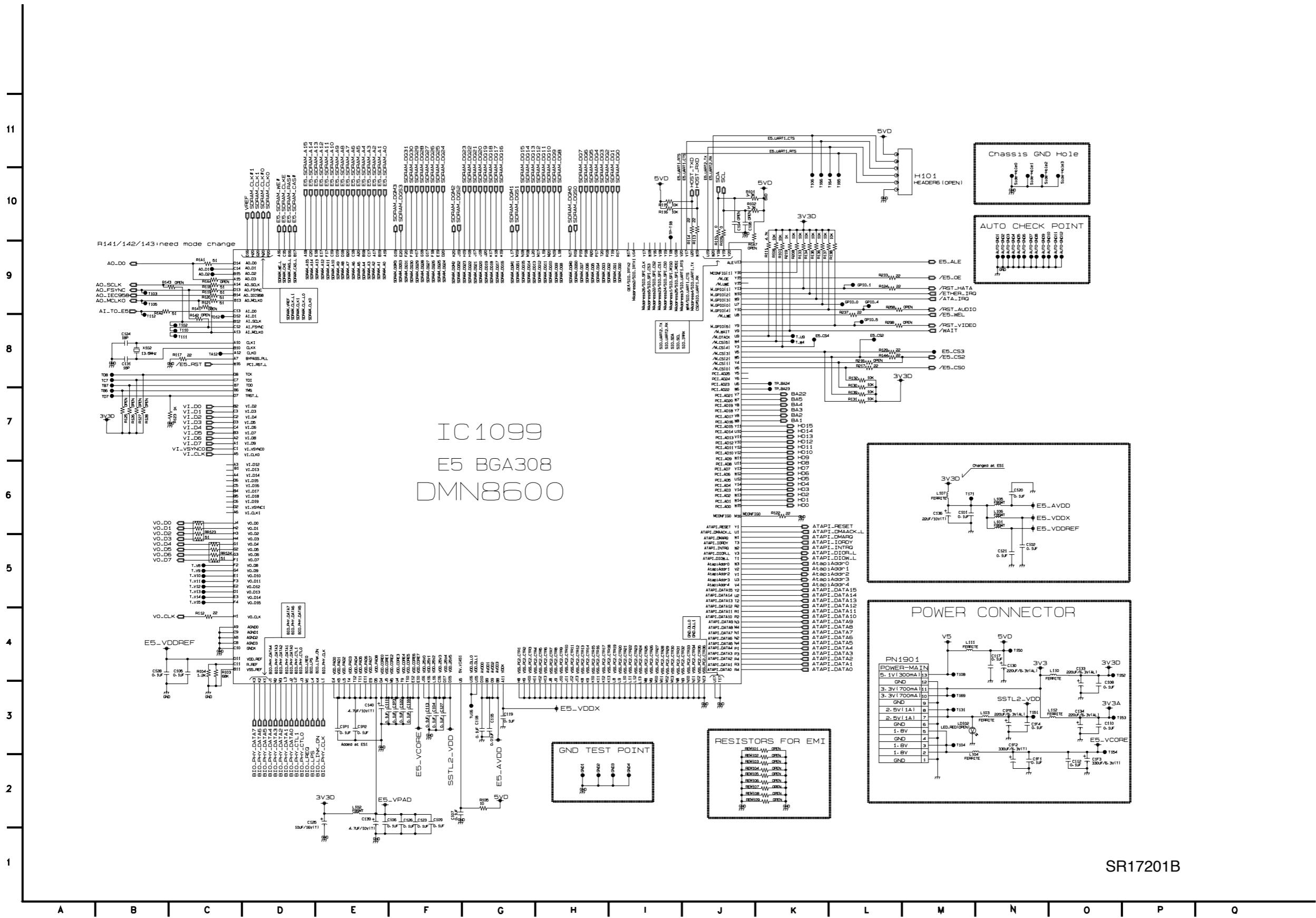


CIRCUIT DIAGRAMS

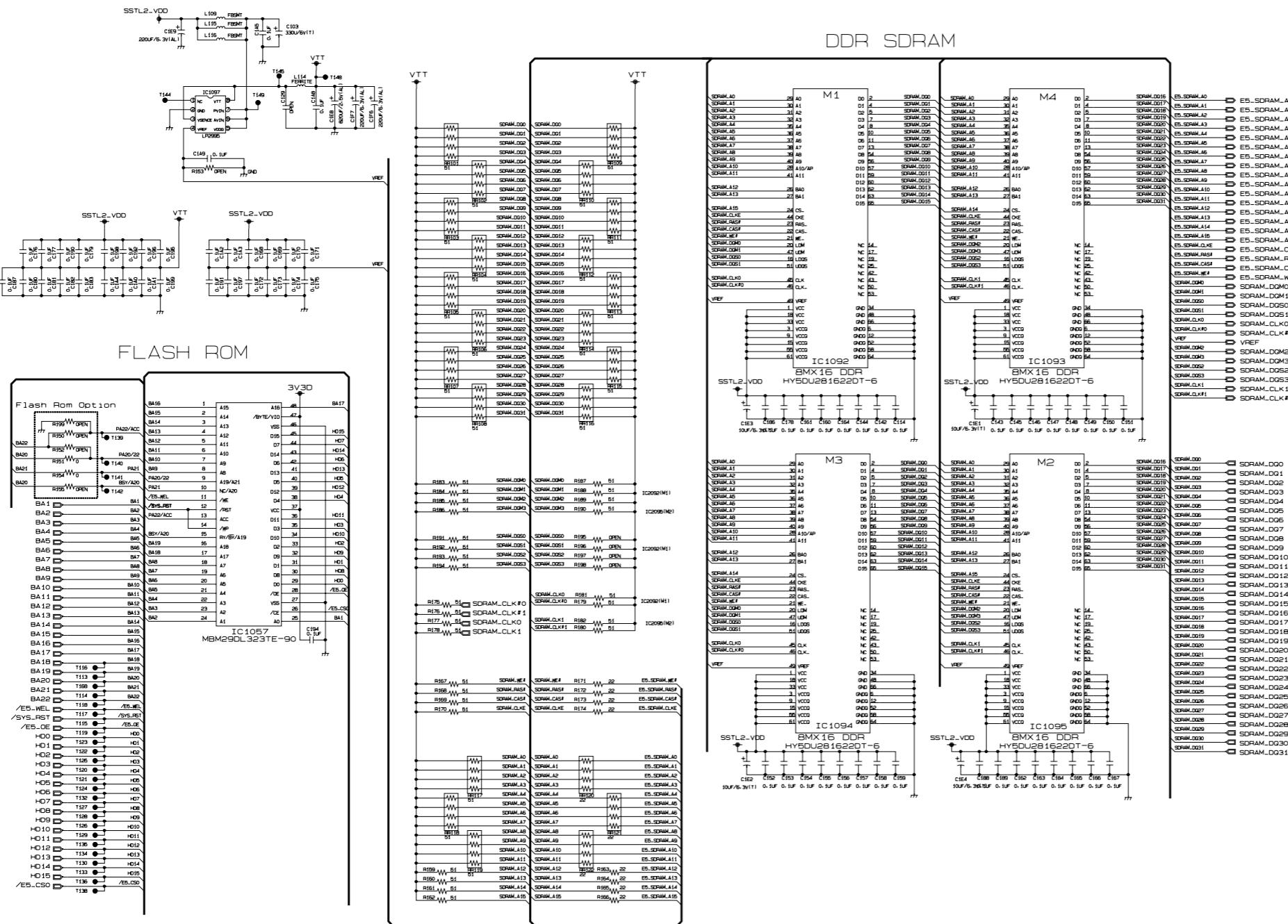
1. POWER CIRCUIT DIAGRAM



2. E5 BGA, POWER, UART2 CIRCUIT DIAGRAM

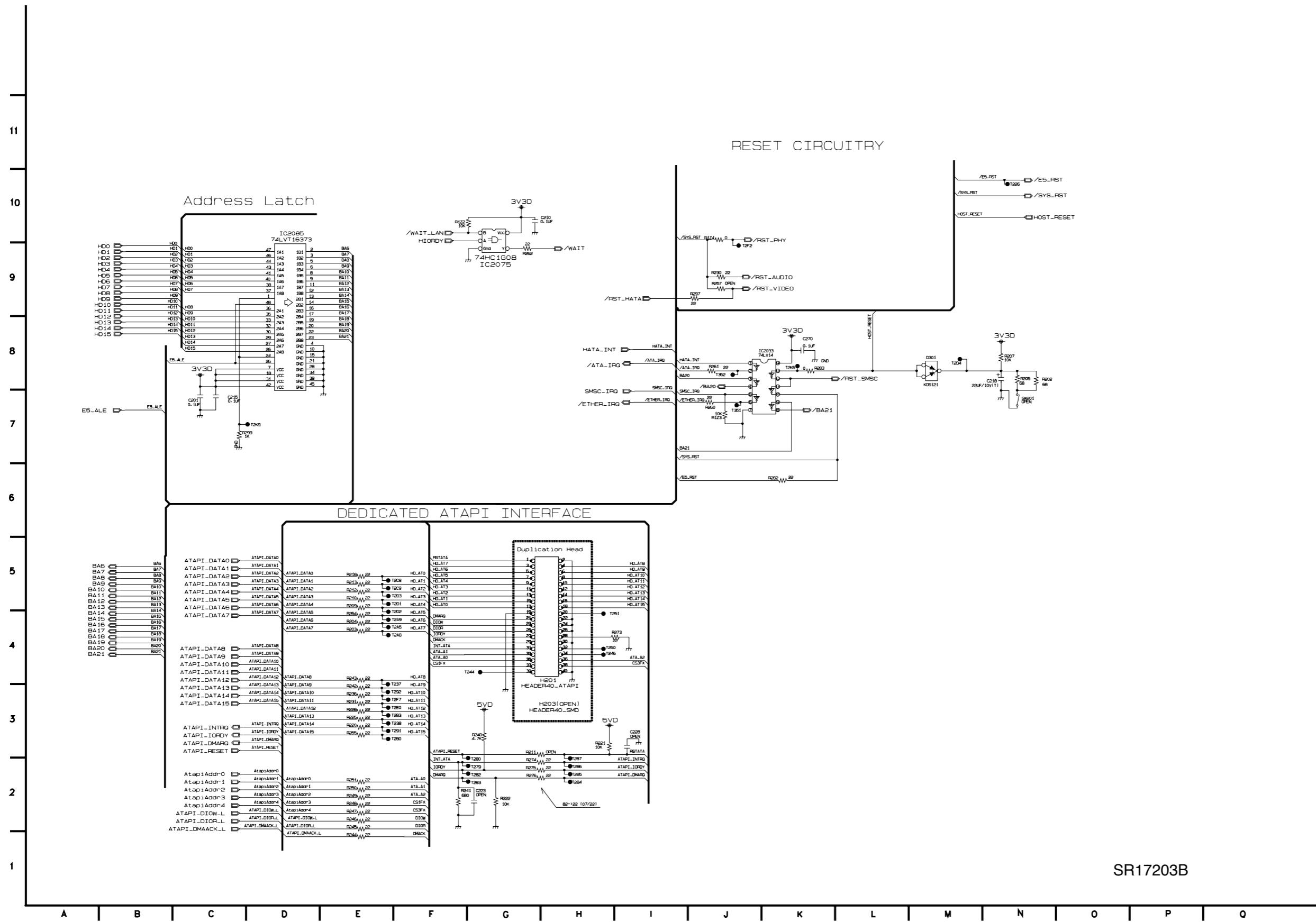


3. DDR SDRAM, FLASH CIRCUIT DIAGRAM

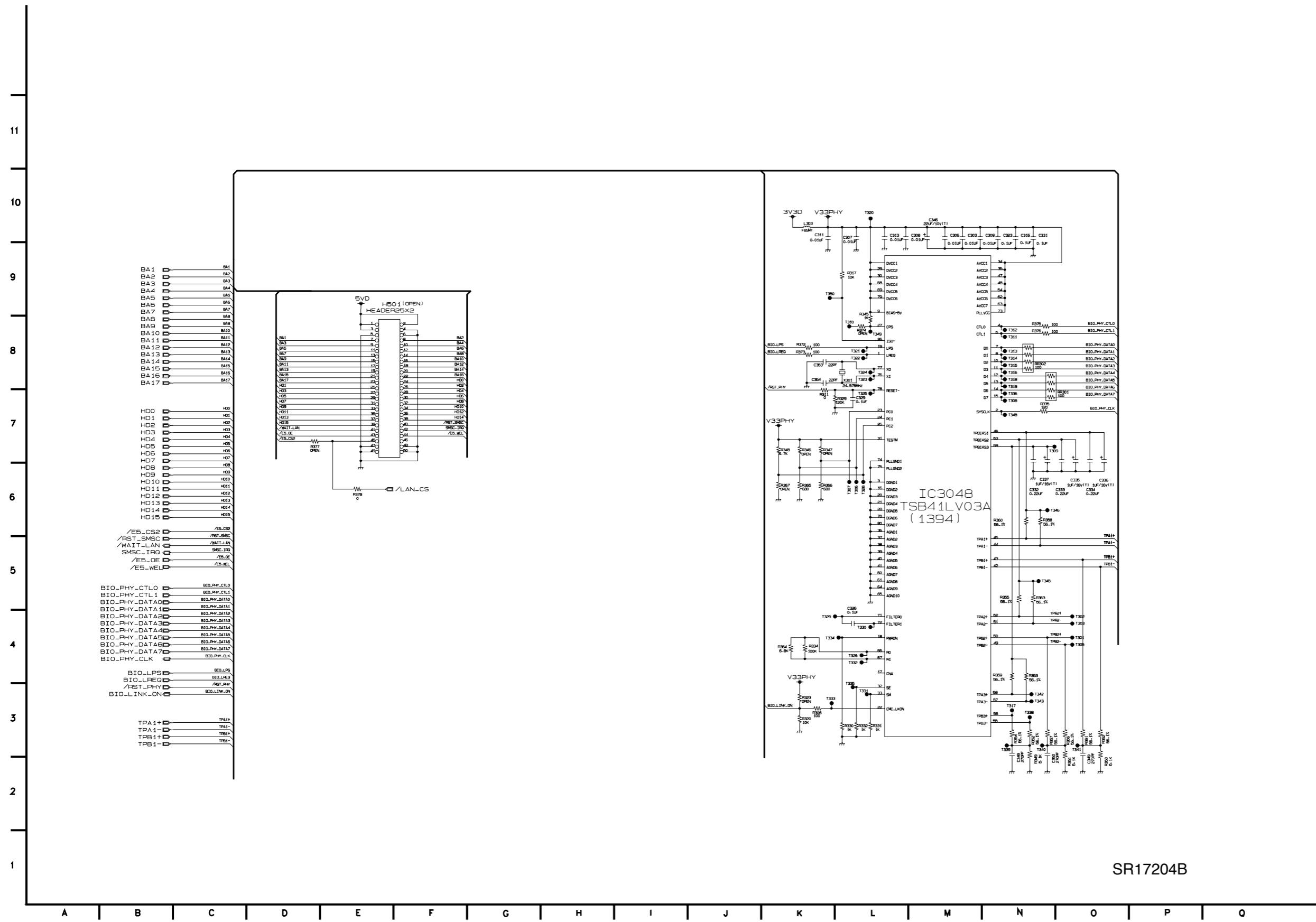


SR17202B

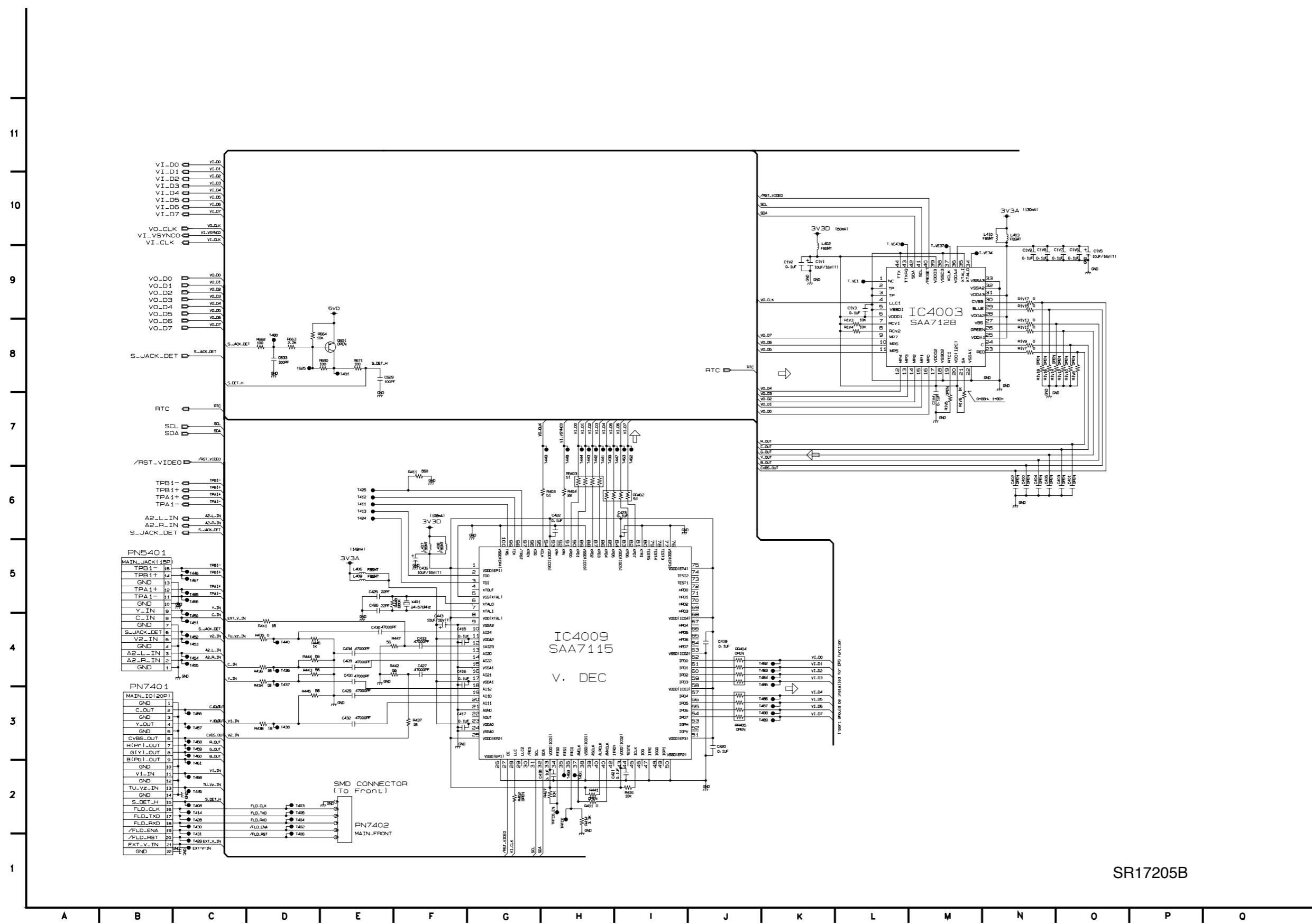
4. RST, CONTROL/STATUS REG, ATAPI, HOST CPLD, LATCH CIRCUIT DIAGRAM



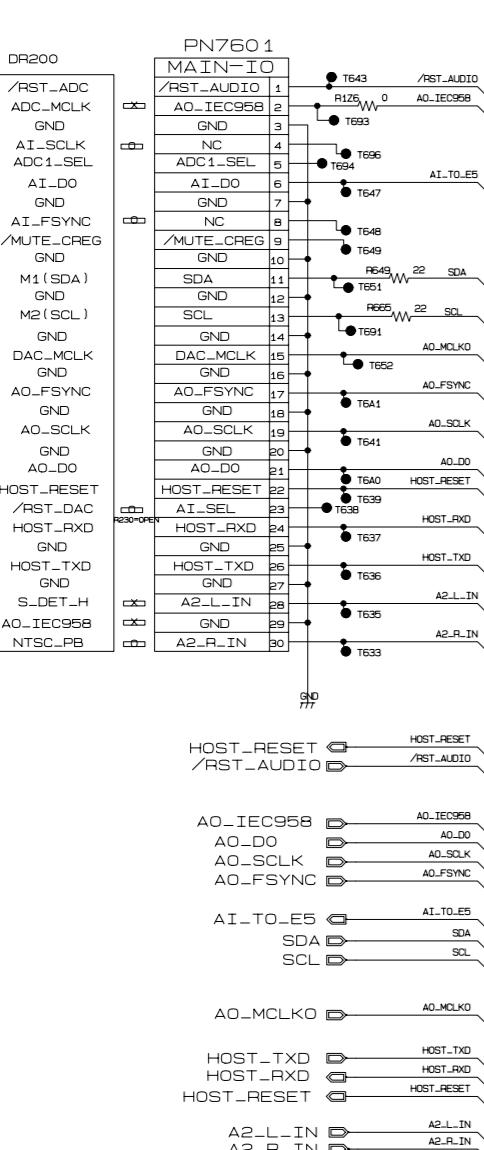
5. 1394, ETHERNET CONNECTOR CIRCUIT DIAGRAM



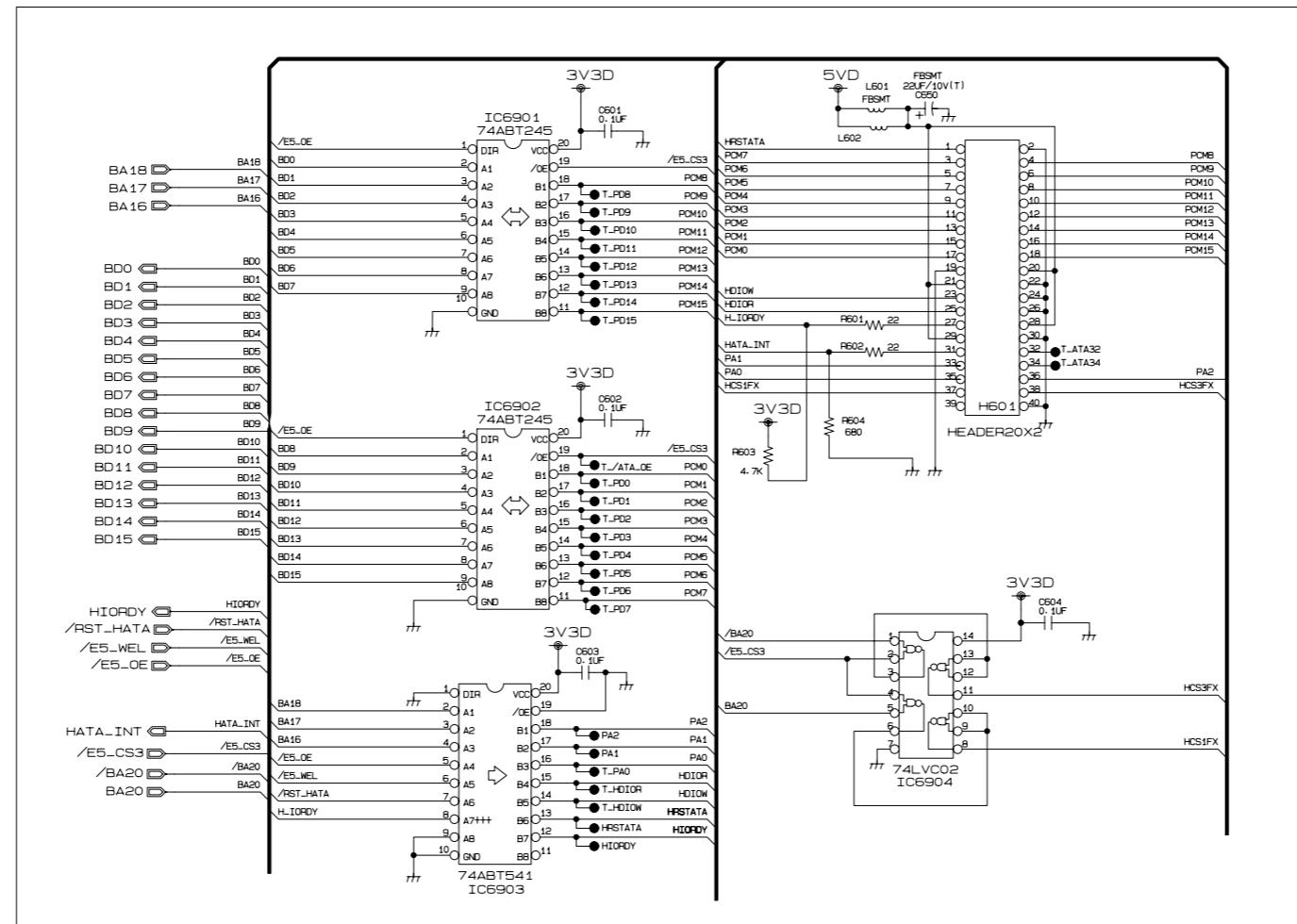
6. VIDEO IN/OUT CIRCUIT DIAGRAM



7. AUDIO IN/OUT NON-STD VIDEO CIRCUIT DIAGRAM

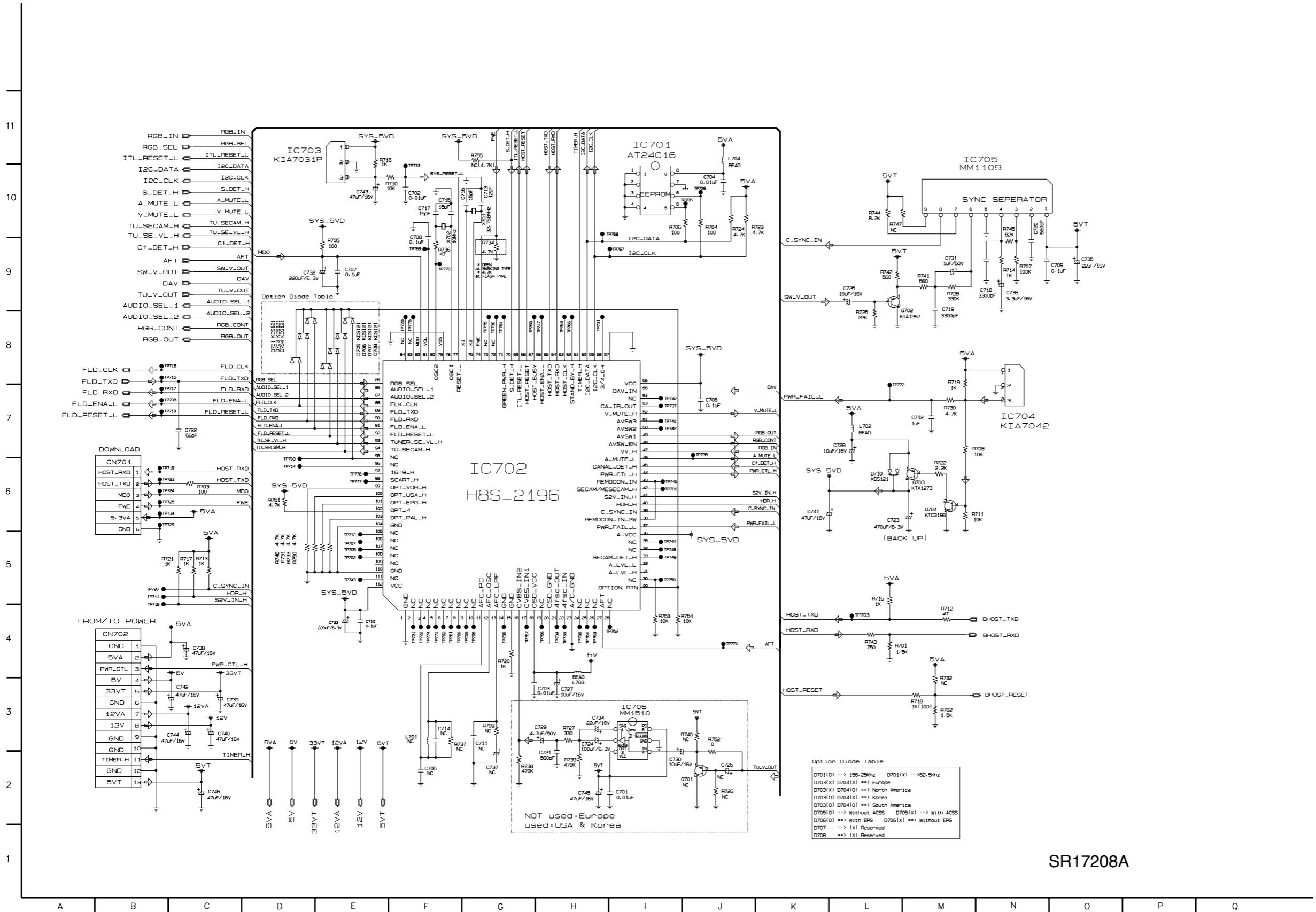


NOT USED FOR DR3800PL



SR17206A

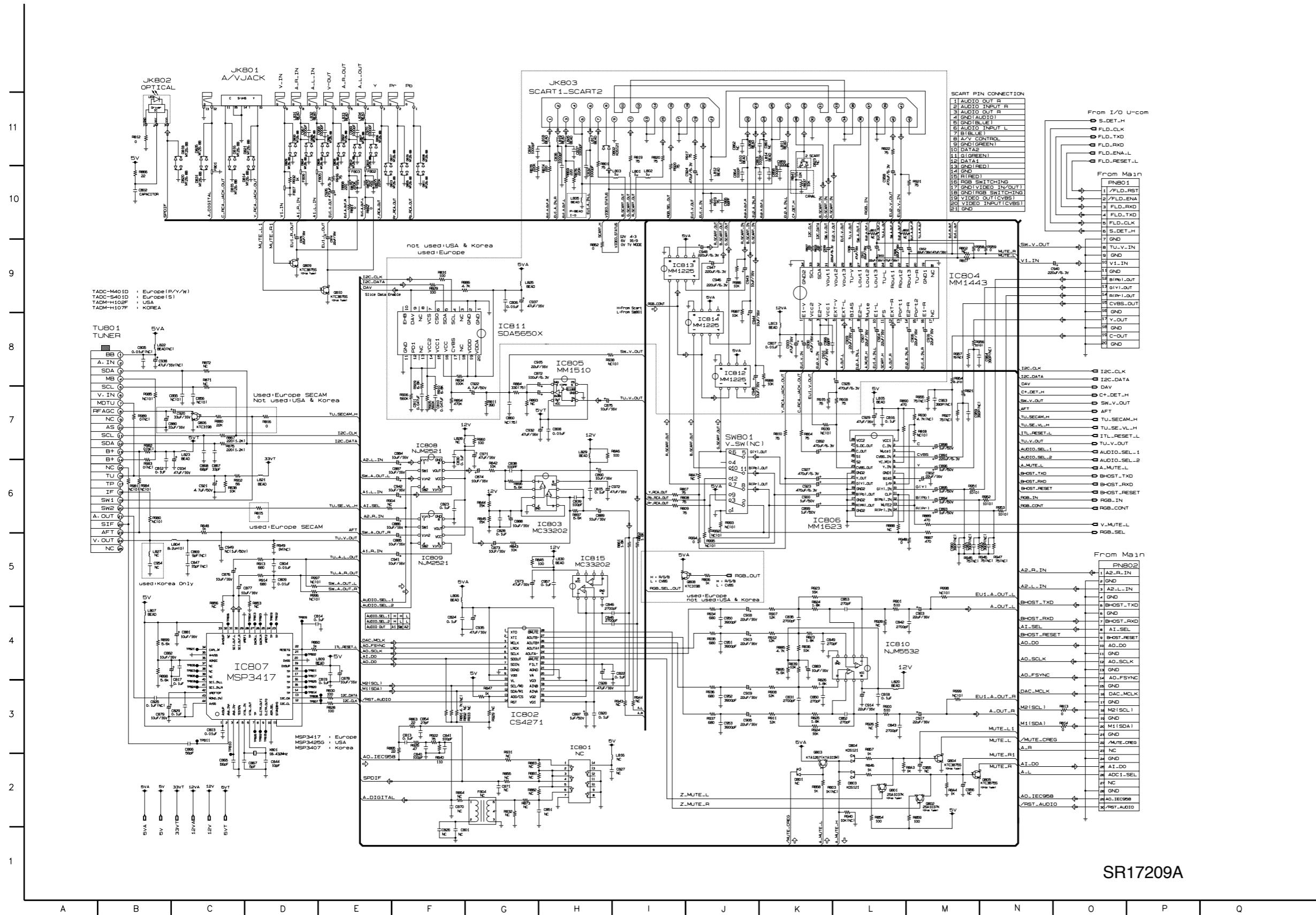
8. I/O MICOM CIRCUIT DIAGRAM



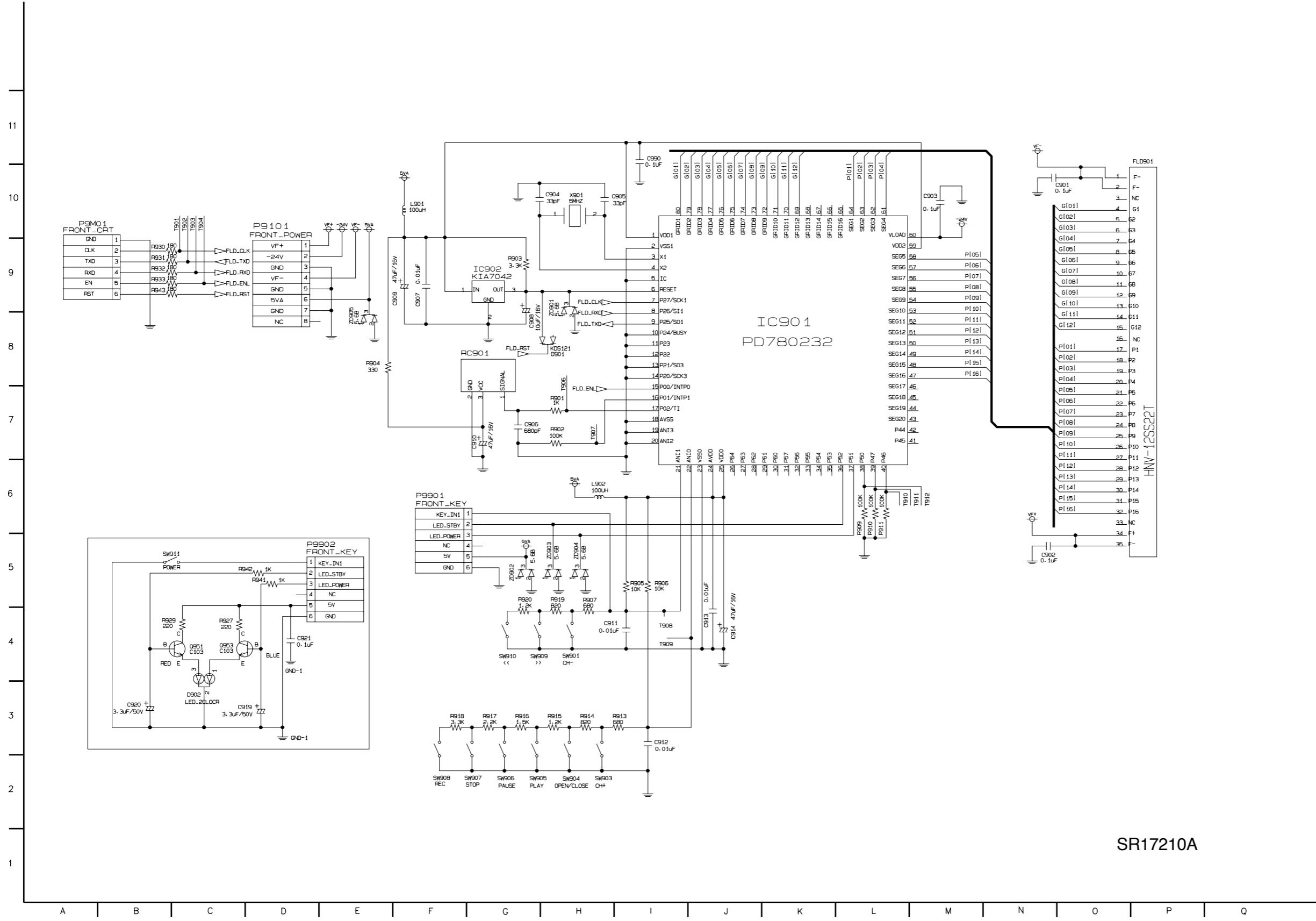
3-56

3-57

9. I/O JACK CIRCUIT DIAGRAM



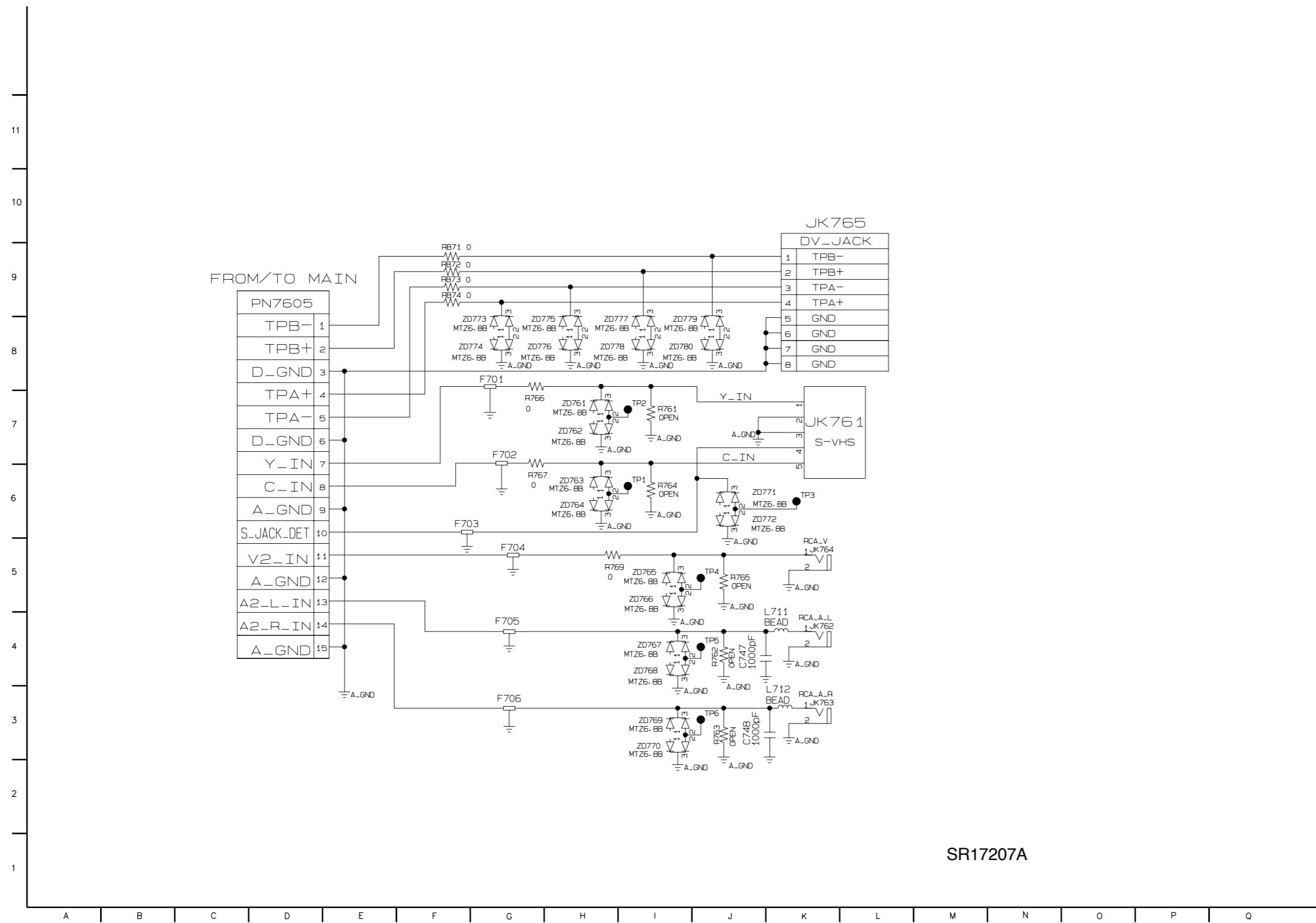
10. FRONT CIRCUIT DIAGRAM

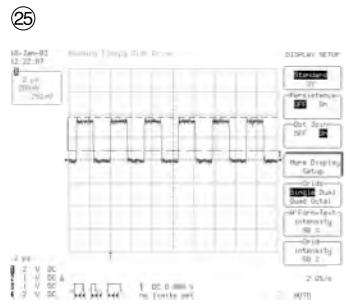


3-60

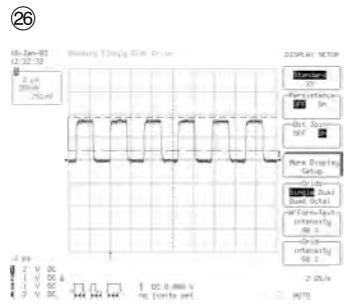
3-6

11. FRONT JACK CIRCUIT DIAGRAM

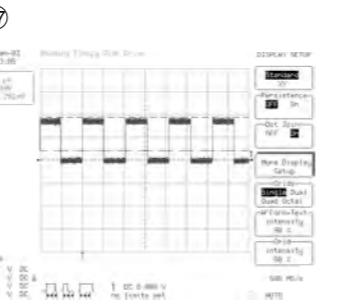




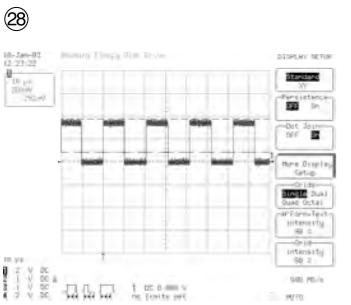
PN7601
PIN4
AI_SCLK



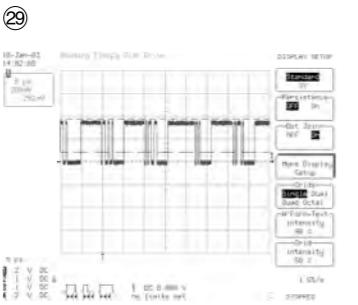
PN7601
PIN19
AO_SCLK



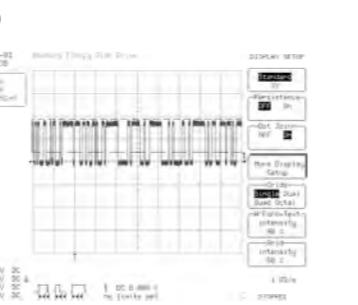
PN7601
PIN8
AI_FSYNC



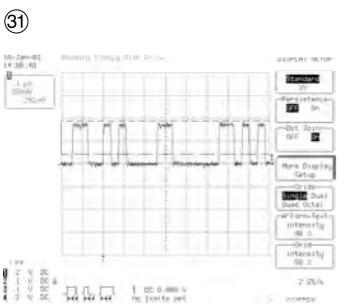
PN7601
PIN17
AO_FSYNC



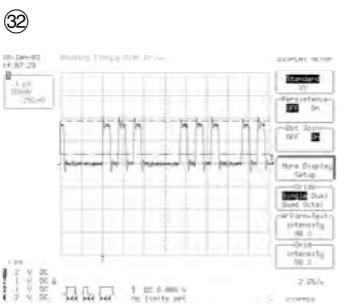
PN7601
PIN6
AI_D0



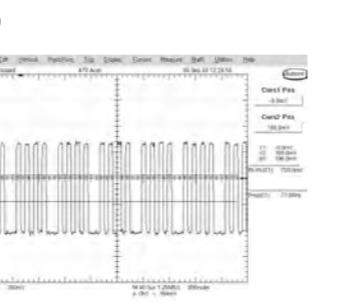
PN7601
PIN1
AO_D0



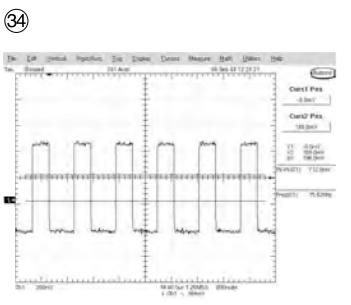
IC4002
PIN40
VO_D0



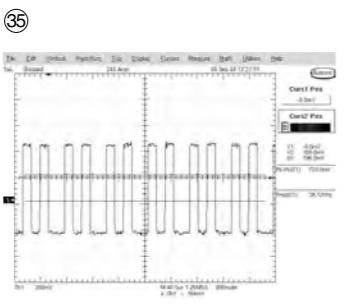
IC4002
PIN90
VI_D0



PN7401
PIN7
R_Pr_OUT



PN7401
PIN8
G_Y_OUT



PN7401
PIN9
B_Pb_OUT

• CIRCUIT VOLTAGE CHART

MODE PIN NO.	EE	PB	REC
IC2066			
1	3.28	3.28	3.28
2	3.28	3.27	3.28
3	0	0	0
4	0	0	0
5	3.28	3.27	3.28
6	0	0	0
7	3.27	3.27	3.28
8	3.28	0	0
9	3.27	3.27	3.28
10	3.27	3.27	3.28
11	0	0	0
12	3.26	3.27	3.28
13	0	0	0
14	3.28	3.27	3.28
15	3.27	3.27	3.28
16	0	0	0
17	3.27	3.27	3.28
18	0	0	0
19	1.03	1.03	1.03
20	0	0	0
21	0	0	0
22	0.025	0.02	0
23	3.26	3.25	3.26
24	0	0	0
25	3.27	0.01	3.27
26	0	0	0
27	3.3	3.28	3.28
28	1.66	1.06	1.66
29	0.9	0.05	0
30	3.3	3.26	3.27
31	3.18	3.1	2.8
32	3.2	3.2	3.2
33	3.3	3.26	3.27
34	3.3	3.26	3.27
35	3.3	3.27	3.28
36	2.97	2.96	3
37	1.27	1.28	1.28
38	0	0	0
39	1.64	1.64	1.64
40	1.6	1.61	1.6
41	1.27	1.28	1.28
42	2.36	2.35	2.35
43	3.3	3.27	3.27
44	0	0.08	0
45	1.73	1.72	1.72
46	0	0	0
47	0	0	0
48	0	0	0
49	0	0	0
50	0	0	0
51	3.3	3.27	3.27
52	0	0	0
53	0	0	0
54	0	0	0
55	0	0	0
56	0	0	0
57	0	0	0
58	3.27	3.27	3.27
59	0	0.03	0.03
60	0	0	0
61	0	0	0
62	0	0	0
63	0	0	0
64	0	0	0

MODE PIN NO.	EE	PB	REC
IC4009			
1	3.27	3.26	3.27
2	1.85	0.3	0.3
3	3.09	2.35	2.34
4	1.3	0.13	0.02
5	0	0	0
6	1.63	1.55	1.56
7	1.53	1.53	1.52
8	3.3	3.26	3.27
9	0	0	0

MODE PIN NO.	EE	PB	REC
IC4003			
1	0	0	0
2	0	0	0
3	0	0	0
4	0	0	0
5	0	0	0
6	3.3	3.26	3.27
7	0	0	0
8	3.3	3.27	3.27
9	2.38	2.35	2.37
10	0.2	0.5	0
11	0.18	0.5	0
12	3.3	3.27	3.27
13	0	0	0
14	3.3	3.27	3.28
15	3.2	3.2	3.2
16	0	0.62	0
17	3.27	3.23	3.27
18	3.23	3.23	3.24

MODE PIN NO.	EE	PB	REC
IC2075			
1	0	0	0
2	0	0	0
3	0	0	0
4	0	0	0
5	0	0	0
6	0.04	0	0.1
7	0.78	1.55	0.8
8	0.42	0.89	0.42
9	0.4	0.89	0.4
10	1.64	1.27	0.96

MODE PIN NO.	EE	PB	REC
IC1057			
1	0.03	0.03	0.03
2	0.03	0.03	0.03
3	0.03	0.03	0.03
4	0.03	0.03	0.03
5	0.03	0.03	0.03
6	0.03	0.03	0.03
7	0.03	0.03	0.03
8	0.03	0.03	0.03
9	0.03	0.03	0.03
10	0.03	0.03	0.03
11	3.26	3.28	3.28
12	3.28	3.28	3.28
13	0	0	0
14	3.26	3.28	3.28
15	0	0	0
16	0.03	0.03	0.03
17	0.03	0.03	0.03
18	0.03	0.03	0.03
19	0.03	0.03	0.03
20	0.03	0.03	0.03
21	0	0	0
22	0	0	0
23	0	0	0
24	0	0	0
25	0	0	0
26	3.26	3.27	3.27
27	3.26	3.26	3.27
28	0	0	0
29	3.27	3.27	3.27
30	3.25	3.27	3.27
31	3.25	3.27	3.28
32	0	0	0
33	0	0	0
34	3.25	3.27	3.26
35	3.25	3.27	3.27
36	0	0	0
37	0	0	0
38	0	0	0
39	0	0	0
40	0	0	0
41	0	0	0
42	0	0	0
43	0	0	0
44	3.26	3.27	3.27
45	3.27	3.27	3.27
46	3.27	3.27	3.26
47	3.26	3.27	3.26
48	0	0	0
49	0	0	0
50	0	0	0
51	3.27	3.27	3.27
52	3.27	3.27	3.26
53	3.26	3.27	3.26
54	0.03	0.03	0.03

MODE PIN NO.	EE	PB	REC
IC3048			
1	0	0	0
2	1.54	1.56	1.54
3	0	0	0
4	1.04	1.04	0
5	0	0	0
6	3.27	3.27	3.27
7	0.01	0.02	0
8	0.01	0.02	0
9	3.25	3.27	3.26
10	0	0	0
11	0	0	0
12	0	0	0
13	0	0	0
14	0	0	0
15	0	0	0
16</td			

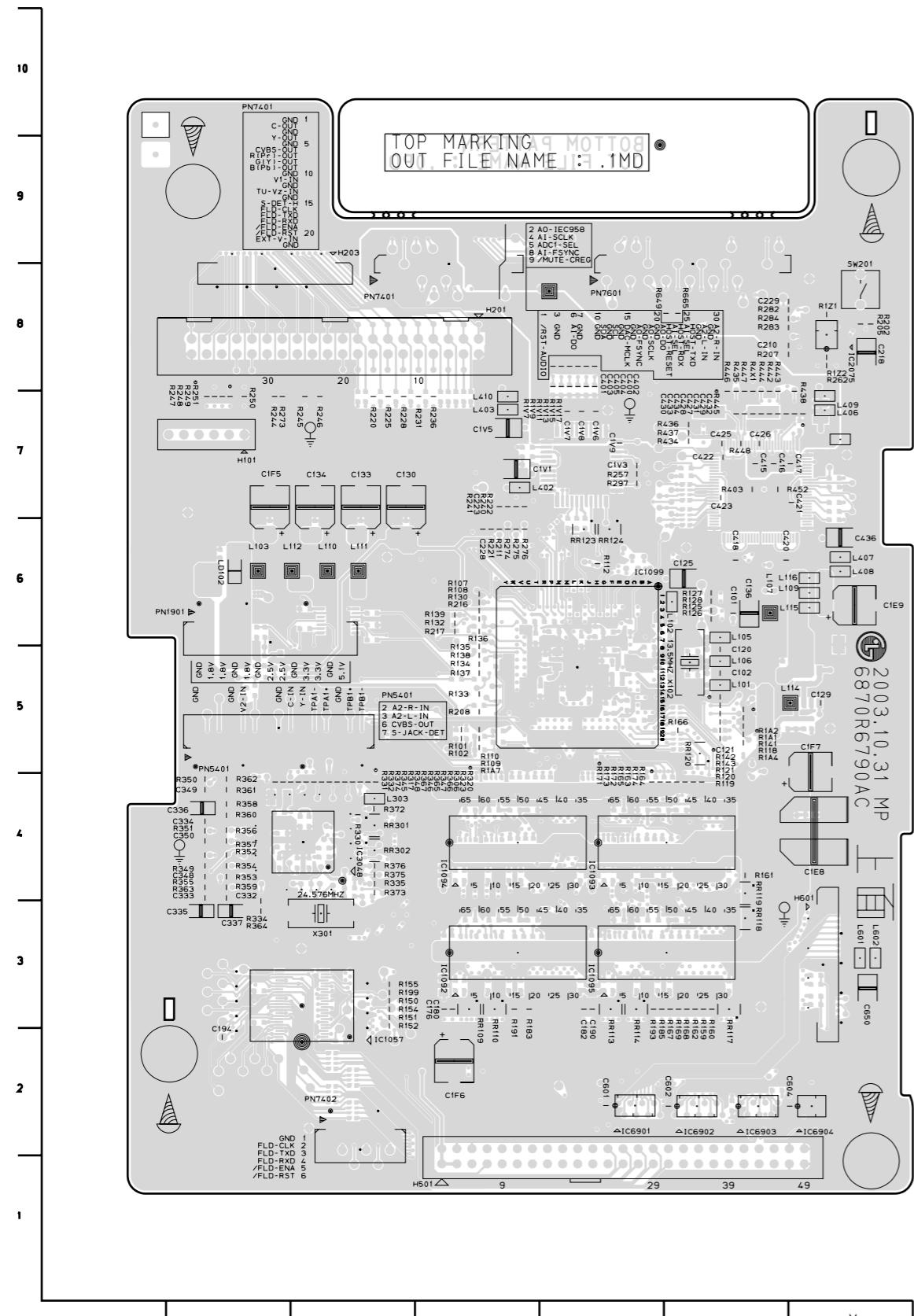
MODE PIN NO.	EE	PB	REC
29	0.1	0.1	0.1
30	0	0	0
31	2.52	2.52	2.51
32	2.5	2.52	2.52
33	0.1	0.1	0.1
34	0.1	0.1	0.1
35	0	0	0
36	4.96	4.94	4.97
37	4.75	4.75	4.75
38	0	0	0
39	4.97	4.96	4.96
40	0.3	0.32	0.32
41	0.32	0.32	0.32
42	0.04	0.04	0.04
43	0	0	0
44	5.1	5.08	5.1
45	0	0	0
46	5.2	5.18	5.2
47	0.02	0.03	0.03
48	2.5	2.5	2.5
49	0.09	0.09	0.09
50	2.56	2.55	2.55
51	2.56	2.55	2.55
52	0.03	0.02	0.02
53	0.03	0.02	0.02
54	0.12	0.1	0.1
55	0	0	0
56	5.1	5.08	5.08
57	0	0	0
58	0	0	0
59	5.11	5.1	5.1
60	5.1	5.1	5.1
61	5.03	5	5
62	0	0	0
63	2.49	2.47	2.47
64	3.84	3.82	3.82
65	0	0	0
66	0.33	0.32	0.31
67	0	0	0
68	4.8	4.9	4.9
69	0	0	0
70	5.2	5.22	5.22
71	0	0	0
72	0	0	0
73	5.1	5.1	5.1
74	0	0	0
75	1.45	1.47	1.47
76	1.43	1.43	1.45
77	5.21	5.21	5.21
78	2.5	2.52	2.52
79	0	0	0
80	0	0	0
81	3.42	3.4	3.41
82	5.08	5.11	5.1
83	0	0	0

MODE PIN NO.	EE	PB	REC
84	0	0	0
85	0	0	0
86	2.53	2.54	2.52
87	2.53	2.54	2.52
88	4.94	4.96	4.96
89	4.04	4.06	4.06
90	2.18	2.2	2.2
91	4.27	4.3	4.3
92	5.06	5.1	5.1
93	0	0	0
94	0.02	0.02	0.02
95	0	0	0
96	0.31	0.31	0.31
97	0	0.02	0.02
IC804			
98	0	0.02	0.02
99	2.06	2.06	2.06
100	0	0	0
101	0	0	0
102	0	0	0
103	0	0	0
104	0	0	0
105	0	0	0
106	0	0	0
107	0	0	0
108	0	0	0
109	0.03	0.06	0.06
110	0	0	0
111	0	0	0
112	5.1	5.2	5.2
IC705			
1	4.95	4.96	4.96
2	1.87	1.9	1.91
3	2.08	2.1	2.13
4	1.98	2	2
5	0	0	0
6	2.68	2.71	2.71
7	4.93	4.95	4.96
8	4.93	4.95	4.96
9	0.04	0.06	0.06
IC802			
1	2.57	2.57	2.57
2	2.56	2.55	2.56
3	0.00	1.64	1.64
4	1.63	1.62	1.62
5	1.63	1.63	1.63
6	0.00	1.30	1.30
7	1.48	1.62	1.55
8	0.00	0.00	0.00
9	0.00	0.00	0.00
IC806			
10	0.00	0.00	0.00
11	5.01	5.01	5.01
12	0.00	0.00	0.00
13	0.00	0.00	0.00
14	3.26	3.25	3.26
15	2.52	2.52	2.52

MODE PIN NO.	EE	PB	REC
16	2.53	2.53	2.53
17	2.53	2.53	2.53
18	2.52	2.52	2.52
19	2.53	2.53	2.53
20	5.07	5.06	5.07
21	0.00	0.00	0.00
22	4.99	4.99	4.99
23	5.06	5.05	5.06
24	2.57	2.57	2.57
25	2.46	2.46	2.46
26	2.47	2.47	2.47
27	2.58	2.57	2.58
28	5.03	5.02	5.02
IC804			
29	0.00	0.00	0.00
30	1.13	1.25	1.24
31	0.00	0.00	0.00
32	0.00	0.00	0.00
33	4.95	4.95	4.95
34	2.41	2.40	2.40
35	0.00	0.00	0.00
36	2.55	2.55	2.54
37	0.00	0.00	0.00
38	0.00	0.00	0.00
39	0.00	0.00	0.00
40	2.54	2.55	2.53
41	0.00	0.00	0.00
42	2.61	2.54	2.60
43	0.00	0.00	0.00
44	0.00	0.00	0.00
IC808			
45	8.16	8.16	8.16
46	5.12	5.12	5.12
47	7.96	7.94	7.94
48	0.00	0.00	0.00
49	8.16	8.16	8.16
50	12.09	12.10	12.10
51	7.31	7.31	7.31
52	0.00	0.00	0.00
IC807			
53	0.00	0.00	0.00
54	1.52	1.52	1.52
55	1.52	1.52	1.52
56	0.00	0.00	0.00
57	2.39	2.42	2.40
58	2.18	2.18	2.18
59	0.40	0.23	0.24
60	0.39	0.00	0.00
61	0.39	0.00	0.00
62	0.00	0.00	0.00
63	0.18	0.18	0.18
64	0.00	0.00	0.00
65	5.19		
66	0.00	5.19	5.19
67	0.00	0.00	0.00
68	4.93	4.93	4.93
69	0.00	0.00	0.00
70	0.00	5.07	5.07
71	0.00	0.00	0.00
72	1.52	1.52	1.52
73	0.00	0.00	0.00
74	0.00	0.00	0.00
75	0.00	0.00	0.00
76	0.00	0.00	0.00
77	0.00	0.00	0.00
78	0.00	0.00	0.00
79	0.00	0.00	0.00
80	0.00	0.00	0.00
81	0.00	0.00	0.00
82	0.00	0.00	0.00
83	0.00	0.00	0.00
IC153			
84	12.8		
85	12		
86	0		
87	4.9		
IC156			
88	12.8		
89	12		
90</			

PRINTED CIRCUIT DIAGRAMS

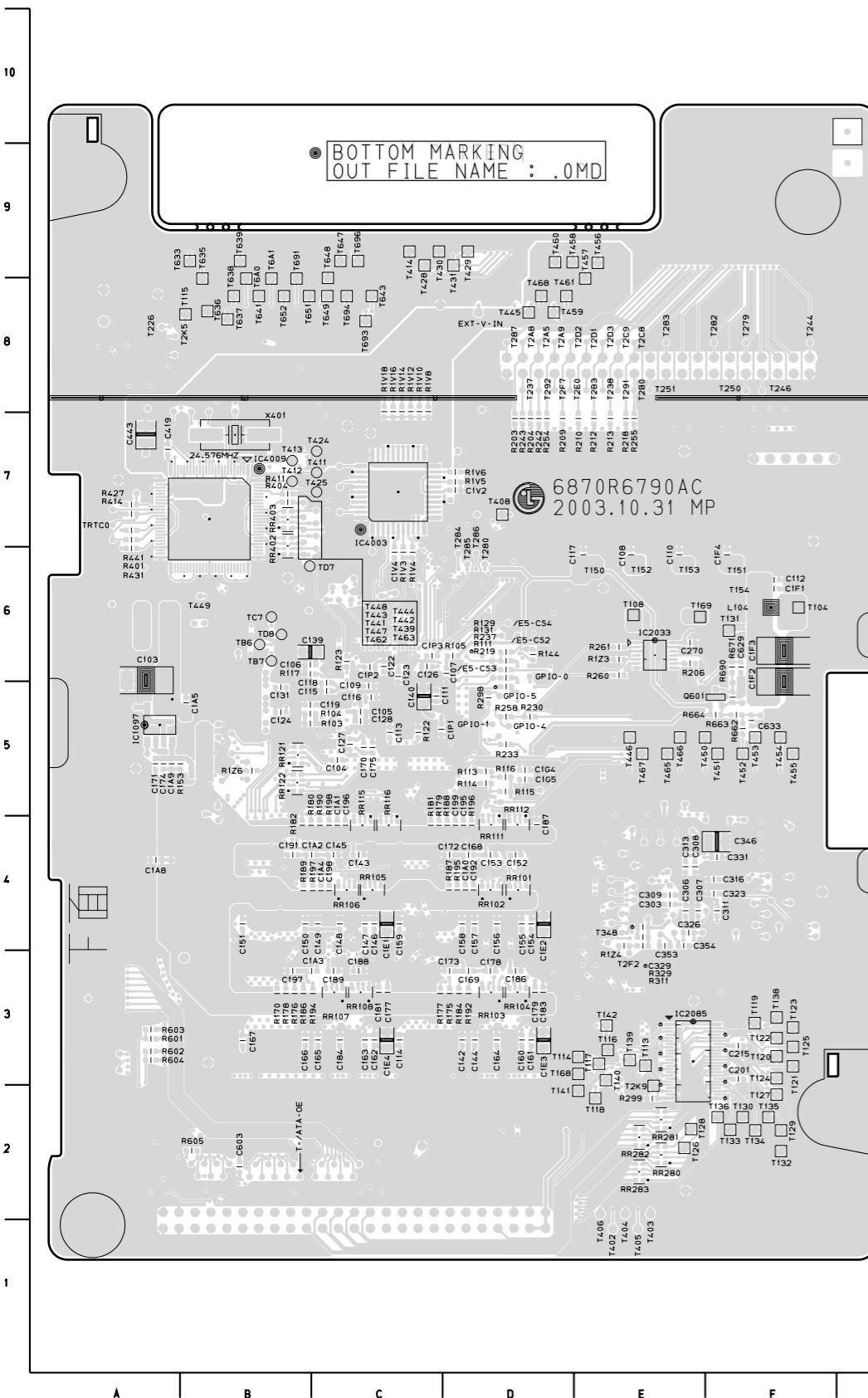
1. MAIN P.C.BOARD(TOP SIDE)



LOCATION GUID

C101	L6	C429	L7	PN740	22	R1V13	K8	R354
C102	L5	C430	L7	PN760	8	R1V15	K8	R355
C120	L5	C431	L7	R101	J5	R1V17	K8	R356
C121	L5	C432	M7	R102	J5	R1V7	K8	R357
C125	L6	C433	L7	R107	J6	R1V9	K8	R358
C129	M5	C434	L7	R108	J6	R1Z1	M8	R359
C130	I7	C436	M6	R109	J5	R1Z2	M8	R360
C133	I7	C601	K2	R110	K6	R202	M8	R361
C134	I7	C602	L2	R112	L5	R205	M8	R362
C136	L6	C604	M2	R118	L5	R207	M8	R363
C176	J3	C650	M3	R119	L5	R208	J5	R364
C180	J3	GND1	I7	R120	L5	R211	J6	R365
C182	K3	GND2	K7	R121	L6	R216	J6	R366
C190	K3	GND3	L3	R125	L6	R220	J7	R367
C194	H2	GND4	H4	R126	L6	R221	J7	R372
C1E8	M4	H101	H7	R127	L6	R222	J7	R373
C1E9	M6	H201	J8	R130	J6	R225	I7	R374
C1F5	H7	H203	H8	R130	J6	R228	I7	R375
C1F6	J2	H501	J1	R132	J5	R231	J7	R376
C1F7	M5	H601	M3	R133	J5	R236	J7	R403
C1V1	J7	IC105	J3	R134	J5	R240	J7	R434
C1V3	K7	IC1092	J3	R135	J5	R241	J7	R435
C1V5	J7	IC1094	J4	R136	J5	R244	H7	R436
C1V6	K7	IC1094	J4	R137	J5	R245	I7	R437
C1V7	K7	IC1095	J3	R138	J5	R246	I7	R438
C1V8	K7	IC1095	K5	R139	J5	R247	I7	R442
C1V9	K7	IC1095	M8	R141	L5	R248	H7	R443
C210	M8	IC304	84	R142	L5	R249	H7	R444
C218	M8	IC690	K2	R143	I3	R250	H7	R445
C223	J7	IC690	022	R150	I3	R251	H7	R446
C228	J6	IC690	B2	R151	I3	R257	K7	R447
C229	M8	IC690	M2	R152	I3	R262	M8	R452
C332	H4	L101	L5	R154	I3	R273	H7	R4X1
C333	H4	L102	L6	R155	I3	R274	J6	R649
C334	H4	L103	H6	R159	I3	R275	J6	R665
C335	H3	L105	L6	R160	I3	R276	J6	R109
C336	H4	L106	L5	R161	I4	R282	M8	R110
C337	H3	L107	L6	R162	I4	R283	M8	R113
C348	H4	L109	M6	R163	I4	R284	M8	R114
C349	H4	L110	I6	R164	I4	R297	K7	R117
C350	H4	L111	I6	R165	I4	R306	K7	R118
C400	K8	L112	I6	R166	I5	R317	I4	R119
C401	K8	L114	M5	R167	I3	R320	I4	R120
C402	K8	L115	M6	R168	I3	R323	I4	R123
C403	K8	L116	M6	R169	I3	R330	I4	R124
C404	K8	L303	I4	R171	K4	R332	H4	R301
C405	K8	L402	J7	R172	K4	R333	H4	R302
C415	L7	L403	J7	R173	K4	R334	H3	-HOL
C416	L7	L406	M7	R174	K4	R335	I4	-MOL
C417	M7	L407	M6	R183	K3	R345	I4	-MOL
C418	L6	L408	M6	R185	K3	R346	I4	-MOL
C420	L6	L409	M7	R191	K3	R347	I4	-HQ
C421	M7	L410	J7	R193	K3	R348	I4	SW201
C422	L7	L601	M3	R199	I3	R349	H4	M8
C423	L7	L602	M3	R1A1	I5	R350	H4	L5
C425	L7	LD102	H6	R1A2	I5	R351	H4	X102
C426	L7	PN190	H6	R1A4	I5	R352	H4	X301
C427	L7	PN540	H5	R1A7	J5	R353	H4	I3
C428	L7	PN740	J8	R1V11	K8			

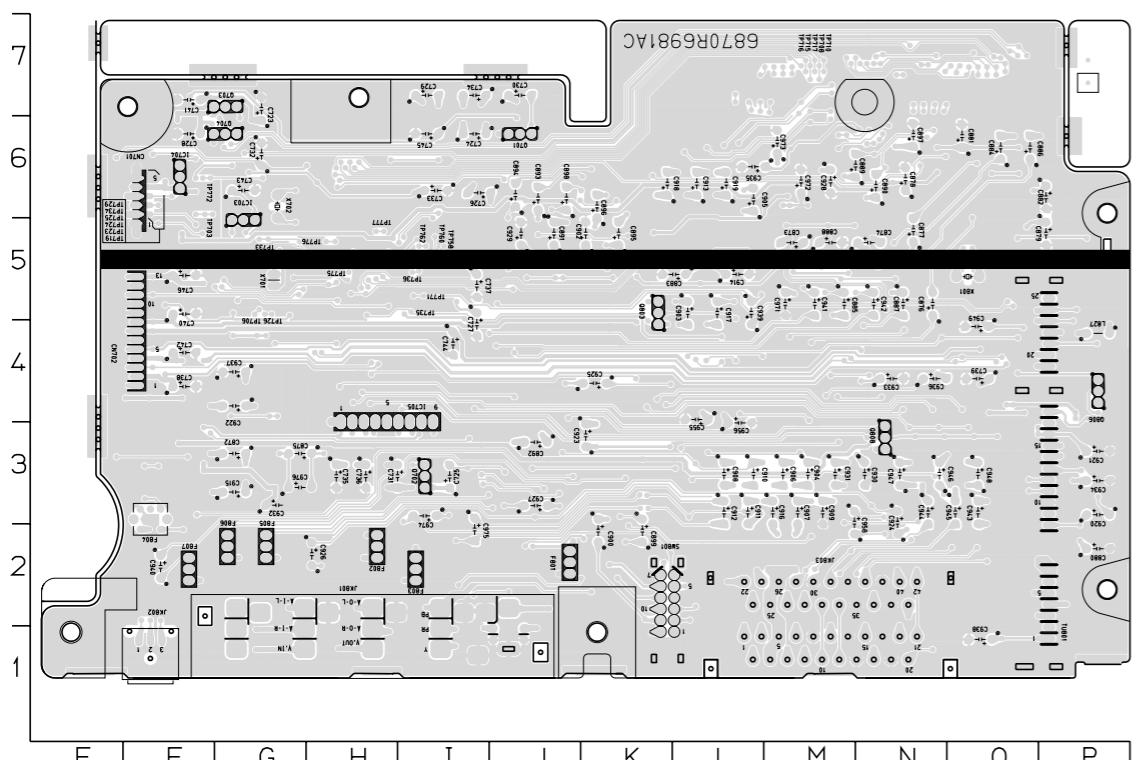
2. MAIN P.C.BOARD(BOTTOM SIDE)



LOCATION GUIDE

/E5±C08	C164	D3	C329	E4	R1V16	C7	RR281E2	F8	T343	F4
/E5±C08	C165	C3	C331	F4	R1V18	C7	RR282E2	F8	T345	F4
A0±D1B4	C167	B3	C353	E4	R1V4	C6	RR402B7	D6	T346	E4
A0±D2B5	C168	D4	C354	E4	R1V5	D7	RR403B7	D6	T348	E4
AUTO-6	C169	D3	C419	A7	R1V6	D7	T102	B5	T641	B8
AUTO-6	C070	C5	C443	A7	R1V8	C7	T103	B5	T643	C8
AUTO-GND	C171	A5	C603	E4	R1Z3	C6	T104	D6	T647	C9
AUTO-B	C272	D4	C629	E4	R1Z4	D7	T105	D6	T648	C9
AUTO-GND	C273	D3	C633	EXT-VDD	R1Z6	B5	T106	D6	T649	C9
AUTO-GND	C174	A5	GPIO10±	D6	R1Z7	B5	T108	D6	T651	B8
AUTO-GND	C175	C5	GPIO10±	D5	R1Z8	B5	T110	D8	T652	B8
AUTO-GND	C177	C3	GPIO10±	D5	R204	E6	T111	D8	T653	B8
AUTO-GND	C178	D3	GPIO10±	D5	R209	E7	T112	D8	T654	B8
AUTO-GND	C179	D3	GPIO10±	D5	R209	E7	T113	D8	T655	B8
AUTO-GND	C181	C3	HIORD	C2	R210	E7	T114	D8	T656	B8
AUTO-GND	C183	D3	HRSTA	C2	R212	E7	T115	D8	T657	B8
C103	A6	C184	IC109A5	C3	R218	E7	T116	D8	T658	B8
C104	C5	C186	IC203	D3	R219	D5	T117	D8	T659	B8
C105	B5	C187	IC208	D4	R230	D5	T118	D8	T660	B8
C106	B6	C188	IC4000	C3	R233	D5	T119	D8	T661	B8
C107	C6	C189	IC4000	C3	R237	D6	T120	D8	T662	B8
C108	C6	C191	I104	D4	R242	D7	T121	D8	T663	B8
C109	C6	C192	PA1	D4	R243	D7	T122	D8	T664	B8
C110	C6	C195	PA2	D4	R254	D7	T123	D8	T665	B8
C111	C6	C196	Q601	D4	R255	D7	T124	D8	T666	B8
C112	C6	C197	R103	D4	R258	D7	T125	D8	T667	B8
C113	C6	C198	R104	D4	R260	D7	T126	D8	T668	B8
C114	C6	C199	R105	D4	R261	D7	T127	D8	T669	B8
C115	C6	C200	R111	D4	R298	D5	T128	D8	T670	B8
C116	C6	C201	R113	D4	R299	E2	T129	D8	T671	B8
C117	C6	C202	R114	D4	R311	E4	T130	D8	T672	B8
C118	C6	C203	R115	D4	R329	E4	T131	D8	T673	B8
C119	C6	C204	R116	D5	R401	A7	T132	F2	T674	B7
C120	C6	C205	R117	D6	R404	B7	T133	F2	T675	C7
C121	C6	C206	R118	D6	R411	B7	T134	F2	T676	C7
C122	C6	C207	R119	D5	R414	A7	T135	F2	T677	C7
C123	C6	C208	R120	D5	R427	A7	T136	F2	T678	C7
C124	C6	C209	R121	D5	R431	A6	T138	F2	T679	C7
C125	C6	C210	R122	D5	R434	A7	T139	F2	T680	C7
C126	C6	C211	R123	D5	R441	B7	T140	F2	T681	C7
C127	C6	C212	R124	D5	R444	B7	T141	F2	T682	C7
C128	C6	C213	R125	D5	R447	B7	T142	F2	T683	C7
C129	C6	C214	R126	D5	R450	B7	T143	F2	T684	C7
C130	C6	C215	R127	D5	R453	B7	T144	F2	T685	C7
C131	C6	C216	R128	D5	R456	B7	T145	F2	T686	C7
C132	C6	C217	R129	D5	R459	B7	T146	F2	T687	C7
C133	C6	C218	R130	D5	R462	B7	T147	F2	T688	C7
C134	C6	C219	R131	D5	R465	B7	T148	F2	T689	C7
C135	C6	C220	R132	D5	R468	B7	T149	F2	T690	C7
C136	C6	C221	R133	D5	R471	B7	T150	F2	T691	C7
C137	C6	C222	R134	D5	R474	B7	T151	F2	T692	C7
C138	C6	C223	R135	D5	R477	B7	T152	F2	T693	C7
C139	C6	C224	R136	D5	R480	B7	T153	F2	T694	C7
C140	C6	C225	R137	D5	R483	B7	T154	F2	T695	C7
C141	C6	C226	R138	D5	R486	B7	T155	F2	T696	C7
C142	C6	C227	R139	D5	R489	B7	T156	F2	T697	C7
C143	C6	C228	R140	D5	R492	B7	T157	F2	T698	C7
C144	C6	C229	R141	D5	R495	B7	T158	F2	T699	C7
C145	C6	C230	R142	D5	R498	B7	T159	F2	T700	C7
C146	C6	C231	R143	D5	R501	B7	T160	F2	T701	C7
C147	C6	C232	R144	D5	R504	B7	T161	F2	T702	C7
C148	C6	C233	R145	D5	R507	B7	T162	F2	T703	C7
C149	C6	C234	R146	D5	R510	B7	T163	F2	T704	C7
C150	C6	C235	R147	D5	R513	B7	T164	F2	T705	C7
C151	C6	C236	R148	D5	R516	B7	T165	F2	T706	C7
C152	C6	C237	R149	D5	R519	B7	T166	F2	T707	C7
C153	C6	C238	R150	D5	R522	B7	T167	F2	T708	C7
C154	C6	C239	R151	D5	R525	B7	T168	F2	T709	C7
C155	C6	C240	R152	D5	R528	B7	T169	F2	T710	C7
C156	C6	C241	R153	D5	R531	B7	T170	F2	T711	C7
C157	C6	C242	R154	D5	R534	B7	T171	F2	T712	C7
C158	C6	C243	R155	D5	R537	B7	T172	F2	T713	C7
C159	C6	C244	R156	D5	R540	B7	T173	F2	T714	C7
C160	C6	C245	R157	D5	R543	B7	T174	F2	T715	C7
C161	C6	C246	R158	D5	R546	B7	T175	F2	T716	C7
C162	C6	C247	R159	D5	R549	B7	T176	F2	T717	C7
C163	C6	C248	R160	D5	R552	B7	T177	F2	T718	C7

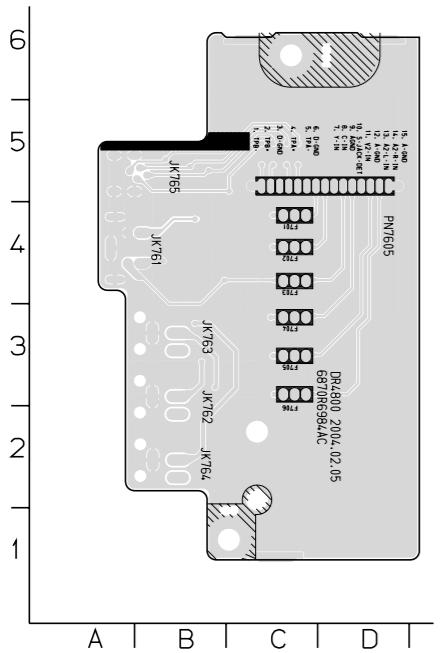
3. I/O P.C.BOARD



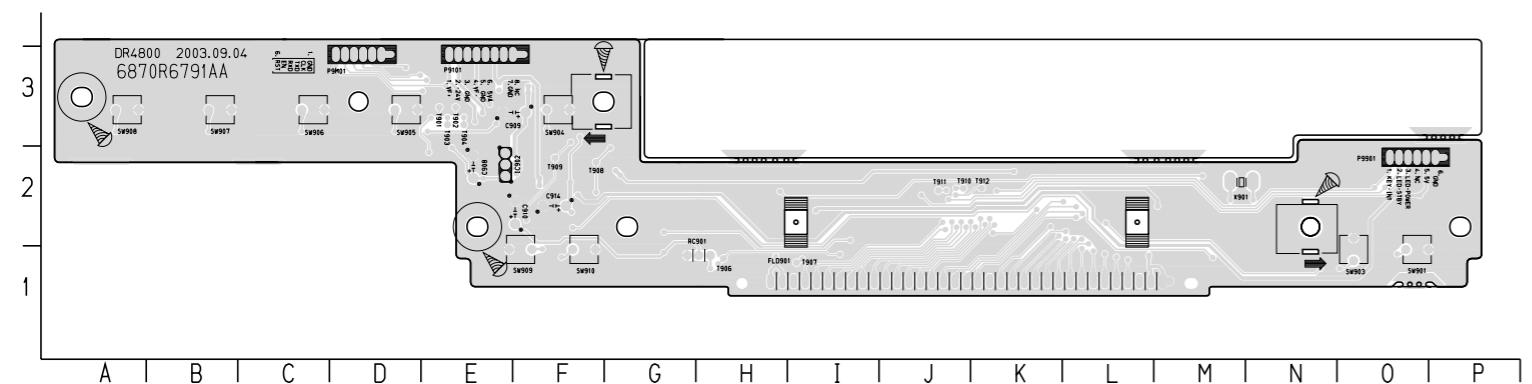
LOCATION GUIDE

C701	J7	C803	05	C856	02	C907	M3	C957	M6	D821	I2	JK764	B2	P10007 M4	P10108 F3	R708	F6	R767	C4	R852	M2	R843	H3	R943	N6	TP707	I6	TP808
C702	G5	C804	M4	C857	03	C908	L3	C958	M3	D822	H2	JK765	B5	P10008 F4	P10109 F3	R709	I5	R769	C3	R853	06	R844	I2	R944	N6	TP709	H6	TP809
C703	V5	C805	01	C858	L2	C909	M3	C959	L6	D823	H2	JK801	H1	P10009 F3	P10122 N7	R710	R5	R770	L4	R854	M6	R845	I6	R945	K6	TP710	H4	TP810
C704	G4	C806	G4	C859	M2	C910	L3	C960	L6	D824	H2	JK802	F1	P10010 F3	P10124 M6	R711	F6	R801	N2	R755	F3	R846	M6	R946	M7	TP711	H4	TP811
C705	V5	C807	N3	C860	F4	C911	L3	C961	L7	D825	H2	JK803	M1	P10011 F3	P10129 M6	R712	F6	R802	L3	R756	F6	R847	J2	R947	L7	TP712	H4	TP812
C706	H4	C808	G3	C861	L2	C912	L3	C962	L7	D826	H2	JK701	I5	P10013 M5	P10136 M4	R713	H4	R803	L4	R757	L4	R848	R2	R948	K5	TP713	H4	TP813
C707	H6	C809	M4	C862	L2	C913	L6	C963	L7	D827	H2	JK702	F6	P10014 M5	P10137 M4	R714	H3	R804	J4	R758	K5	R849	L2	R949	N6	TP720	H4	TP814
C708	H4	C810	G3	C863	M2	C914	L5	C964	L6	D828	H2	JK703	I5	P10015 F4	P10138 M4	R715	F6	R805	J2	R759	L4	R850	I3	R951	L6	TP721	I5	TP815
C709	H3	C811	G3	C864	L2	C915	G3	C965	N2	D829	F2	JK704	G4	P10016 L5	P10140 J5	R716	G5	R806	H5	R760	N2	R851	M6	R952	M7	TP722	H4	TP816
C710	I6	C812	P3	C865	M5	C916	M3	C966	M3	D830	F2	JK705	I5	P10017 LG	P10141 K5	R717	H4	R807	J2	R761	N6	R853	C3	R953	L7	TP723	H5	TP817
C711	V5	C813	F3	C866	M5	C917	L5	C967	D2	D831	J2	JK711	B3	P10018 KG	P10142 J5	R718	G5	R808	K1	R762	N5	R854	L5	R954	L7	TP730	G5	TP818
C712	F6	C814	N6	C867	M5	C918	K6	C968	H2	D832	F2	JK701	B3	P10019 KG	P10143 J4	R719	F6	R809	K2	R763	F3	R855	I3	R955	L7	TP731	H4	TP819
C713	G5	C815	M6	C868	C3	C919	M3	C969	H2	D833	F2	JK702	B1	P10020 M6	P10147 M4	R720	I5	R810	J2	R764	F3	R856	I3	R956	L7	TP732	H4	TP820
C714	I5	C816	J5	C869	P3	C920	P3	C970	H2	D834	F2	JK703	C4	P10021 M6	P10149 H4	R721	H4	R811	I3	R765	F3	R857	I3	R957	L6	TP733	H4	TP821
C715	G6	C817	06	C870	F3	C921	P3	C971	M5	D874	C3	JK803	N3	P10022 F3	P10150 M4	R722	G6	R812	F2	R766	I2	R858	F3	R958	L5	TP738	I5	TP822
C716	G5	C818	L5	C871	F3	C922	G4	C972	M6	D875	C3	JK804	O5	P10026 G3	P10153 G3	R723	G4	R813	O7	R767	I3	R859	I3	R959	L3	TP740	H4	TP823
C717	G6	C819	N6	C872	C3	C923	K3	C973	M6	D876	C3	JK805	L5	P10027 L5	P10157 G4	R724	G5	R814	O7	R768	I3	R860	I3	R960	M5	TP742	H2	TP824
C718	H3	C820	N6	C873	M5	C924	N3	C974	I3	D877	J2	JK806	M6	P10028 L5	P10158 G4	R725	I3	R815	O4	R769	I3	R861	H2	R960	P4	TP743	I6	TP825
C719	H3	C822	M6	C874	N5	C925	K4	C975	H2	D878	H2	JK807	N5	P10029 F3	P10173 O4	R726	I6	R816	P4	R770	H3	R969	G3	TP826	I4	TP827		
C720	H3	C823	M5	C875	G3	C926	H2	C976	G3	D879	F2	JK808	B2	P10030 L5	P10185 L4	R727	I7	R817	K3	R771	O2	R910	K3	TP828	I4	TP829		
C721	I7	C824	N6	C876	N5	C927	J3	C977	N5	D880	F3	JK809	N6	P10031 L5	P10186 L4	R728	H3	R818	K5	R772	O2	R911	L6	TP829	P3	TP747		
C722	M7	C825	O5	C877	N5	C928	M6	C978	N4	D881	F4	JK810	G2	P10032 F3	P10189 J2	R729	F6	R819	O2	R773	F3	R912	L6	TP830	P3	TP748		
C723	G7	C826	F2	C878	N6	C929	J5	C979	S5	D870	H6	JK806	D2	P10033 L5	P10210 H1	R731	H6	R820	P2	R774	I2	R913	M4	TP831	P4	TP749		
C724	I6	C827	F4	C879	P5	C930	N3	C980	D7	D882	F2	JK812	N2	P10040 H6	P10211 H6	R732	G6	R821	L6	R767	I5	R914	M4	TP832	N2	TP750		
C725	I3	C828	N5	C880	P2	C931	M3	C981	D7	D883	H2	JK813	G5	P10042 G4	P10212 H6	R733	H6	R822	N2	R768	I5	R915	J4	TP833	M5	TP752		
C726	I6	C829	O6	C881	P6	C932	G3	C982	D7	D884	H5	JK814	I5	P10043 H6	P10213 H6	R734	G5	R823	L6	R769	I7	R916	H2	TP834	M2	TP753		
C727	I4	C830	M5	C882	P6	C933	N4	C983	D7	D885	H6	JK815	G5	P10045 L1	P10214 H6	R735	G6	R824	L5	R770	I2	R917	N2	TP835	I4	TP764		
C728	F6	C831	L6	C883	L5	C934	P3	C984	D7	D886	H5	JK816	F6	P10046 L5	P10215 H6	R736	G6	R825	L5	R771	I2	R918	K2	TP836	I4	TP765		
C729	I7	C832	M2	C884	O6	C935	L6	C985	D7	D887	H6	JK817	N4	P10047 H6	P10216 H6	R737	I5	R826	L6	R772	I4	R919	K1	TP837	I5	TP766		
C730	J7	C834	L2	C885	M5	C936	N4	C986	D7	D888	H7	JK818	J7	P10048 H6	P10203 H7	R738	I7	R827	M4	R773	I2	R920	N4	TP838	I2	TP767		
C731	H3	C835	L6	C886	O6	C937	G4	C987	D8	D889	H7	JK819	P4	P10049 I2	P10204 H7	R739	J6	R828	O4	R774	F3	R921	N2	TP839	I1	TP768		
C732	G6	C836	M2	C887	O1	C938	O1	C988	D8	D890	M2	JK820	L5	P10051 H7	P10205 H7	R740	I3	R829	F4	R775	F3	R922	I2	TP840	M4	TP769		
C733	I6	C837	L2	C888	M5	C939	L5	C989	D8	D891	M2	JK821	O4	P10052 E6	P10206 I2	R741	I3	R830	O4	R776	I2	R923	K5	TP841	M5	TP770		
C734	I7	C838	N6	C889	N6	C940	F2	C990	D8	D892	L4	JK822	H4	P10053 E6	P10207 H2	R742	F5	R831	R5	R777	H3	R924	I4	TP842	M5	TP771		
C735	H3	C839	M6	C890	N6	C941	N5	C991	D8	D893	L4	JK823	G5	P10054 L1	P10214 H6	R743	G6	R832	F3	R778	I2	R925	K2	TP843	M5	TP772		
C736	H3	C840	F3	C891	J5	C942	N5	C992	D8	D894	H2	JK824	N5	P10055 H6	P10217 F2	R745	H3	R833	R4	R779	H2	R926	K2	TP844	M5	TP773		
C737	I5	C841	F3	C892	J3	C943	N3	C993	D8	D895	H2	JK825	O6	P10056 L1	P10218 G4	R746	H3	R834	G4	R780	J5	R927	L6	TP845	M5	TP774		
C738	F4	C842	K5	C893	J6	C944	N2	C994	D8	D896	H2	JK826	P5	P10057 H6	P10208 J2	R747	I3	R835	L6	R781	J5	R928	K2	TP846	M5	TP775		
C739	O4	C843	L5	C894	J6	C945	O3	C995	D8	D897	H2	JK827	M5	P10058 H6	P10209 S3	R748	M5	R836	R7	R782	M5	R929	N7	TP847	H5	TP776		
C740	F5	C845	O5	C895	K5	C946	N2	C996	D8	D898	H2	JK828	L5	P10059 H6	P10210 H7	R749	I3	R837	O3	R783	F4	R930	N7	TP848	I5	TP777		
C741	F7	C845	N6	C896	K6	C947	N2	C997	D8	D899	H2	JK829	G4	P10060 I2	P10211 H6	R750	J6	R838	O3	R784	F4	R931	K5	TP849	I5	TP778		
C742	F4	C846	N6	C897	N6	C948	O3	C998	D8	D900	H2	JK830	N3	P10061 H6	P10212 H6	R751	J6	R839	I3	R785	F3	R932	F2	TP850	I4	TP779		
C743	G4	C847	O5	C898	J6	C949	O4	C999	D8	D901	H2	JK831	O3	P10062 H6	P10213 H6	R752	J6	R840	F3	R786	F3	R933	K5	TP851	I5	TP780		
C744	I4	C848	L5	C899	P3	C950	L6	C990	D8	D902	H2	JK832	N3	P10063 H6	P10214 H6	R753	I4	R841	M4	R787	I4	R934	N7	TP852	I5	TP781		
C745	I6	C850	L5	C900	K3	C951	M6	C991	D8	D903	H2	JK833	N3	P10064 H6	P10215 H6	R754	I4	R842	M6	R788	I4	R935	N7	TP853	I5	TP782		
C746	F5	C851	F3	C902	K5	C952	L6	C992	D8	D904	H2	JK834	J2	P10065 H6	P10216 H6	R755	I4	R843	M6	R789	I4	R936	N7	TP854	I5	TP783		
C747	B3	C852	L5	C903	K3	C953	N5	C993	D8	D905	H2	JK835	I2	P10066 H6	P10217 H6	R756	I4	R844	M6	R790	I6	R937	L6	TP855	I5	TP784		
C748	B3	C853	K5	C904	C9	C954	P5	C994	D8	D906	F2	JK836	B3	P10067 H6	P10218 H6	R757	I4	R845	M6	R791	I6	R938	I3	TP856	I5	TP785		
C749	F2	C854	C9	C905	L6	C955	I2	C995	D8	D907	F2	JK837	B3	P10068 H6	P10219 H6	R758	I4	R846	M6	R792	I6	R939	J4	TP857	I5	TP786		
C750	C801	F2	C854	C906	L6	C956	I2	C996	D8	D908	F2	JK838	B3	P10069 H6	P10220 H6	R759	I4	R847	M6	R793	I6	R940	K3	TP858	I5	TP787		

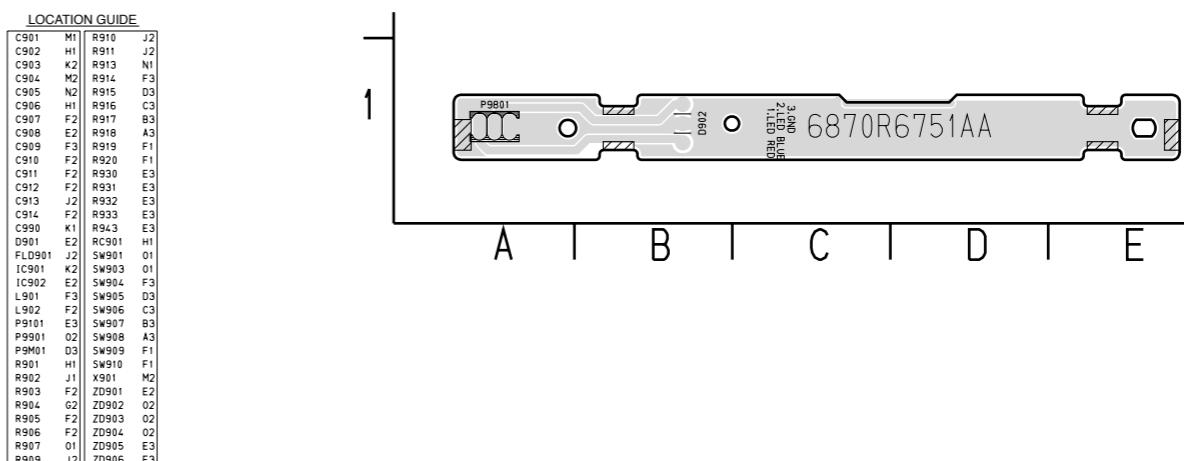
4. JACK P.C.BOARD



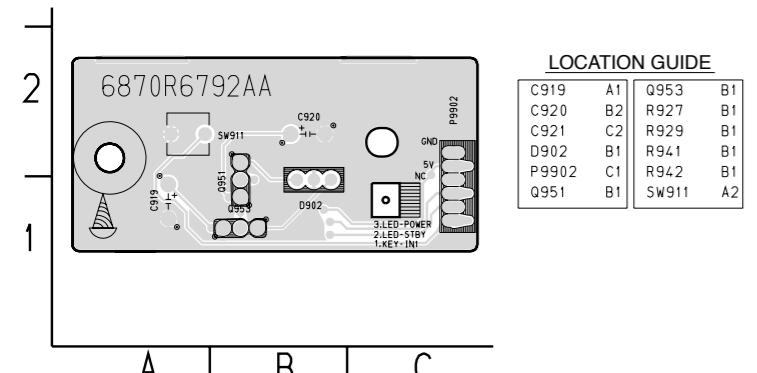
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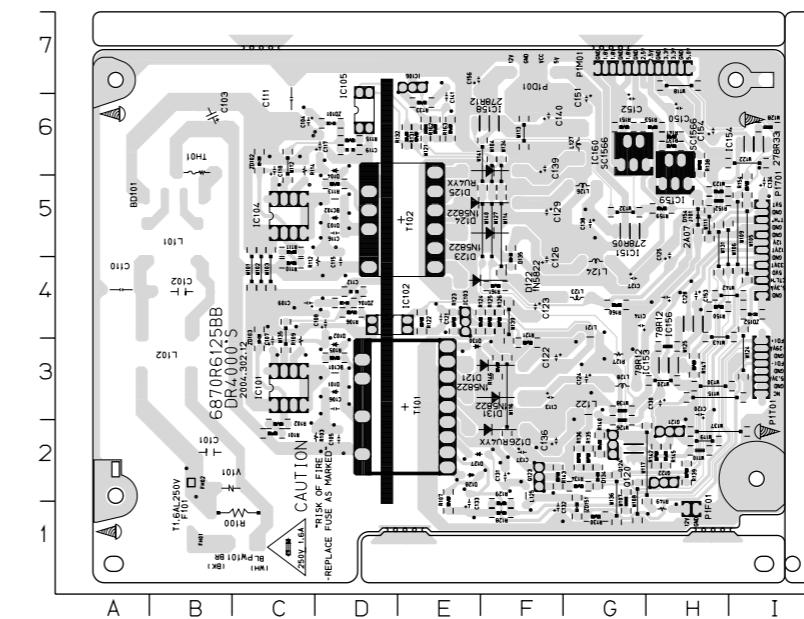
6. LED P.C.BOARD (9TOOL ONLY)



7. KEY P.C.BOARD

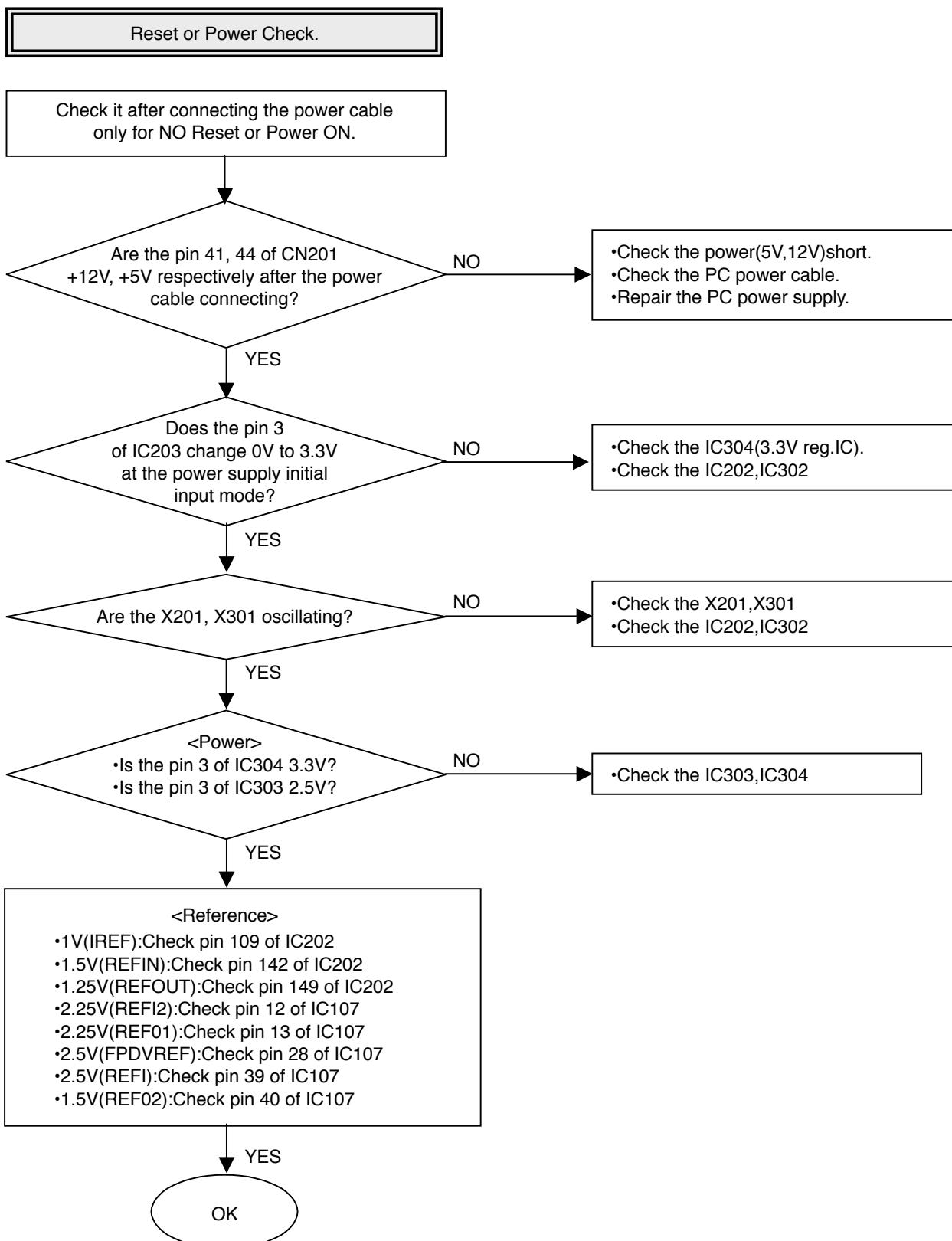


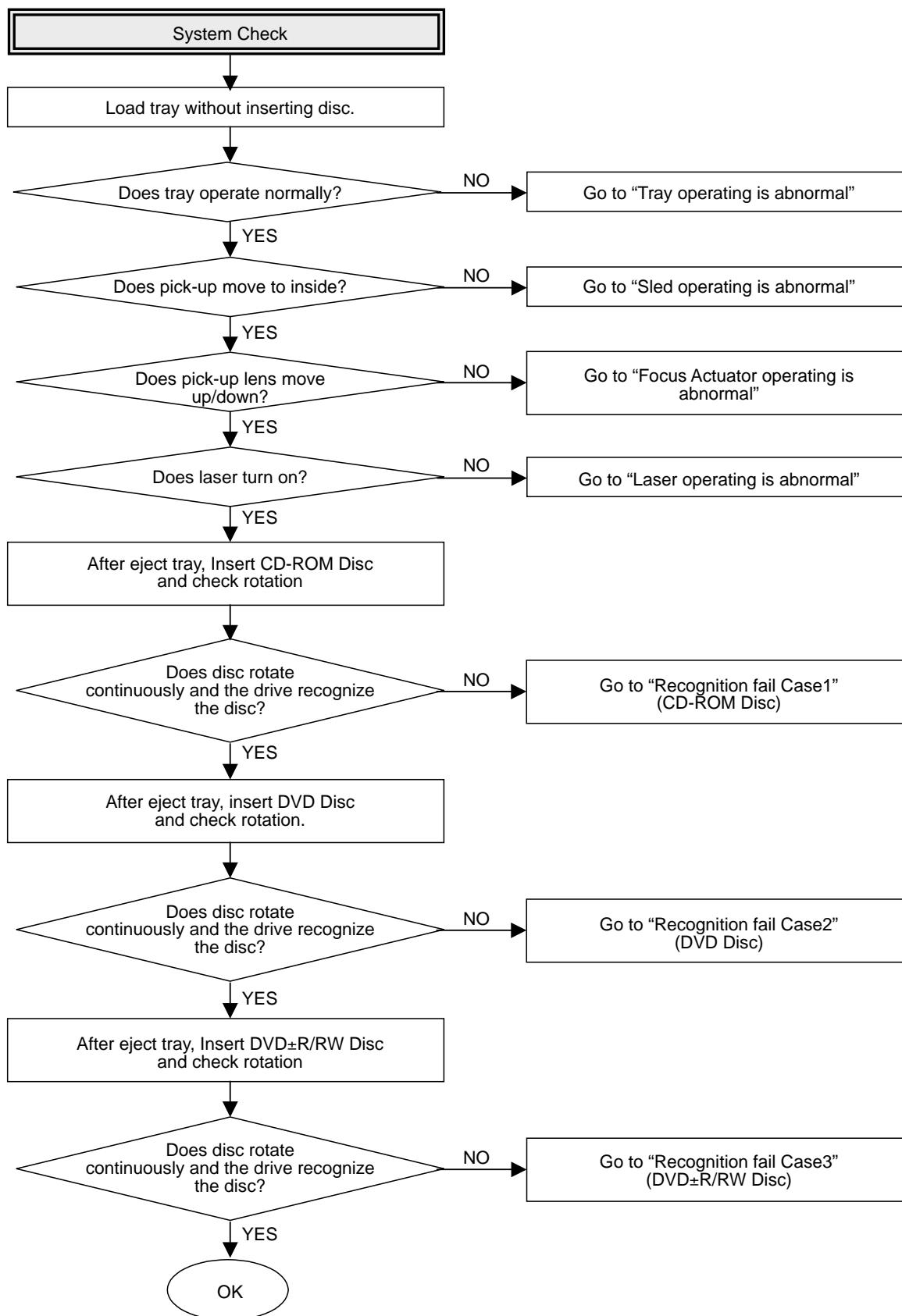
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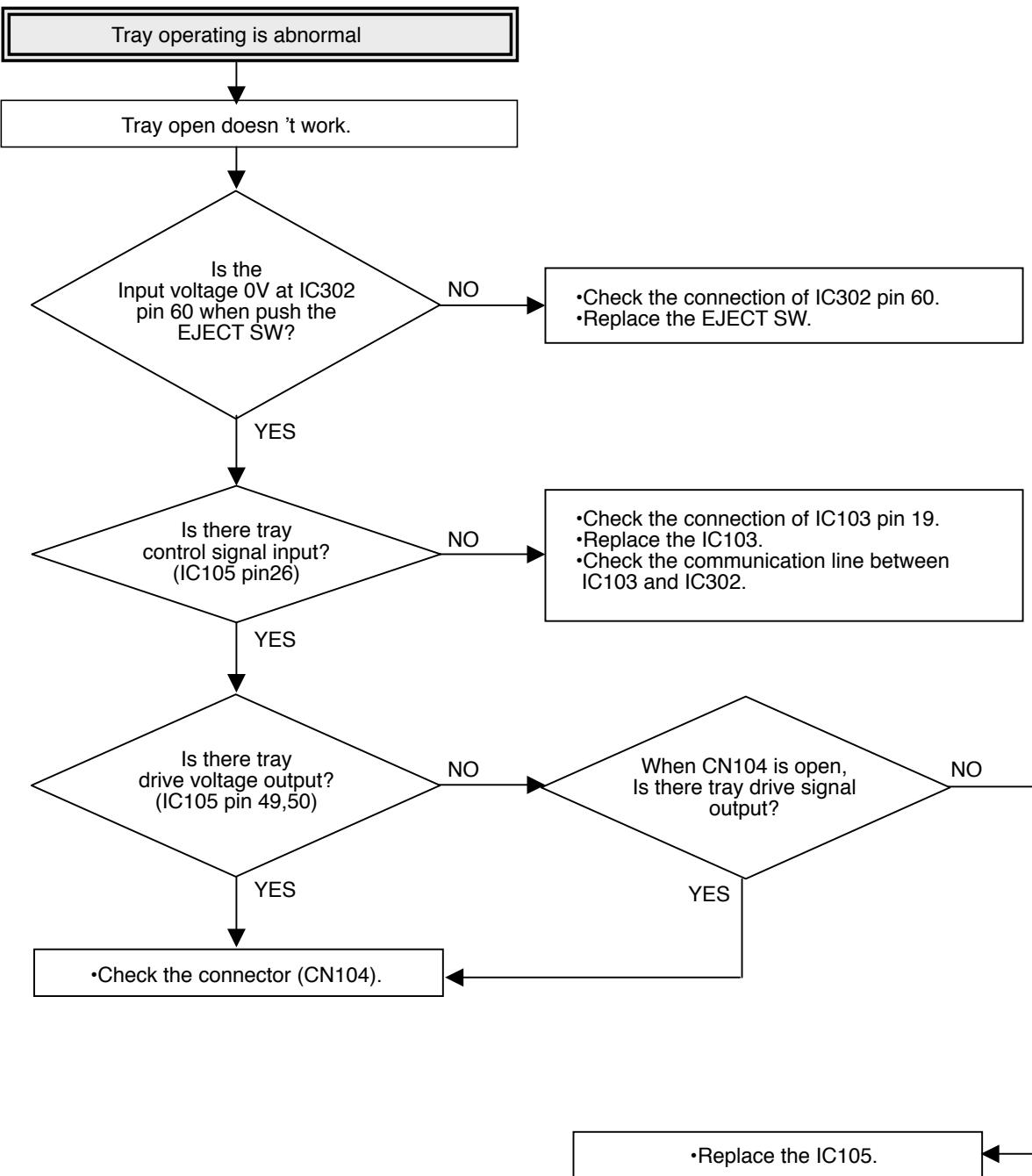


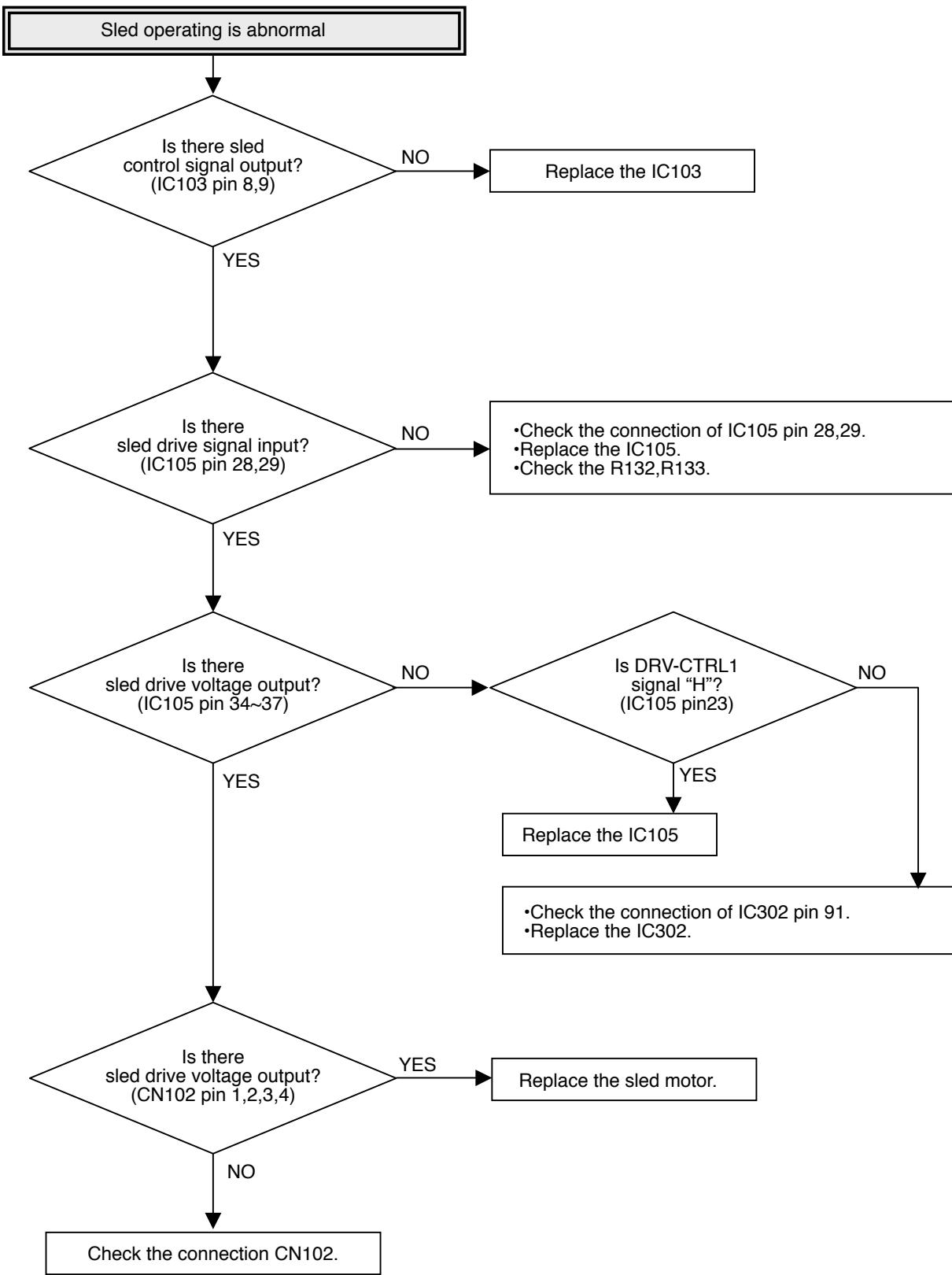
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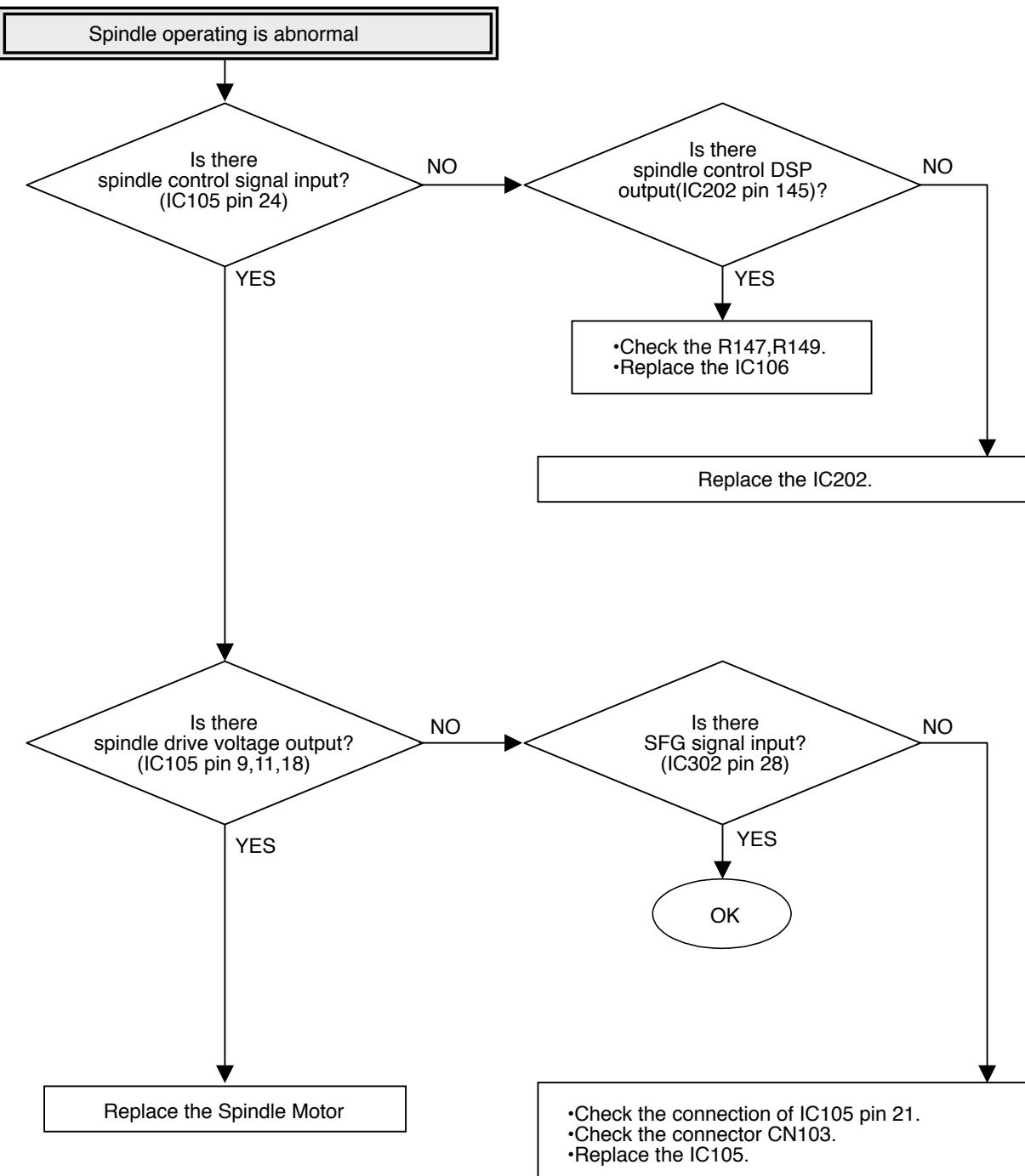
RL-01A LOADER PART ELECTRICAL TROUBLESHOOTING GUIDE

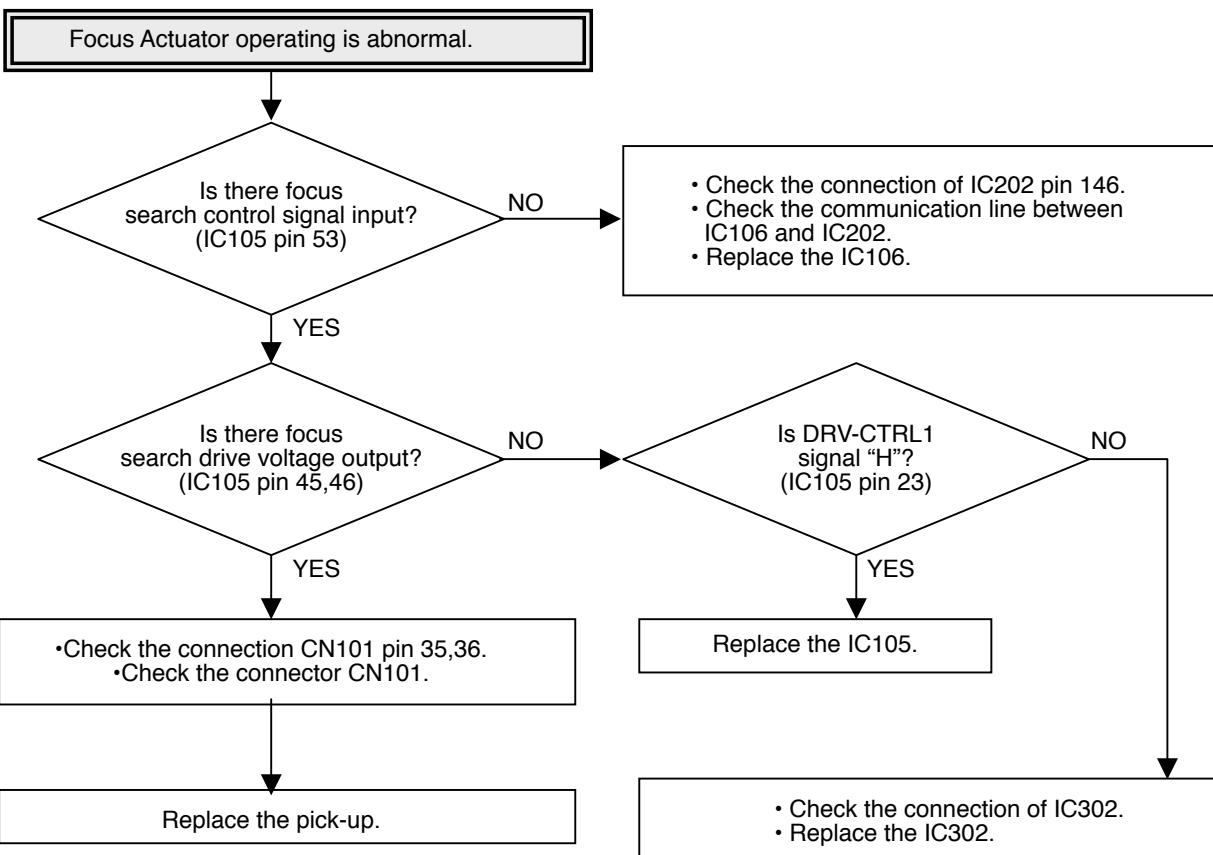
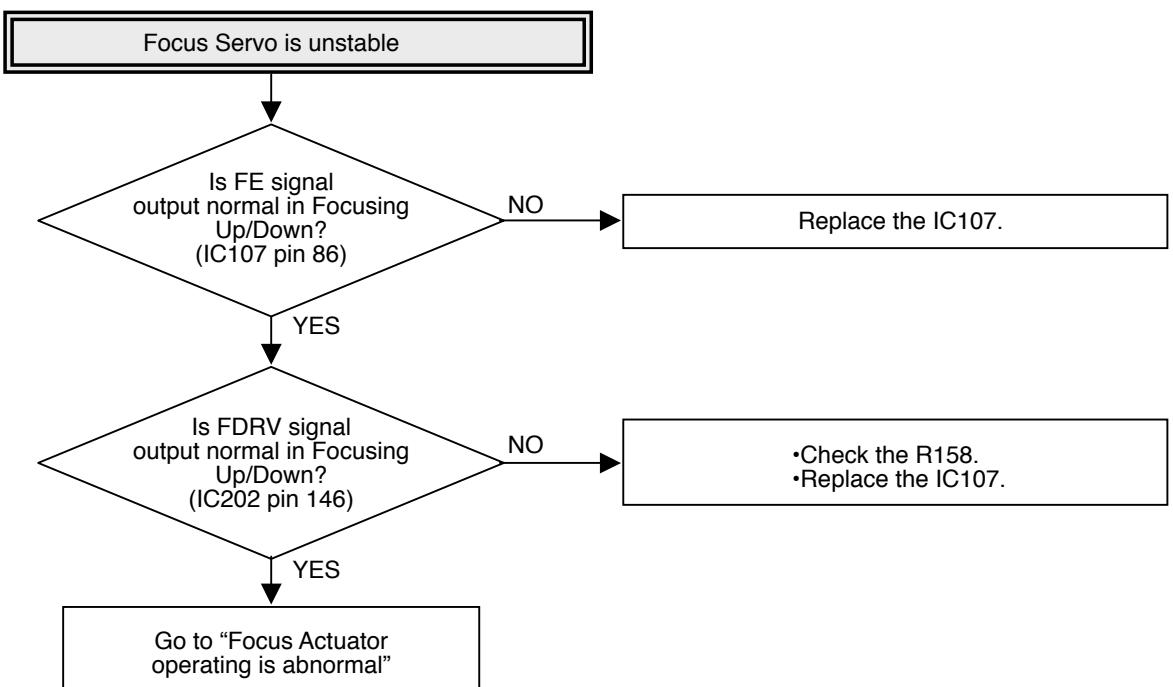


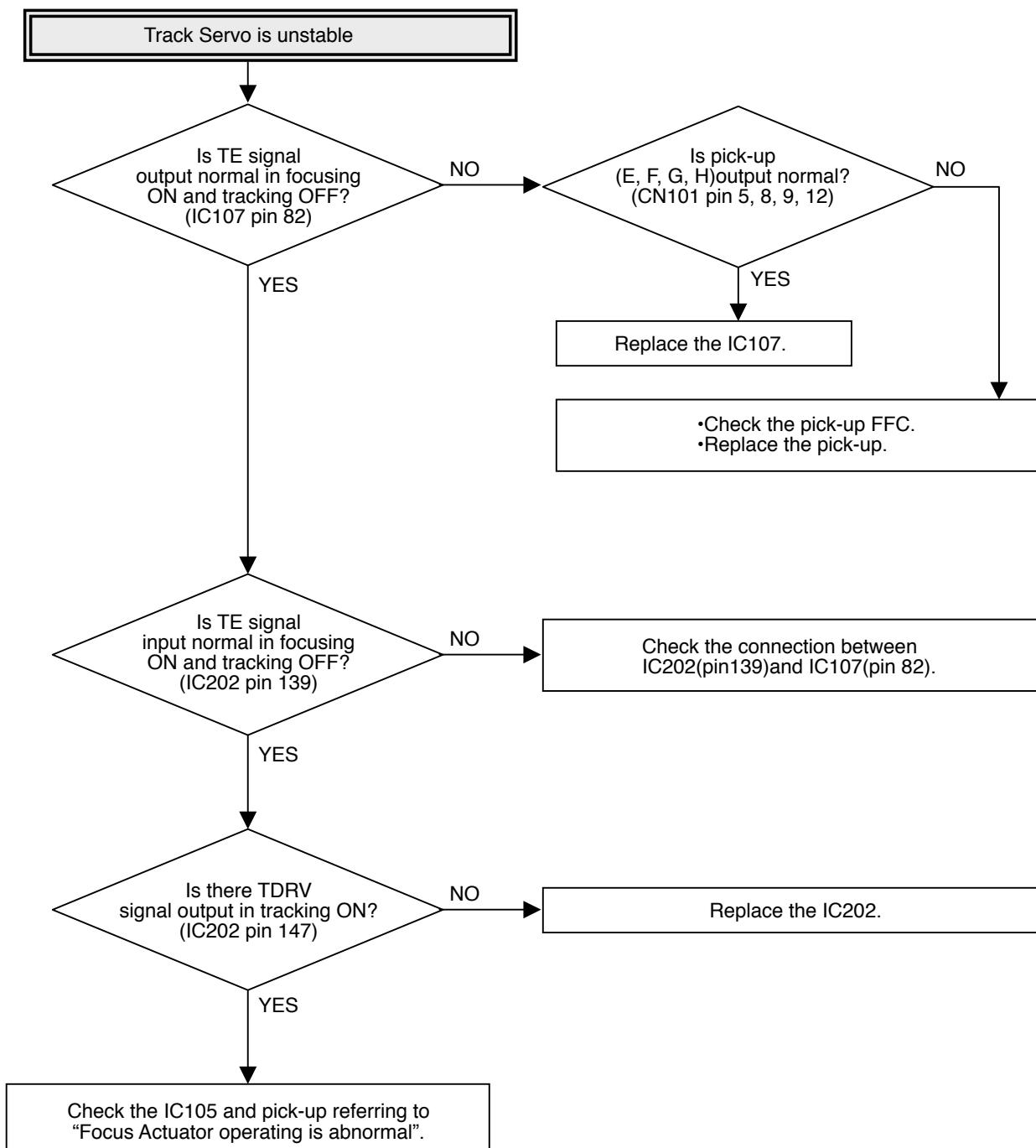


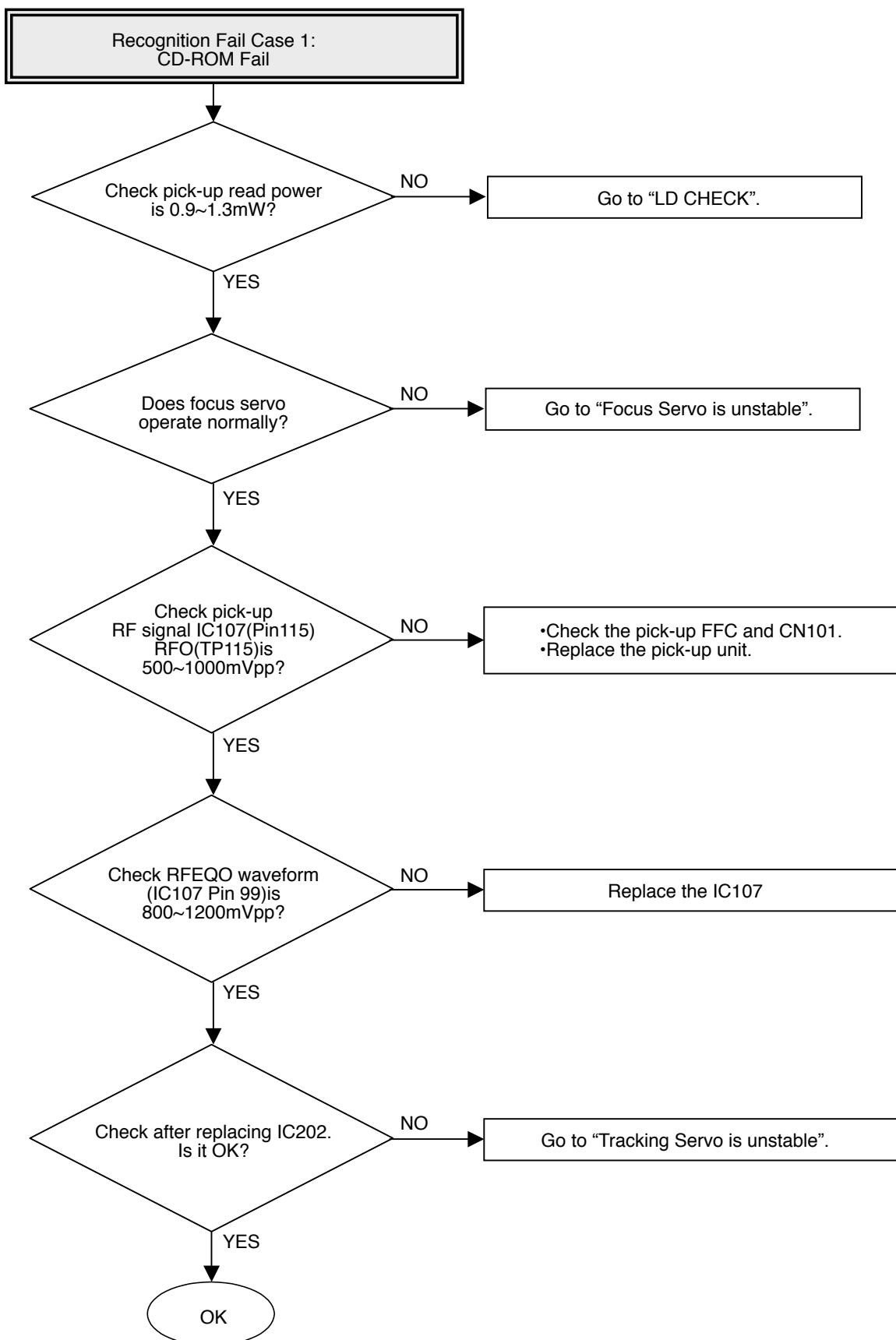


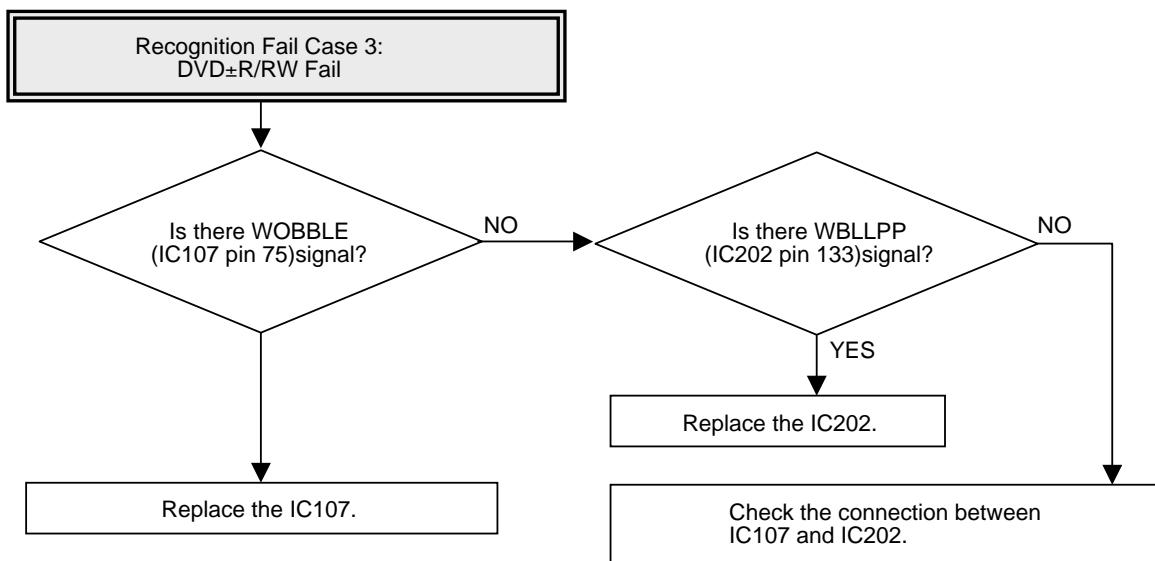
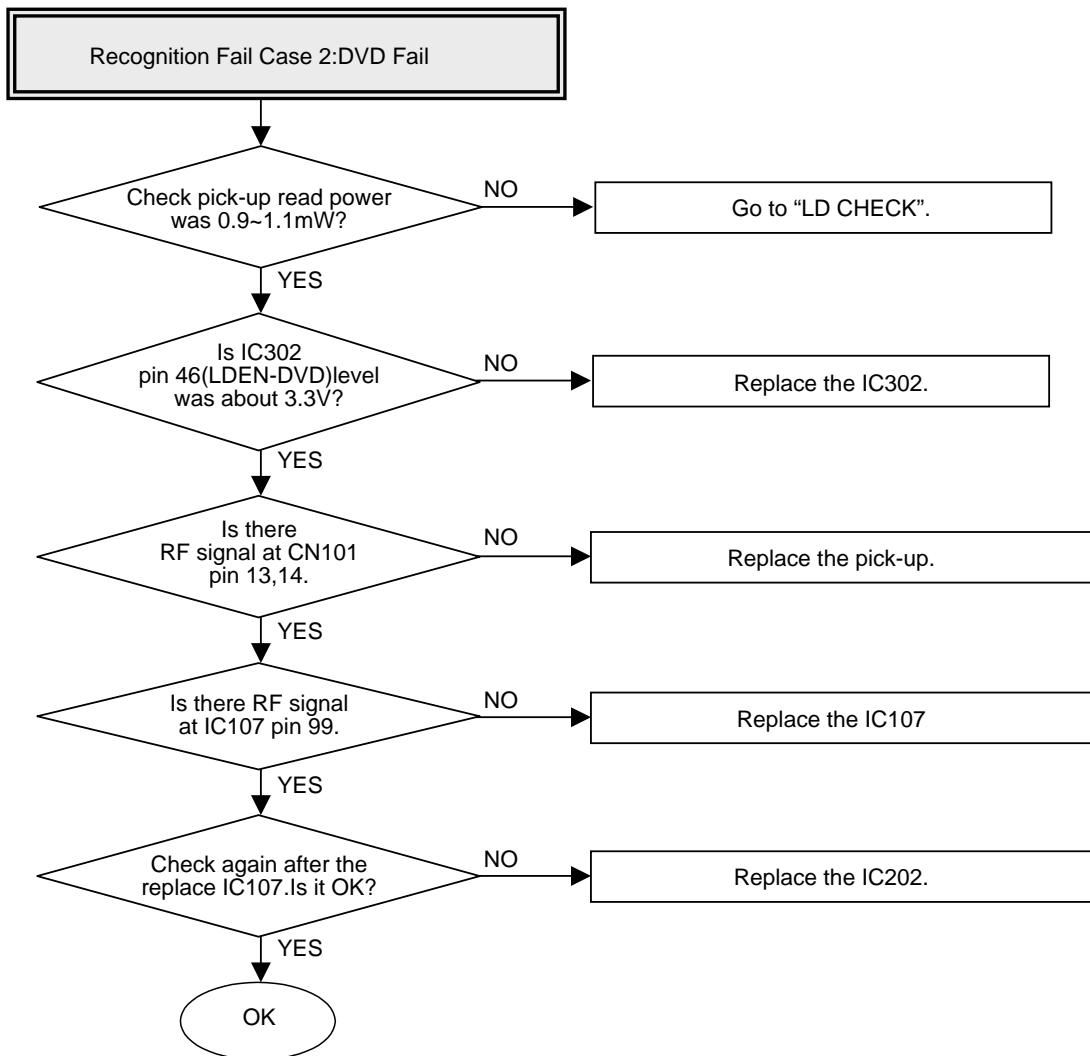


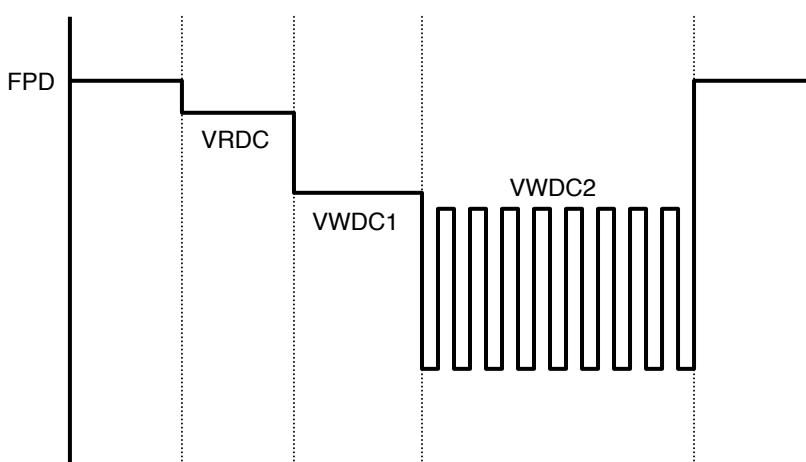
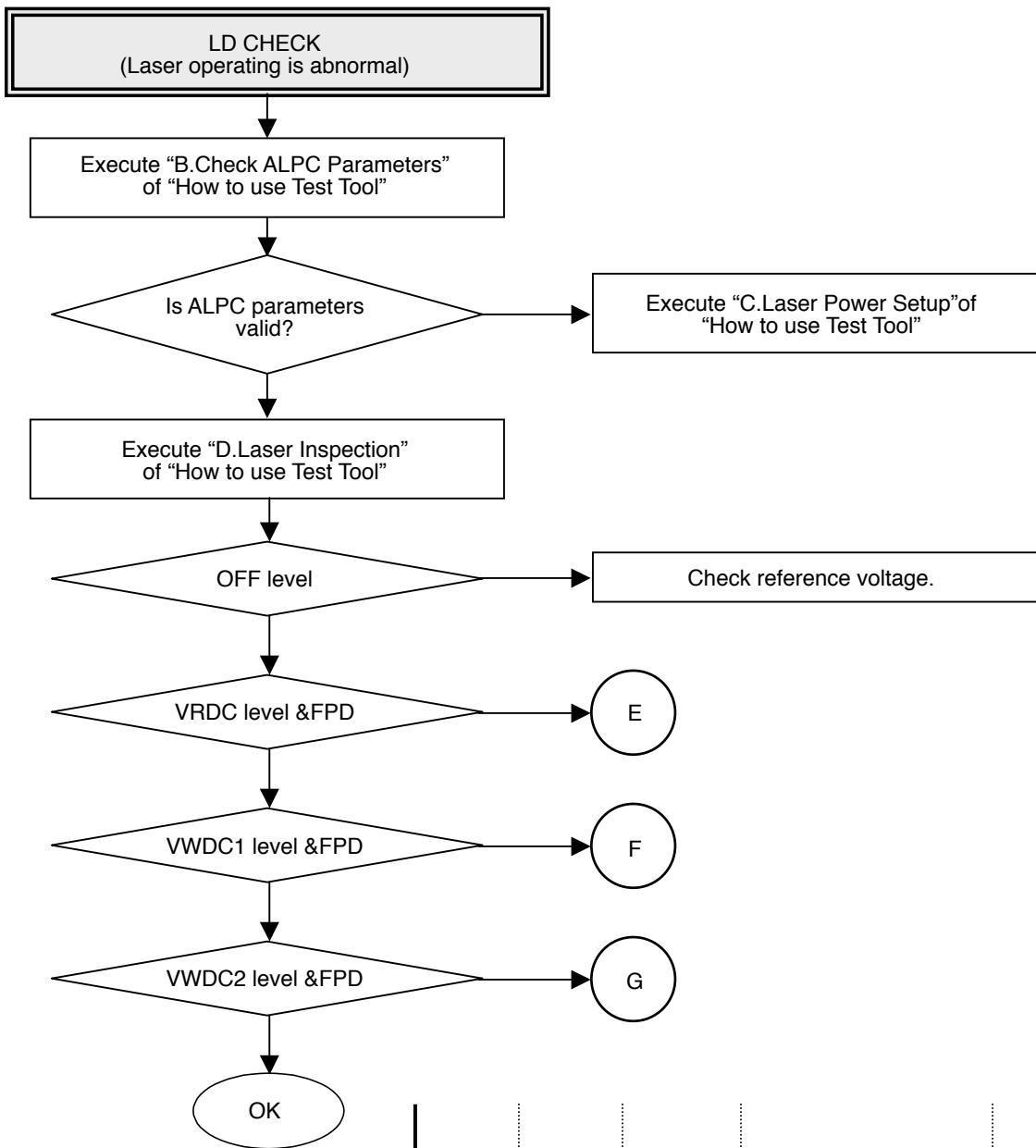


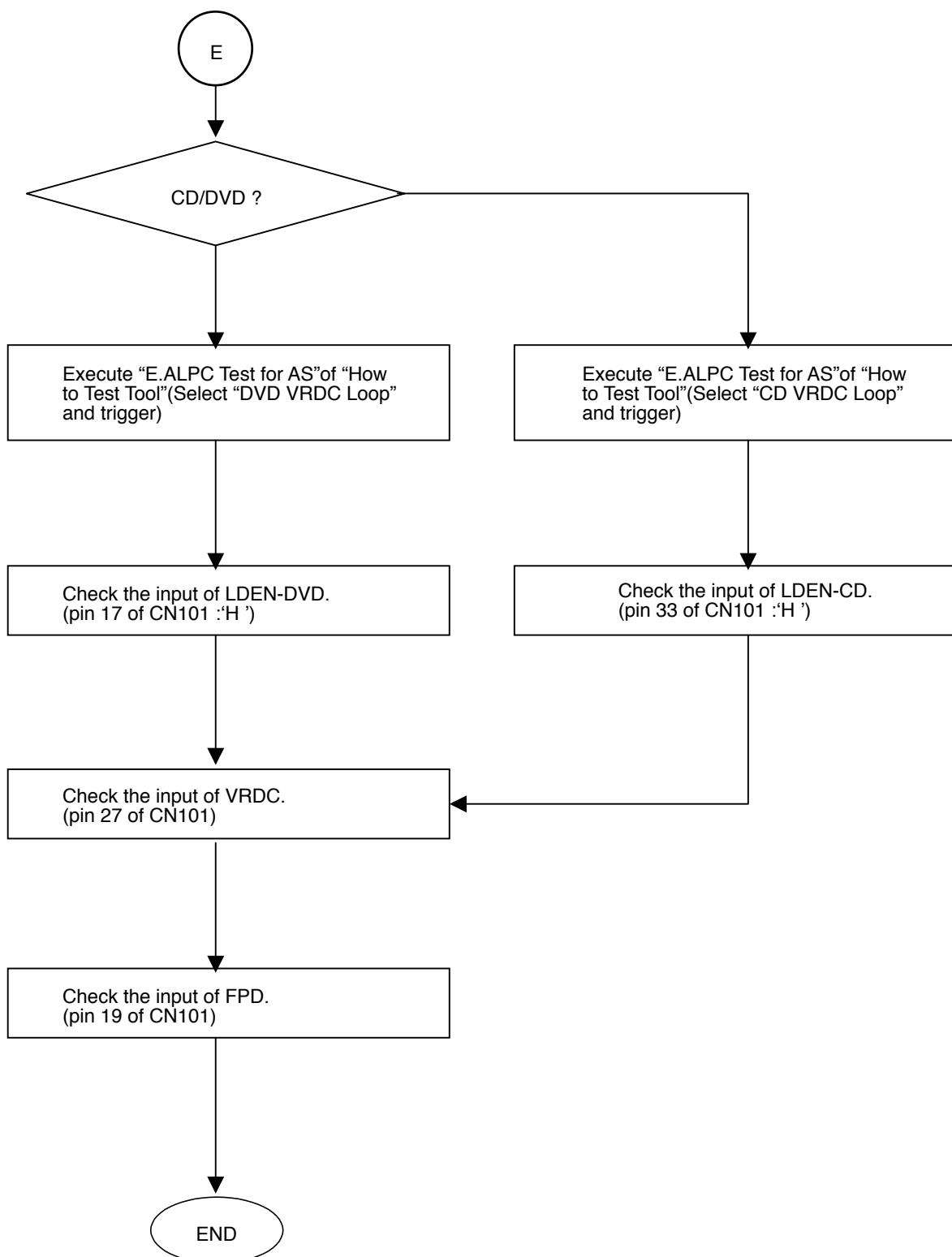


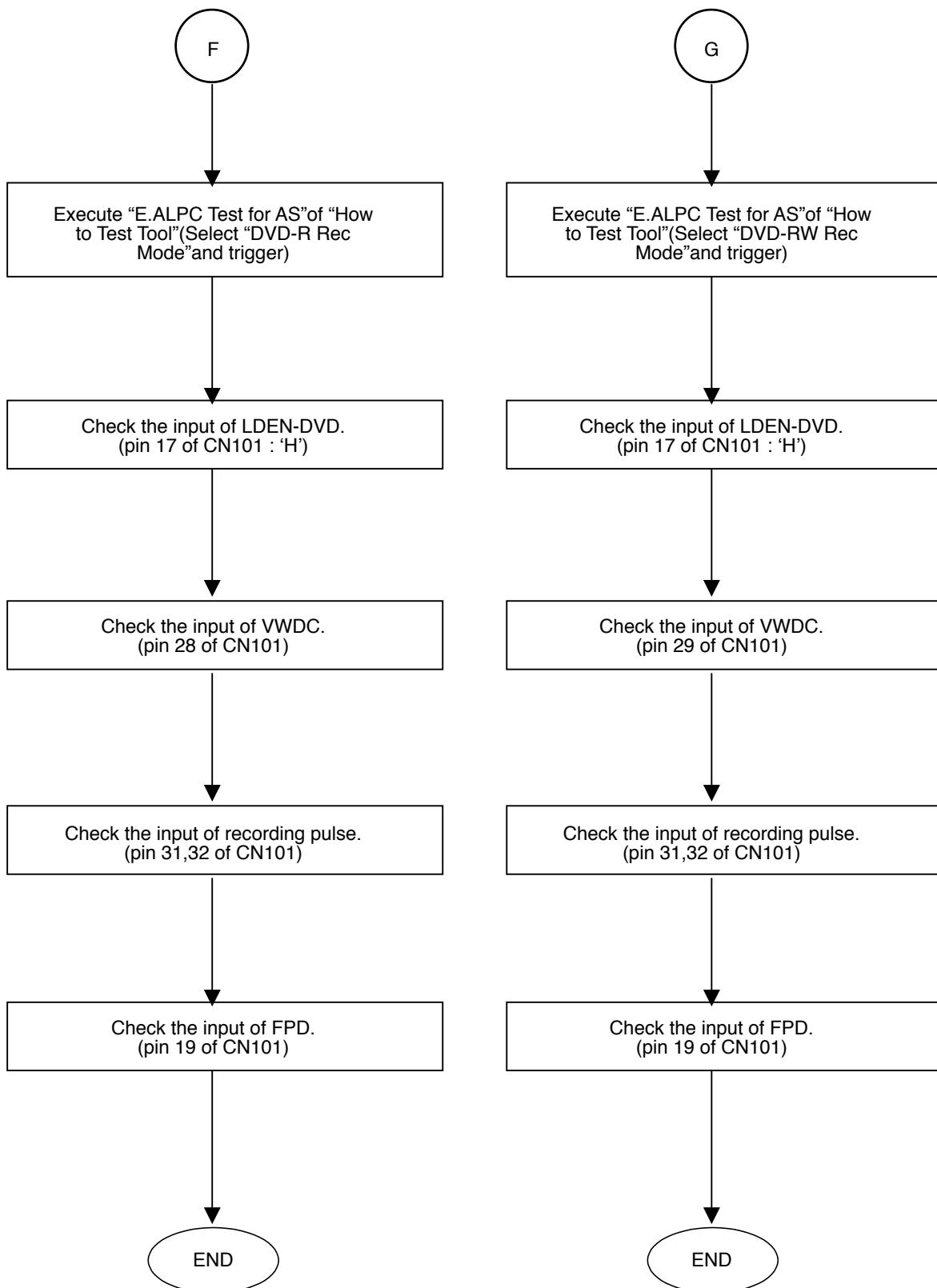


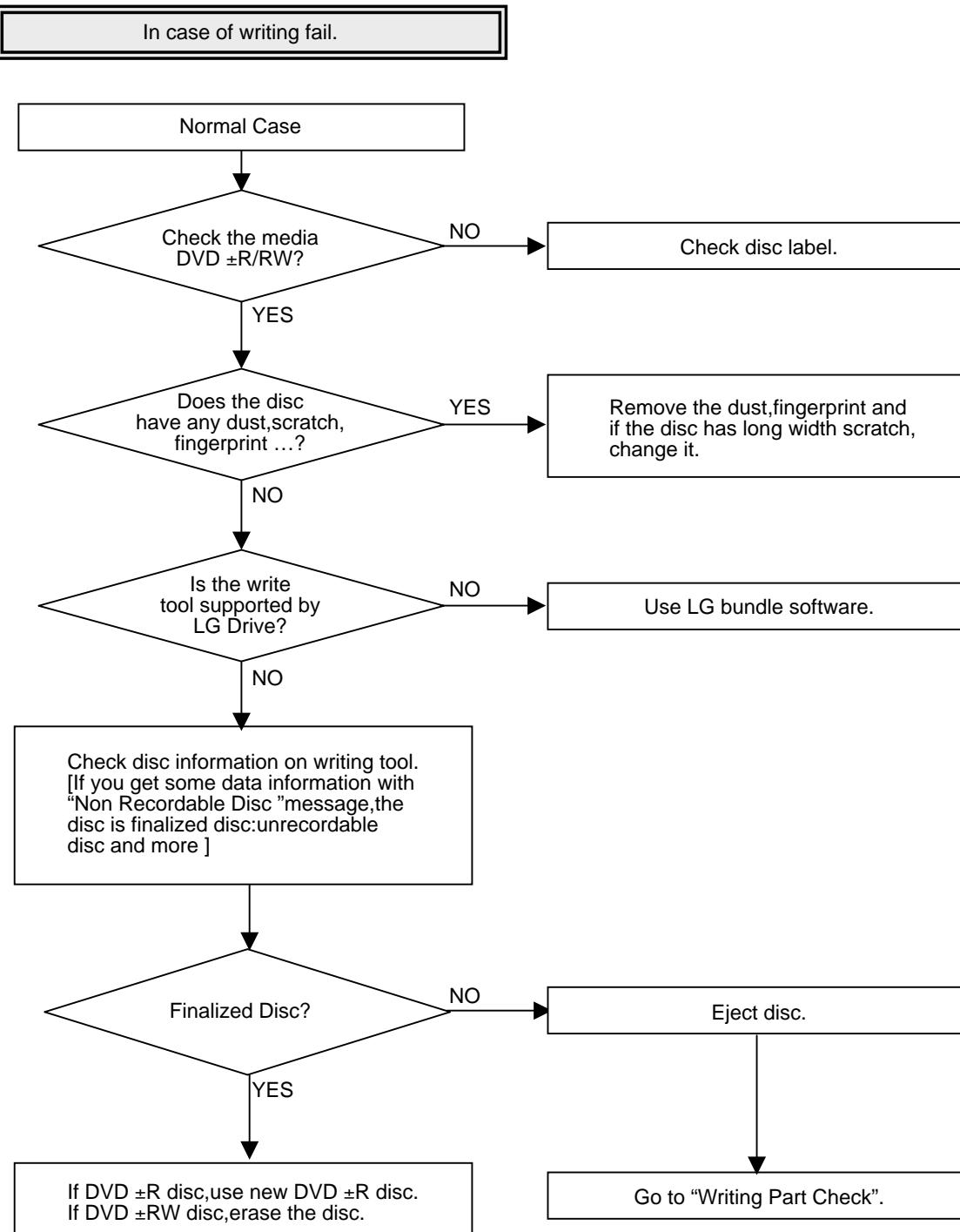


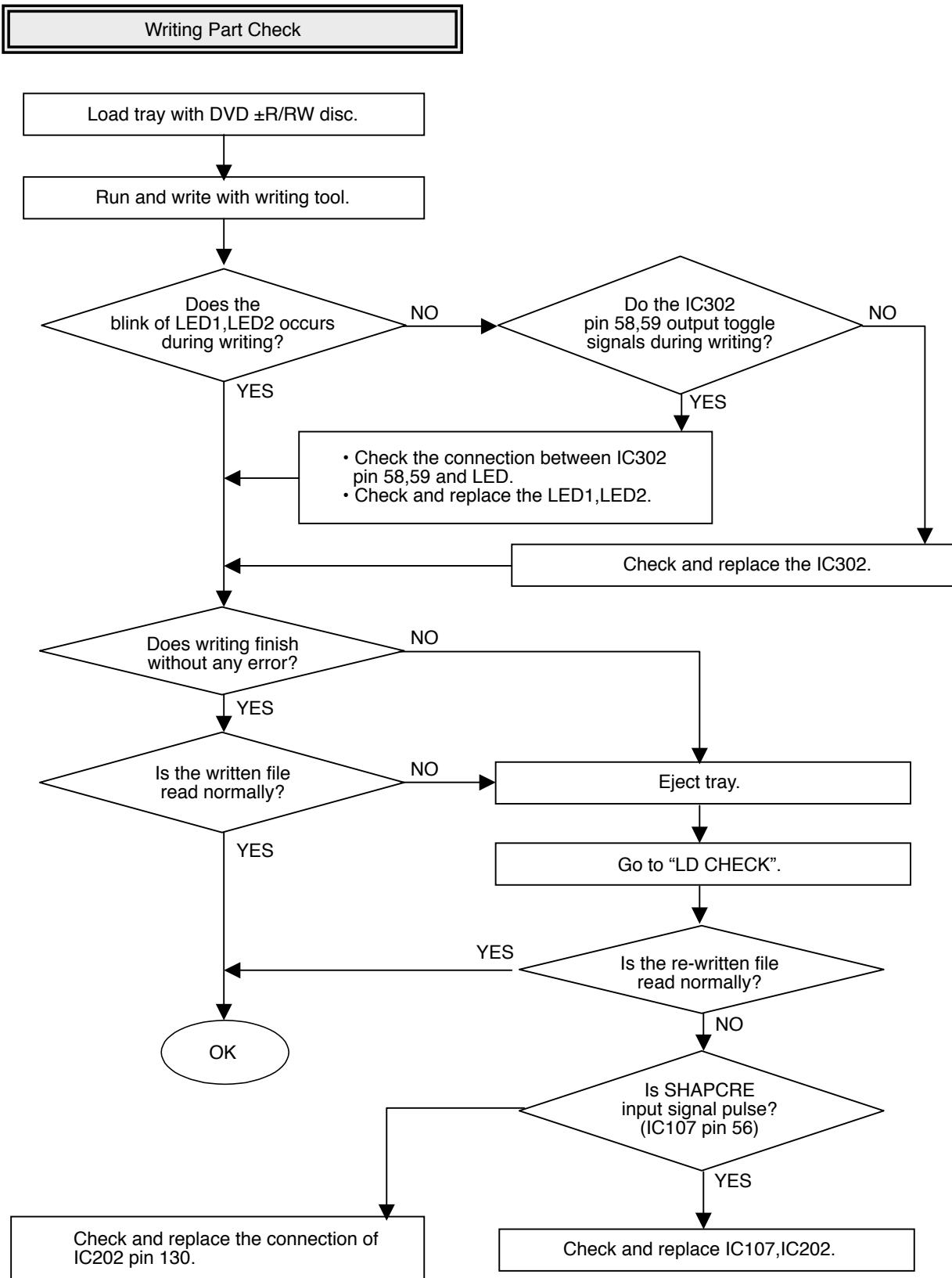






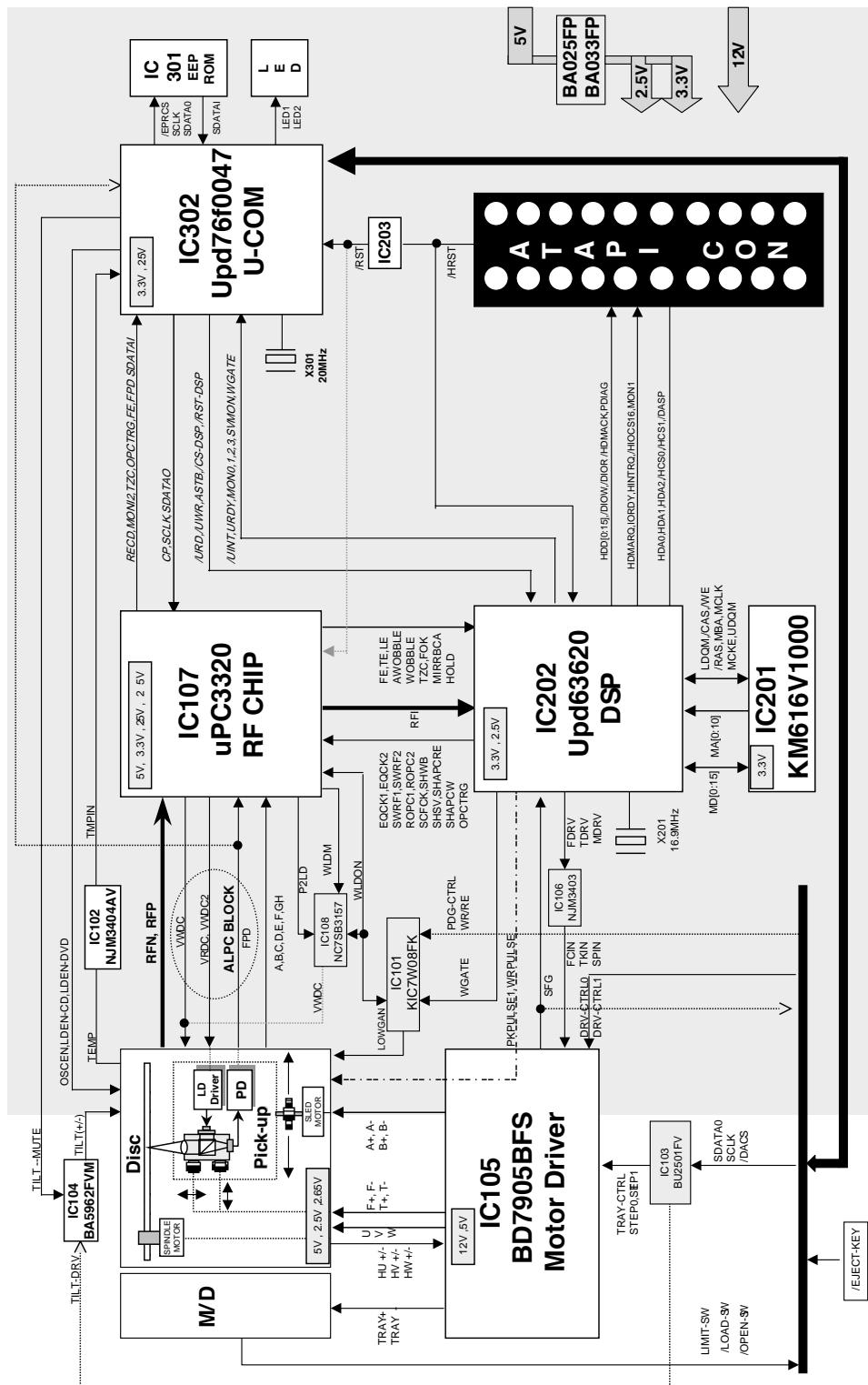






BLOCK DIAGRAMS & DESCRIPTION

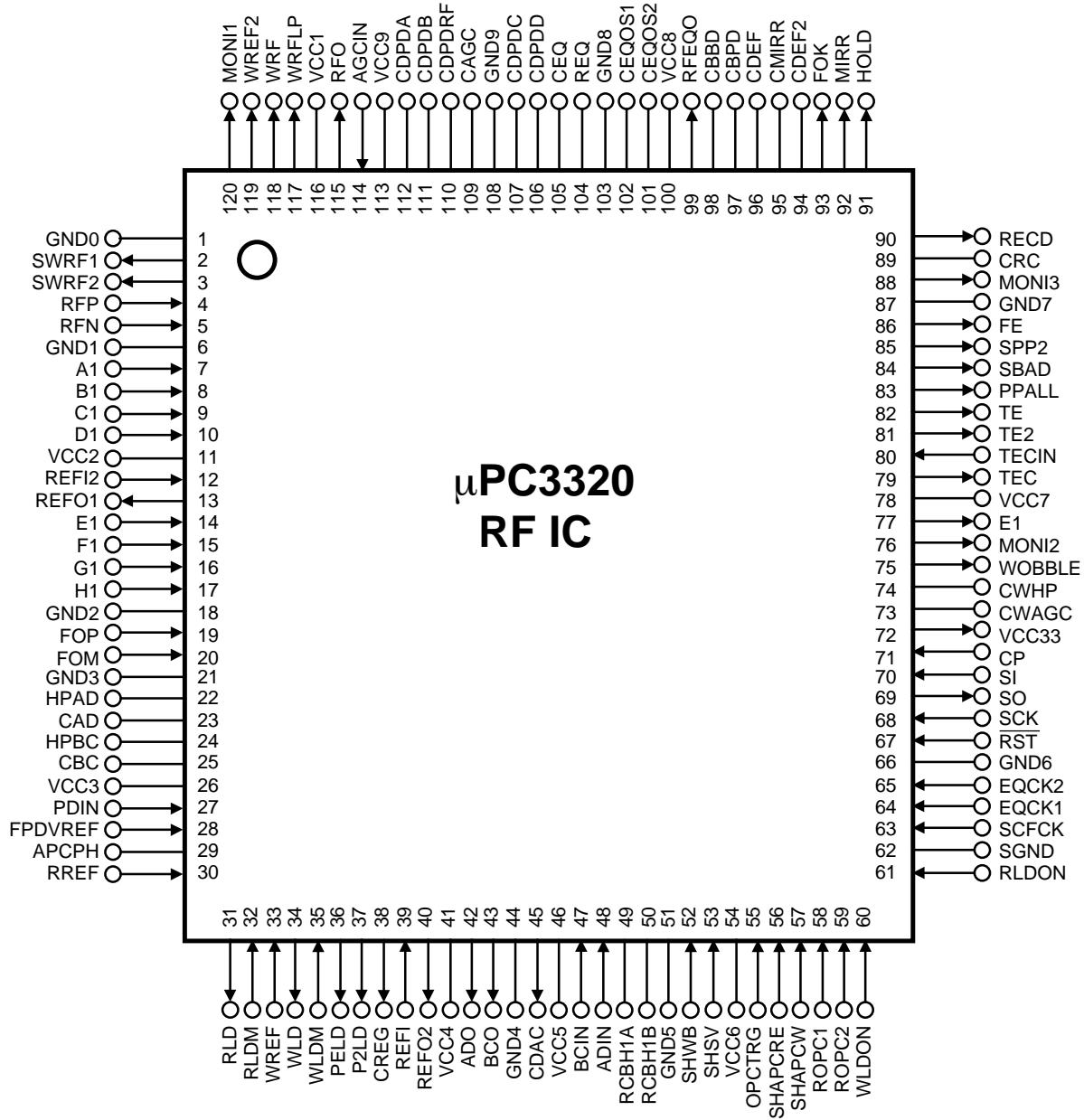
1. Overall Block Diagram



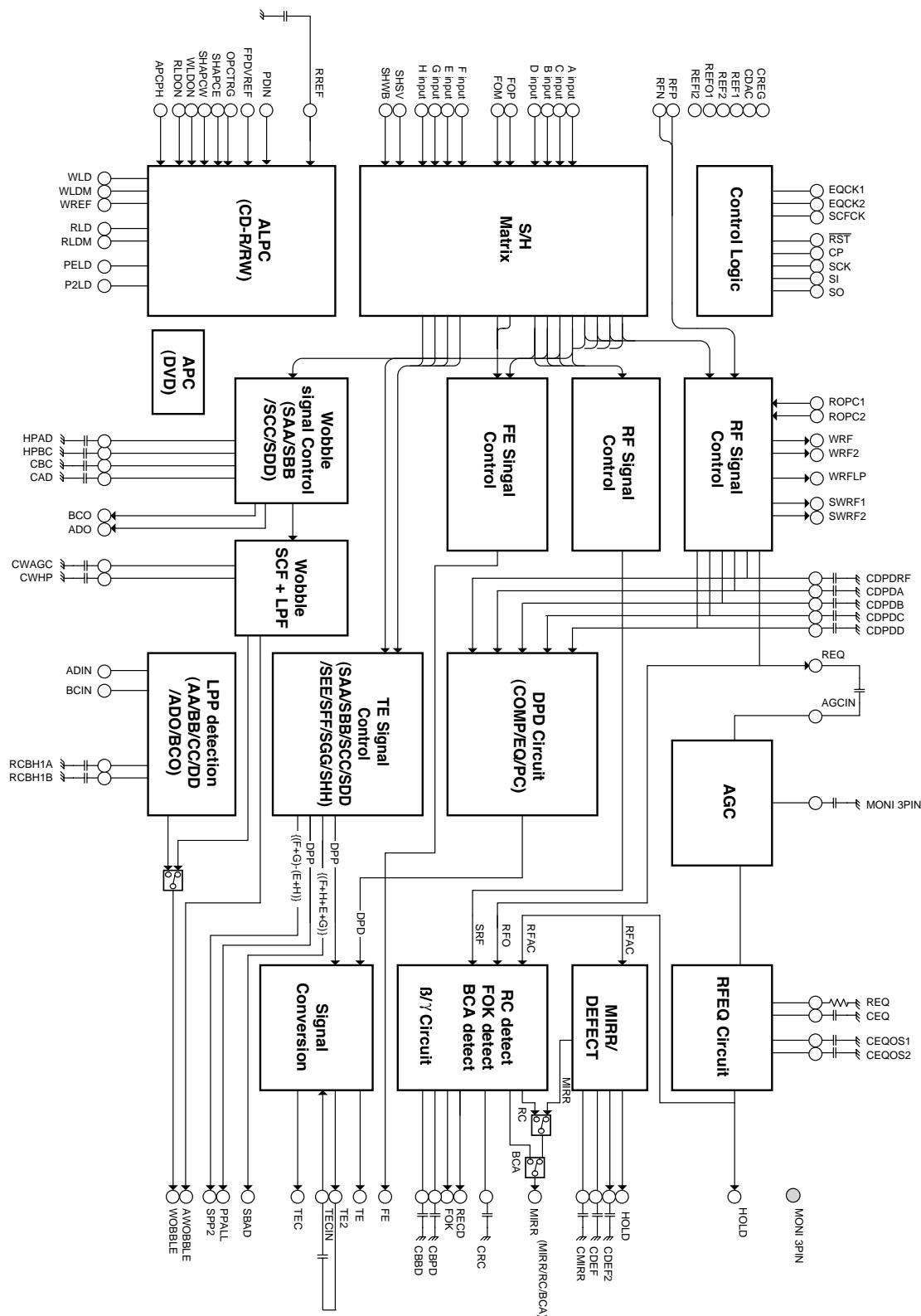
2. MAJOR IC INTERNAL BLOCK DIAGRAM AND PIN DESCRIPTION

IC101 (μ PC3320) : RF Signal Processor for CD/DVD

Pin Assignment



Block Diagram



Pin description

No.	Pin Name	Type	Description
1	GND0	-	Analog GND
2	SWRF1	OUTPUT	WRF signal sampling & hold [S/H] signal output.
3	SWRF1	OUTPUT	WRF signal sampling & hold [S/H] signal output.
4	RFP	INPUT	RF differerential signal[+] input.
5	RFN	INPUT	RF differerential signal[-] input.
6	GND1	-	Analog GND
7	A1	INPUT	Main beam signal [A1] input.
8	B1	INPUT	Main beam signal [B1] input.
9	C1	INPUT	Main beam signal [C1] input.
10	D1	INPUT	Main beam signal [D1] input.
11	VCC2	-	Analog power.
12	REFI2	INPUT	Reference voltage input pin for PDIC.
13	REFO1	OUTPUT	Pick-up internal reference voltage output[at REFI pin 2.5V: 2.25V output.]
14	E1	INPUT	Sub beam signal [E1] input.
15	F1	INPUT	Sub beam signal [F1] input.
16	G1	INPUT	Sub beam signal [G1] input.
17	H1	INPUT	Sub beam signal [H1] input.
18	GND2	-	Analog GND
19	FOP	INPUT	FO+ signal input for Focus.
20	FOM	INPUT	FO- signal input for Focus.
21	GND3	-	Analog GND
22	HPAD	-	Wobble circuit HPF band setting condenser connecting port.
23	CAD	-	Wobble circuit AGC response time setting condenser connecting port.
24	HPBC	-	Wobble circuit HPF band setting condenser connecting port.
25	CBC	-	Wobble circuit AGC response time setting condenser connecting port.
26	VCC3	-	Analog power.
27	PDIN	INPUT	Laser monitor current input.
28	FPDVREF	INPUT	Reference voltage input pin for front monitor.
29	APCPH	-	Peak-hold condenser connecting pin for ALPC .
30	RREF	-	Read ALPC Condenser connecting port.
31	RLD	OUTPUT	Read Laser drive control output.
32	RLDM	INPUT	Read Laser drive control Amp[-] input.
33	WREF	-	Write ALPC Condenser connecting port.
34	WLD	OUTPUT	Write Laser drive control output.
35	WLDM	INPUT	Write Laser drive control Amp[-] input.
36	PELD	OUTPUT	Pick power output port1.
37	P2LD	OUTPUT	Pick power output port 2.
38	CREG	OUTPUT	Regulater voltage[2.5V] output.
39	REF1	INPUT	DSP power voltage input[2.5V].
40	REFO2	OUOTPTU	DSP Reference voltage output [at REFI port 2.5V: 1.5V output].

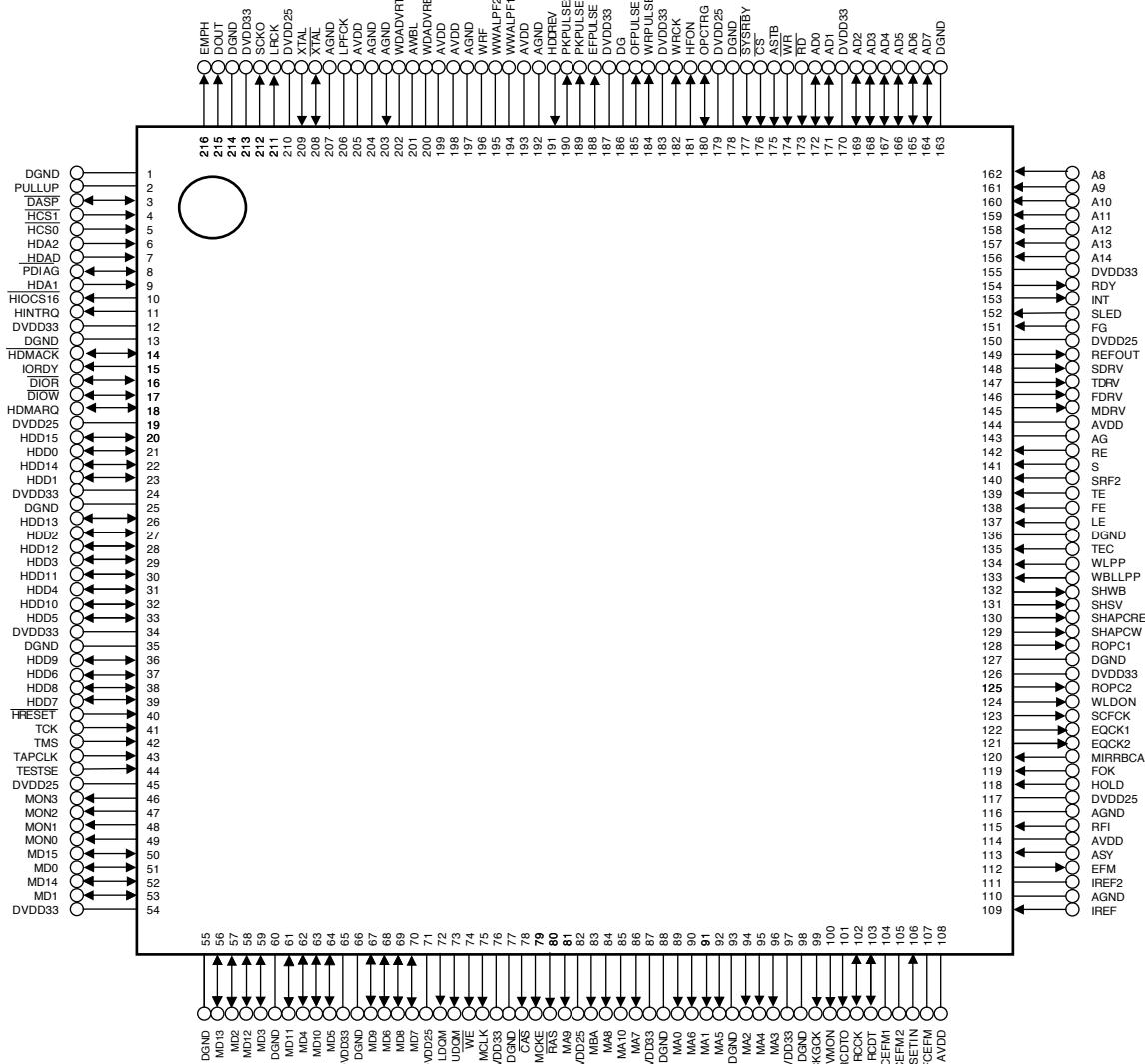
No.	Pin Name	Type	Description
41	VCC4	-	Analog power.
42	ADO	OUTPUT	Wobble circuit [A+D] signal output.
43	BCO	OUTPUT	Wobble circuit [B+C] signal output.
44	GND4	-	Analog GND
45	CDAC	OUTPUT	DAC reference voltage output.
46	VCC5	-	Digital power.
47	BCIN	INPUT	[B+C] signal input.
48	ADIN	INPUT	[A+D] signal input.
49	RCBH1A	-	RLPP circuit bottom hold condenser connecting port.
50	RCBH1B	-	RLPP circuit bottom hold condenser connecting port.
51	GND5	-	Analog GND
52	SHWB	INPUT	Sample hold pulse input for Wobble signal.
53	SHSV	INPUT	Sample hold pulse input for Servo signal.
54	VCC6	-	Digital power.
55	OPCTRG	INPUT	OPCTRG signal input.
56	SHAPCRE	INPUT	Sample hold pulse input for Read/Erase ALPC.
57	SHPCW	INPUT	Sample hold pulse input for Write ALPC.
58	ROPC1	INPUT	Sample hold pulse input 1 for WRF signal.
59	ROPC2	INPUT	Sample hold pulse input 2 for WRF signal.
60	WLDON	INPUT	Write ALPC Center signal input.
61	RLDON	INPUT	Read ALPC Center signal input.
62	SGND	-	Sub straight GND.
63	SCFCK	INPUT	SCF clock input.
64	EQCK1	INPUT	Fixed clock input.
65	EQCK2	INPUT	Equalize automatic control clock input.
66	GND6	-	Analog GND
67	RST	INPTU	Register reset input.
68	SCK	INPUT	Register setting clock input.
69	SO	OUTPUT	Serial data output.
70	SI	INPUT	Serial data input.
71	CP	INPUT	Address
72	VCC33	OUTPUT	Power voltage [3.3V monitor].
73	CWAGC	-	Wobble circuit AGC response time setting condenser connecting port.
74	CWHP	-	Wobble circuit HPF band setting condenser connecting port.
75	WOBBLE	OUTPUT	Wobble signal output [Digital signal].
76	AWOBBLE	OUTPUT	Wobble signal output [Analog signal].
77	MONI2	OUTPUT	Internal signal monitor port.
78	VCC7	-	Digital power.
79	TEC	OUTUPT	Tracking zero cross signal output.
80	TECIN	INPUT	Tracking zero cross signal input.

No.	Pin Name	Type	Description
81	TE2	OUTPUT	Tracking error signal output.
82	TE	OUTPUT	Tracking error signal output for Servo.
83	PPALL	OUTPUT	Main side push-pull signal output.
84	SBAD	OUTPUT	Sub beam signal output [(E+F+G+H) signal].
85	SPP2		Sub beam signal output [(F+G)-(E+H) signal].
86	FE	OUTPUT	Focus error signal.
87	GND7	-	Analog GND
88	MONI3	OUTPUT	Internal signal monitor port.
89	CRC	-	Radial contrast circuit condenser connecting port.
90	RECD	OUTPUT	No recording area detection.
91	HOLD	OUTPUT	Detection signal output.
92	MIRR	OUTPUT	Mirror detection/RCA signal output.
93	FOK	OUTPUT	Focus OK signal.
94	CDEF2	-	Detect circuit condenser connecting port 2.
95	CMIRR	-	Mirror circuit condenser connecting port.
96	CDEF	-	Detect circuit condenser connecting port .
97	CBPD	-	β, γ adetection[peak]condenser connecting port.
98	CBBB	-	β, γ adetection[bottom]condenser connecting port.
99	RFEQO	OUTPUT	Equalizer output.
100	VCC8	-	Analog power.
101	CEQOS2	-	RF Equalizer circuit condenser connecting port 2.
102	CEQOS1	-	RF Equalizer circuit condenser connecting port 1.
103	GND8	-	Analog GND
104	REQ	-	RF Equalizer circuit volatage setting resistance connecting port.
105	CEQ	-	Equalizer fc automatic control curcuit condenser connecting port.
106	CDPDD	-	DPD [D signal] HPF band setting condenser connecting port.
107	CDPDC	-	DPD [C signal] HPF band setting condenser connecting port.
108	GND9	-	Analog GND
109	CDPDC	-	RFAGC circuit condenser connecting port.
110	CDPDRF	-	DPD [RF signal] HPF band setting condenser connecting port.
111	CDPDRF	-	DPD [B signal] HPF band setting condenser connecting port.
112	CDPDA	-	DPD [A signal] HPF band setting condenser connecting port.
113	VCC9	-	Analog power.
114	AGCIN	INPUT	AGC input
115	RFO	OUTPUT	Read RF signal output.
116	VCC1	-	Analog power.
117	WRFLP	OUTPUT	Write RF LPF output.
118	WRF	OUTPUT	Write RF signal output.
119	WFR2	OUTPUT	Write RF2 signal output.
120	MONI1	OUTPUT	Internal signal monitor port.

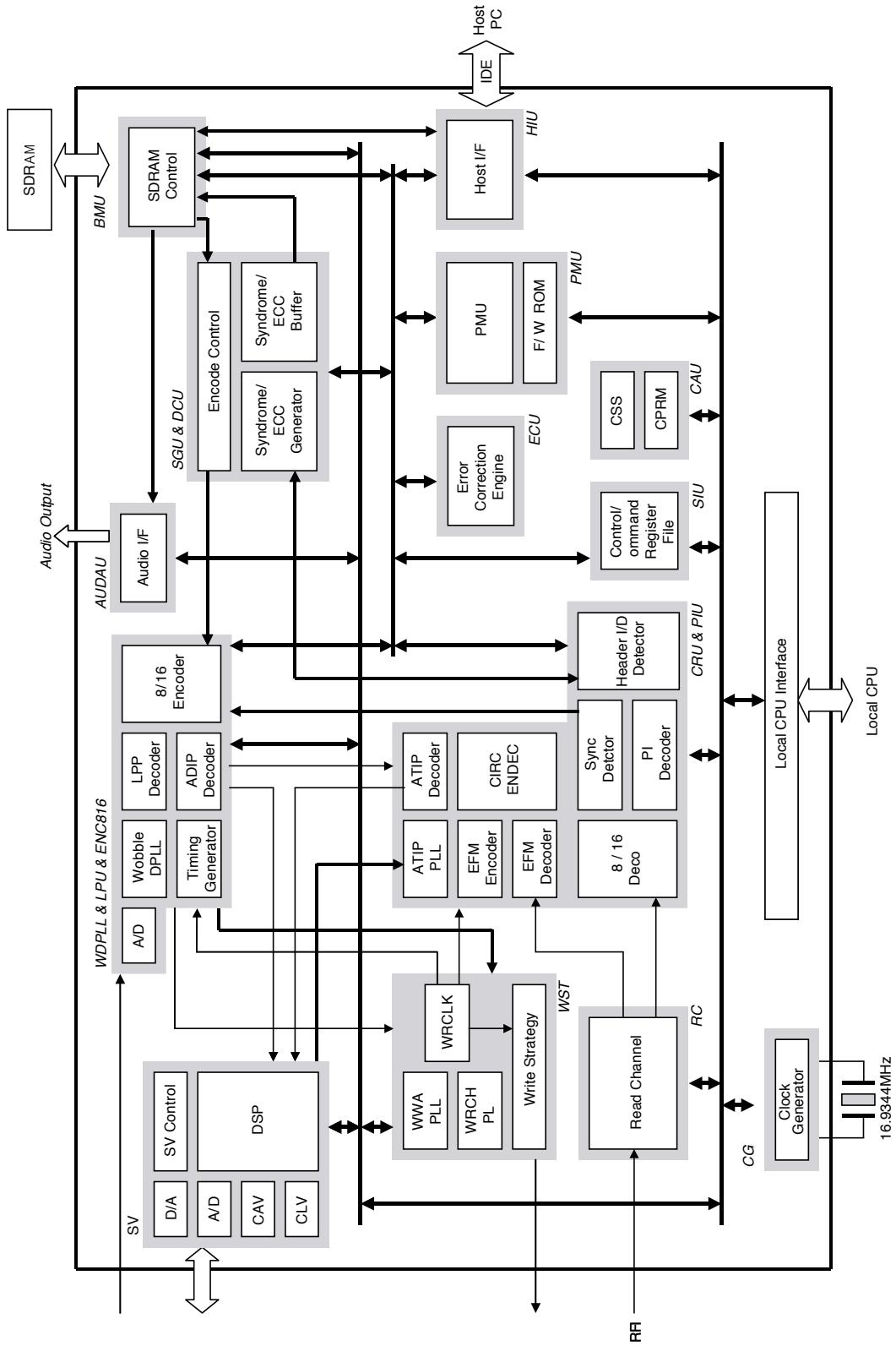
3. MAJOR IC INTERNAL BLOCK DIAGRAM AND PIN DESCRIPTION

IC201(μPD63620) : Encoder, Decoder & DSP Signal Processor

Pin Assignment



Block Diagram



Pin description

Pin No.	Pin Name		Type		Description
1	DGND	-	-	-	Digital GND
2	PULLUP	-	-	-	Pull-up resistance connecting port.[5V or 3.3V]
3	<u>DASO</u>	5V_tolerant	I/O	Pull-up	Drive active slave presesnt signal.[open/drain]
4	<u>HCS1</u>	5V_tolerant	I	-	Host interface chip, pull-up selection input.
5	<u>HCS0</u>	5V_tolerant	I	-	Host interface chip, pull-up selection input.
6	HDA2	5V_tolerant	I	-	Host interface chip, address signal input.
7	HDAO	5V_tolerant	I	-	Host interface chip, address signal input.
8	PDIAG	5V_tolerant	I/O	Pull-up	Diagnostic signal [open/drain]
9	HDA1	5V_tolerant	I	-	Host interface chip, address signal input.
10	<u>HIOCS16</u>	5V_tolerant	I	Pull-up	16 bit I/O signal [open/drain]. When Ultra DMA burst, this is 3 state port.
11	HINTRQ	5V_tolerant	O	Pull-up	Host interrupt signal output.
12	DVDD33	-	-	-	Digital power[3.3V]
13	DGND	-	-	-	Digital GND
14	<u>HDMACK</u>	5V_tolerant	I/O	-	DMA acknowledge signal.
15	IORDY	5V_tolerant	I	Pull-up	I/O Channel ready[open/drain]. When Ultra DMA burst, this is DDMARDY: DSTROBE signal.
16	<u>DIOR</u>	5V_tolerant	I/O	-	Host interface read input signal. When Ultra DMA burst, this is HDMARDY: HSTROBE signal.
17	<u>DIOW</u>	5V_tolerant	I/O	-	Host interface write input signal. When Ultra DMA burst, this is STOP signal.
18	HDMARQ	5V_tolerant	O	Pull-up	DMA request signal output.
19	DVDD25	-	-	-	Digital power[2.5V]
20	<u>HDD15</u> <u>HDD8</u>	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
21	<u>HDD0</u> <u>HDD6</u>	5V_tolerant	O	Pull-up	Host interface data bus.[within slave resistance]
22	<u>HDD14</u> <u>HDD9</u>	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
23	HDD1	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
24	DVDD33	-	-	-	Digital power[3.3V]
25	DGND	-	-	-	Digital GND
26	<u>HDD13</u> <u>HDD10</u>	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
27	<u>HDD2</u> <u>HDD4</u>	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]

Pin No.	Pin Name		Type		Description
28	DGND HDD4	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
29	HDD3 HDD3	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
30	HDD11 HDD12	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
31	HDD4 HDD2	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
32	HDD7 HDD13	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
33	HDD7 HDD1	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance].
34	DVDD33	-	-	-	Digital power[3.3V]
35	DGND	-	-	-	Digital GND
36	HDD9 HDD14	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance].
37	HDD6 HDD0	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance].
38	HDD8 HDD15	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance].
39	HDD7	5V_tolerant	I/O	Pull-up	Host interface data bus.
40	HRESET	5V_tolerant	I/O	-	Host reset input.
41	TCK	3V	I	-	Test port. It must be connected to DGND.
42	TMS	3V	I	-	Test port. It must be connected to DGND.
43	TAPCLK	3V	I	-	Test port. It must be connected to DGND.
44	TESTSE	3V	I	-	Test port. It must be connected to DGND.
45	DVDD25	-	-	-	Digital power[2.5V]
46	MON3	3V	O	L	Monitor: test signal.
47	MON2	3V	O	L	Monitor: test signal.
48	MON1	3V	I/O	L	Monitor: test signal.
49	MON0	3V	I/O	L	Monitor: test signal.
50	MD15	3V	I/O	Pull-up	Buffer memory , Interface data bus.
51	MD0	3V	I/O	Pull-up	Buffer memory , Interface data bus.
52	MD14	3V	I/O	Pull-up	Buffer memory , Interface data bus.
53	MD1	3V	I/O	Pull-up	Buffer memory , Interface data bus.
54	DVDD33	-	-	-	Digital power.[3.3V](Buffer. Memory. Block)

Pin No.	Pin Name		Type		Description
55	DGND	-	-	-	Digital GND.(Buffer. Memory. Block)
56	MD13	3V	I/O	Pull-up	Buffer memory , Interface data bus.
57	MD2	3V	I/O	Pull-up	Buffer memory , Interface data bus.
58	MD12	3V	I/O	Pull-up	Buffer memory , Interface data bus.
59	MD3	3V	I/O	Pull-up	Buffer memory , Interface data bus.
60	DGND	-	-	-	Digital GND.
61	MD11	3V	I/O	Pull-up	Buffer memory , Interface data bus.
62	MD4	3V	I/O	Pull-up	Buffer memory , Interface data bus.
63	MD10	3V	I/O	Pull-up	Buffer memory , Interface data bus.
64	MD5	3V	I/O	Pull-up	Buffer memory , Interface data bus.
65	DVDD33	-	-	-	Digital power.[3.3V](Buffer. Memory. Block)
66	DGND	-		-	Digital GND.(Buffer. Memory. Block)
67	MD9	3V	I/O	Pull-up	Buffer memory , Interface data bus.
68	MD6	3V	I/O	Pull-up	Buffer memory , Interface data bus.
69	MD8	3V	I/O	Pull-up	Buffer memory , Interface data bus.
70	MD7	3V	I/O	Pull-up	Buffer memory , Interface data bus.
71	DVDD25	-	-	-	Digital power.[2.5V]
72	LDQM	3V	O	H	Low byte, data input/output mask control signal.
73	UDQM	3V	O	H	High byte, data input/output mask control signal.
74	WE	3V	O	H	Buffer memory , Interface write enable signal.
75	MCLK	3V	O	Pull-up	SDRAM clock output.
76	DVDD33	-	-	-	Digital power.[3.3V](Buffer. Memory. Block)
77	DGND	-	-	-	Digital GND.(Buffer. Memory. Block)
78	CAS	3V	O	H	Buffer memory , Interface column address strobe control signal.
79	MCKE	3V	O	H	SDRAM clock enable control signal.
80	RAS	3V	O	H	Buffer memory , Interface row address strobe control signal.
81	MA9	3V	O	L	Buffer memory , Interface address bus.
82	DVDD25	-	-	-	Digital power.[2.5V]
83	MBA	3V	O	L	Buffer memory , Interface bank address signal.
84	MA8	3V	O	L	Buffer memory , Interface data bus.
85	MA10	3V	O	L	Buffer memory , Interface data bus.
86	MA7	3V	O	L	Buffer memory , Interface data bus.
87	DVDD33	-	-	-	Digital power.[3.3V](Buffer. Memory. Block)

Pin No.	Pin Name		Type		Description
88	DGND	-	-	-	Buffer memory , Interface data bus.
89	MA0	3V	O	H	Buffer memory , Interface data bus.
90	MA6	3V	O	L	Buffer memory , Interface data bus.
91	MA1	3V	O	L	Buffer memory , Interface data bus.
92	MA5	3V	O	L	Buffer memory , Interface data bus.
93	DGND	-	-	-	Digital GND
94	MA2	3V	O	L	Buffer memory , Interface data bus
95	MA4	3V	O	L	Buffer memory , Interface data bus.
96	MA3	3V	O	L	Buffer memory , Interface data bus.
97	DVDD3	-	-	-	Digital power.[3.3V](Buffer. Memory. Block)
98	DGND	-	-	-	Digital GND (Buffer. Memory. Block)
99	CKGCK	3V	O		Clock, Generator output.
100	SVMON	3V	O	L	Servo, block monitor signal output.
101	RCDTO	3V	O	L	Read channel data output.
102	RCCK	3V	I/O	Hi-Z	Read channel clock output.
103	RCDT	3V	I/O	Hi-Z	Read channel data output.
104	PCEFM1	Analog	-	-	Read channel phase discriminator condenser connecting port.
105	PCEFM2	Analog	-	-	Read channel phase discriminator condenser connecting port.
106	OFFSETIN	Analog	-	-	Read channel phase discriminator charge pump control port.
107	FCEFM	Analog	-	-	Read channel frequency discriminator condenser connecting port.
108	AVDD	-	-	-	Analog power[2.5V]
109	IREF	Analog	O	-	Read channel analog reference voltage input.
110	AGND	-	-	-	Analog GND[EFM PLL]
111	IREF2	Analog	O	-	Non connecting port.
112	EFM	Analog	O	Pull-up	EFM comparator output.
113	ASY	Analog	O	-	EFM comparator asymmetry control voltage input.
114	AVDD	-	-	-	Analog power[2.5V]
115	RFI	Analog	O		EFM comparator RF signal input.
116	AGND	-	-	-	Analog GND[EFM]
117	DVDD25	-	-	-	Digital power.[2.5V]
118	HOLD	3v	O	-	HOLD control signal input.
119	FOK	3v	O	-	FOK signal input.
120	MIRRBCA	3V	O	-	Mirror signal or BCA signal input.

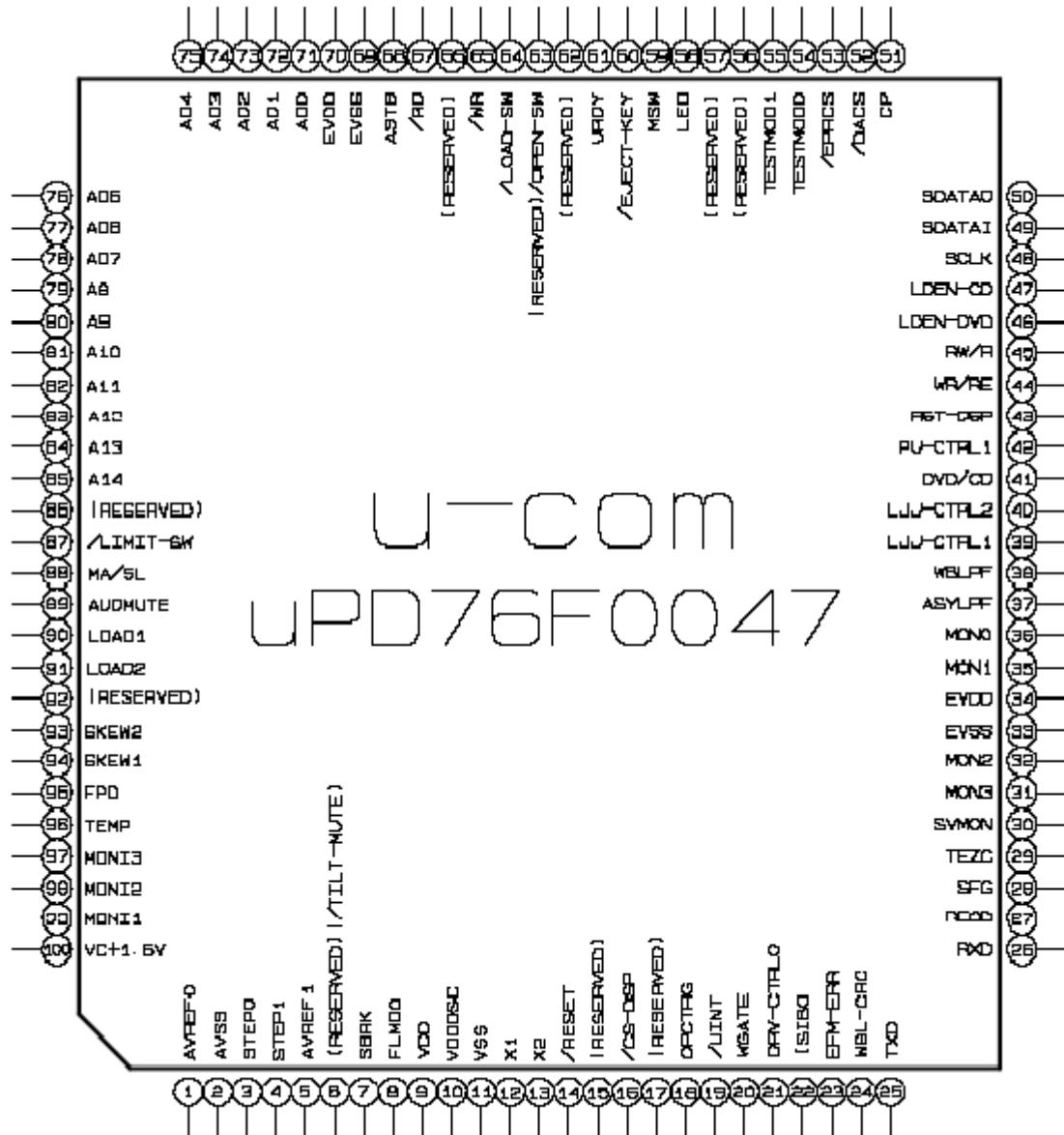
Pin No.	Pin Name		Type		Description
121	MIRRBCA	3V	O	Pull-up	RF AMP PC3320 RF equalizer automatic follow-up clock output.
122	EQCK1	3V	O	Pull-up	RF AMP PC3320 RF equalizer fixed clock output.
123	SCFCK	3V	O	P	RF AMP PC3320 RF equalizer automatic follow-up clock output.
124	WLDON	3V	O	L	Laser, Driver write laser control signal.
125	ROPC2	3V	O	L	Running OPC,sample hold signal.
126	DVDD33	-	-	-	Digital power.[3.3V]
127	DGND	-	-	-	Digital GND
128	ROPC1	3V	O	L	Running OPC,sample hold signal.
129	ROPC1	3V	O	L	APC write, sample hold signal.
130	SHAPCRE	3V	O	H	APC read/erase, sample hold signal.
131	SHSV	3V	O	H	Servo, sample hold signal.
132	SHWB	3V	O	H	Wobble, sample hold signal.
133	WBLLPP	3V	I	-	CD: 2 direct Wobble signal input, DVD: RLPP signal input.
134	TEC	3V	I	-	Test port. It must be connected to DGND.
135	TEC	3V	I	-	Tracking, zero, cross signal input.
136	DGND	-	-	-	Digital GND
137	LE	Analog	I	-	Lens error signal input [A/D convertor].
138	FE	Analog	I	-	Focus error signal input [A/D convertor].
139	TE	Analog	I	-	Tracking error signal input [A/D convertor].
140	SWRF2	Analog	I	-	WRF sample hold signal input [A/D convertor].
141	SWRF1	Analog	i	-	WRF sample hold signal input [A/D convertor].
142	REFIN	Analog	I	-	Reference voltage input [A/D convertor].
143	AGND	-	-	-	Analog GND[Servo A/D, D/A block]
144	AVDD	-	-	-	Analog power 2.5V[Servo A/D, D/A block].
145	MDRV	Analog	O		Spindle drive output [D/A convertor output].
146	FDRV	Analog	O		Focus drive output [D/A convertor output].
147	TDRV	Analog	O		Trackng drive output [D/A convertor output].
148	SDRV	Analog	O		Sled drive output [D/A convertor output].
149	REFOUT	Analog	O	1/2AVDD	Reference voltage output.
150	DVDD25	-	-	-	Digital power[2.5V]
151	FG	5V_tolerant	I	-	FG signal input
152	SLED	5V_tolerant	O	-	Sled position sensor input.
153	INT	5V_tolerant	O	L	Interrupted request signal output to Local CPU

Pin No.	Pin Name	Type		Description
154	RDY	5V_tolerant	I	-
155	DVDD33	-	-	Digital power[3.3V]
156	A14	5V_tolerant	I	-
157	A13	5V_tolerant	I	-
158	A12	5V_tolerant	I	-
159	A11	5V_tolerant	I	-
160	A10	5V_tolerant	I	-
161	A9	5V_tolerant	I	-
162	A8	5V_tolerant	I	-
163	DGND	-	-	Digital GND
164	AD7	5V_tolerant	I/O	-
165	AD6	5V_tolerant	I/O	-
166	AD5	5V_tolerant	I/O	-
167	AD4	5V_tolerant	I/O	-
168	AD3	5V_tolerant	I/O	-
169	AD2	5V_tolerant	I/O	-
170	DVDD33	-	-	Digital power[3.3V]
171	AD1	5V_tolerant	I/O	-
172	AD0	5V_tolerant	I/O	-
173	RD	5V_tolerant	I	-
174	WR	5V_tolerant	I	-
175	ASTB	5V_tolerant	I	-
176	CS	5V_tolerant	I	-
177	SYSRST	5V_tolerant	I	-
178	DGND	-	-	Digital GND.
179	DVDD25	-	-	Digital power[2.5V]
180	OPCTRG	3V	I/O	-
181	HFON	3V	O	H
182	WRCK	-	-	L
183	DVDD33	-	-	-
184	WRPULSE	3V	O	L
185	OFPULSE	3V	O	H
186	DGND	-	-	-

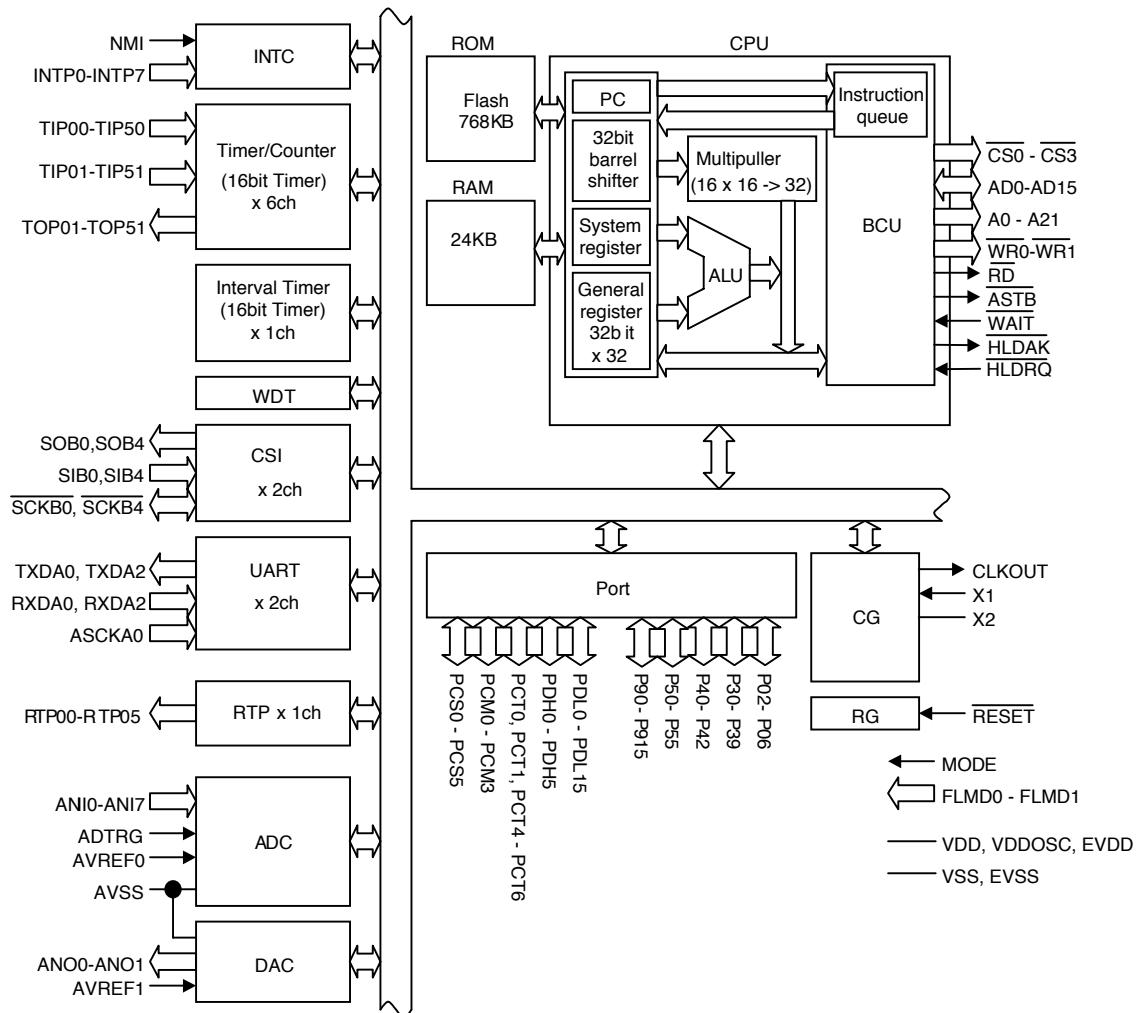
Pin No.	Pin Name		Type		Description
187	DVDD33	-	-	-	Digital power[3.3V]
188	EFPULSE	3V	O	L	OFF pluse output[write laser/driver control signal].
189	PKPULSE1	3V	O	L	Peak pluse output[write laser/driver control signal].
190	PKPULSE2	3V	O	L	Peak pluse output[write laser/driver control signal].
191	HDDREV	3V	I	-	Host interface data bus selector.[H: general, L: reverse]
192	AGND	-	-	-	Analog GND[WWAPLL]
193	AVDD	-	-	-	Analog power 2.5V [WWAPLL]
194	WWALPF1	Analog	-	-	WWAPLL condenser connecting port.
195	WWALPF2	Analog	-	-	WWAPLL condenser connecting port.
196	WRLPF	Analog	-	-	WST DLL condenser connecting port.
197	AGND	-	-	-	Analog GND[WST DLL block]
198	AVDD	-	-	-	Analog power 2.5V [WST DLL block]
199	AVDD	-	-	-	Analog power 2.5V [WDPLL A/D block]
200	WDADVRT	Analog	-	-	WDPLL block A/D convertor condenser connecting port.
201	AWBL	Analog	I	-	Analog wobble signal input port.
202	WDADVRB	Analog	-	-	WDPLL block A/D convertor condenser connecting port.
203	AGND	-	-	-	Analog GND[WDPLL A/D block]
204	AGND	-	-	-	Analog GND[PLL block]
205	AVDD	-	-	-	Analog power 2.5V [PLL block]
206	LPFCK	Analog	-	-	Test port. It must be connected to AGND.
207	AGND	-	-	-	Analog GND[Crystal block]
208	XTAL	-	I/O	-	Crystal oscillator connecting port.
209	XTAL	-	I	-	Crystal oscillator connecting port.
210	DVDD25	-	-	-	Digital power[2.5V]
211	LRCK	3V	O	Pull-up	DOUT serial audio data.
212	SCKO	3V	O	Pull-up	Serial audio data synchronizing clock output port.
213	DVDD33	-	-	-	Digital power[3.3V]
214	DGND	-	-	-	Digital GND
215	DOUT	3V	O	Pull-up	Serial audio data output port.
216	EMPH	3V	O	Pull-up	Emphasis distingish signal.

4. IC302(uPD76f0047):MICOM

Pin Assignment



Block Diagram



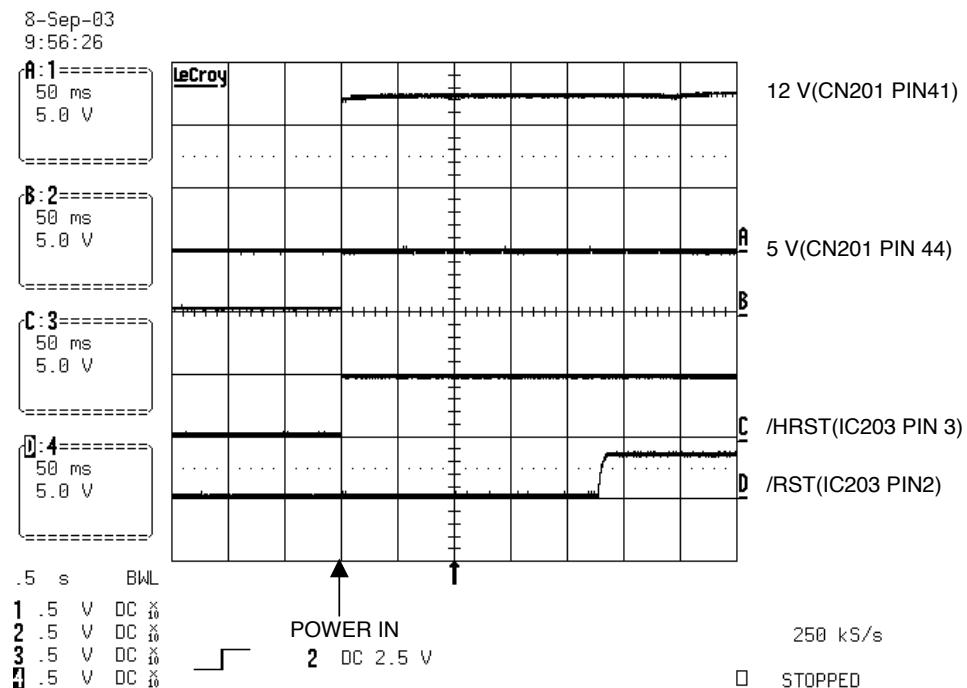
Pin description

Pin No.	Pin Name	Type	Description
1	AVREF0	I	A/D CONVERTER REFERENCE VOLTAGE INPUT
2	AVSS	-	A/D,D/A CONVERTER POTENTIAL
3	STEP0	O	STEPPING MOTOR CONTROL SIGNAL
4	STEP1	O	STEPPING MOTOR CONTROL SIGNAL
5	AVREF1	I	D/A CONVERTER REFERENCE VOLTAGE INPUT
6	TILT-MUTE	O	TILT DRIVE MUTE SIGNAL
7	SBRK	O	EXTERNAL MEMORY ADDRESS BUS
8	FLMD0	I	FLASH PROFLAMING MODE
9	VDD	-	INTERNAL CONSTANT POWER
10	VDDOSC	-	CONSTANT POWER
11	VSS	-	INTERNAL GROUND POTENTIAL
12	X1	I	MAIN CLOCK
13	X2	-	MAIN CLOCK
14	/RESET	I	SYSTEM RESET
15	(RESERVED)	-	-
16	/CS-DSP	O	CHIP SELECTOR OUPUT
17	(RESERVED)	-	-
18	OPCTRG	I/O	WOBBLE FM DEMODUL ATI ON DATA
19	/UINT	I	INTERRUPTED REQUEST SIGNAL INPUT
20	WGATE	I	DRIVER WRITER LASER CONTROL SIGNAL
21	DRV-CTRL0	-	-
22	SIB0	I	SERIAL CLOCK
23	EFM-ERR	-	-
24	WBL-CRC	-	-
25	TXD	O	SERIAL CLOCK
26	RXD	I	SERIAL CLOCK
27	RECD	I	NO RECODRNG AREA DETECTION
28	SFG	I	FG SIGNAL INPUT
29	TEZC	I	TRACK ZERO CROSS SIGNAL INPUT
30	SVMON	I	SERVO BLOCK MONITOR SIGNAL
31	MON3	I	MONITOR TEST SINGNAL
32	MON2	I	MONITOR TEST SINGNAL
33	EVSS	-	EXTERNAL CONSTANT POWER
34	EVDD	-	EXTERNAL CONSTANT POWER
35	MON1	I	MONITOR TEST SINGNAL
36	MON0	I	MONITOR TEST SINGNAL
37	ASYLPF	-	-
38	WBLPF	-	-
39	LJJ-CTRL1	-	-
40	LJJ-CTRL2	-	-
41	DV D/CD	-	-
42	PU-CTRL1	O	PD IC GAIN CONTROL SIGNAL
43	RST-DSP	O	RESET OUT
44	WR/RE	O	PD IC GAIN COTTROL SI NAL(WRITE/READ)
45	RW/R	-	-
46	LDEN-DVD	O	PICK-UP LD ENABLE SIGNAL (DV D)
47	LDEN-CD	O	PICK-UP LD ENABLE SIGNAL (C D)
48	SCLK	O	REGISTER SETTING CLOCK
49	SDATAI	I	REGISTER SETTING DATA INPUT
50	SDATAO	O	REGISTER SETTING DATA OUTPUT

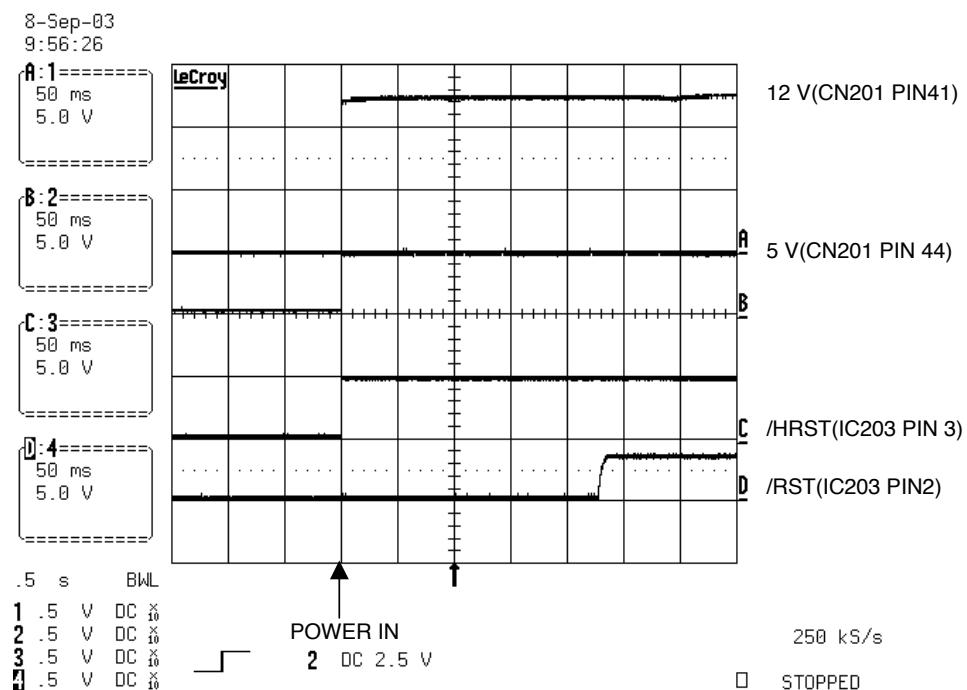
Pin No.	Pin Name	Type	Description
51	CP	O	REGISTER ADDRESS OUPUT
52	/DACS	O	CHIP SELECTOR
53	/BPRCS	O	EEPROM COMM UNICATON LINE
54	TEST MOD0	-	-
55	TEST MOD1	-	-
56	(RESERVED)	-	-
57	(RESERVED)	-	-
58	LED	O	LED ENABLE LINE
59	MSW	O	LED ENABLE LINE
60	/EJECT-KEY	O	TRAY OPE N LINE
61	URDY	O	ACCESS CONTROL SIGNAL INPUT FRON CPU TO SDRAM
62	(RESERVED)	-	-
63	/OPEN-SW	I	OPEN S/W INPUT
64	/LOAD-SW	I	LOAD S/W INPUT
65	/WR	O	WRITE STROBE SIGNAL OUTPUT
66	(RESERVED)	-	-
67	/RD	O	READ STROBE SIGNAL OUPUT
68	ASTB	O	ADDRESS STROBE OUPUT
69	EVSS	-	EX TERNAL CONSTANT POWER
70	EVDD	-	EX TERNAL CONSTANT POWER
71	AD0	I/O	PORT DL 16BIT INPUT/OUTPUT
72	AD1	I/O	PORT DL 16BIT INPUT/OUTPUT
73	AD2	I/O	PORT DL 16BIT INPUT/OUTPUT
74	AD3	I/O	PORT DL 16BIT INPUT/OUTPUT
75	AD4	I/O	PORT DL 16BIT INPUT/OUTPUT
76	AD5	I/O	PORT DL 16BIT INPUT/OUTPUT
77	AD6	I/O	PORT DL 16BIT INPUT/OUTPUT
78	AD7	I/O	PORT DL 16BIT INPUT/OUTPUT
79	A8	I/O	PORT DL 16BIT INPUT/OUTPUT
80	A9	I/O	PORT DL 16BIT INPUT/OUTPUT
81	A10	I/O	PORT DL 16BIT INPUT/OUTPUT
82	A11	I/O	PORT DL 16BIT INPUT/OUTPUT
83	A12	I/O	PORT DL 16BIT INPUT/OUTPUT
84	A13	I/O	PORT DL 16BIT INPUT/OUTPUT
85	A14	I/O	PORT DL 16BIT INPUT/OUTPUT
86	(RESERVED)	I/O	PORT DL 16BIT INPUT/OUTPUT
87	/LIMIT-SW	I	TRAY LIMIT S/W INPUT
88	MA/SL	I	MASTER/SLAVE MODE SELECTOR
89	AUDMUTE	-	-
90	LOAD1	O	STANDBY/BRAKE CONTROL SIGNAL
91	LOAD2	O	STANDBY/BRAKE CONTROL SIGNAL
92	(RESERVED)	-	-
93	SKEW2	-	-
94	SKEW1	-	-
95	FPD	I	TEMPERATURE MONITOR CURRENT INPUT
96	TEMP	I	MONITOR TEST SINGNAL
97	MONI3	I	FOCUS ERROR INPUT
98	MONI2	I	LASER MONITOR CURRENT INPUT
99	MONI1	I	PDIC REFERNEC VOLTAGE
100	VC+1.5V	I	VCC 1.5V INPUT

WAVEFORMS

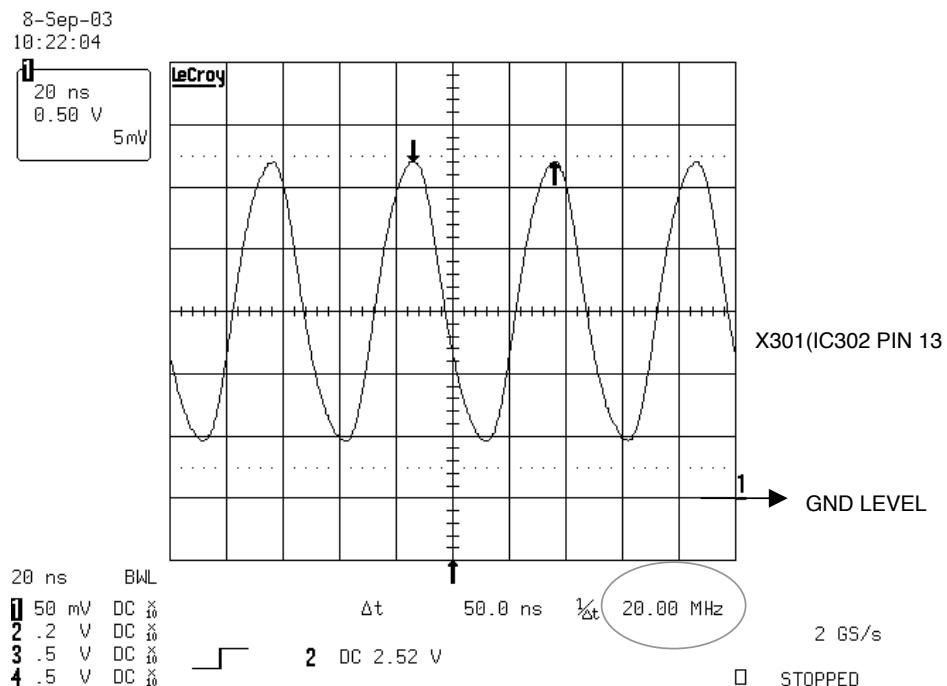
1. POWER & RESET Signal



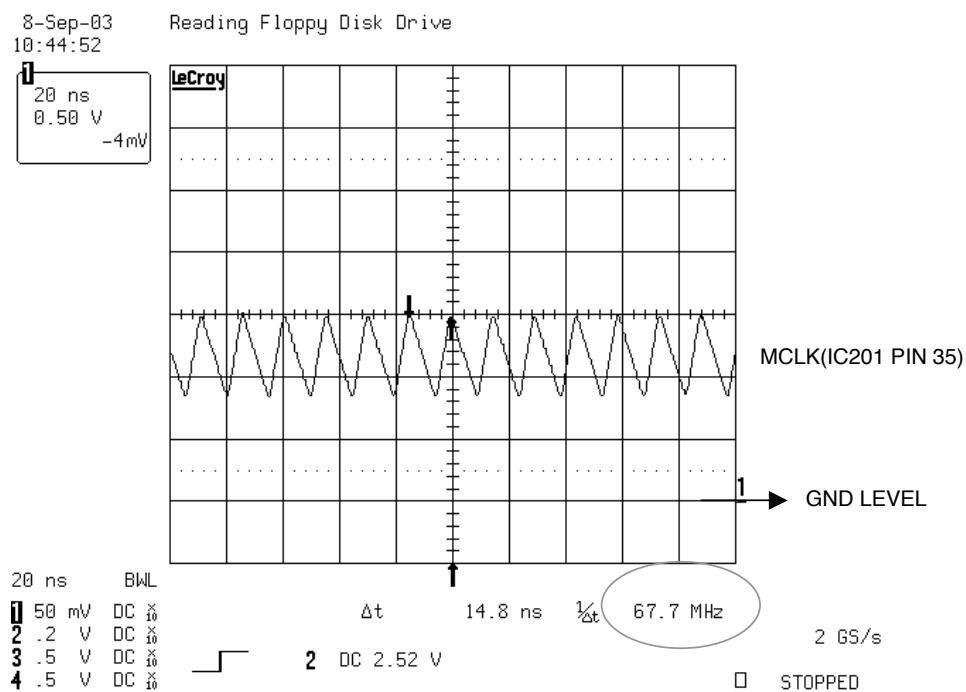
2. Main Clock1 for IC202 (16.9MHz)



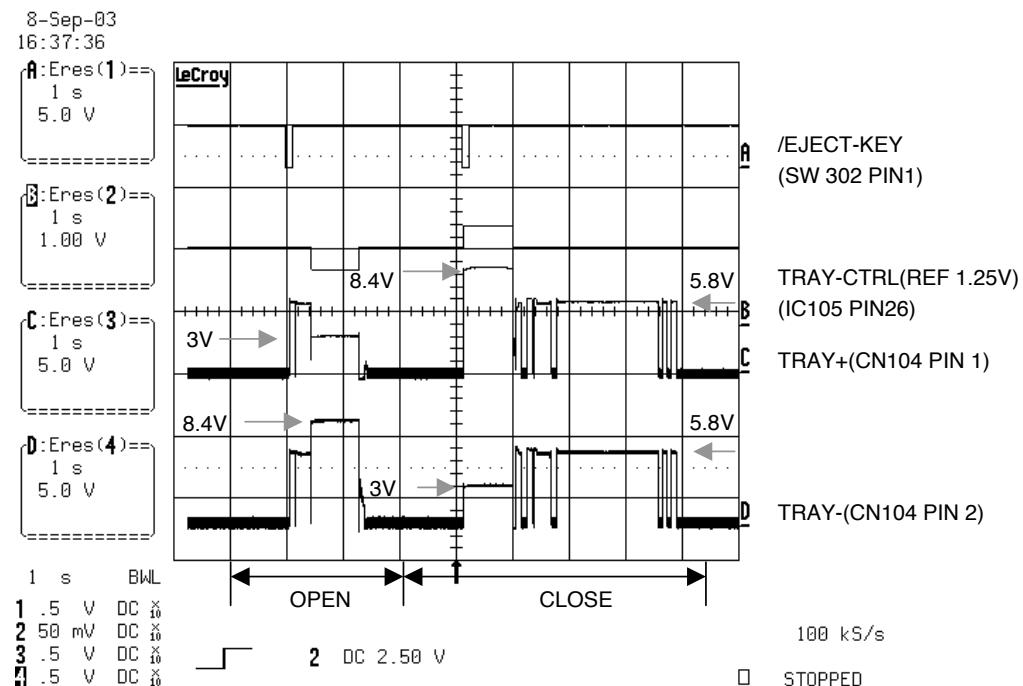
3. Main Clock2 for IC302 (20MHz)



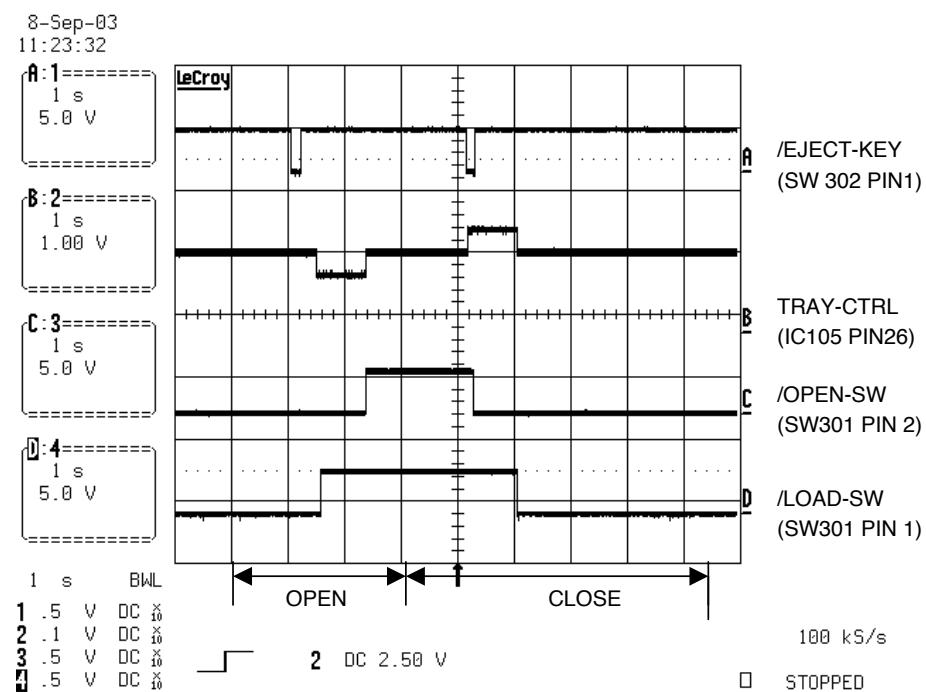
4. SDRAM Clock



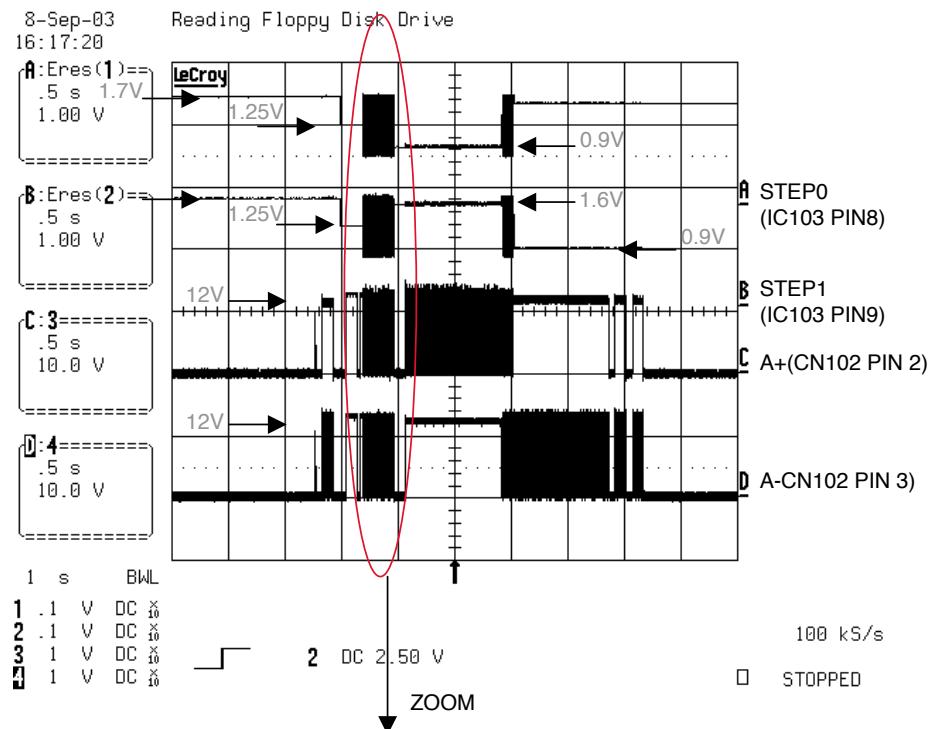
5. TRAY OPEN/CLOSE SIGNAL 1



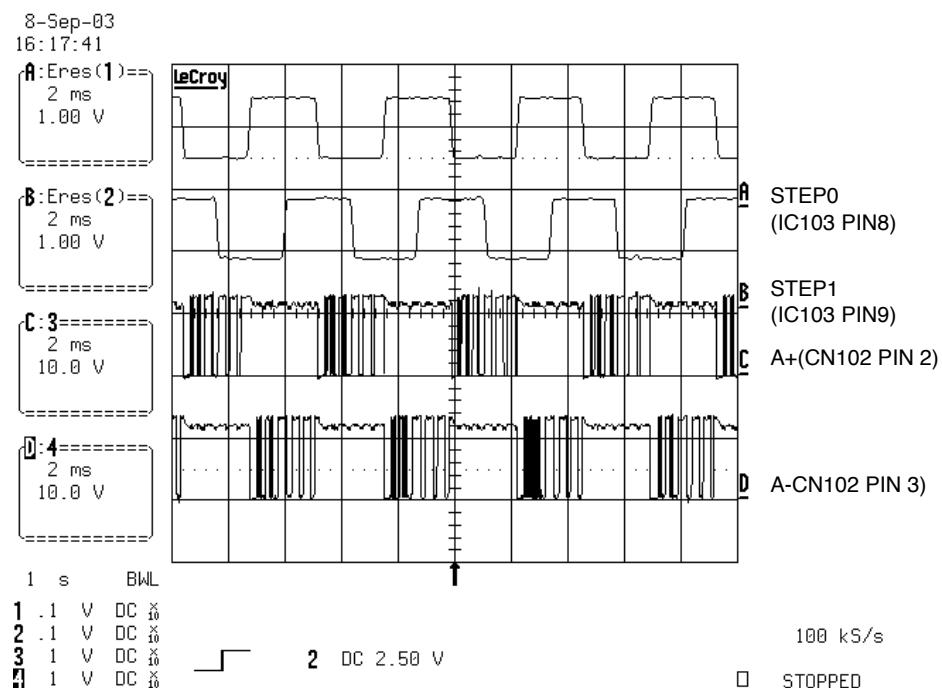
6. TRAY OPEN/CLOSE SIGNAL 2



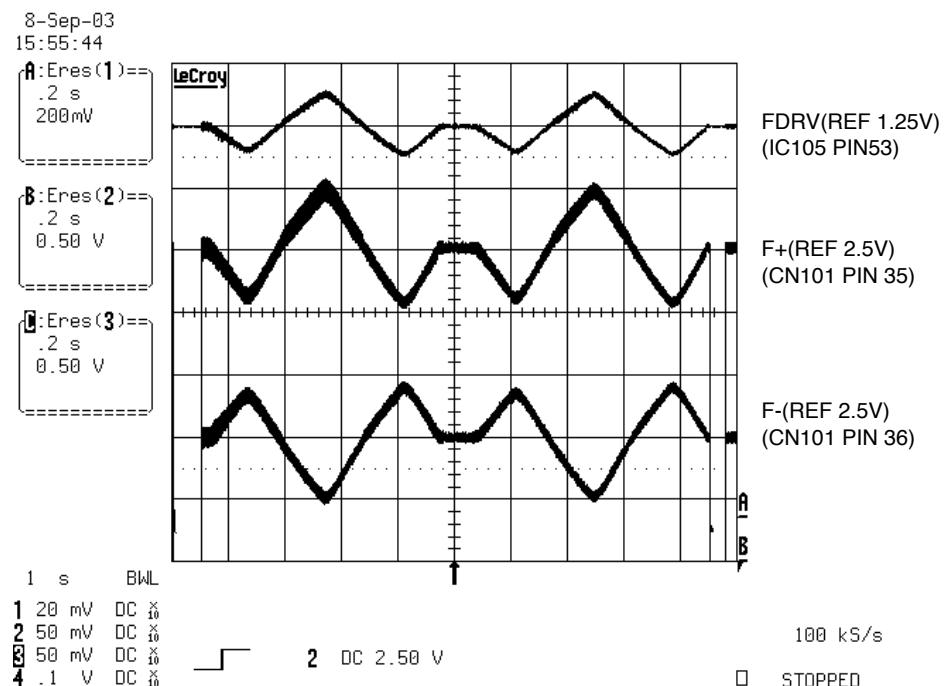
7. SLED MOVE SIGNAL 1



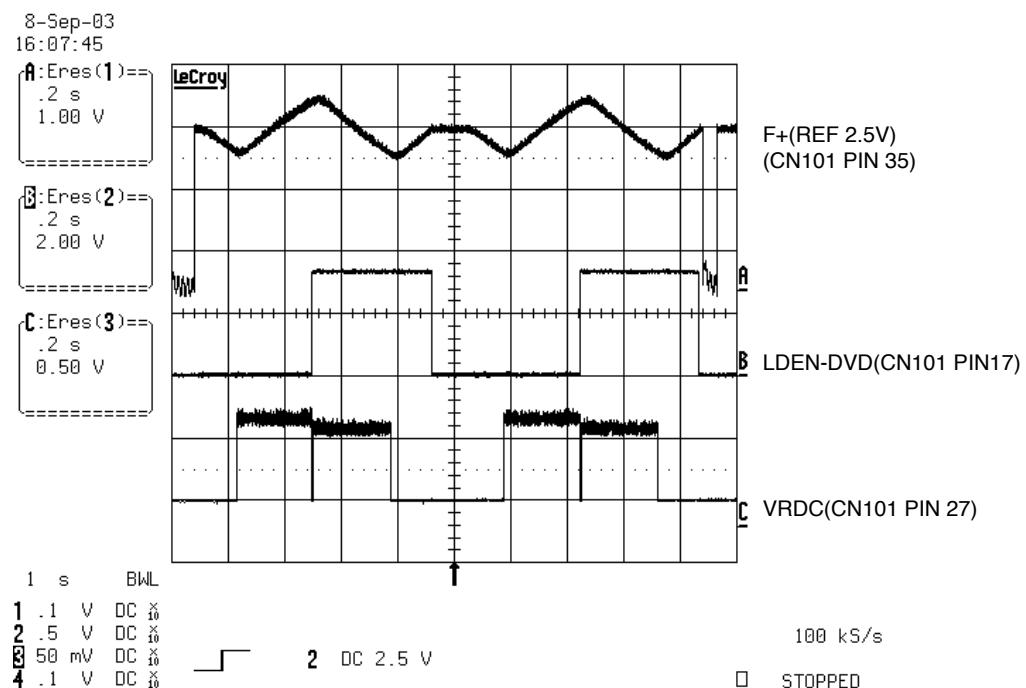
8. SLED MOVE SIGNAL 2



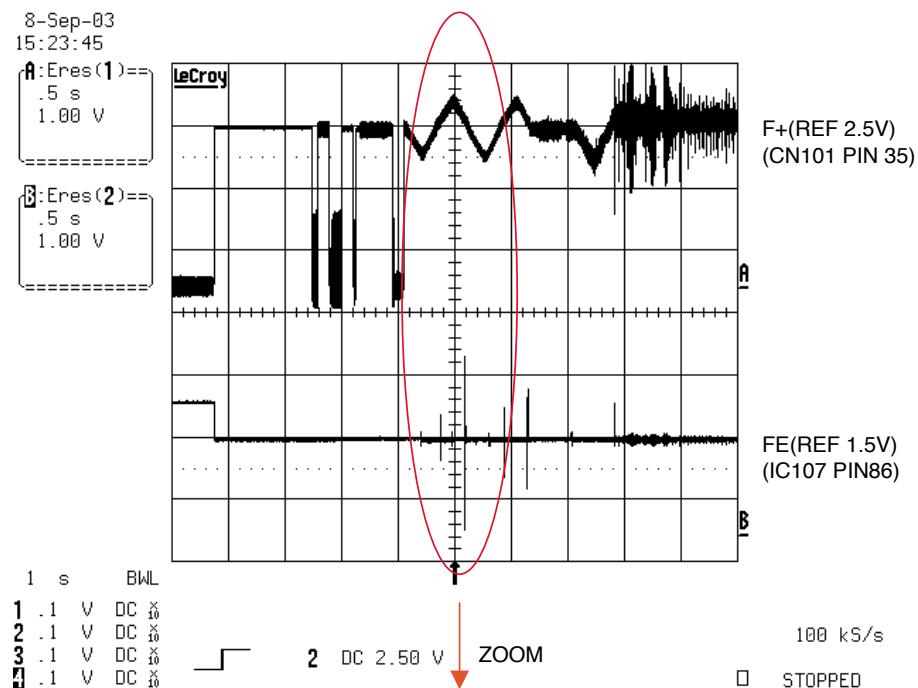
9. FOCUS SEARCH SIGNAL



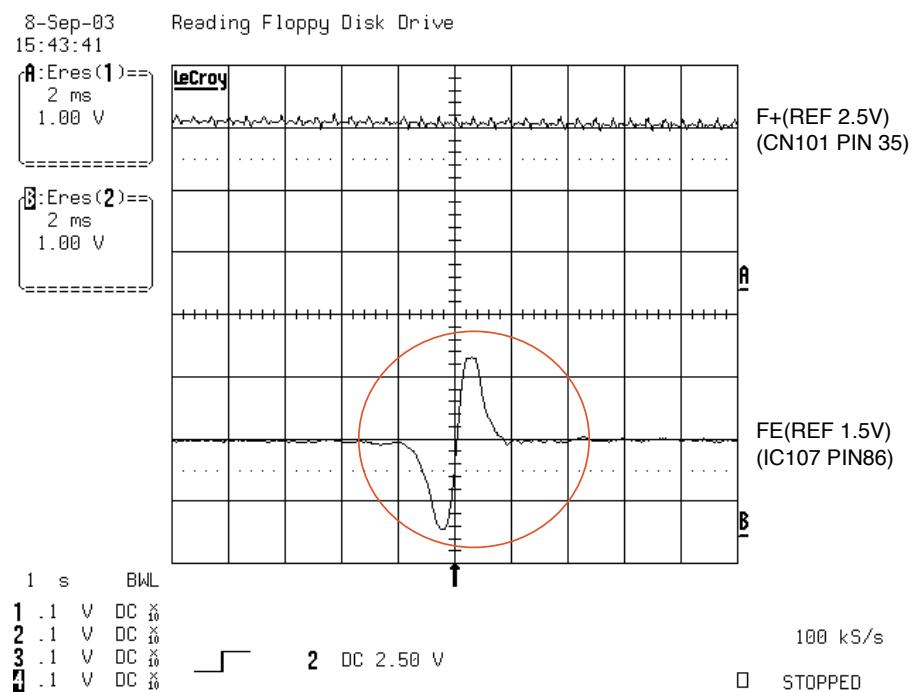
10. LASER TURN ON SIGNAL



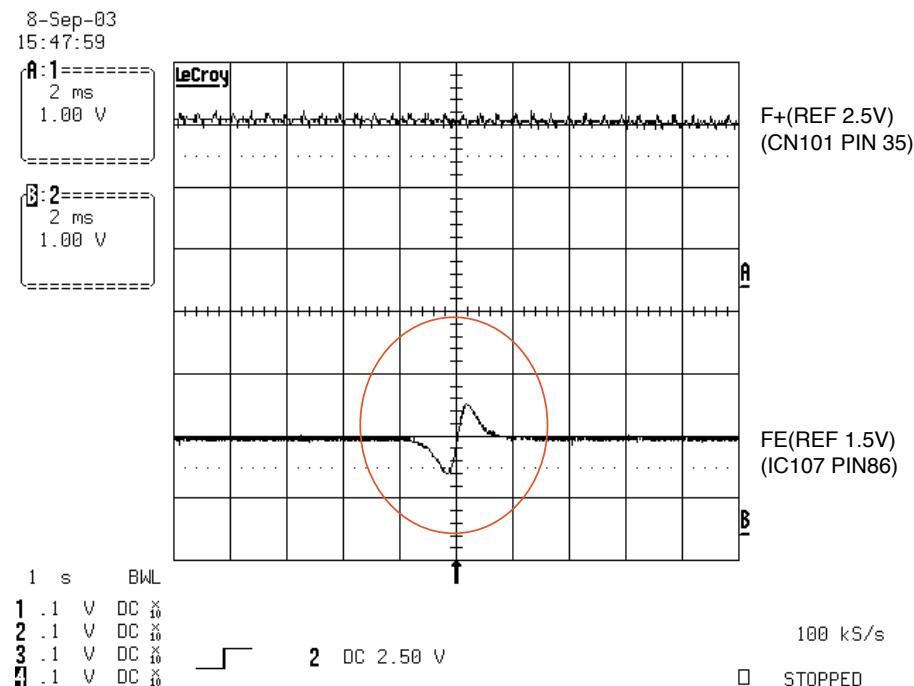
11. DISC TYPE JUDGEMENT WAVEFORM (CD SERIES)



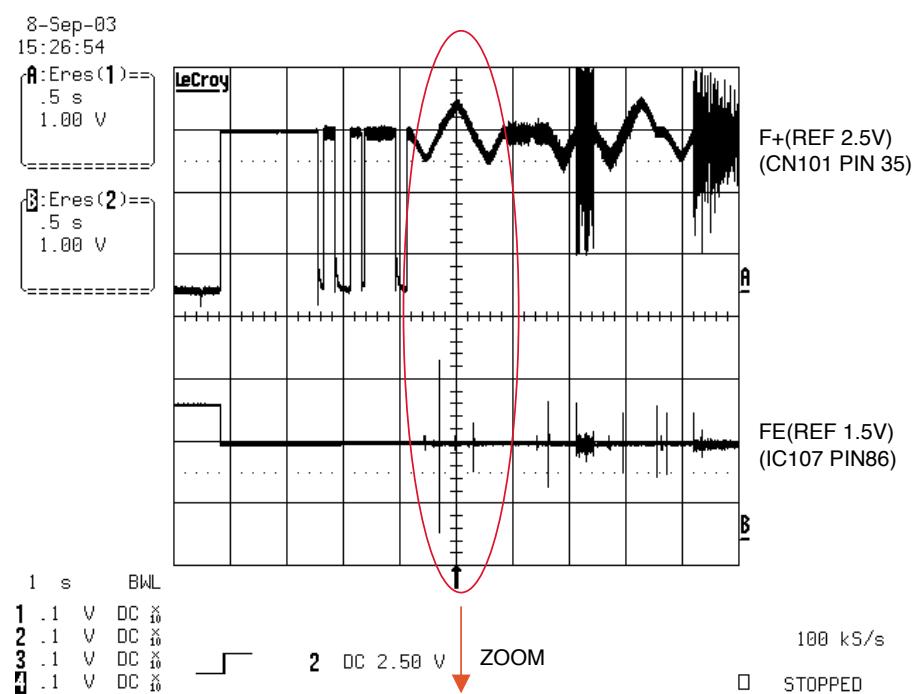
12. DISC TYPE JUDGEMENT WAVEFORM (CD&CD-R)



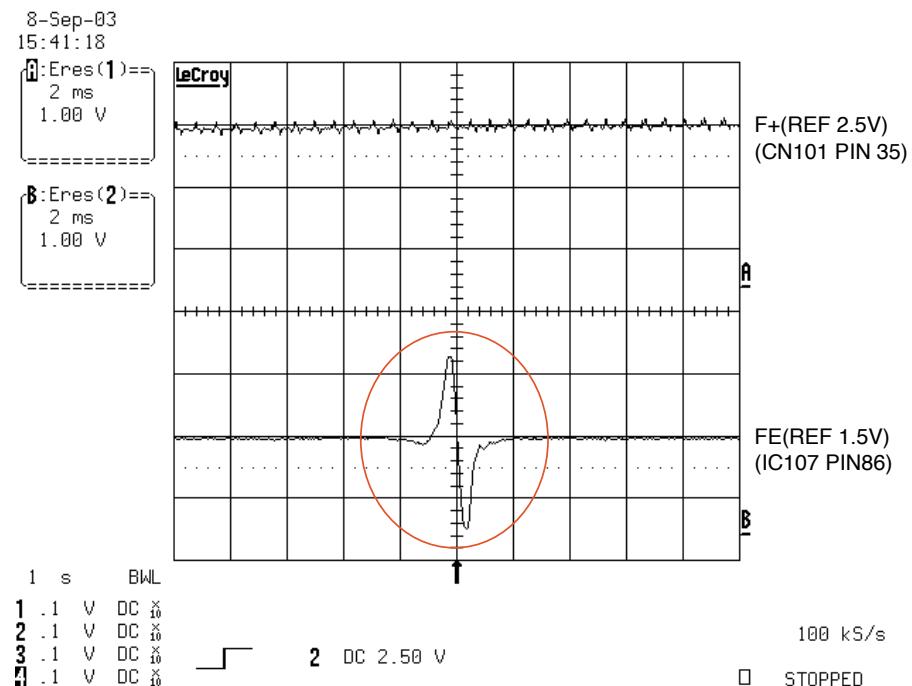
13. DISC TYPE JUDGEMENT WAVEFORM (CD-RW)



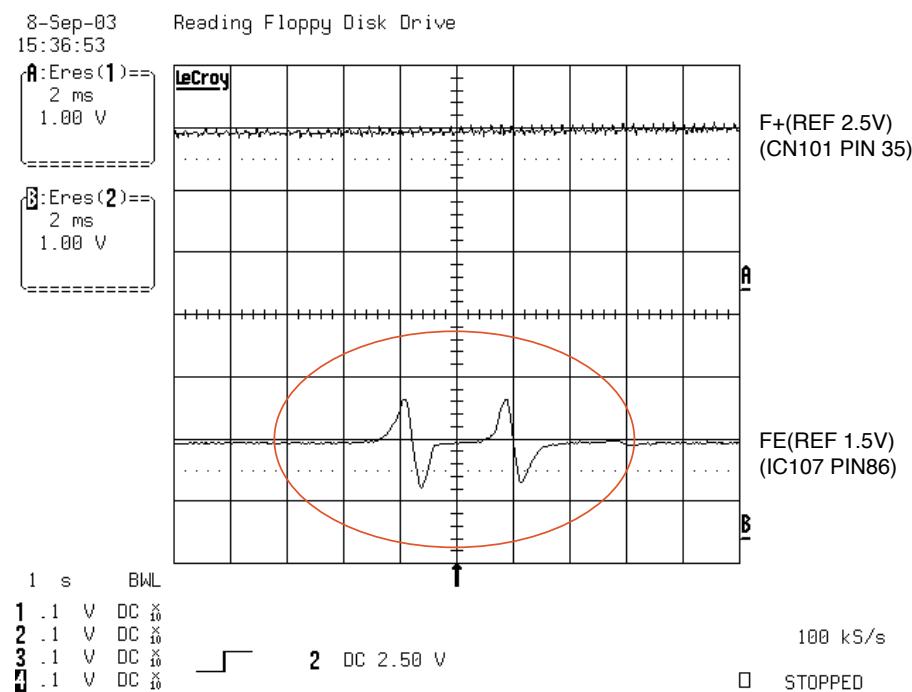
14. DISC TYPE JUDGEMENT WAVEFORM (DVD SERIES)



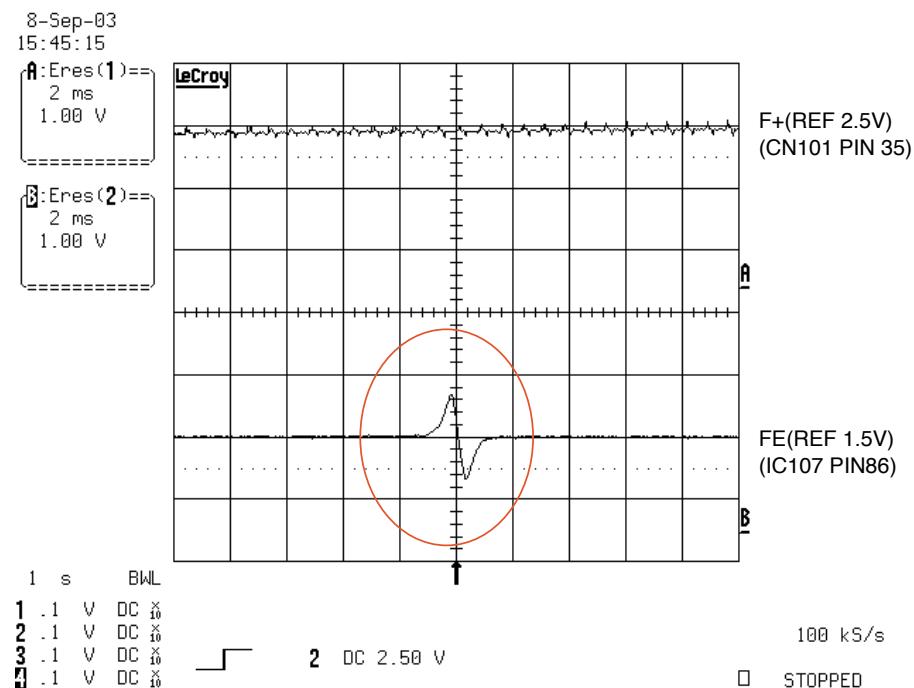
15. DISC TYPE JUDGEMENT WAVEFORM (DVD_SINGLE&R)



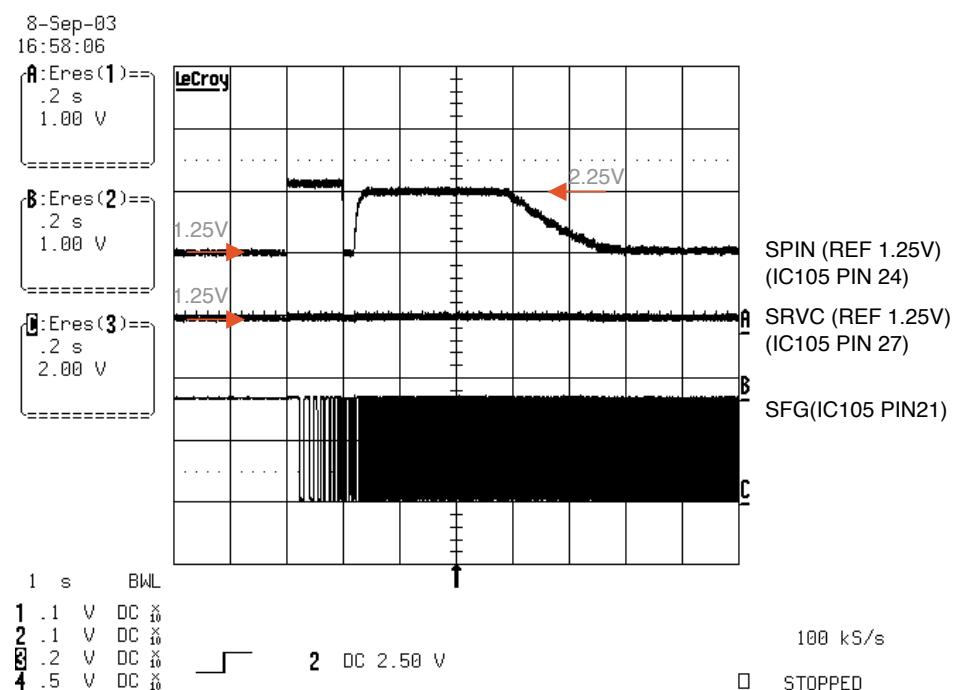
16. DISC TYPE JUDGEMENT WAVEFORM (DVD_DUAL)



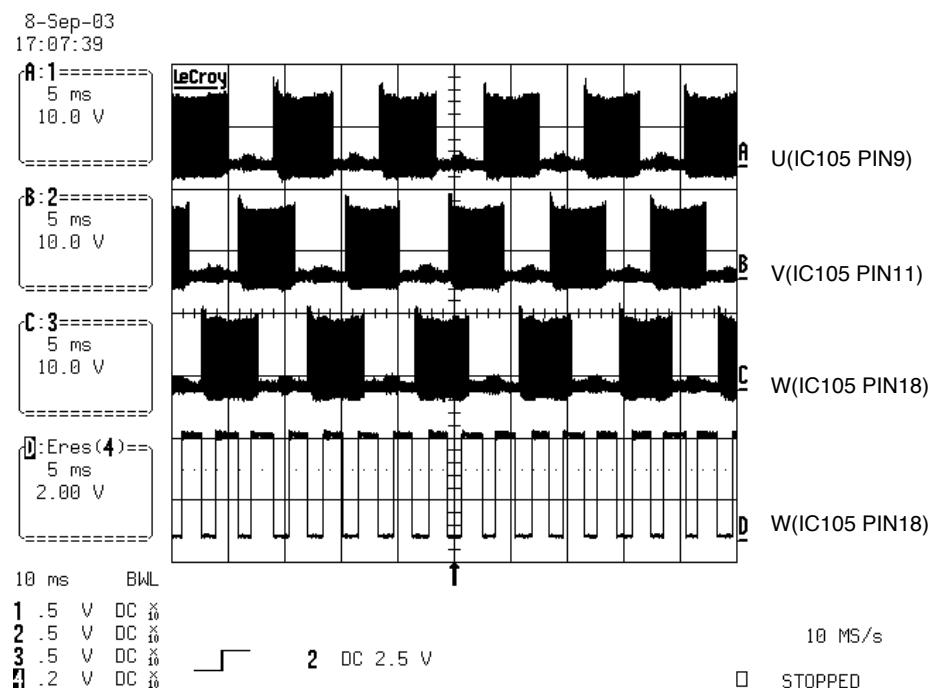
17. DISC TYPE JUDGEMENT WAVEFORM (DVDRW)



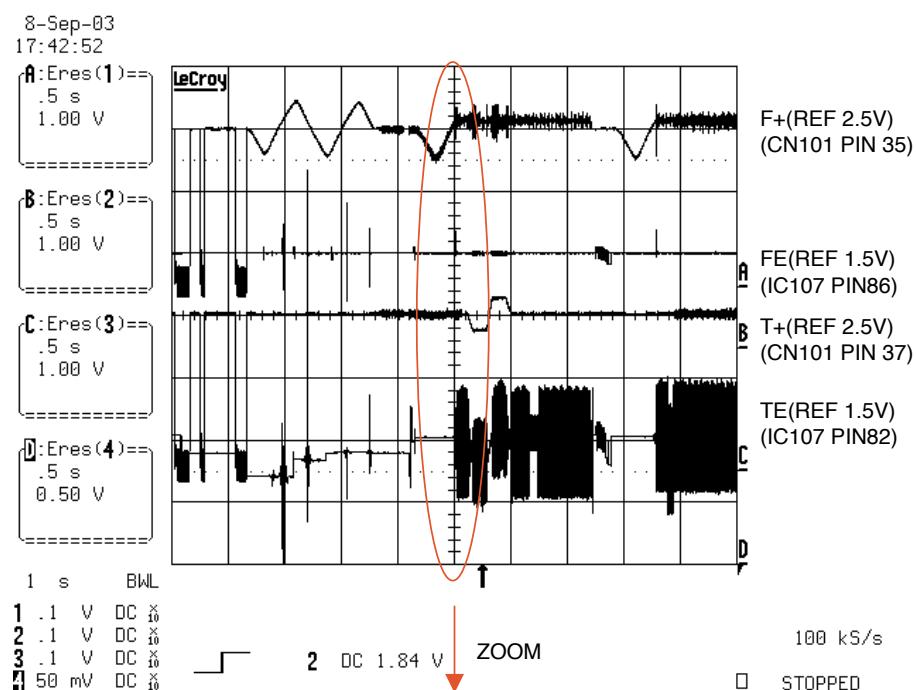
18. SPINDLE WAVEFORM1



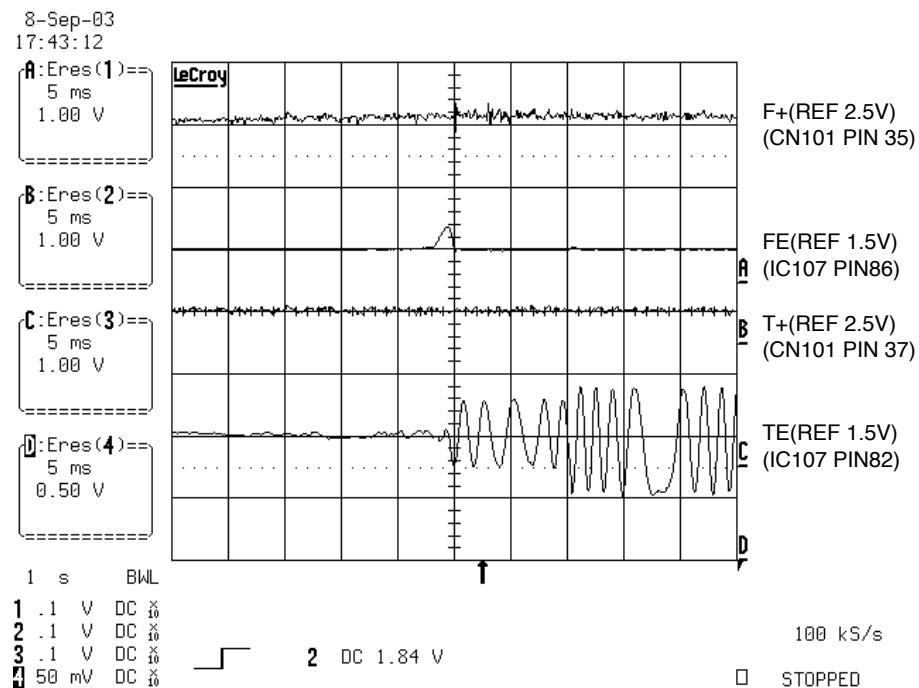
19. SPINDLE WAVEFORM2



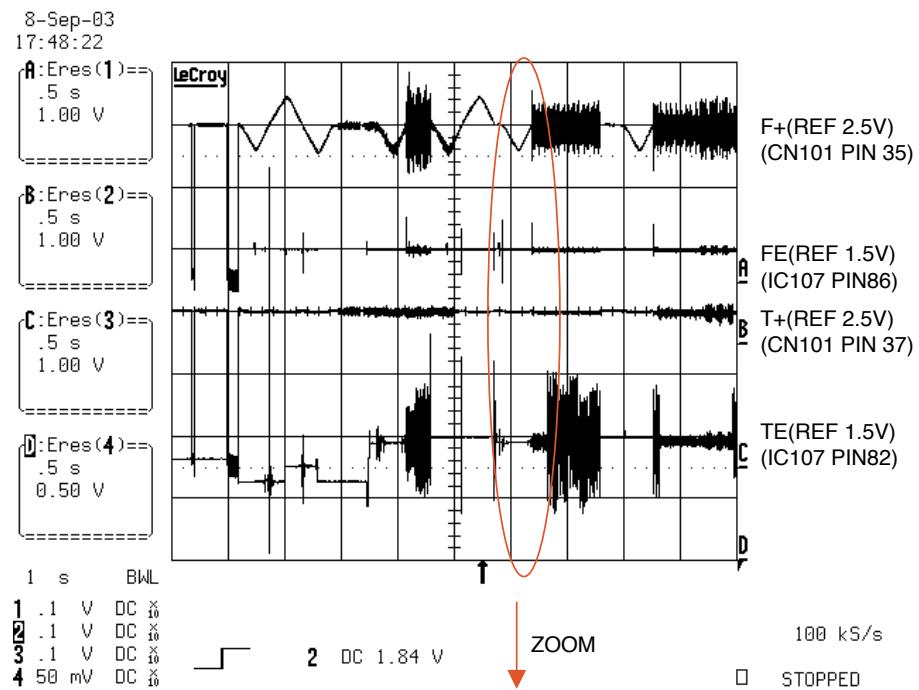
20. FOCUS ON SIGNAL(CD)



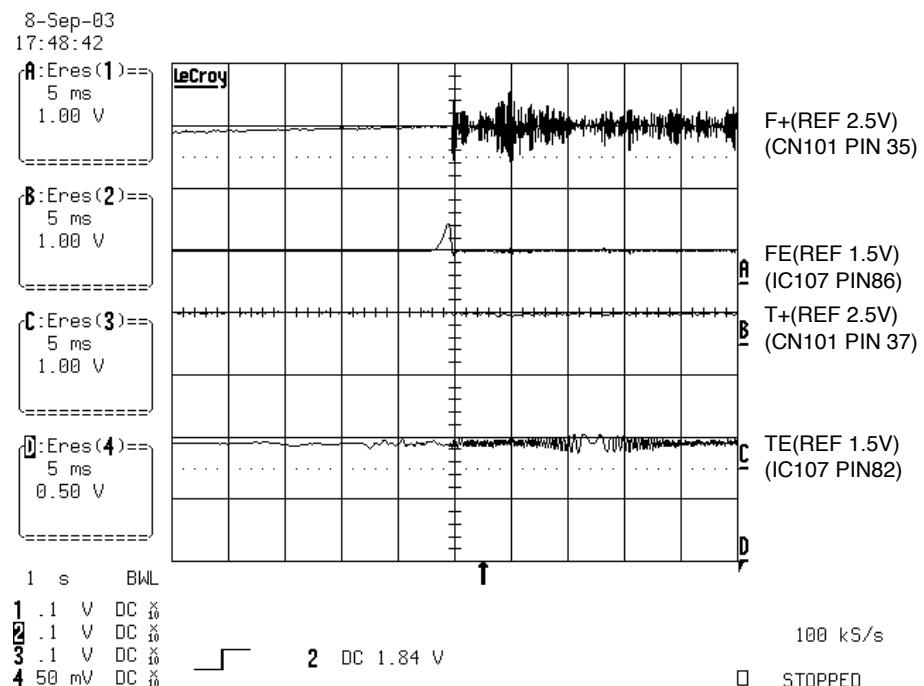
21. FOCUS ON SIGNAL(CD)



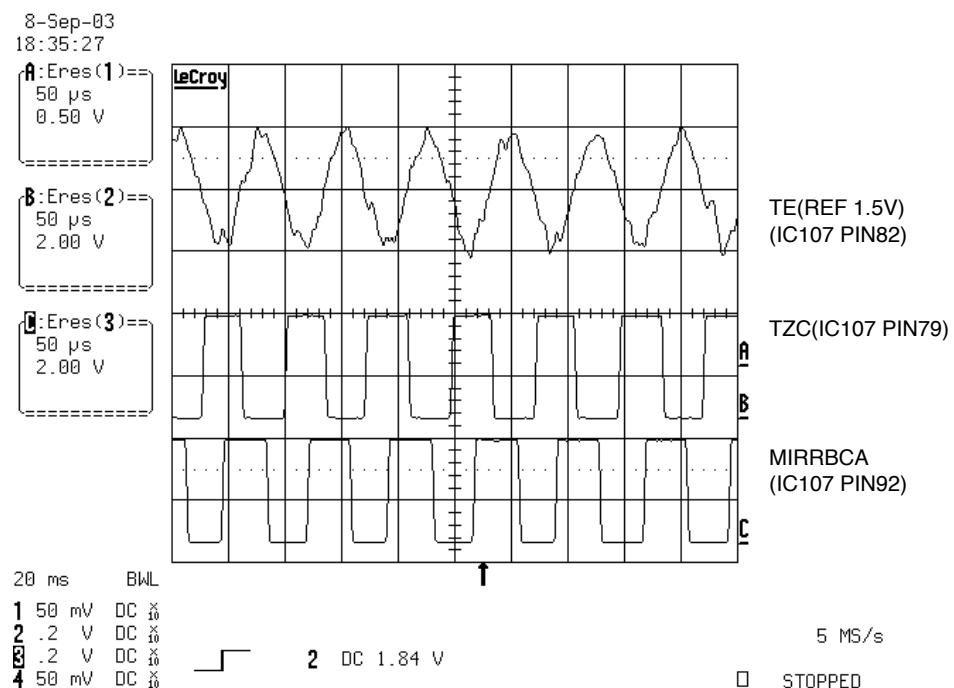
22. FOCUS ON SIGNAL(DVD)



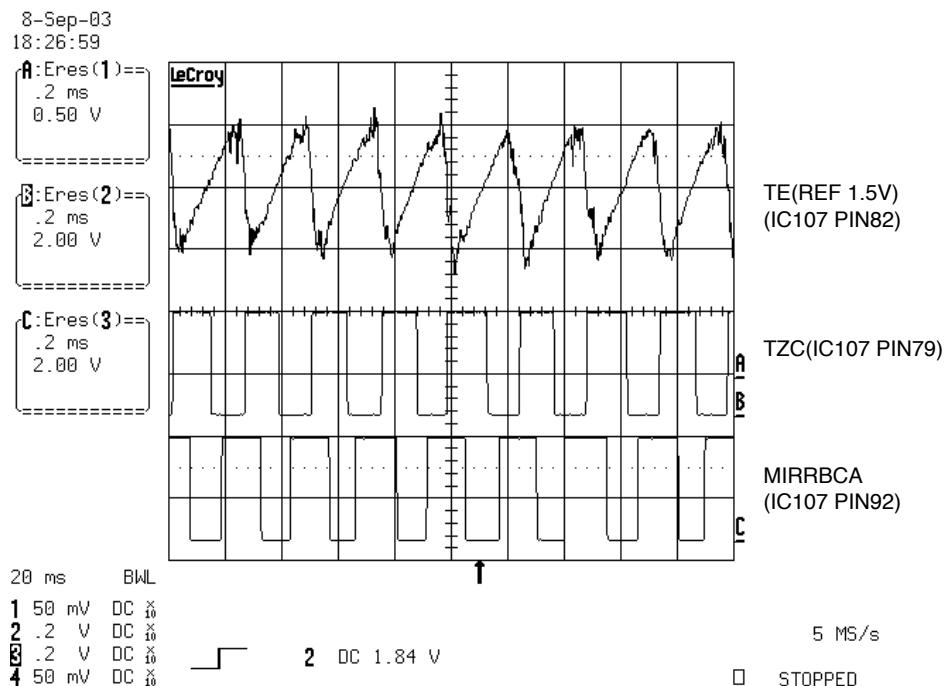
23. FOCUS ON SIGNAL (DVD)



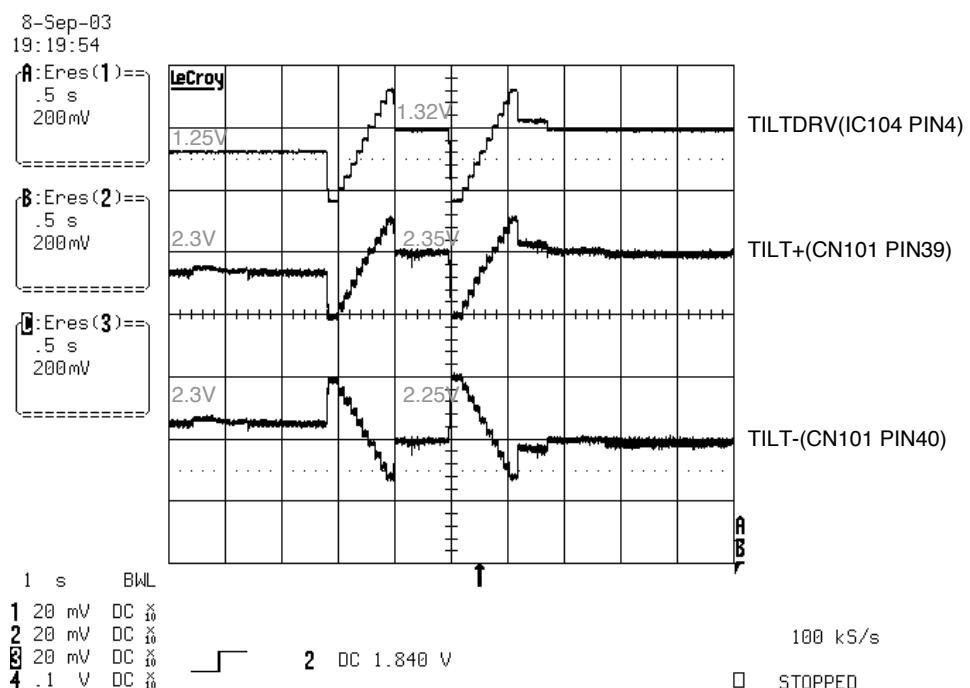
24. TRACK OFF SIGNAL(CD)



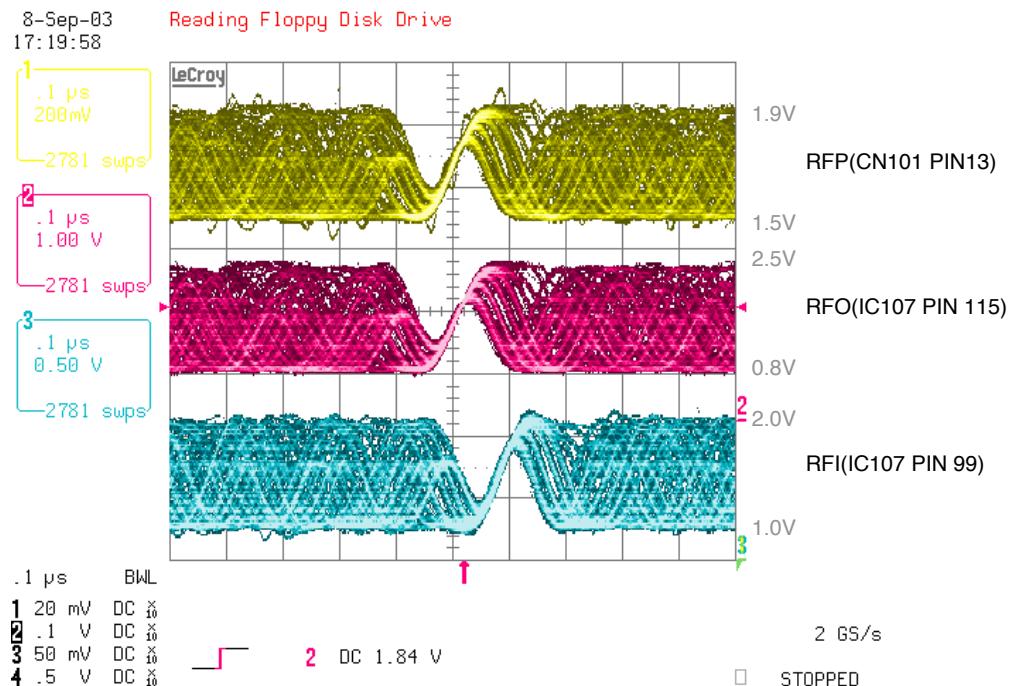
25. TRACK OFF SIGNAL(DVD)



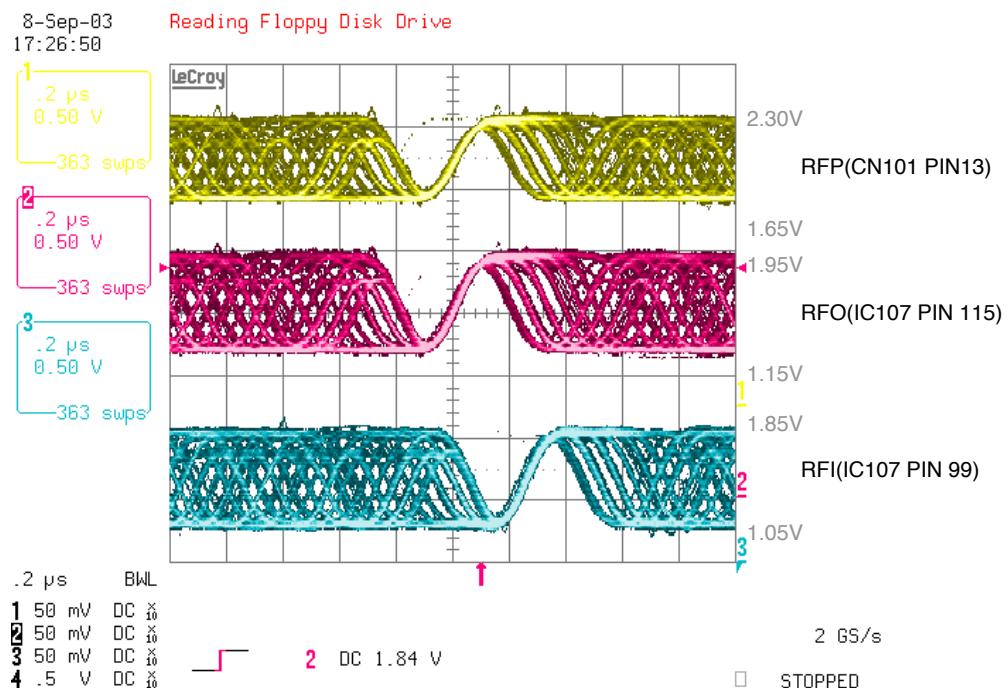
26. Tilt Driver signal(Disc reading)



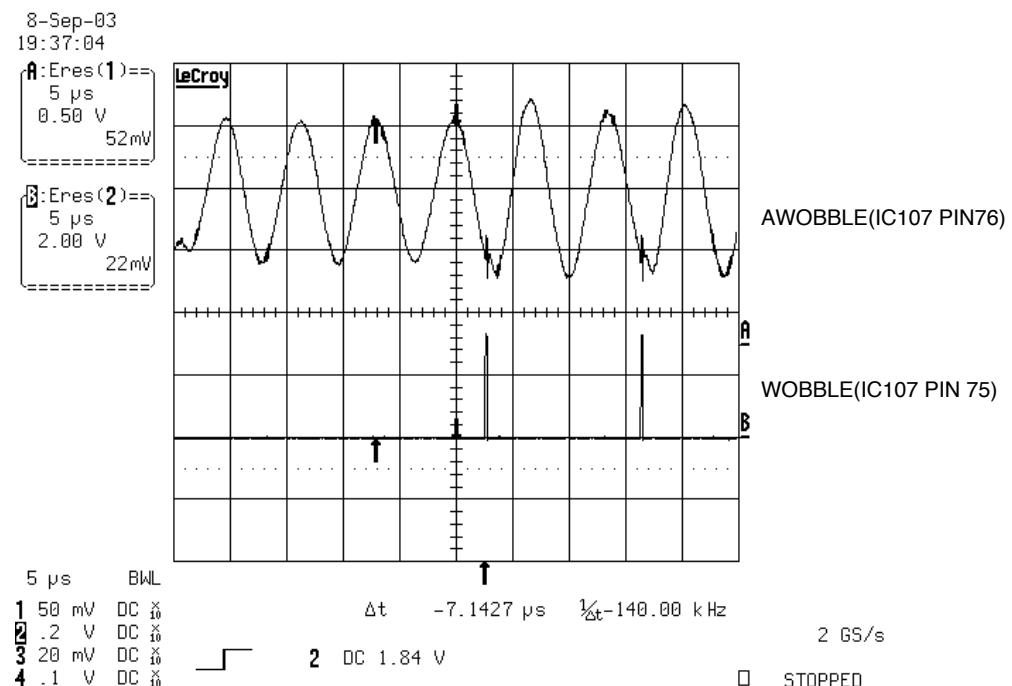
27. RF WAVEFORM(DVD)



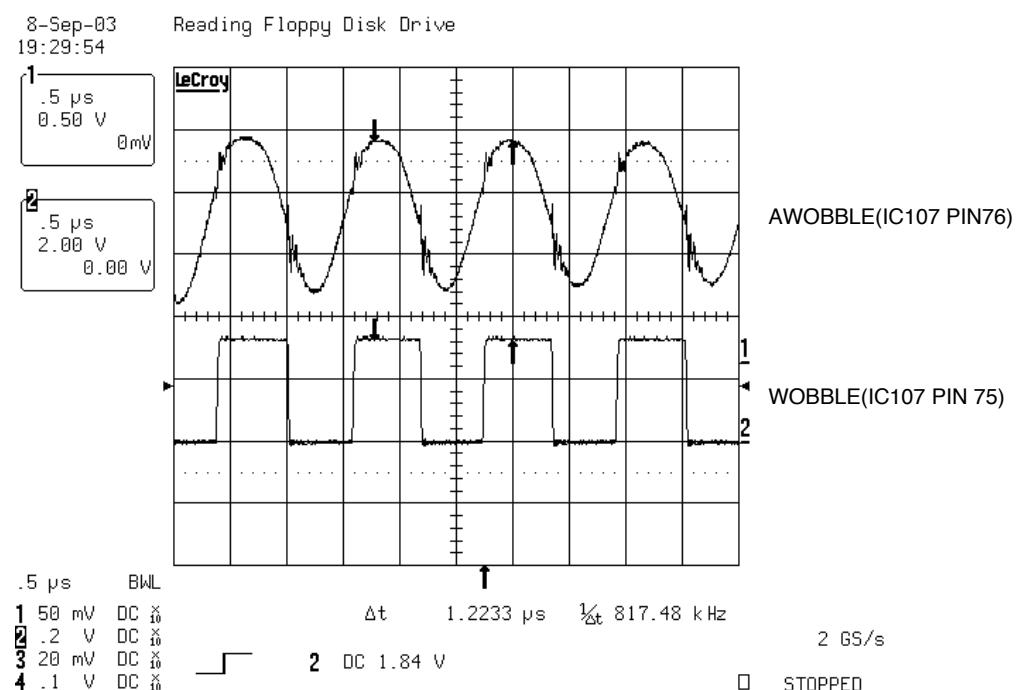
28. RF WAVEFORM(CD)



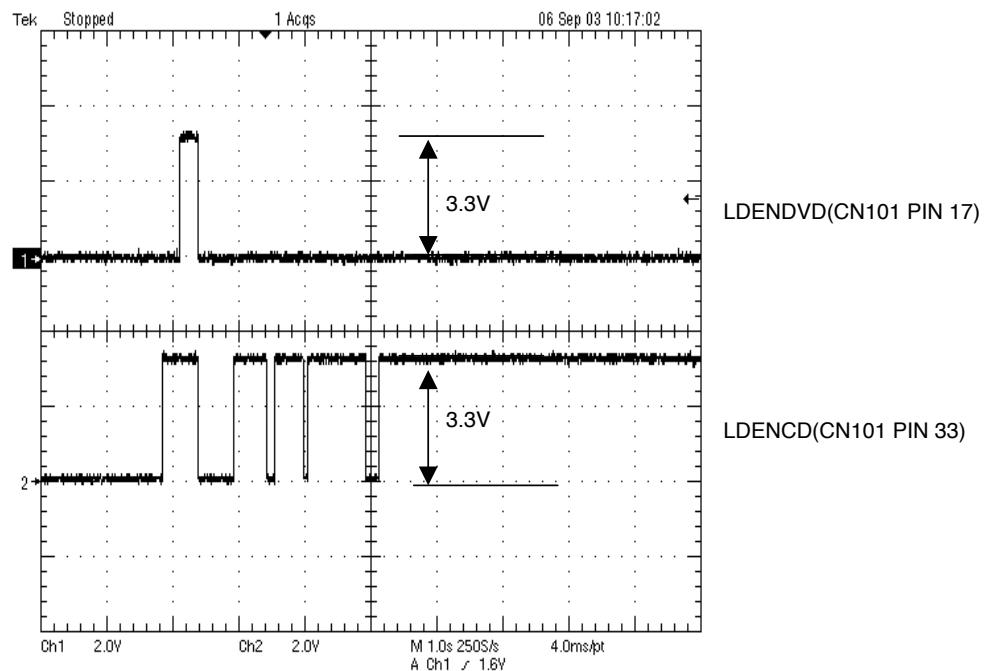
29. WOBBLE(DVD-R/RW)_READING



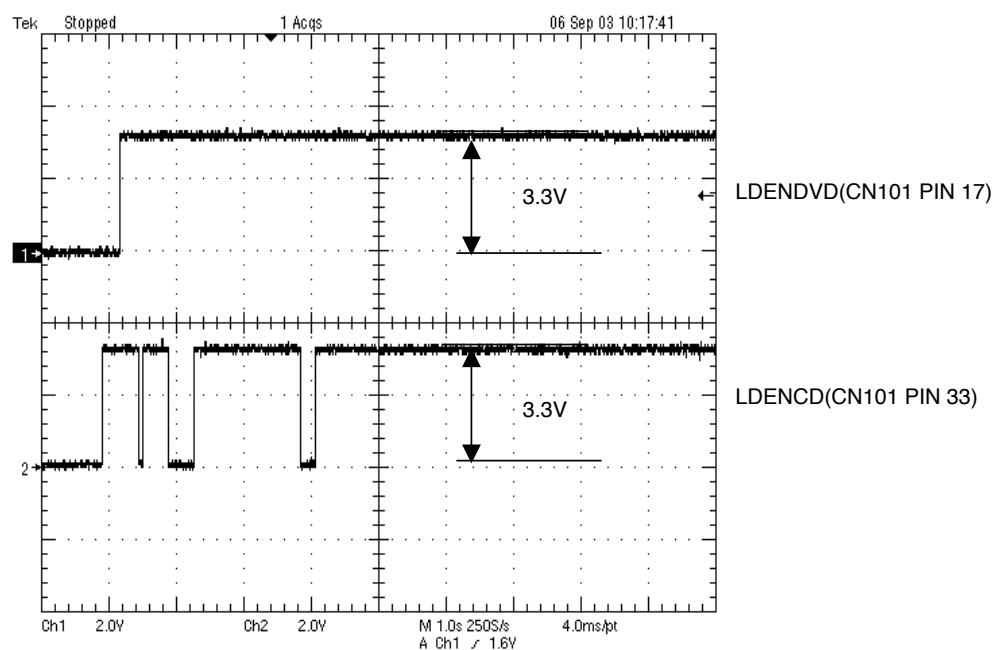
30. WOBBLE(DVD+R/RW)_READING&WRITING =>X1 SPEED



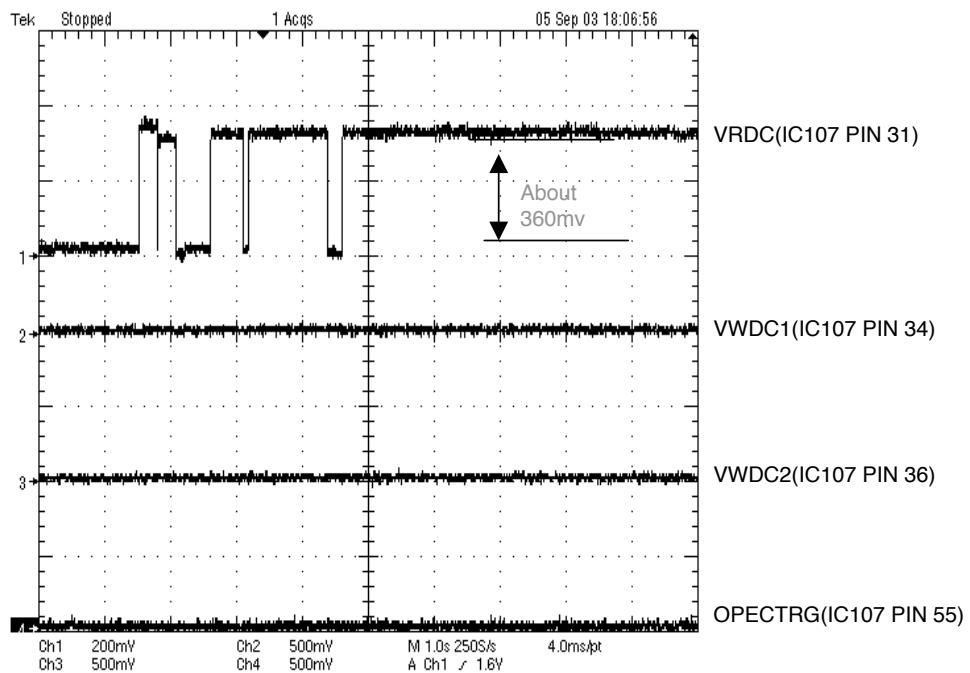
31. LD Enable(DVD)



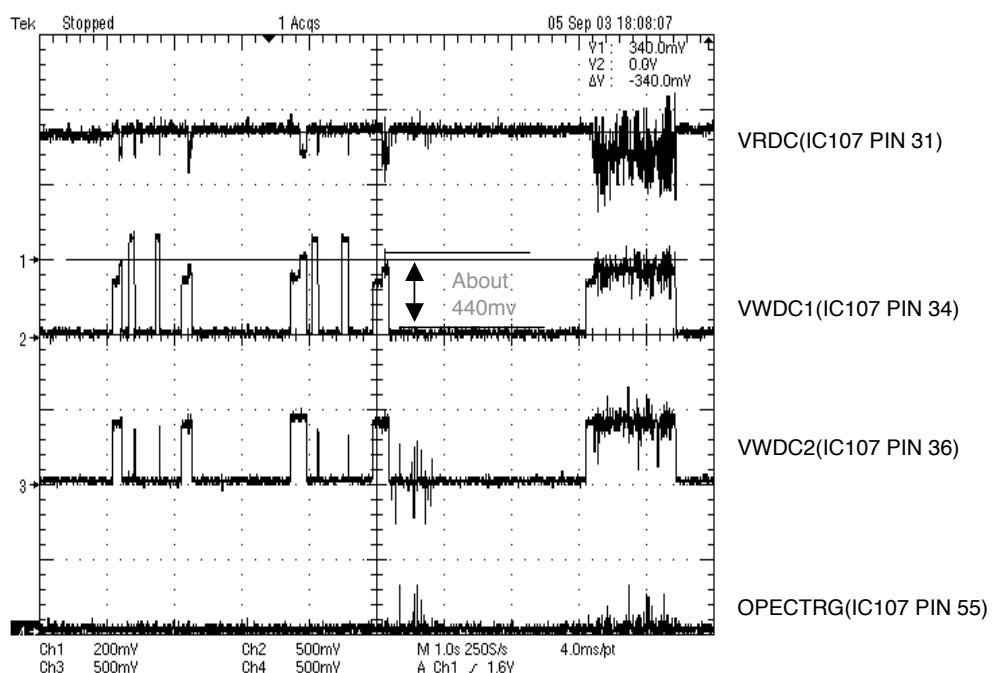
32. LD Enable(CD)



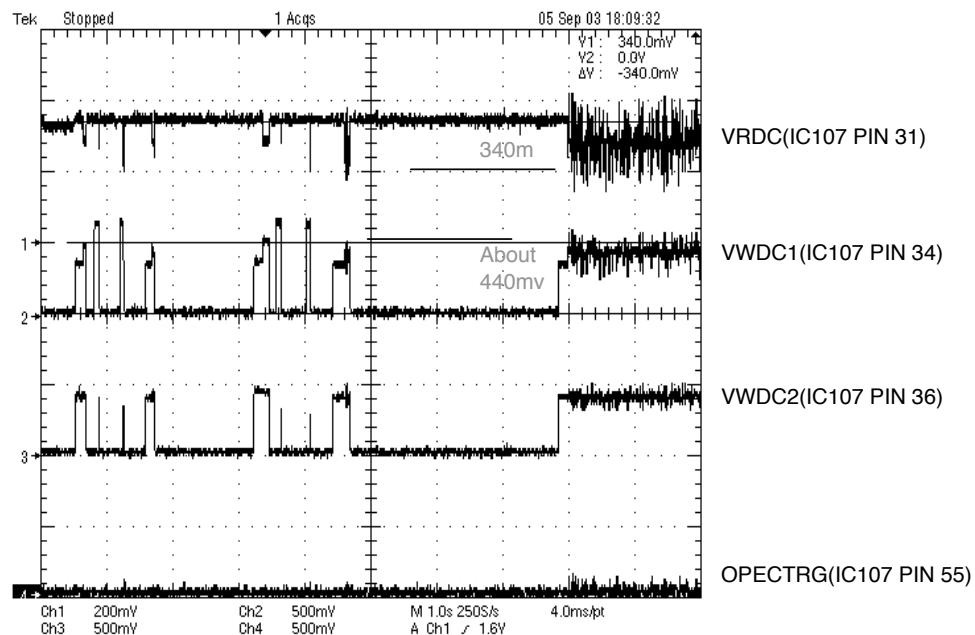
33. Laser Power(reading) _DVD+RW



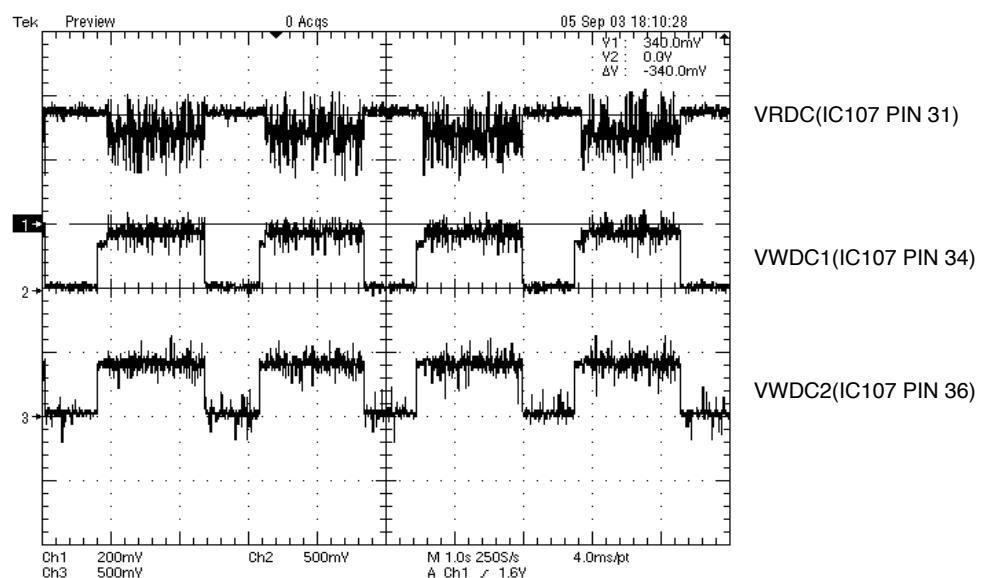
34. Laser Power(Erase) _DVD+RW



35. Laser Power(Writing)_initial state



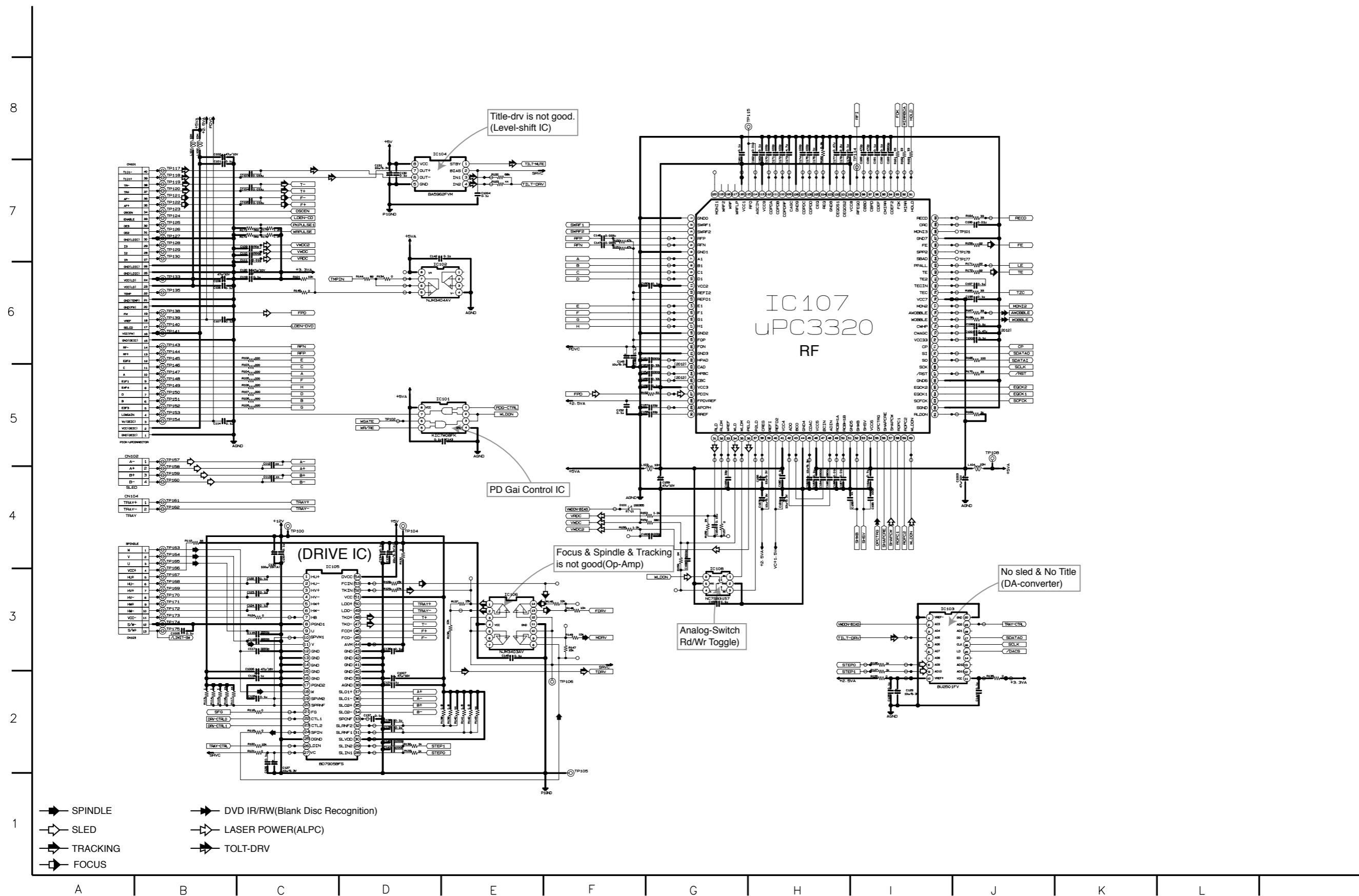
36. Laser Power(Writing)_Processing



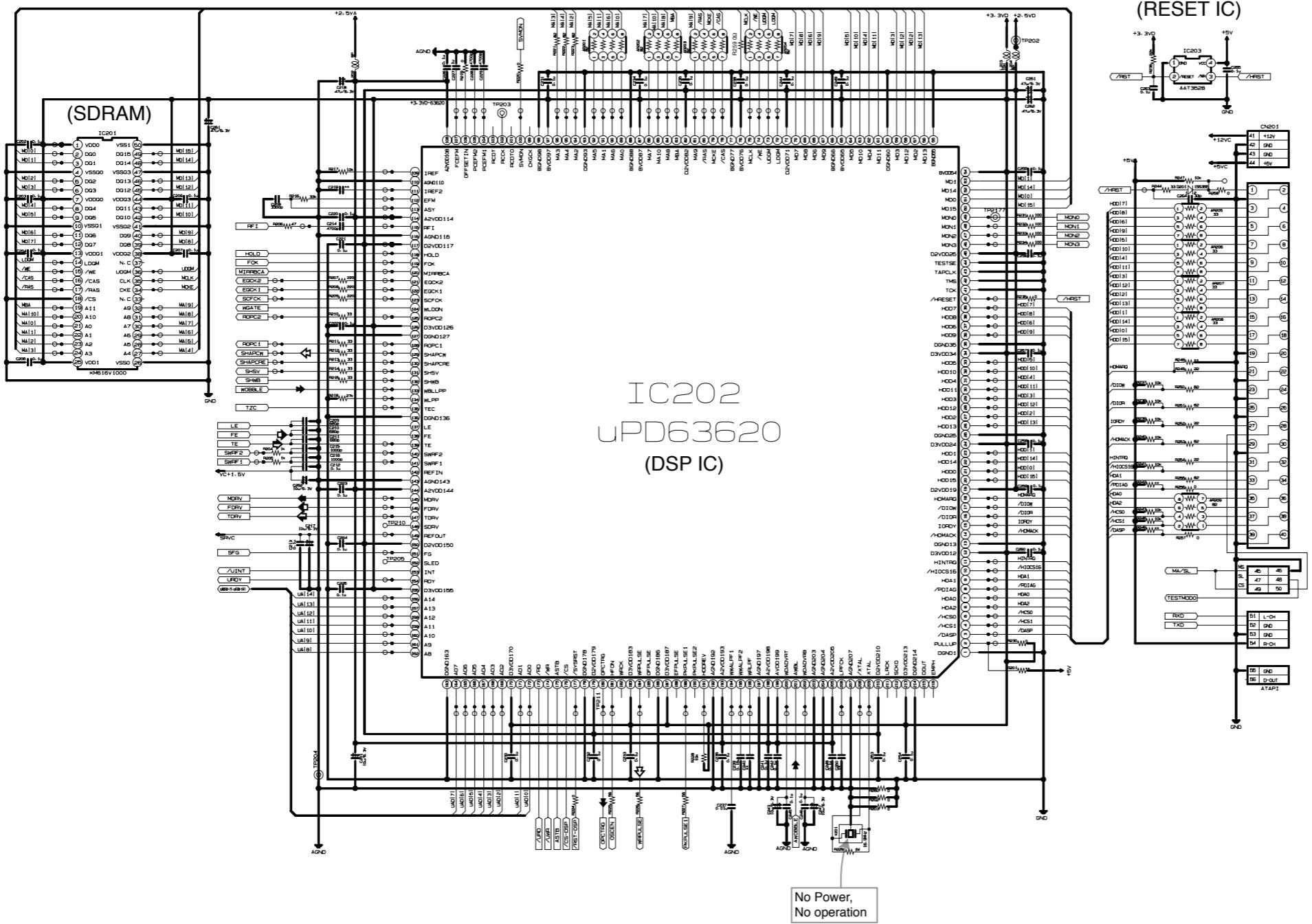
MEMO

CIRCUIT DIAGRAMS

1. RF CIRCUIT DIAGRAM



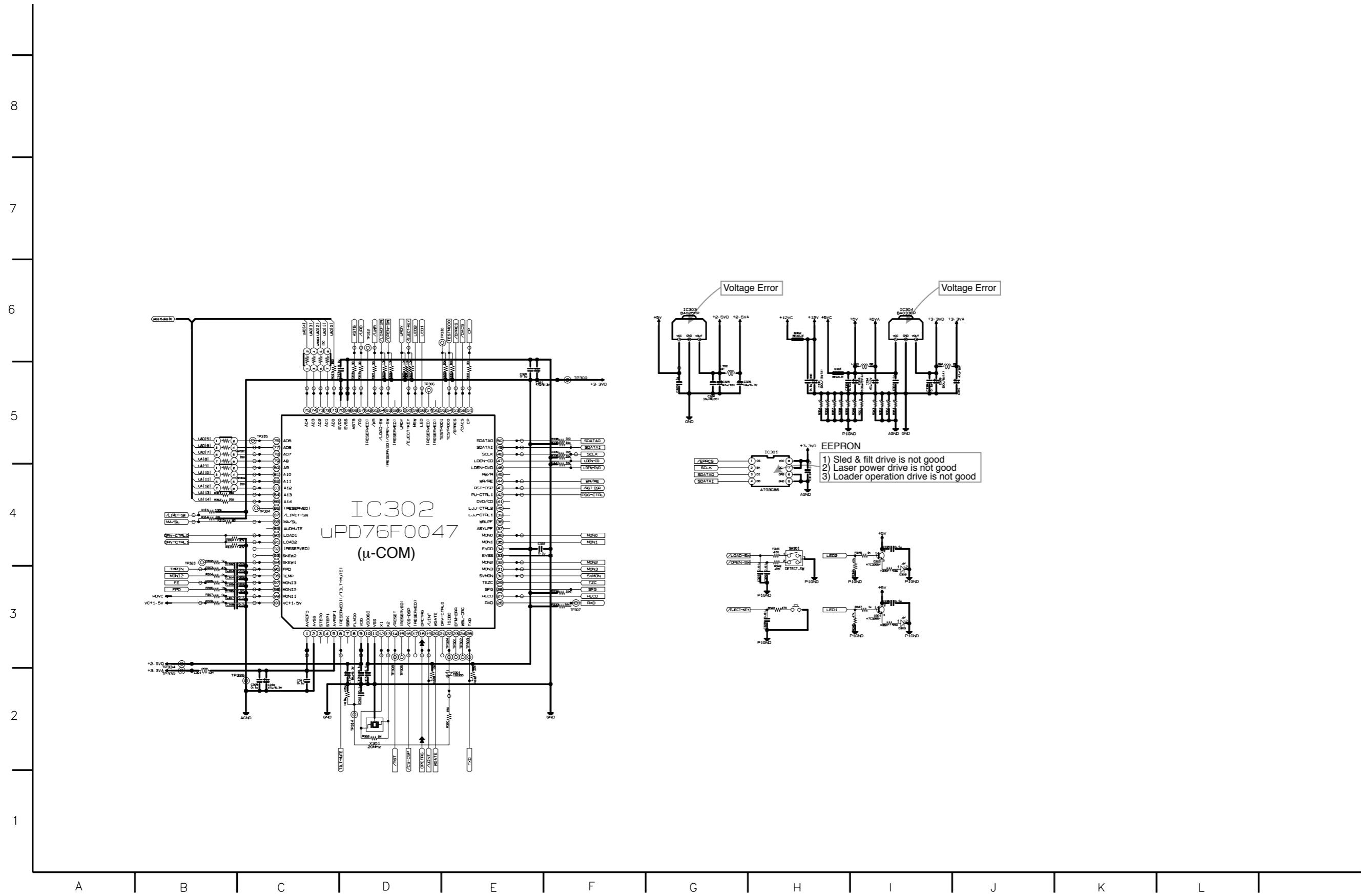
2. DSP CIRCUIT DIAGRAM



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3. μ -COM CIRCUIT DIAGRAM

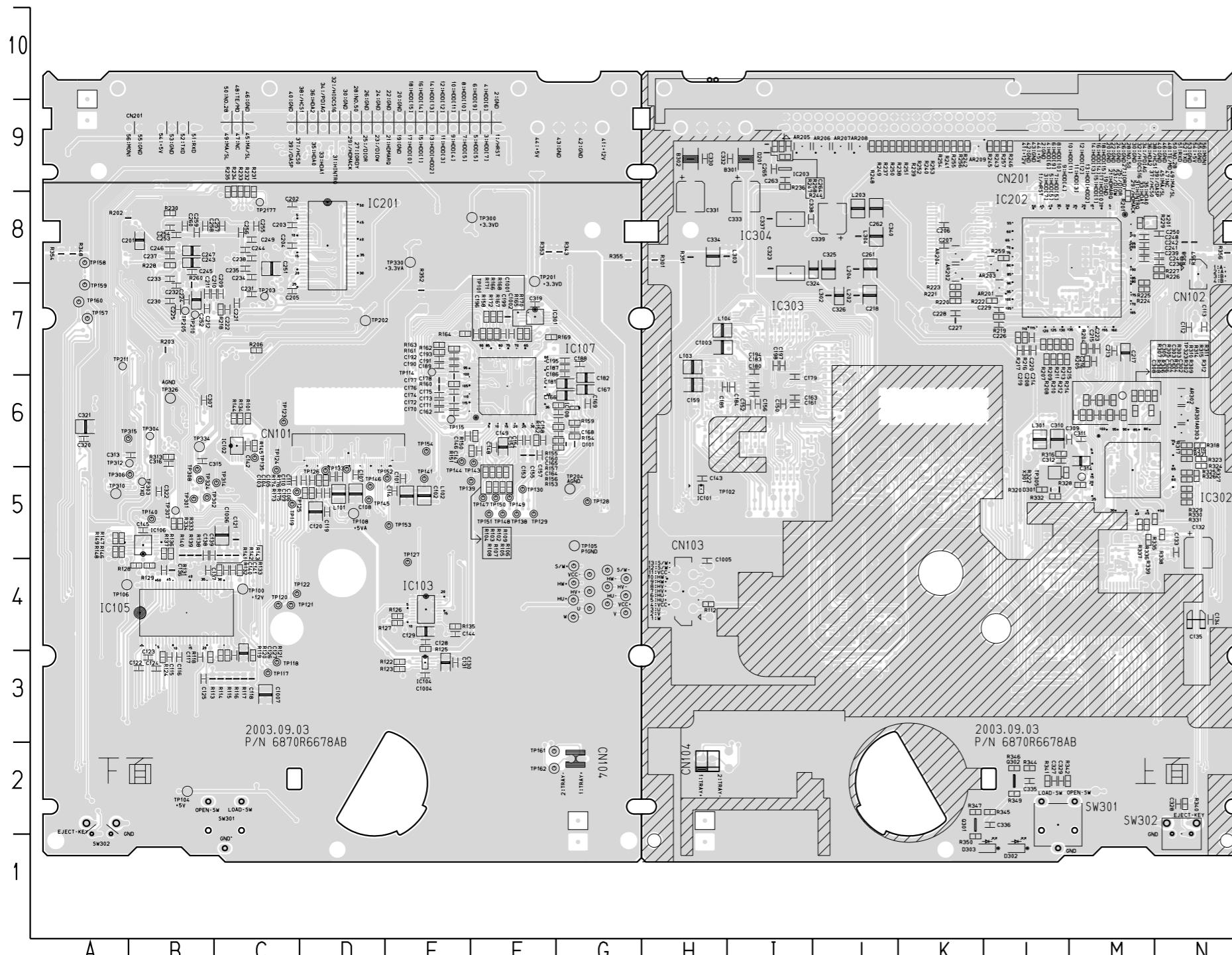


3-13

3-13

PRINTED CIRCUIT DIAGRAMS

1. MAIN P.C.BOARD



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LOCATION GUIDE	
C1001	F7
C1002	E3
C1003	C3
C1004	C176
C1005	C178
C1006	C104
C1007	C106
C101	E5
C102	E5
C103	C5
C104	C186
C105	D5
C106	D5
C107	D5
C108	D5
C109	C193
C110	C193
C111	C195
C112	C196
C113	B3
C114	C197
C115	C197
C116	C202
C117	C202
C118	C203
C119	D5
C120	C205
C121	C205
C122	C210
C123	B3
C124	B3
C125	C212
C126	C212
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C136	C213
C137	C213
C138	C213
C139	C213
C140	C213
C141	C213
C142	C213
C143	C213
C144	E4
C145	C246
C146	C246
C147	F6
C148	F6
C149	F6
C150	C253
C151	C253
C152	F6
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