



SERVICE MANUAL

MODELS : HDR899/1000

DVB-T/HDD/DVD RECORDER

SERVICE MANUAL

MODELS : HDR899/1000

CAUTION

BEFORE SERVICING THE UNIT, READ THE "SAFETY PRECAUTIONS"
IN THIS MANUAL.



SERVICE INFORMATION FOR EEPROM IC SETTING

1. Please the CLEAR button on the remote together with ▲ OPEN the Front Panel about 6 sec.



The picture on OSD will be bellow ;

Model :

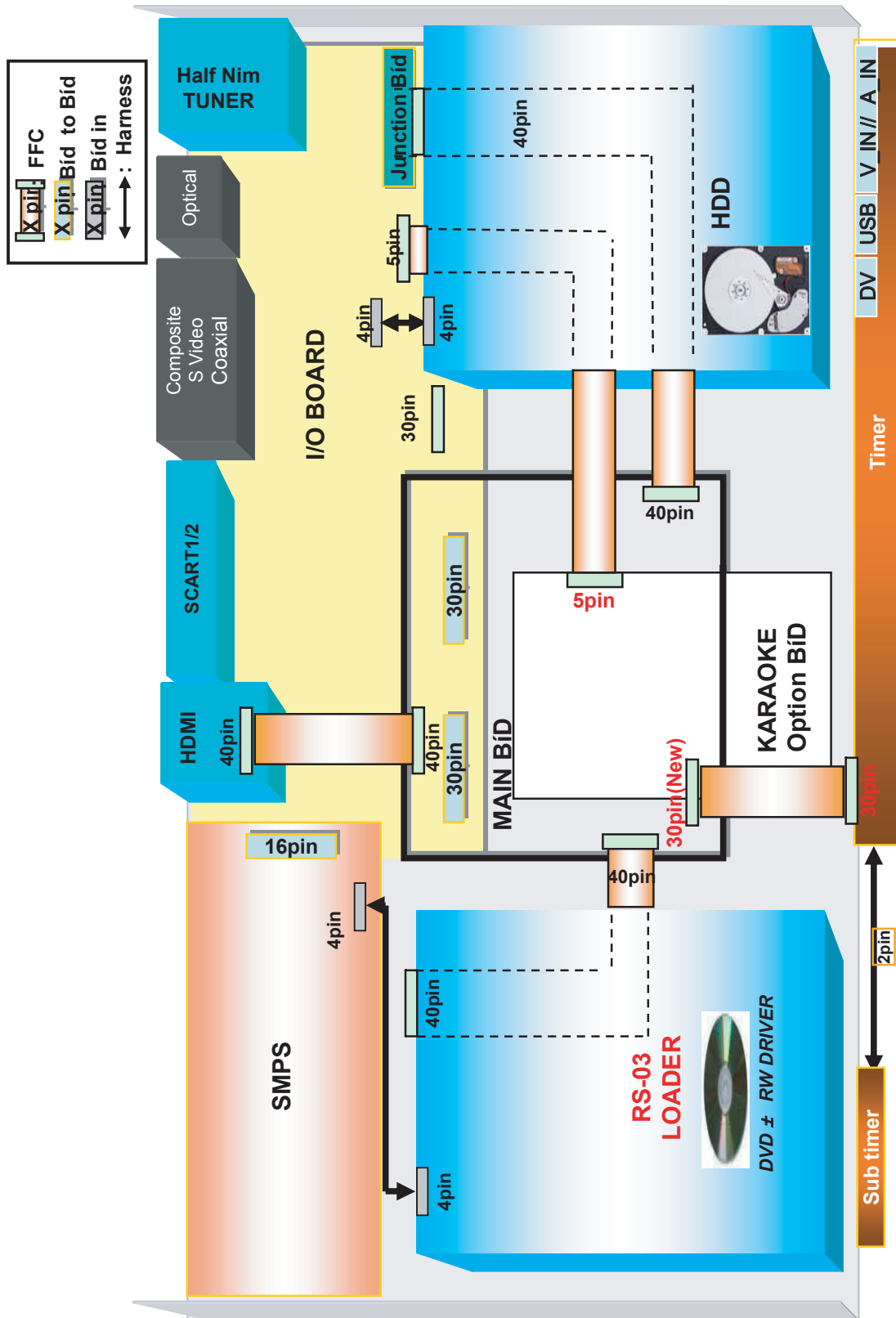
	Disc	Read Time
	DVD	018 Sec OK
	CD	021 Sec NG
OP1 : 00	00000000	M : 10.013
OP2 : 00	00000000	E : FF(OR)
OP3 : 00	00000000	
OP4 : 00	00000000	
OP5 : 00	00000000	
OP6 : 00	00000000	
OP7 : 00	00000000	Write : OK
OP8 : 00	00000000	Exit : MP
OP9 : 00	00000000	Move : <>
OPA : 00	00000000	Edit :



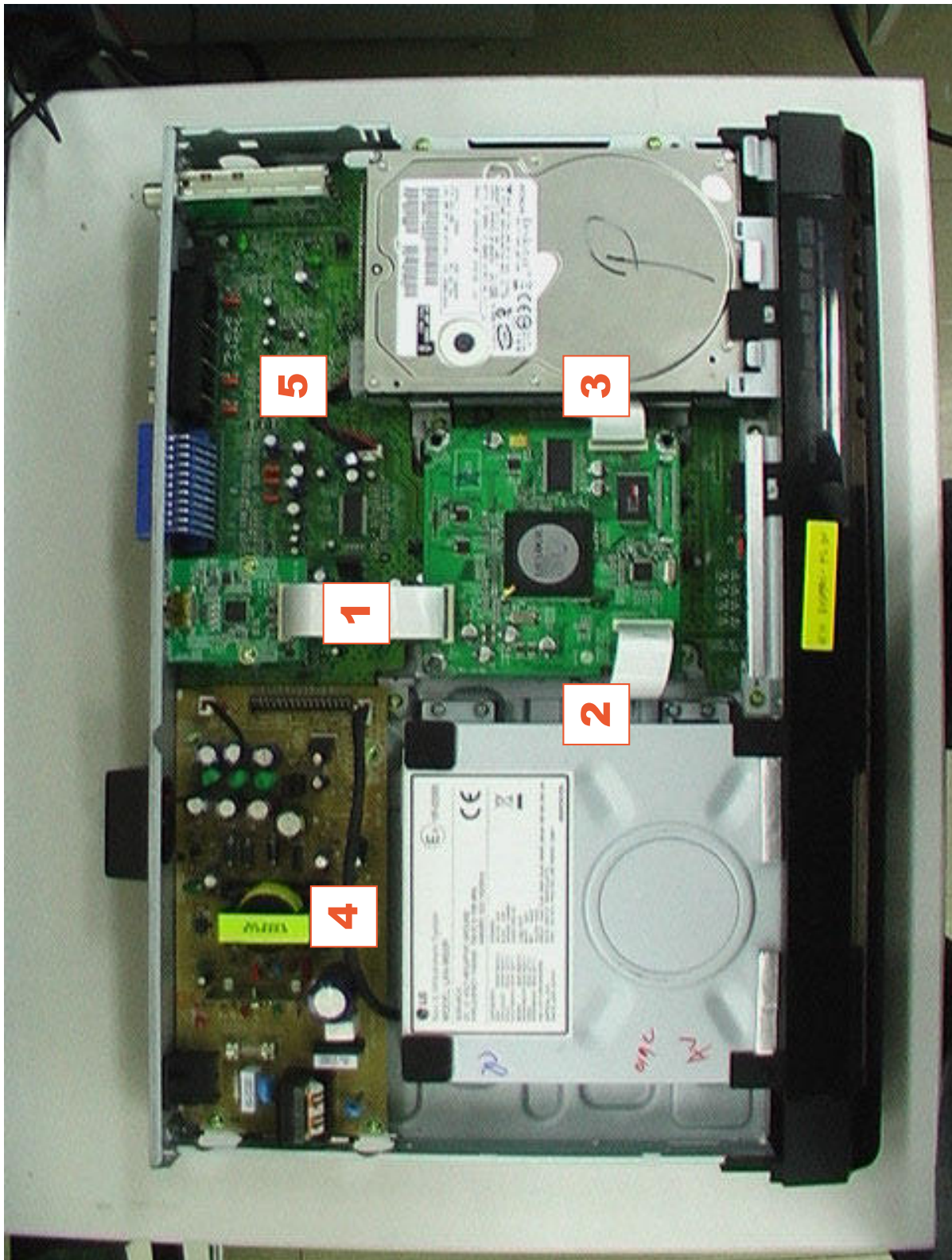
2. To MOVE from OP! to another option. Please ◀▶ button on the remote control.
3. To CHANGE the Option Code. Please ⬆ button on the remote control.
4. To APPLY the Option Code. After change the option press ENTER button on remote control.
5. To INITIALIZE the system. Press CLEAR button on the remotecontrol together with OPEN on the Front panel about 2sec.
6. To exit from the Option Code menu without Initialize the system.
Just turn off the power and turn on again.

WIRING CONNECTION DIAGRAM

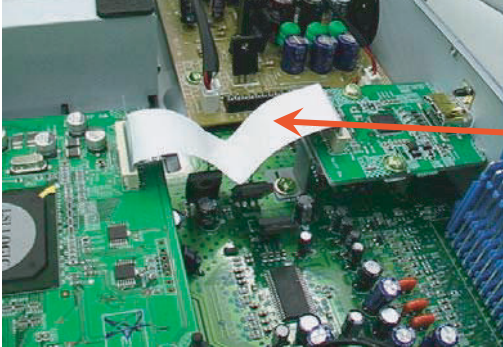
1. WIRING CONNECTION DIAGRAM 1



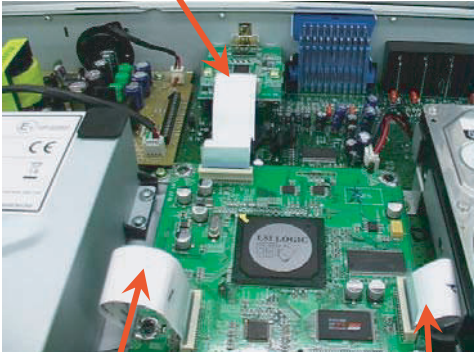
2. WIRING CONNECTION DIAGRAM 2



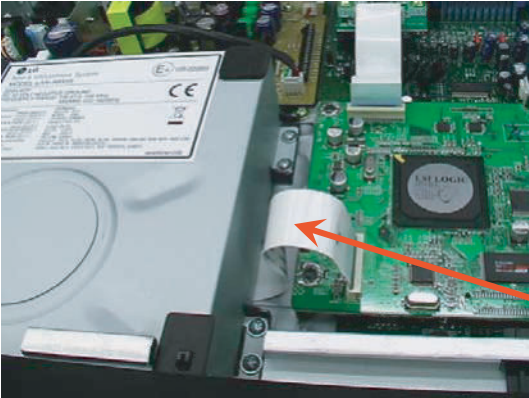
3. WIRING CONNECTION DIAGRAM 3



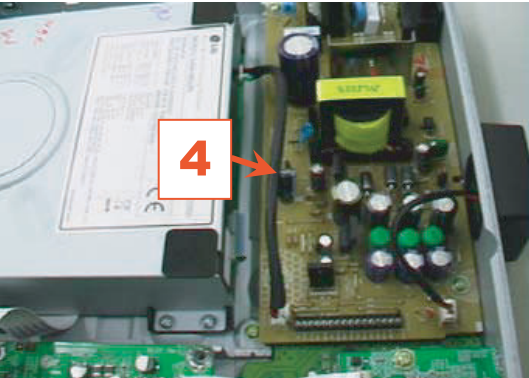
1



2



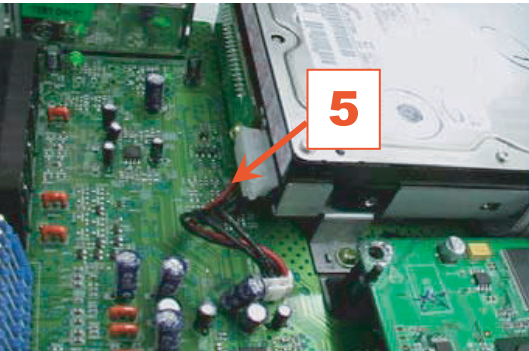
3



4

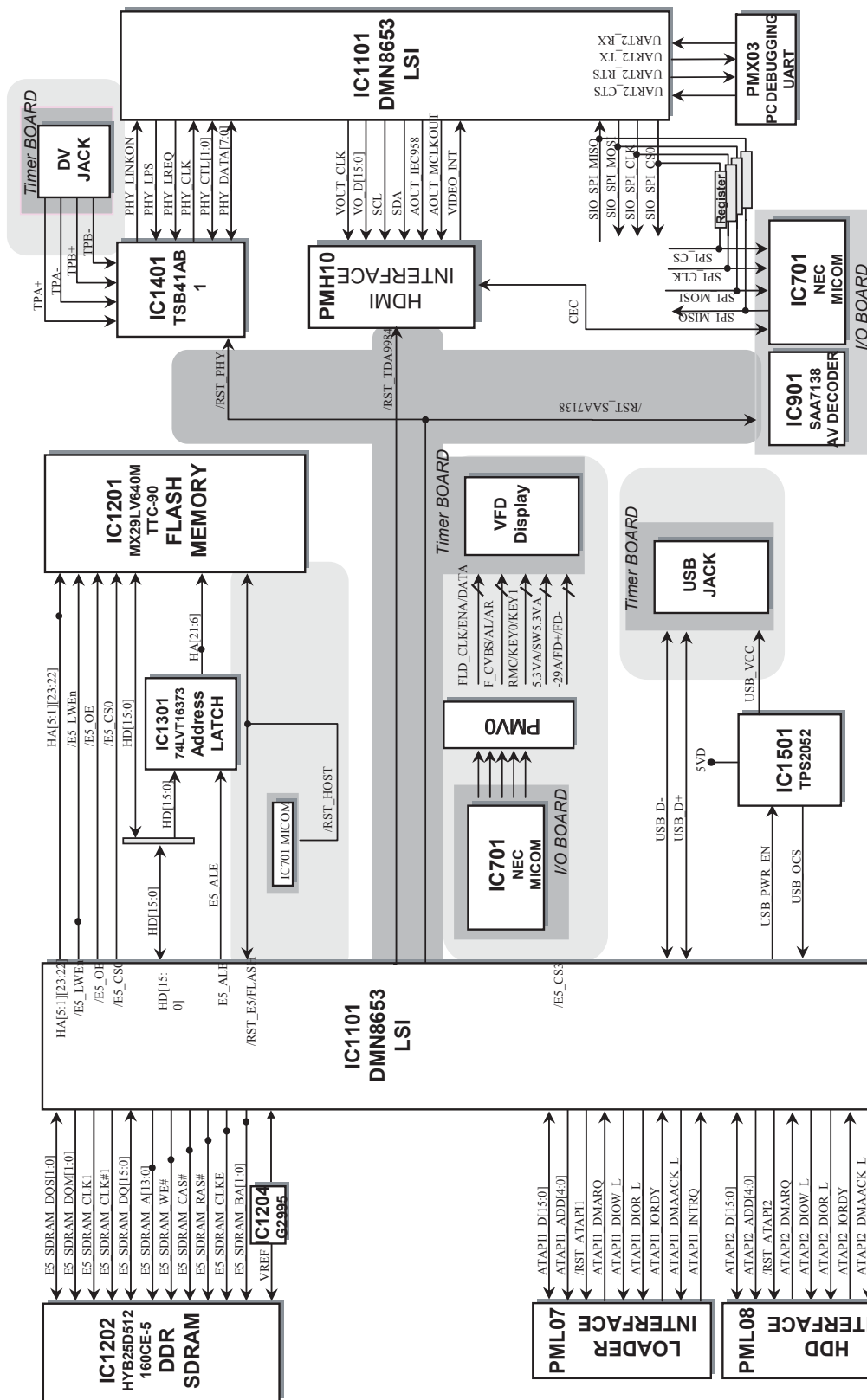


5

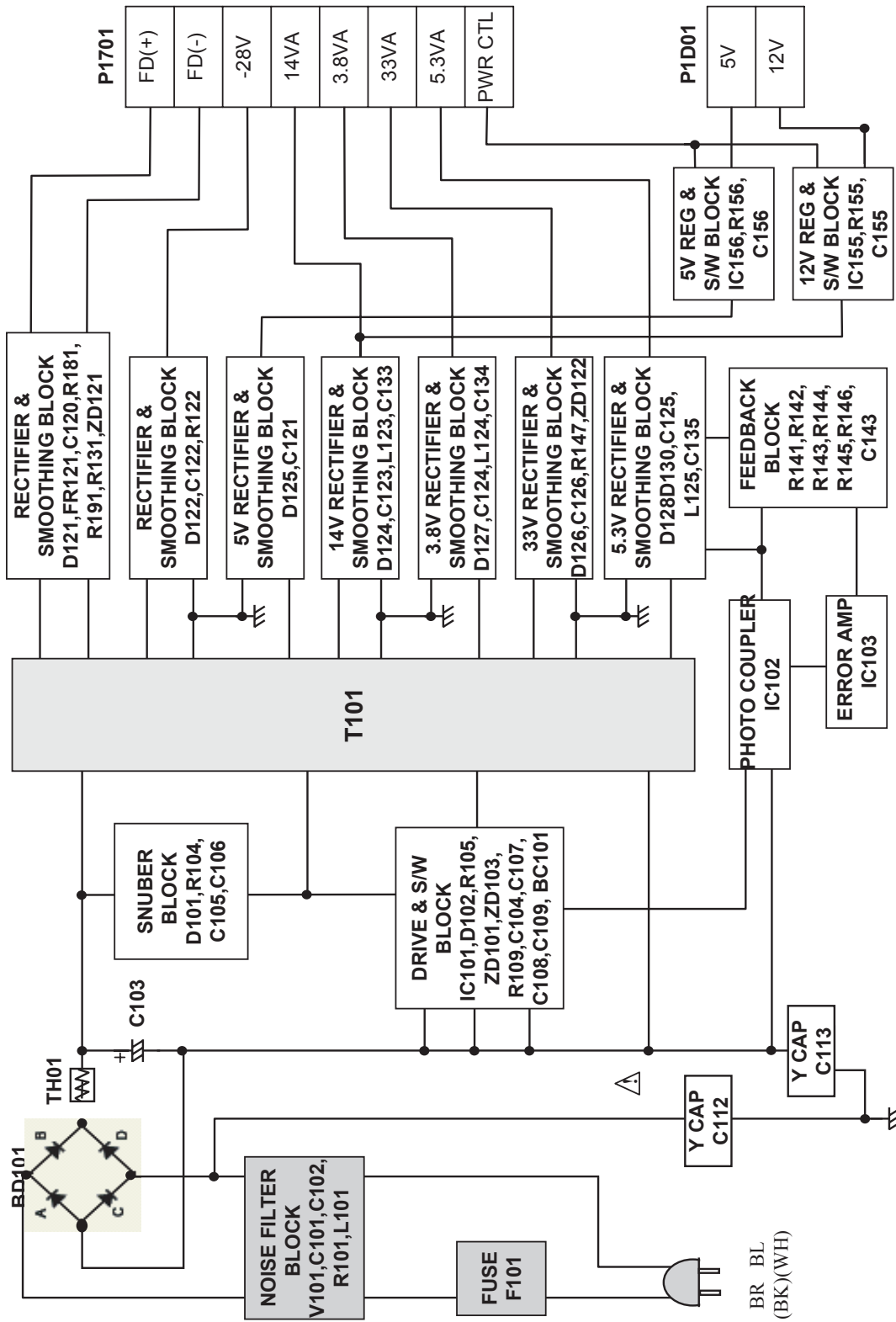


BLOCK DIAGRAMS

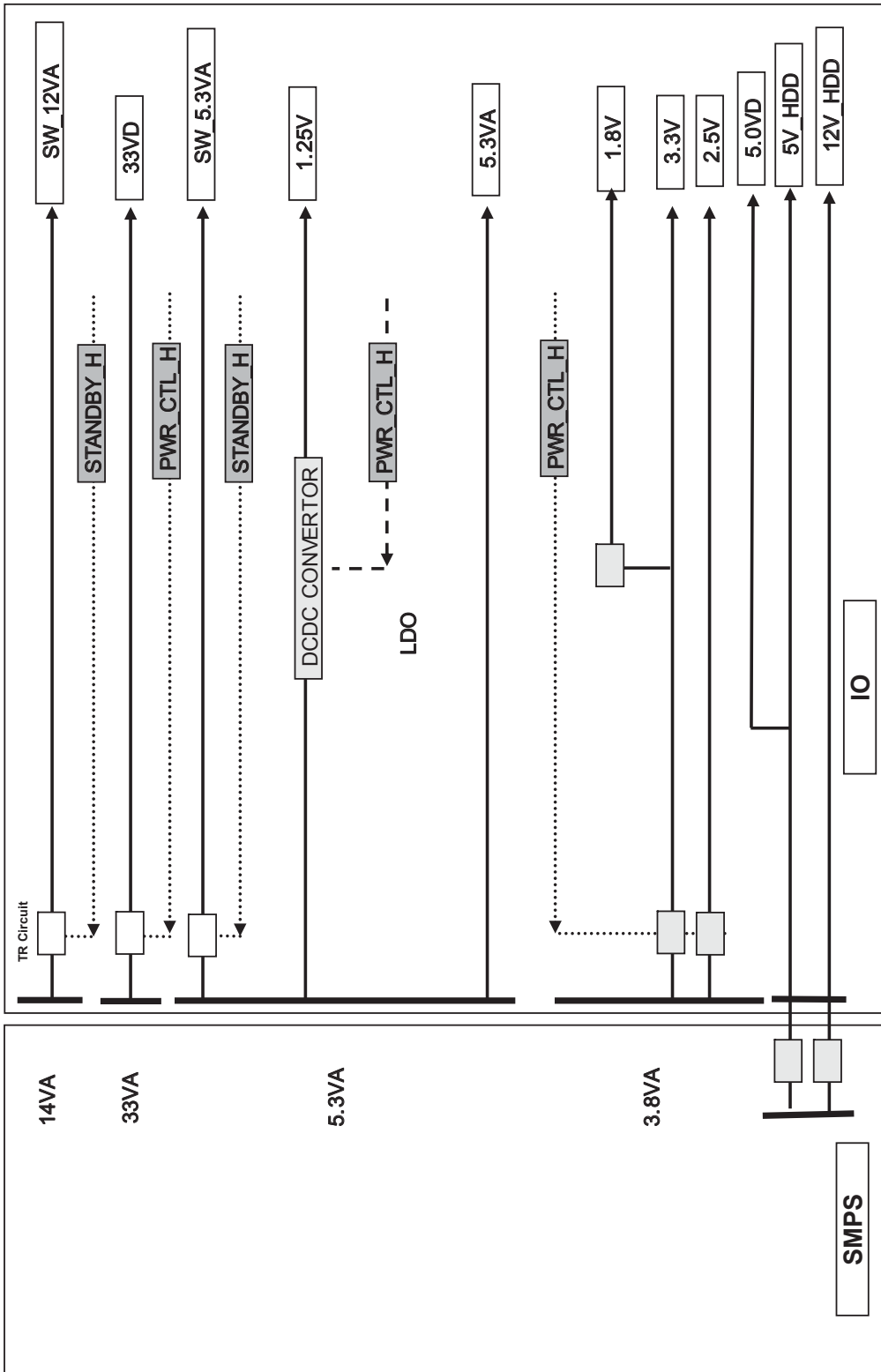
1. OVERALL BLOCK DIAGRAM



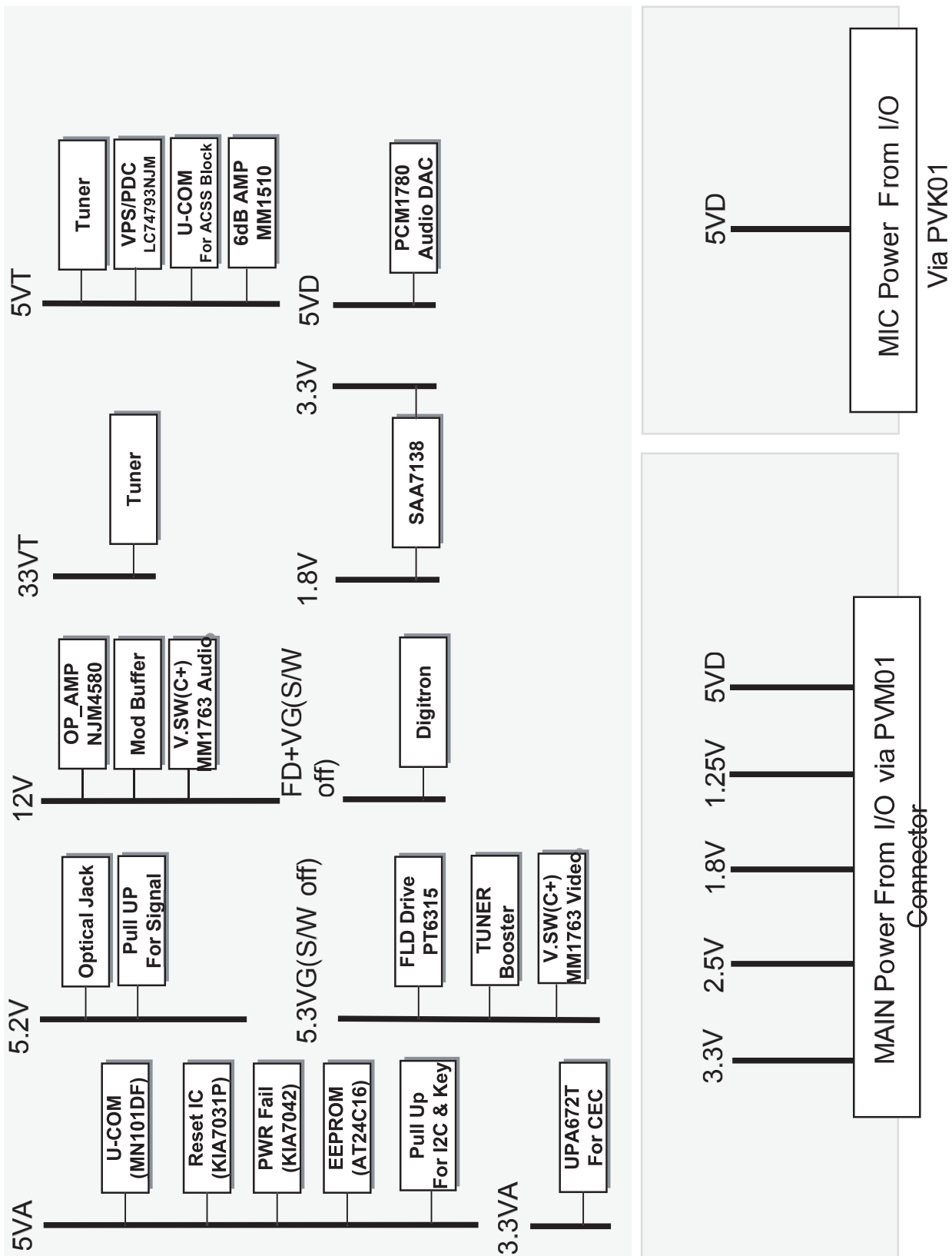
2. SMPS BLOCK DIAGRAM



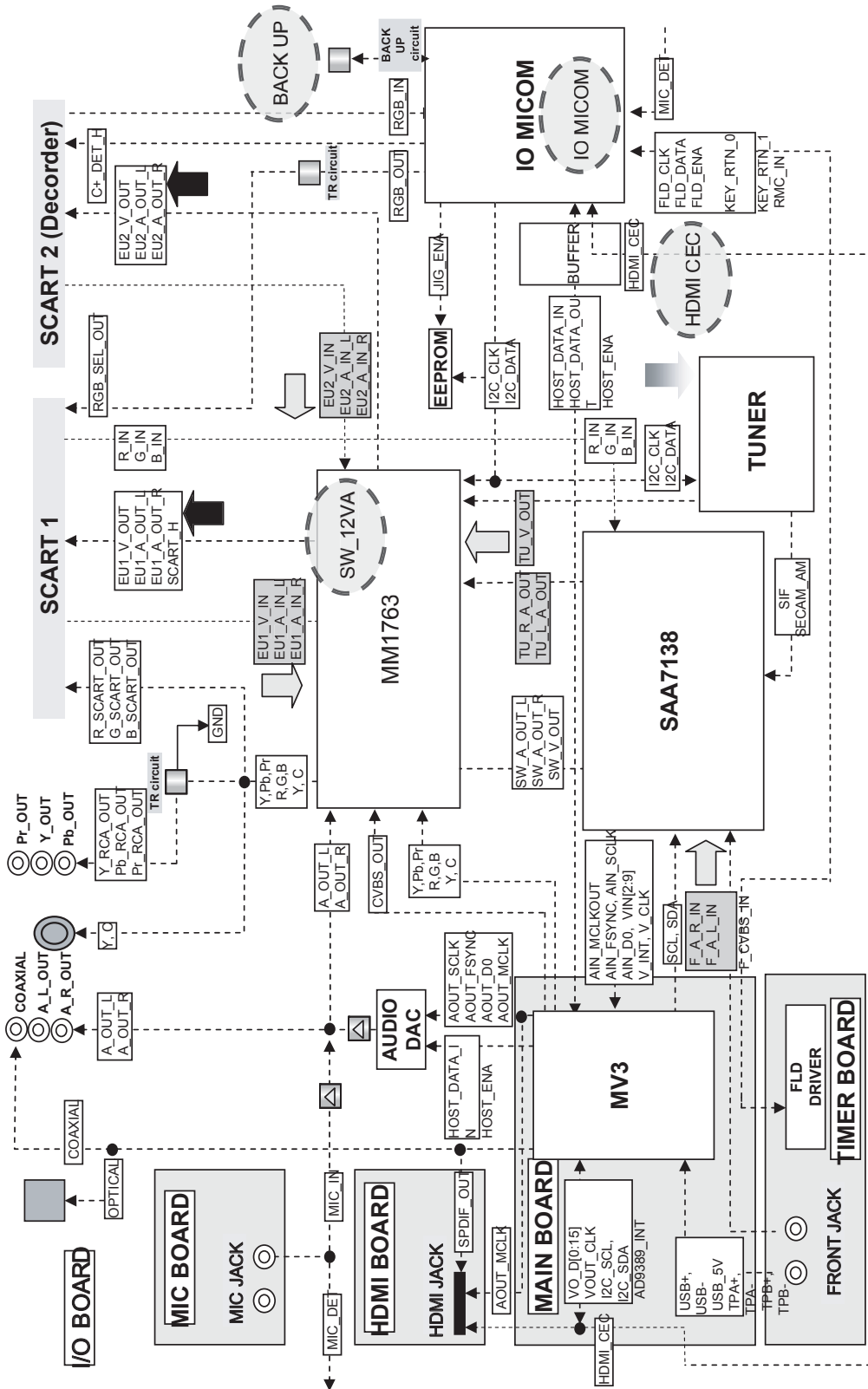
3. POWER MAIN BLOCK DIAGRAM



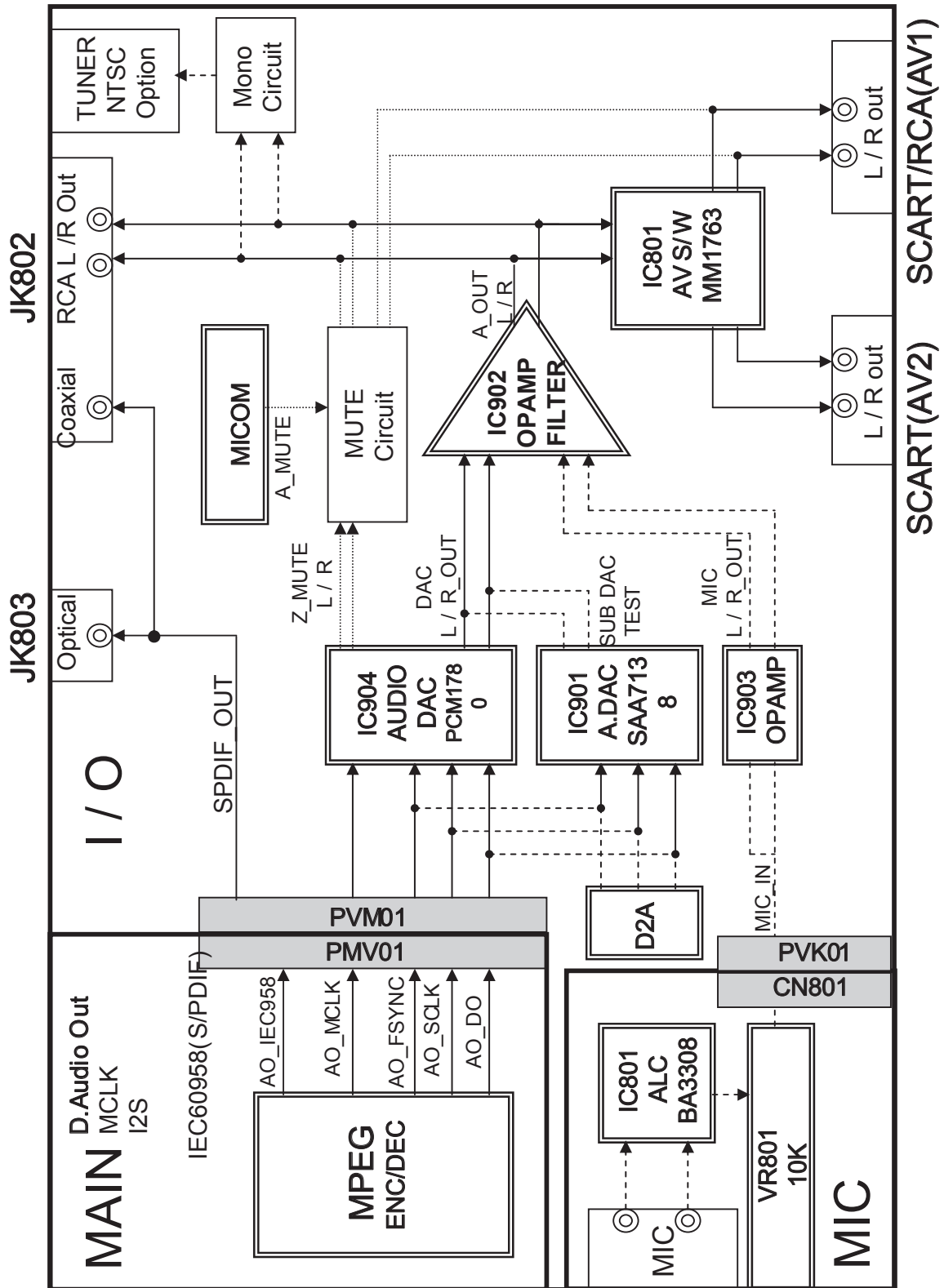
4. POWER I/O BLOCK DIAGRAM



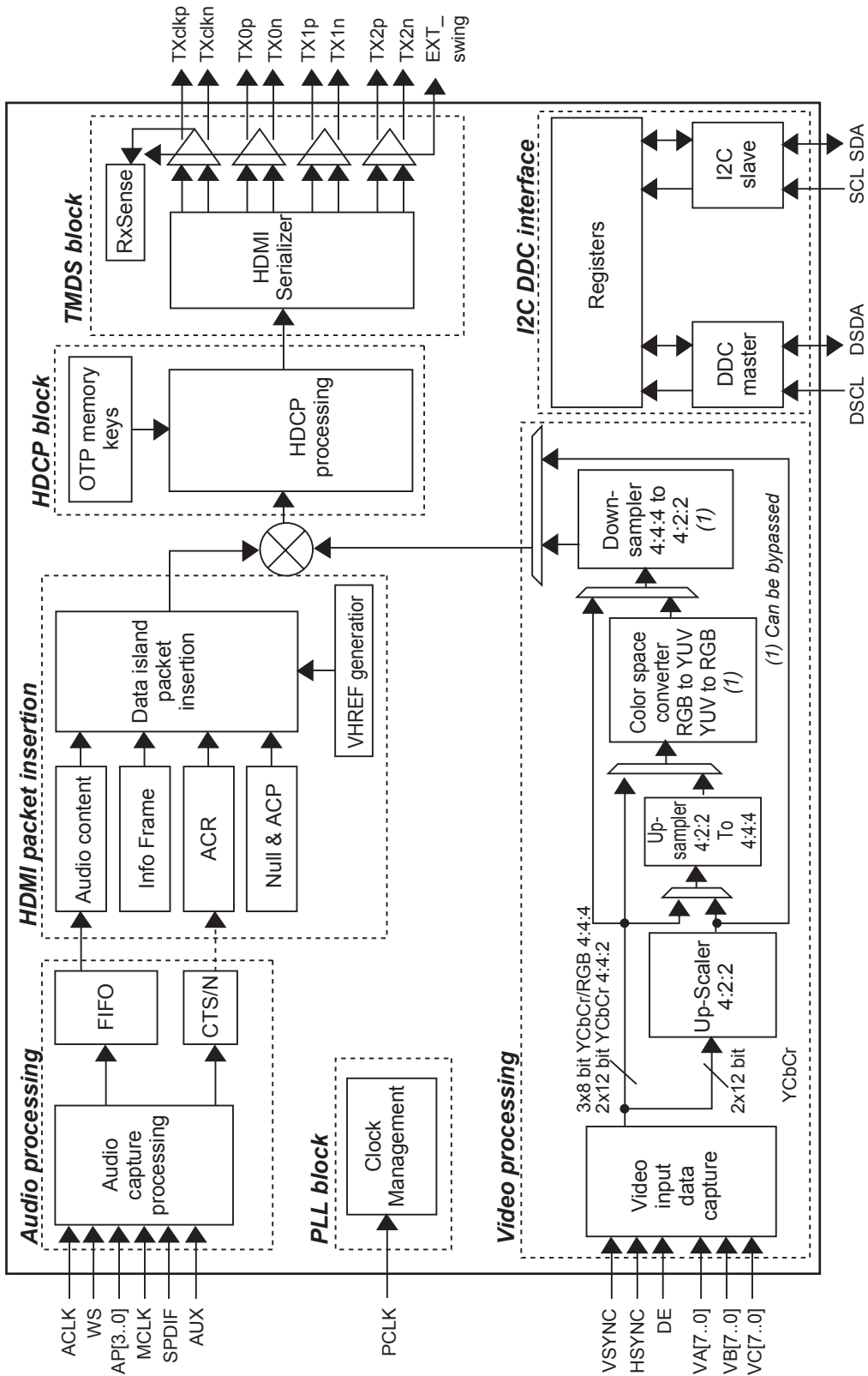
5. AV BLOCK DIAGRAM



6. AUDIO OUT BLOCK DIAGRAM



7. HDMI BLOCK DIAGRAM



CIRCUIT DIAGRAMS

1. SMPS 1 CIRCUIT DIAGRAM

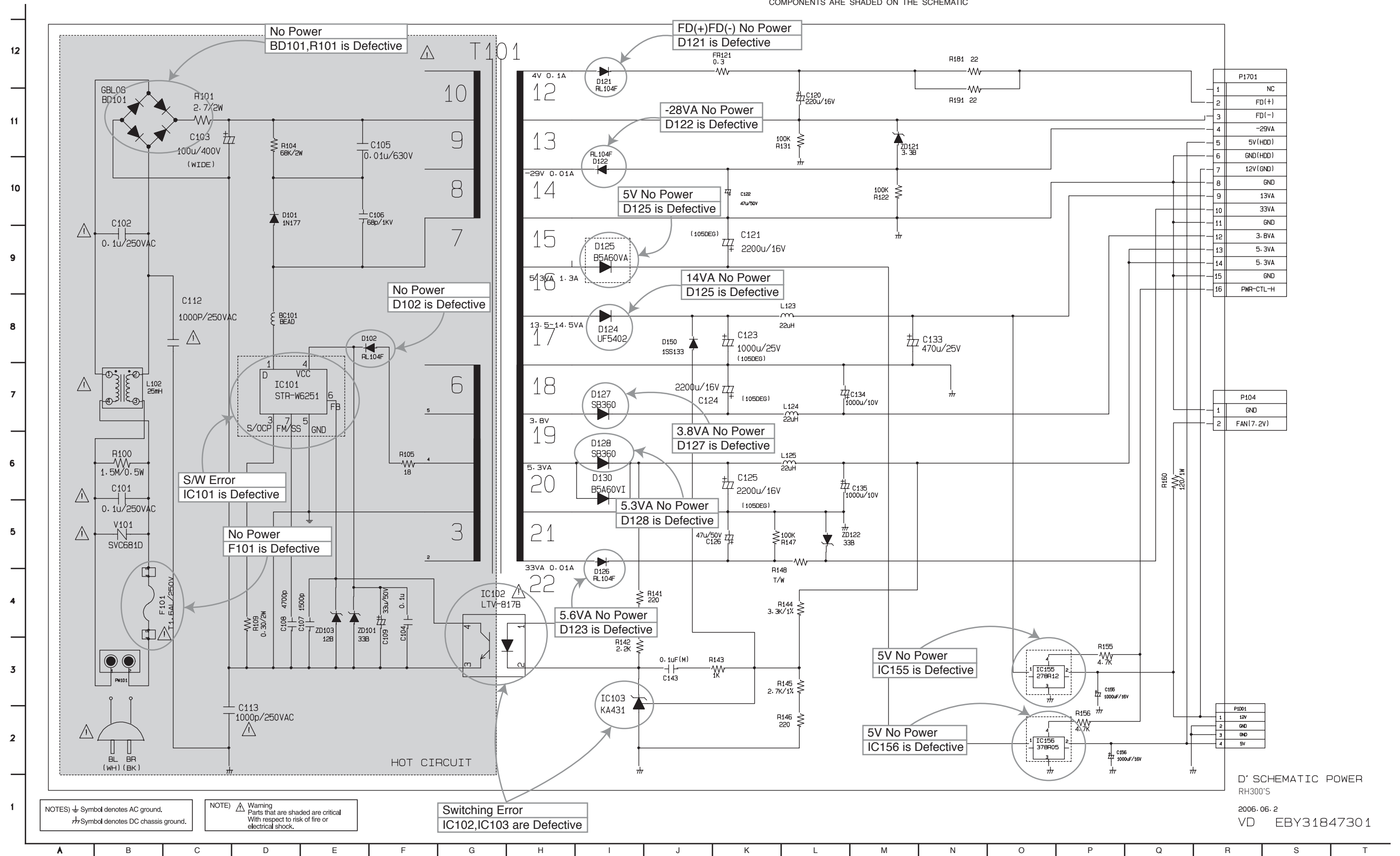
IMPORTANT SAFETY

WHEN SERVICING THIS CHASSIS, UNDER NO CIRCUMSTANCES SHOULD THE ORIGINAL DESIGN BE MODIFIED OR ALTERED WITHOUT PERMISSION FROM THE LG CORPORATION. ALL COMPONENTS SHOULD BE REPLACED ONLY WITH TYPES IDENTICAL TO THOSE IN THE ORIGINAL CIRCUIT. SPECIAL COMPONENTS ARE SHADED ON THE SCHEMATIC

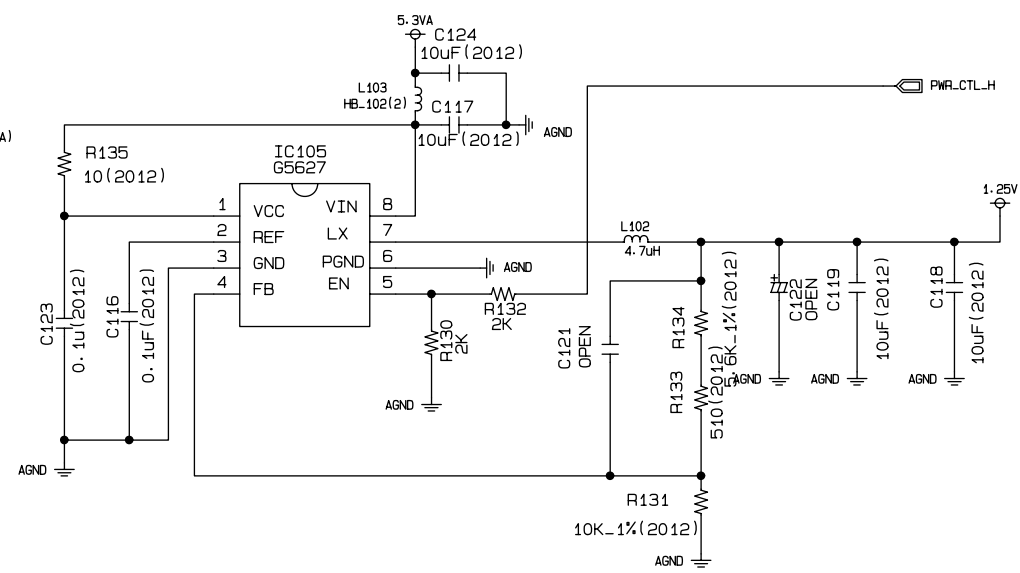
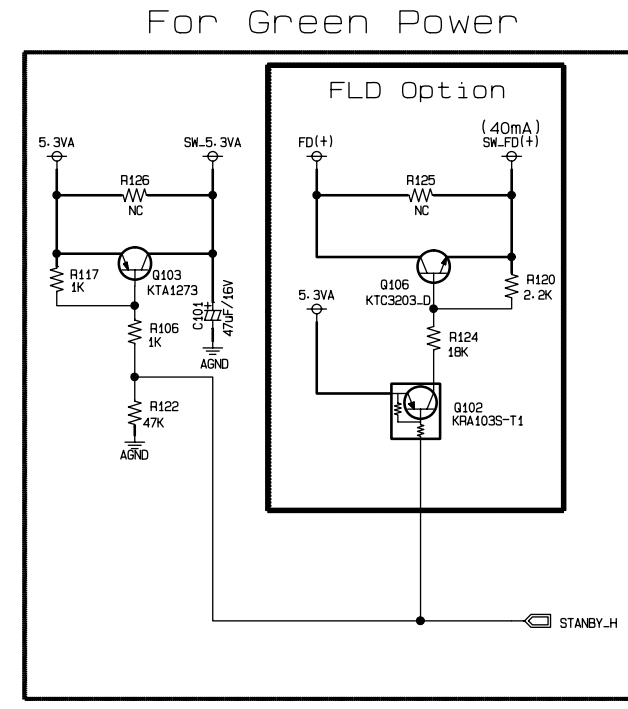
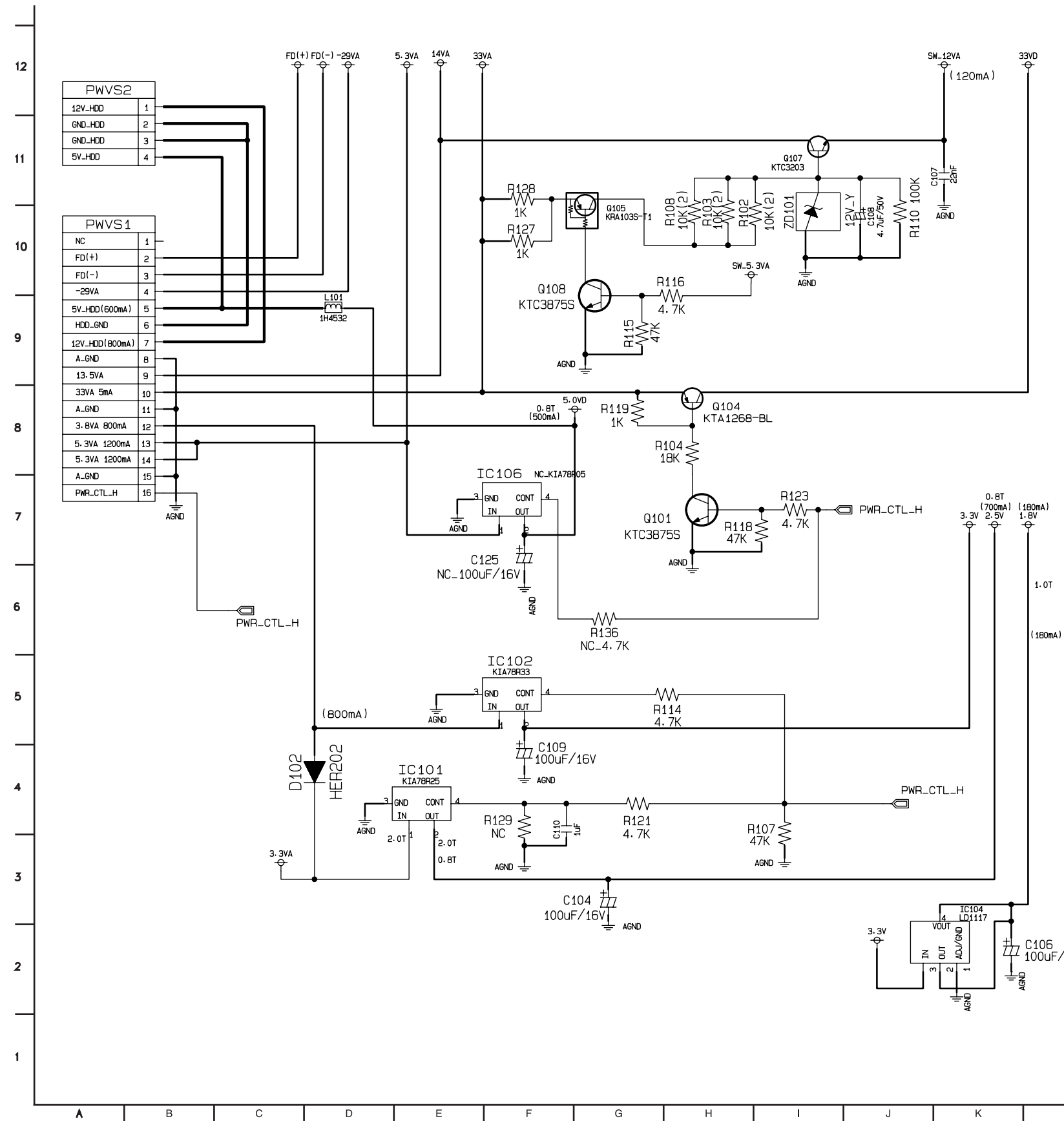
FOR EASY IDENTIFICATION, THIS CIRCUIT DIAGRAM MAY OCCASIONALLY DIFFER FROM THE ACTUAL CIRCUIT USED. THIS WAY, IMPLEMENTATION OF THE LATEST SAFETY AND PERFORMANCE IMPROVEMENT CHANGES INTO THE SET IS NOT DELAYED UNTIL THE NEW SERVICE LITERATURE IS PRINTED.

NOTE :

- Shaded parts are critical for safety. Replace only with specified part number.
- Voltages are DC-measured with a digital voltmeter during Play mode.



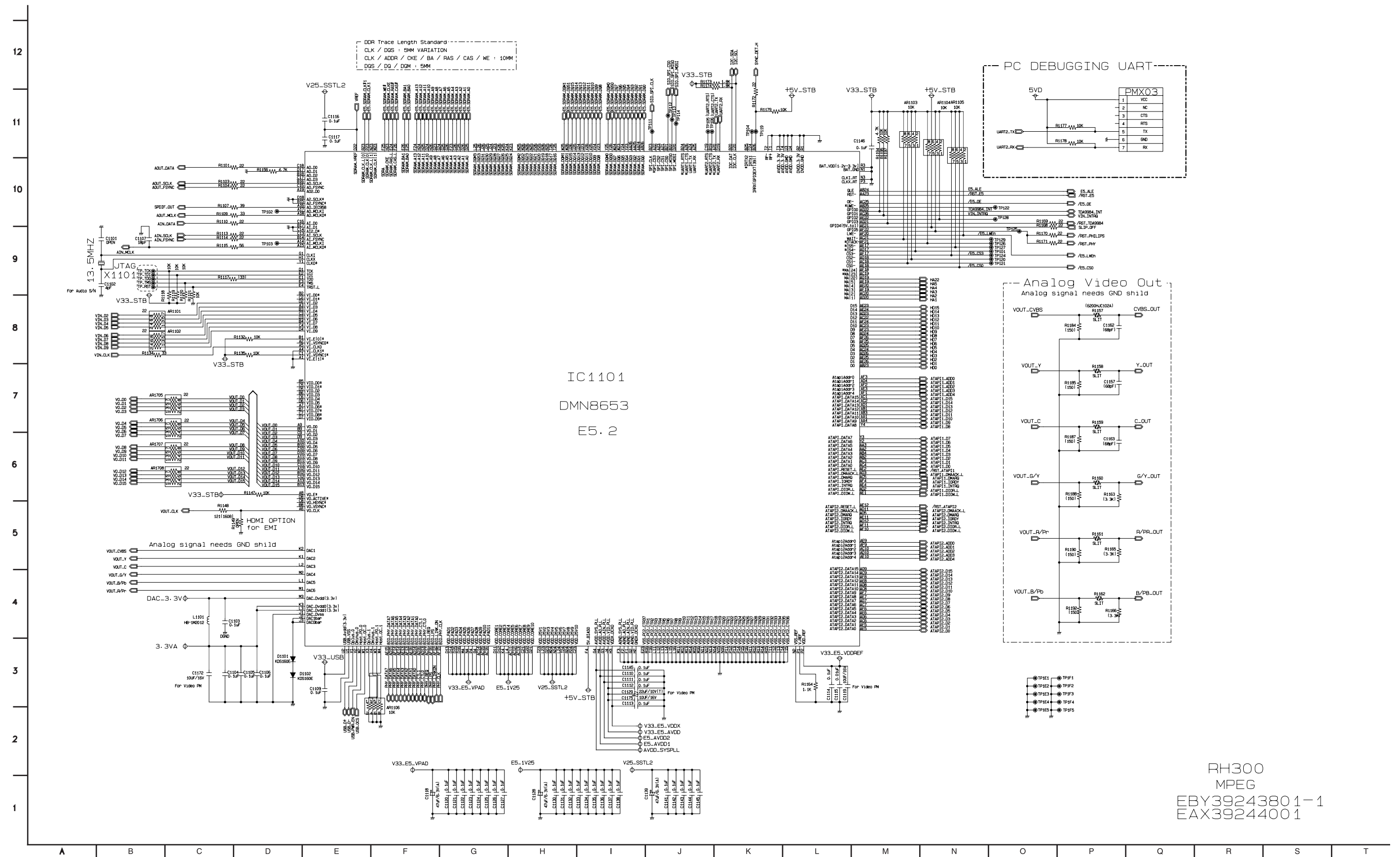
2. SMPS 2 CIRCUIT DIAGRAM



4/4 PAGE

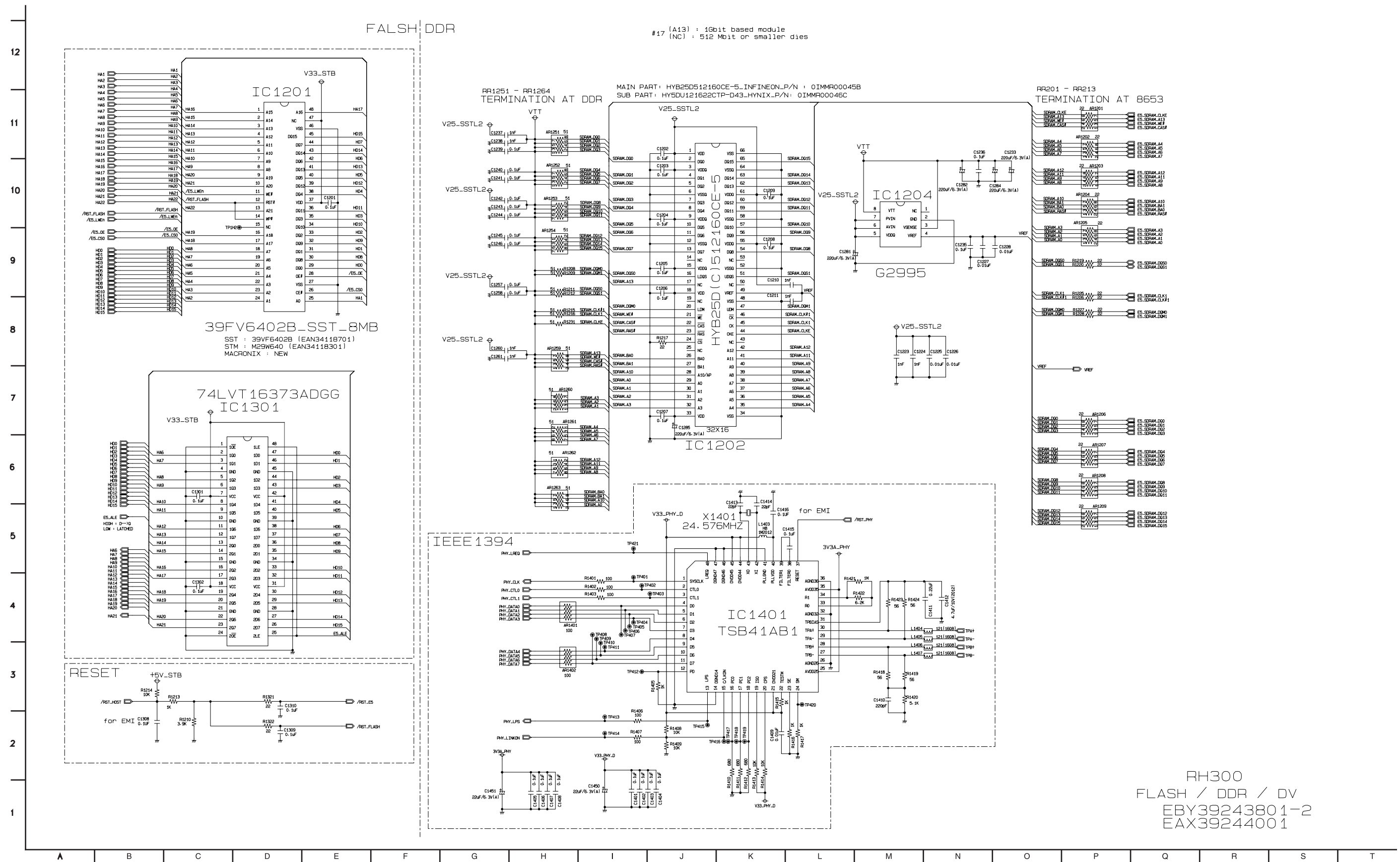
RH300
 SMPS
 EBY32801401-4

3. MPEG CIRCUIT DIAGRAM

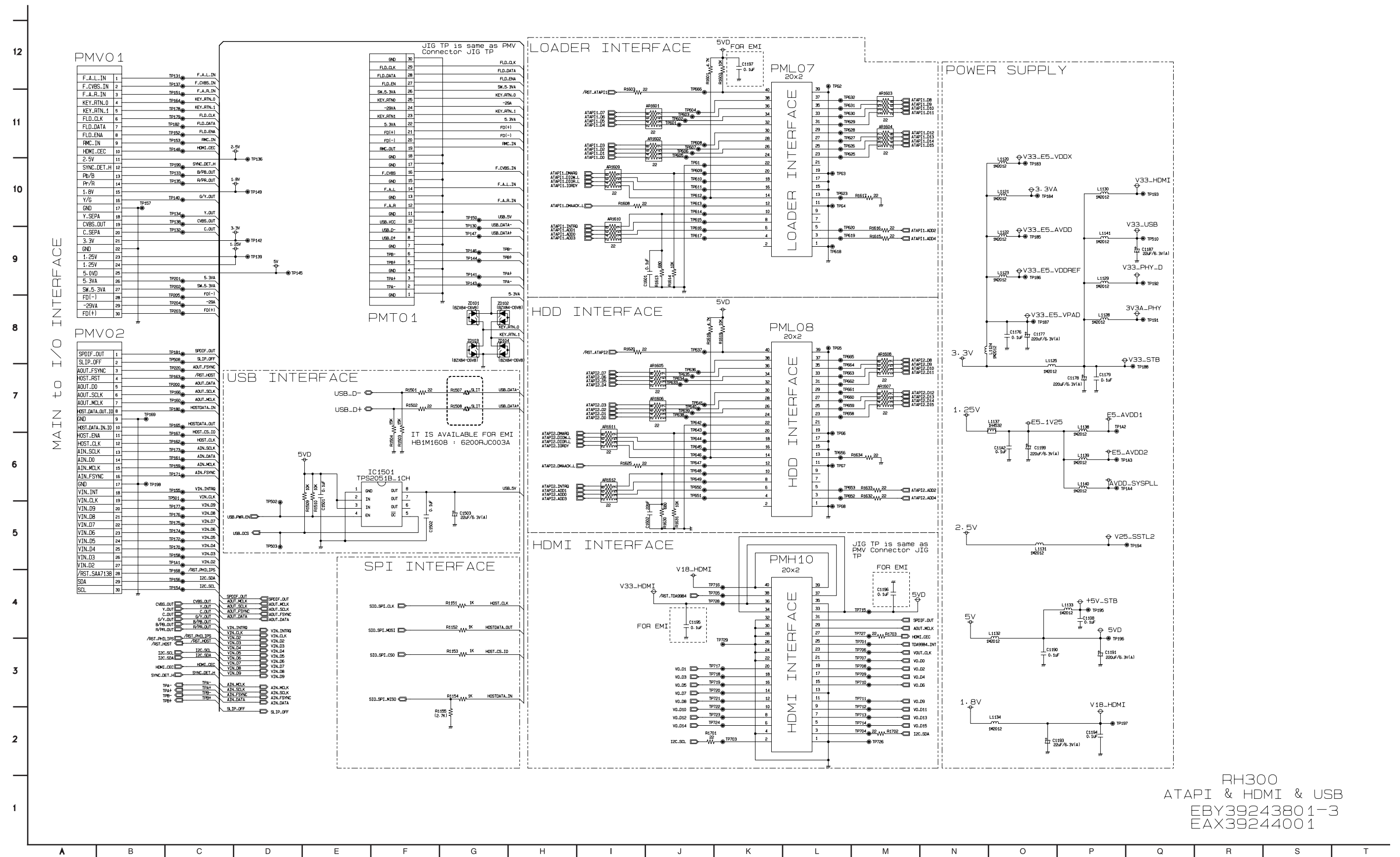


RH300
MPEG
EBY3924380 1-1
EAX3924400 1

4. FLASH/DDR/DV CIRCUIT DIAGRAM

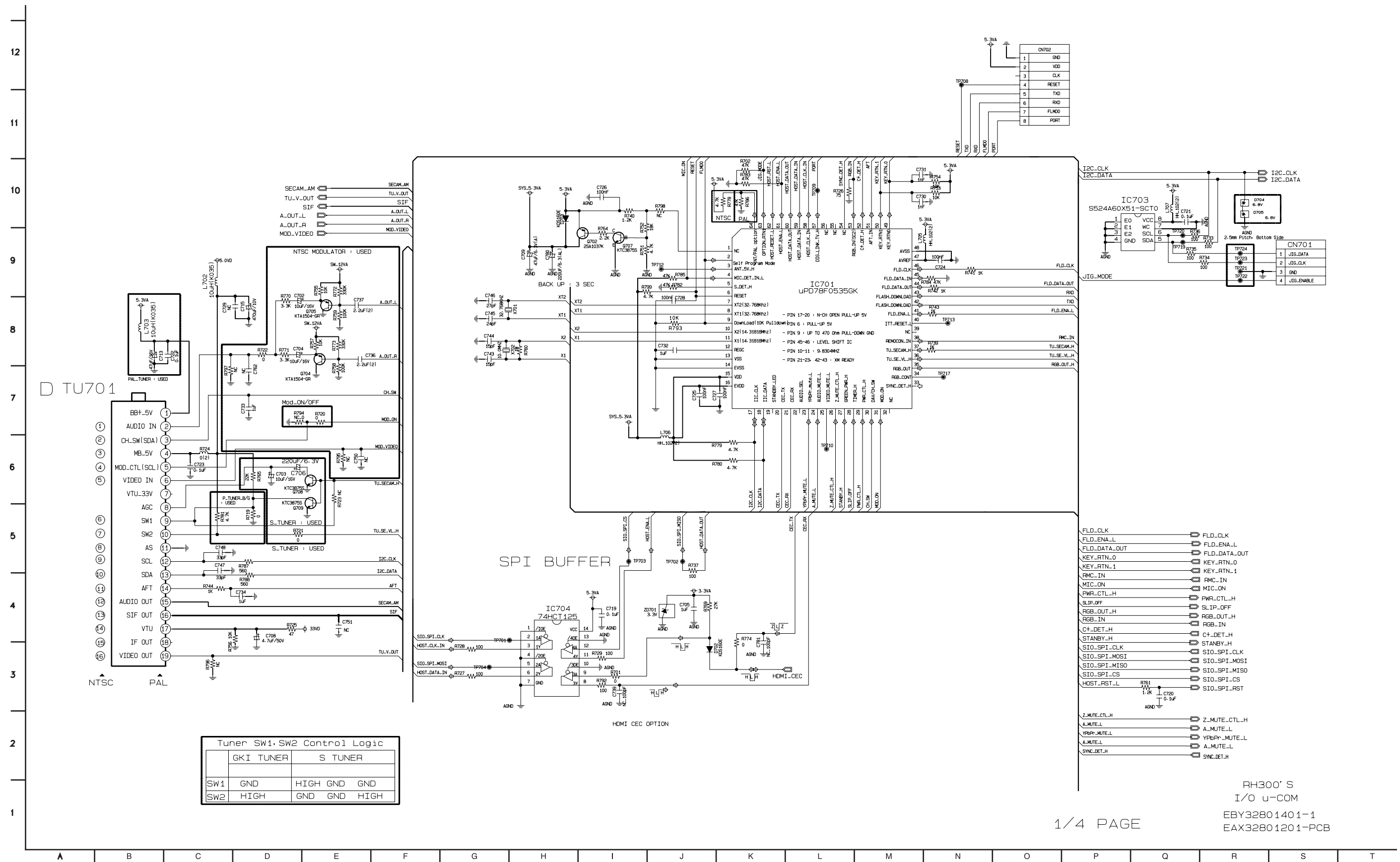


5. ATAPI, HDMI & USB CIRCUIT DIAGRAM



RH300
ATAPI & HDMI & USB
EBY39243801-3
EAX39244001

6. I/O, μ -com CIRCUIT DIAGRAM



Tuner SW1, SW2 Control Logic

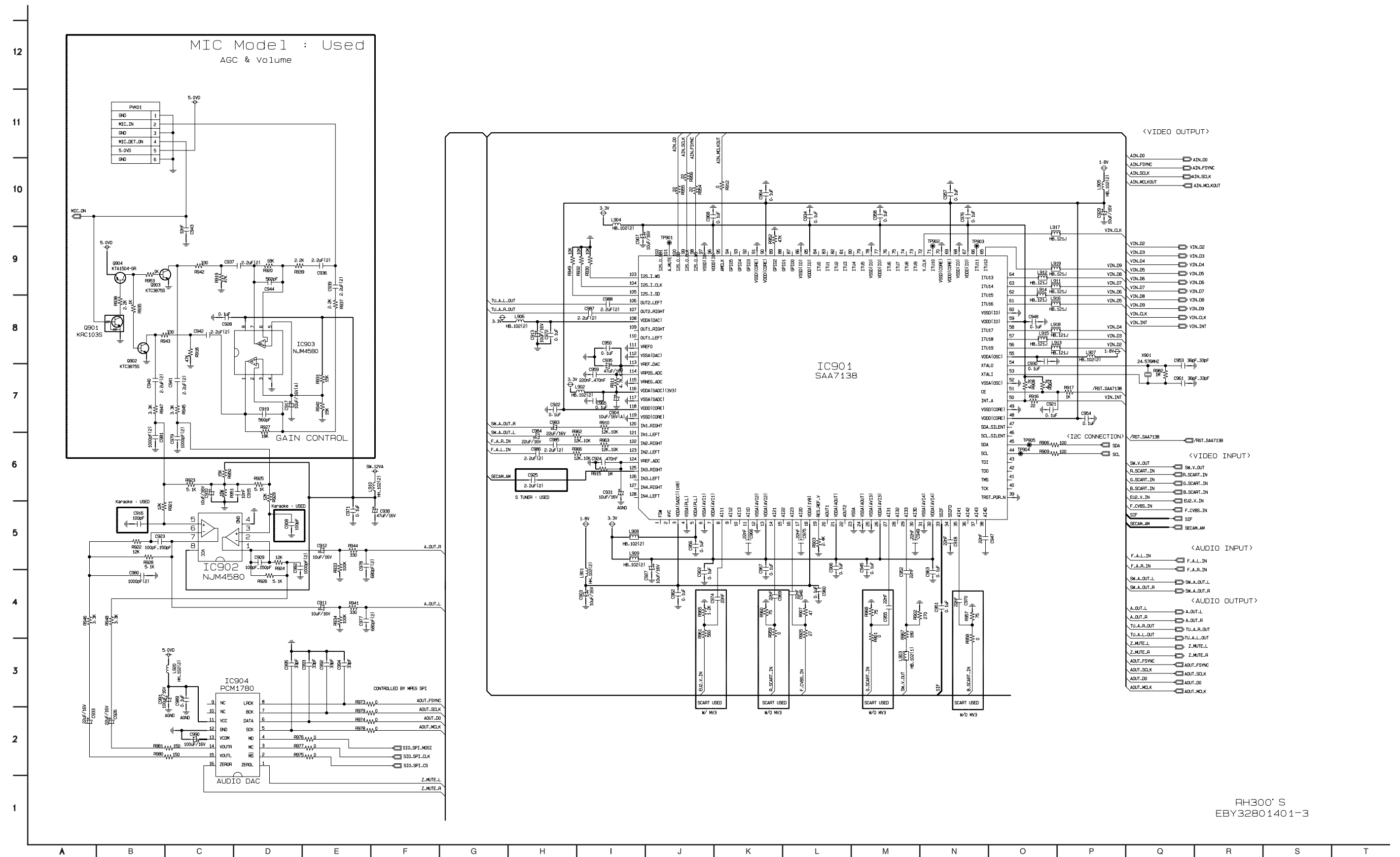
	GKI TUNER		S TUNER	
SW1	GND	HIGH	GND	GND
SW2	HIGH	GND	GND	HIGH

- FLD_CLK
- FLD_ENA_L
- FLD_DATA_OUT
- KEY_RTN_0
- KEY_RTN_1
- RMC_IN
- MIC_ON
- PWR_CTL_H
- SLIP_OFF
- RGB_OUT_H
- RGB_IN
- C+DET_H
- STANBY_H
- SIO_SPI_CLK
- SIO_SPI_MOSI
- SIO_SPI_MISO
- SIO_SPI_CS
- SIO_SPI_RST
- Z_MUTE_CTL_H
- A_MUTE_L
- YPRP_MUTE_L
- A_MUTE_L
- SYNC_DET_H

RH300' S
I/O u-COM
EBY32801401-1
EAX32801201-PCB

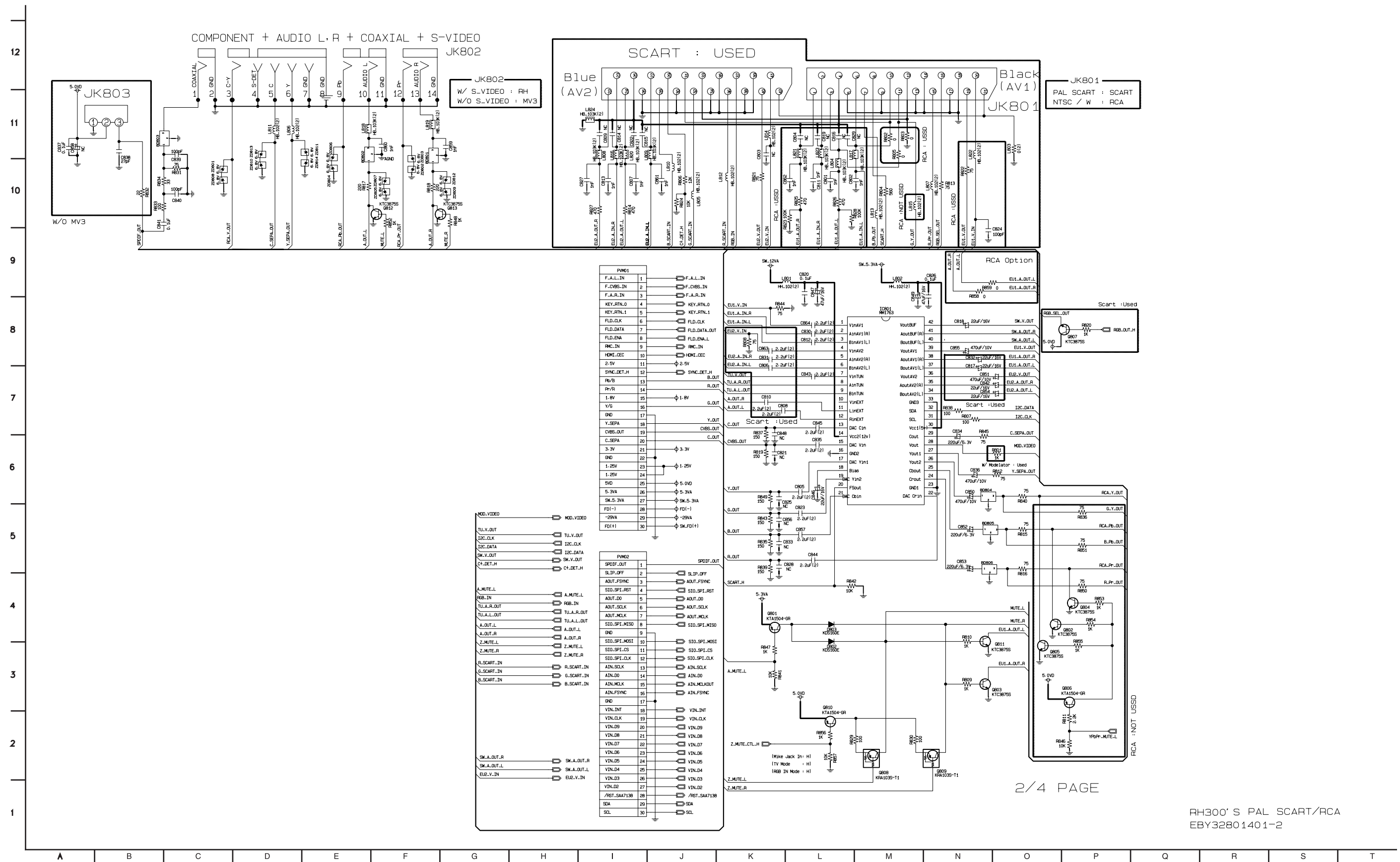
1/4 PAGE

7. DECODER & MIC CIRCUIT DIAGRAM



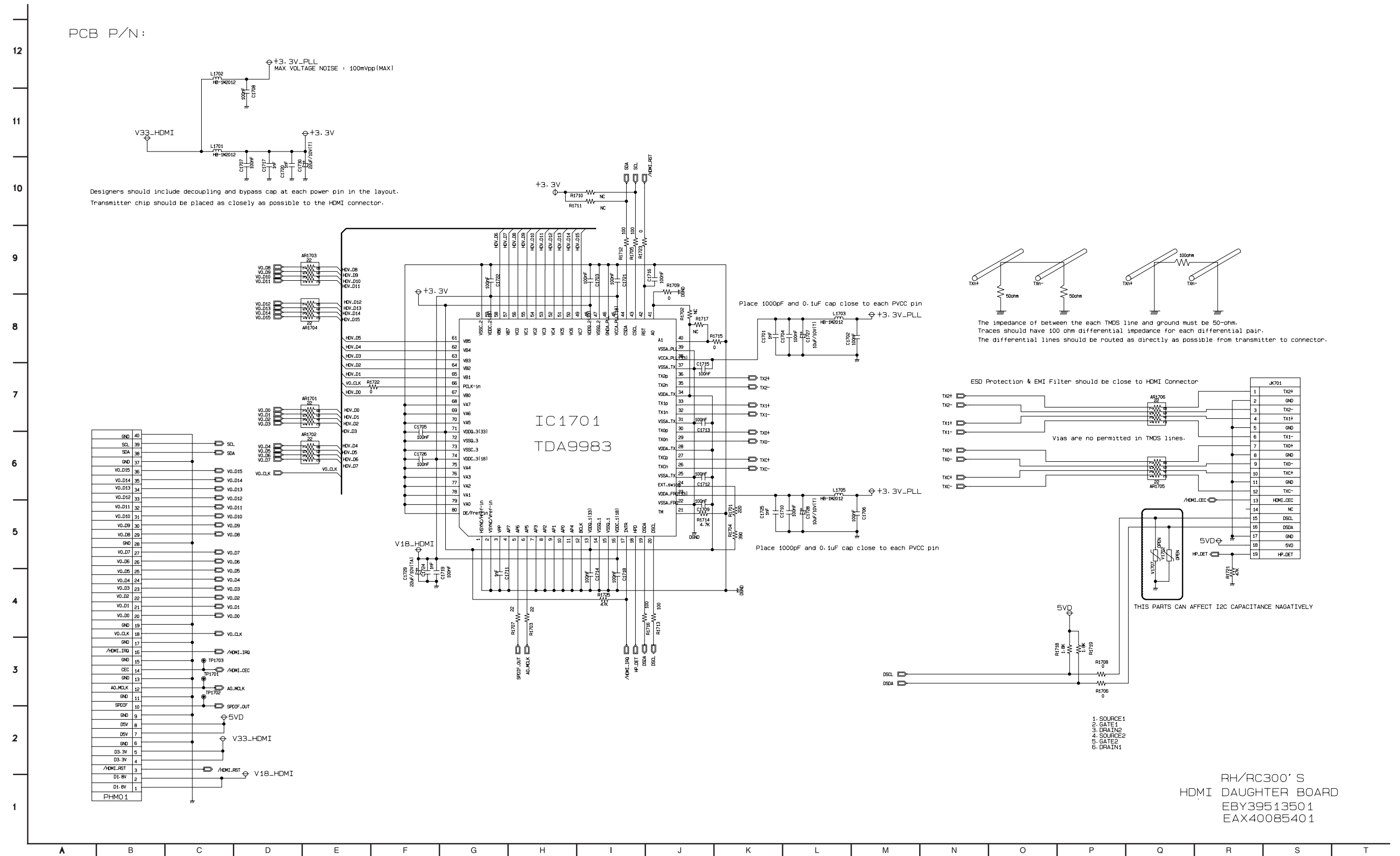
RH300' S
EBY32801401-3

8. SCART/RCA CIRCUIT DIAGRAM



RH300' S PAL SCART/RCA
EBY32801401-2

9. HDMI CIRCUIT DIAGRAM

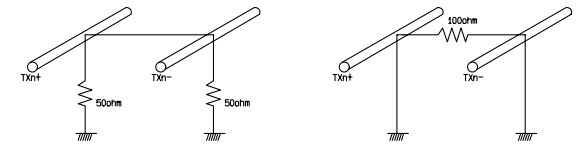


PCB P/N :

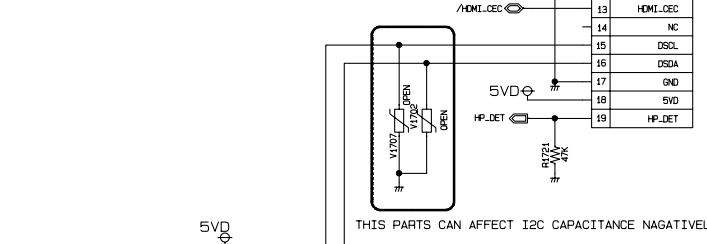
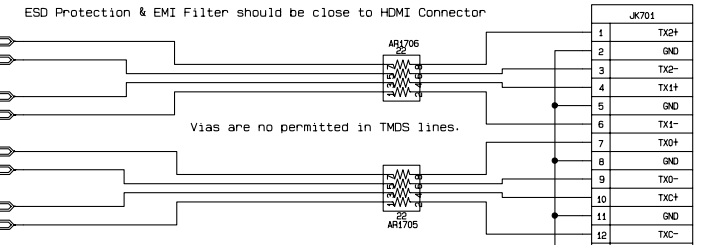
Designers should include decoupling and bypass cap at each power pin in the layout.
Transmitter chip should be placed as closely as possible to the HDMI connector.

Place 1000pF and 0.1uF cap close to each PVCC pin

Place 1000pF and 0.1uF cap close to each PVCC pin



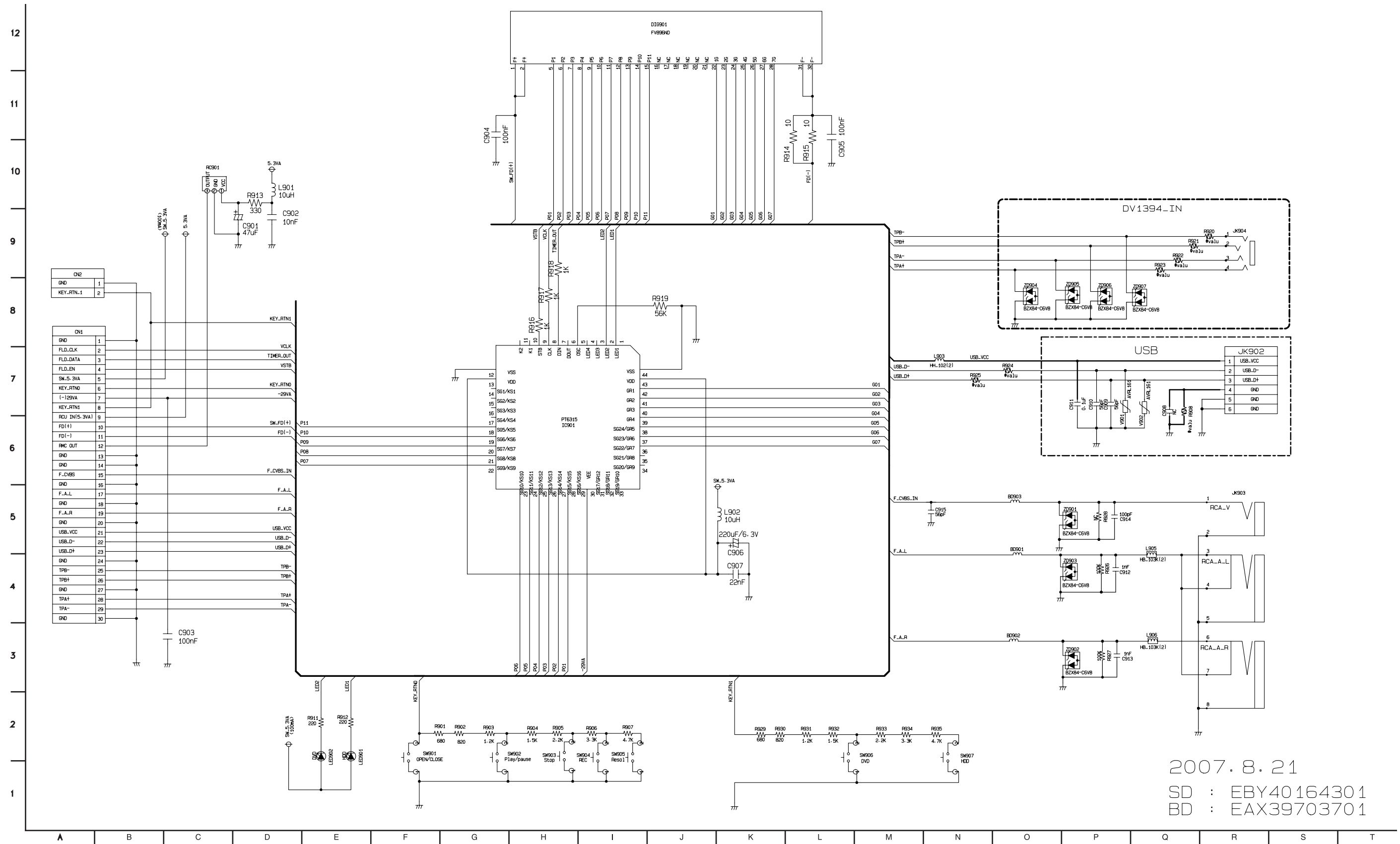
The impedance of between the each TMS line and ground must be 50-ohm.
Traces should have 100 ohm differential impedance for each differential pair.
The differential lines should be routed as directly as possible from transmitter to connector.



- 1. SOURCE1
- 2. GATE1
- 3. DRAIN3
- 4. SOURCE2
- 5. GATE2
- 6. DRAIN1

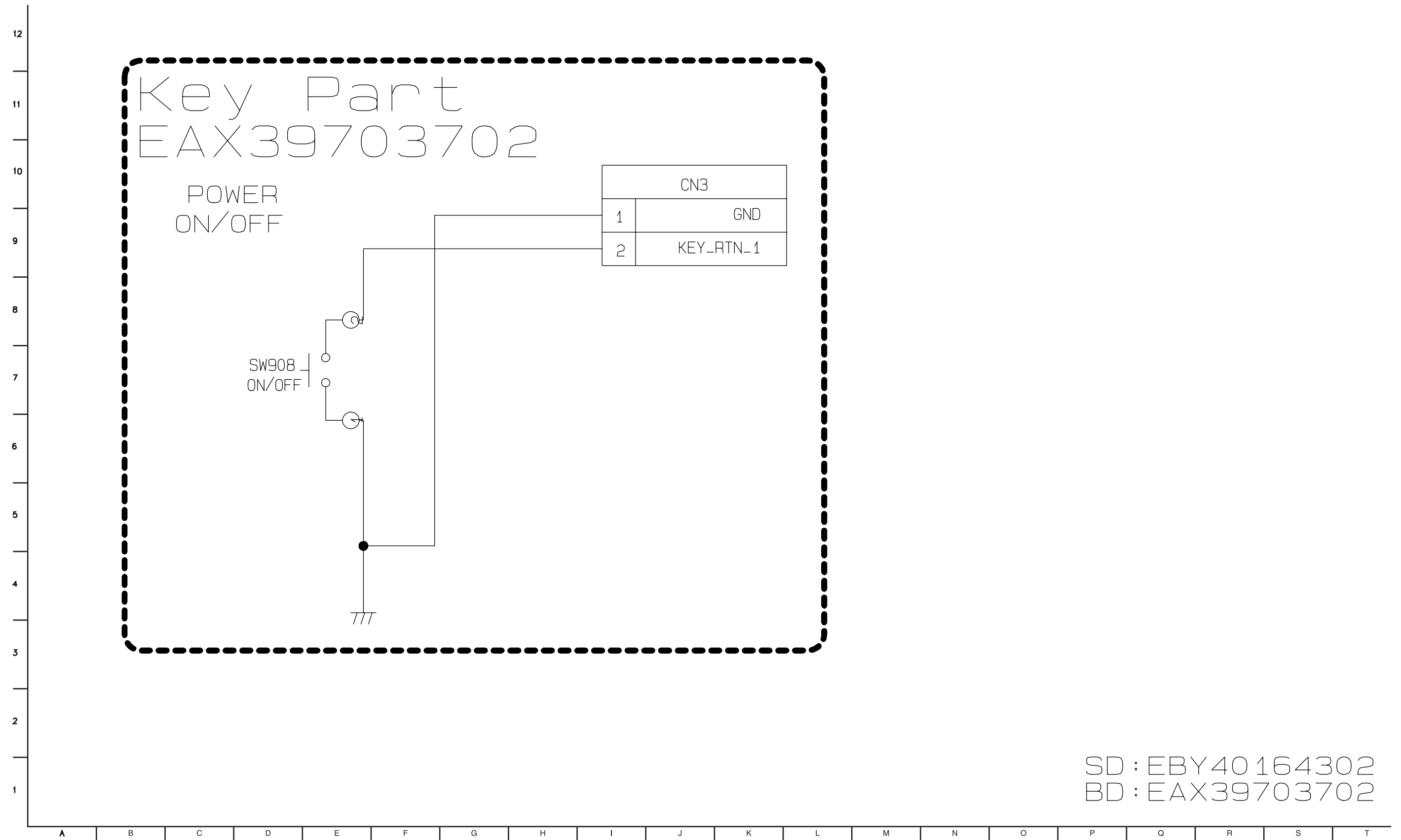
RH/RC300' S
HDMI DAUGHTER BOARD
EBY39513501
EAX40085401

10. TIMER CIRCUIT DIAGRAM (8 & 9 TOOLS)



2007.8.21
 SD : EBY40164301
 BD : EAX39703701

11. KEY CIRCUIT DIAGRAM (8 & 9 TOOLS)



SD : EBY40164302
BD : EAX39703702