



LG Electronics Inc.

P/NO : AFN30347550

MAY,2006

LG Electronics Inc.

MODEL : RH200MH/HDRF899X

SERVICE MANUAL

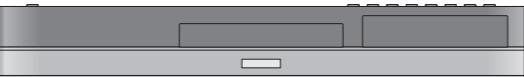


HDD/DVD RECORDER **SERVICE MANUAL**

MODEL : RH200MH/HDRF899X

CAUTION

BEFORE SERVICING THE UNIT, READ THE "SAFETY PRECAUTIONS"
IN THIS MANUAL.



CONTENTS

- SECTION 1 . . .SUMMARY**
- SECTION 2 . . .CABINET & MAIN CHASSIS**
- SECTION 3 . . .ELECTRICAL**
- SECTION 4 . . .RS-01A LOADER PART**
- SECTION 5 . . .REPLACEMENT PARTS LIST**

SECTION 1

SUMMARY

CONTENTS

NEW FUNCTIONS OF HDD/DVD-RECORDER	1-2
PRODUCT SAFETY SERVICING GUIDELINES FOR VIDEO PRODUCTS.....	1-3
SERVICING PRECAUTIONS	1-4
• General Servicing Precautions	
• Insulation Checking Procedure	
• Electrostatically Sensitive Devices	
SERVICE INFORMATION FOR EEPROM IC SETTING	1-5
SPECIFICATIONS	1-6

NEW FUNCTIONS OF HDD/DVD-RECORDER

• HDMI

HDMI IS THE SPECIFICATION FOR THE HIGH-DEFINITION MULTIMEDIA INTERFACE. HDMI IS PROVIDED FOR TRANSMITTING DIGITAL TELEVISION AUDIOVISUAL SIGNALS FROM HDD-DVD RECORDER TO TELEVISION SETS, OTHER VIDEO DISPLAYS. HDMI CAN CARRY HIGH QUALITY MULTI-CHANNEL AUDIO DATA AND CAN CARRY ALL STANDARD AND HIGH DEFINITION CONSUMER ELECTRONICS VIDEO FORMATS. CONTENT PROTECTION TECHNOLOGY IS AVAILABLE. HDMI CAN ALSO CARRY CONTROL AND STATUS INFORMATION IN BOTH DIRECTIONS.

<< OPERATING >>

AUDIO, VIDEO AND AUXILIARY DATA IS TRANSMITTED ACROSS THE THREE TMDS DATA CHANNELS. THE VIDEO PIXEL CLOCK IS TRANSMITTED ON THE TMDS CLOCK CHANNEL AND IS USED BY THE RECEIVER AS A FREQUENCY REFERENCE FOR DATA RECOVERY ON THE THREE TMDS DATA CHANNELS.

VIDEO DATA IS CARRIED AS A SERIES OF 24-BIT PIXELS ON THE THREE TMDS DATA CHANNELS. TMDS ENCODING CONVERTS THE 8BIT PER CHANNEL INTO THE 10BIT DC-BALANCED.

VIDEO PIXEL RATES CAN RANGE FROM 25MHZ TO 165MHZ. THE VIDEO PIXELS CAN BE ENCODED IN EITHER RGB,YCBCR 4:4:4 OR YCBCR 4:2:2 FORMATS. IN ALL THREE CASES, UP TO 24 BITS PER PIXEL CAN BE TRANSFERRED.

FAST DUBBING

DUBBING MEANS A COPYING FUNCTION BETWEEN HDD TO DVD DISCS.

COPYING BETWEEN HDD TO DVD IS A COMPLETELY DIGITAL PROCESS AND THEREFORE INVOLVES NO LOSS OF QUALITY IN THE AUDIO OR VIDEO. SO THIS MEANS THAT COPYING CAN BE CARRIED OUT AT THE MAXIMUM SPEED POSSIBLE.

<< DUBBING SPEED RATE >>

NORMAL DUBBING : SPEED RATE MAX X1

FAST DUBBING : SPEED RATE MAX X4

WHEN FAST DUBBING FROM HDD TO DVD , THE SPEED OF COPYING DEPENDS ON THE RECORDING MODE AND THE KIND OF USING THE DVD DISC, AND THIS MODE IS NOT AVAILABLE FOR EDITED VIDEO TITLE IN HDD.

WHEN FAST DUBBING FROM DVD TO HDD , ONLY AVAILABLE WHEN COPYING VR MODE DISC(DVD-RW) TO HDD , AND ONLY NORMAL DUBBING AVAILABLE WHEN COPYING VIDEO MODE DISC (DVD+R/RW, DVD-R) TO HDD

PRODUCT SAFETY SERVICING GUIDELINES FOR VIDEO PRODUCTS

IMPORTANT SAFETY NOTICE

This manual was prepared for use only by properly trained audio-video service technicians.

When servicing this product, under no circumstances should the original design be modified or altered without permission from LG Electronics Corporation. All components should be replaced only with types identical to those in the original circuit and their physical location, wiring and lead dress must conform to original layout upon completion of repairs.

Special components are also used to prevent x-radiation, shock and fire hazard. These components are indicated by the letter "x" included in their component designators and are required to maintain safe performance. No deviations are allowed without prior approval by LG Electronics Corporation.

Circuit diagrams may occasionally differ from the actual circuit used. This way, implementation of the latest safety and performance improvement changes into the set is not delayed until the new service literature is printed.

CAUTION: Do not attempt to modify this product in any way. Never perform customized installations without manufacturer's approval. Unauthorized modifications will not only void the warranty, but may lead to property damage or user injury.

Service work should be performed only after you are thoroughly familiar with these safety checks and servicing guidelines.

GRAPHIC SYMBOLS



The exclamation point within an equilateral triangle is intended to alert the service personnel to important safety information in the service literature.



The lightning flash with arrowhead symbol within an equilateral triangle is intended to alert the service personnel to the presence of noninsulated "dangerous voltage" that may be of sufficient magnitude to constitute a risk of electric shock.



The pictorial representation of a fuse and its rating within an equilateral triangle is intended to convey to the service personnel the following fuse replacement caution notice:

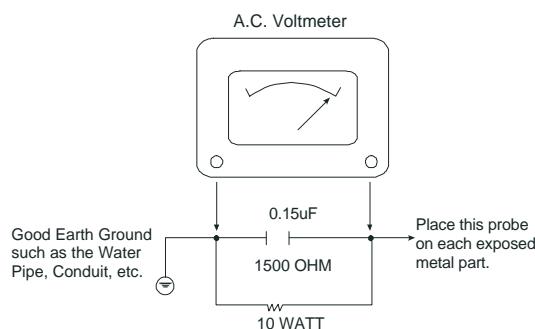
CAUTION: FOR CONTINUED PROTECTION AGAINST RISK OF FIRE, REPLACE ALL FUSES WITH THE SAME TYPE AND RATING AS MARKED NEAR EACH FUSE.

SERVICE INFORMATION

While servicing, use an isolation transformer for protection from AC line shock. After the original service problem has been corrected, make a check of the following:

FIRE AND SHOCK HAZARD

1. Be sure that all components are positioned to avoid a possibility of adjacent component shorts. This is especially important on items trans-ported to and from the repair shop.
2. Verify that all protective devices such as insulators, barriers, covers, shields, strain reliefs, power supply cords, and other hardware have been reinstalled per the original design. Be sure that the safety purpose of the polarized line plug has not been defeated.
3. Soldering must be inspected to discover possible cold solder joints, solder splashes, or sharp solder points. Be certain to remove all loose foreign particles.
4. Check for physical evidence of damage or deterioration to parts and components, for frayed leads or damaged insulation (including the AC cord), and replace if necessary.
5. No lead or component should touch a high current device or a resistor rated at 1 watt or more. Lead tension around protruding metal surfaces must be avoided.
6. After reassembly of the set, always perform an AC leakage test on all exposed metallic parts of the cabinet (the channel selector knobs, antenna terminals, handle and screws) to be sure that set is safe to operate without danger of electrical shock. DO NOT USE A LINE ISOLATION TRANSFORMER DURING THIS TEST. Use an AC voltmeter having 5000 ohms per volt or more sensitivity in the following manner: Connect a 1500 ohm, 10 watt resistor, paralleled by a .15 mfd 150V AC type capacitor between a known good earth ground water pipe, conduit, etc.) and the exposed metallic parts, one at a time. Measure the AC voltage across the combination of 1500 ohm resistor and .15 mfd capacitor. Reverse the AC plug by using a non-polarized adaptor and repeat AC voltage measurements for each exposed metallic part. Voltage measured must not exceed 0.75 volts RMS. This corresponds to 0.5 millamp AC. Any value exceeding this limit constitutes a potential shock hazard and must be corrected immediately.



TIPS ON PROPER INSTALLATION

1. Never install any receiver in a closed-in recess, cubbyhole, or closely fitting shelf space over, or close to, a heat duct, or in the path of heated air flow.
2. Avoid conditions of high humidity such as: outdoor patio installations where dew is a factor, near steam radiators where steam leakage is a factor, etc.
3. Avoid placement where draperies may obstruct venting. The customer should also avoid the use of decorative scarves or other coverings that might obstruct ventilation.
4. Wall- and shelf-mounted installations using a commercial mounting kit must follow the factory-approved mounting instructions. A product mounted to a shelf or platform must retain its original feet (or the equivalent thickness in spacers) to provide adequate air flow across the bottom. Bolts or screws used for fasteners must not touch any parts or wiring. Perform leakage tests on customized installations.
5. Caution customers against mounting a product on a sloping shelf or in a tilted position, unless the receiver is properly secured.
6. A product on a roll-about cart should be stable in its mounting to the cart. Caution the customer on the hazards of trying to roll a cart with small casters across thresholds or deep pile carpets.
7. Caution customers against using extension cords. Explain that a forest of extensions, sprouting from a single outlet, can lead to disastrous consequences to home and family.

SERVICING PRECAUTIONS

CAUTION : Before servicing the HDD/DVD Recorder covered by this service data and its supplements and addends, read and follow the *SAFETY PRECAUTIONS*. **NOTE :** if unforeseen circumstances create conflict between the following servicing precautions and any of the safety precautions in this publications, always follow the safety precautions.

Remember Safety First:

General Servicing Precautions

1. Always unplug the HDD/DVD Recorder AC power cord from the AC power source before:
 - (1) Removing or reinstalling any component, circuit board, module, or any other assembly.
 - (2) Disconnection or reconnecting any internal electrical plug or other electrical connection.
 - (3) Connecting a test substitute in parallel with an electrolytic capacitor.
- Caution :** A wrong part substitution or incorrect polarity installation of electrolytic capacitors may result in an explosion hazard.
2. Do not spray chemicals on or near this HDD/DVD Recorder or any of its assemblies.
3. Unless specified otherwise in this service data, clean electrical contacts by applying an appropriate contact cleaning solution to the contacts with a pipe cleaner, cotton-tipped swab, or comparable soft applicator.
Unless specified otherwise in this service data, lubrication of contacts is not required.
4. Do not defeat any plug/socket B+ voltage interlocks with which instruments covered by this service manual might be equipped.
5. Do not apply AC power to this HDD/DVD Recorder and/or any of its electrical assemblies unless all solid-state device heat sinks are correctly installed.
6. Always connect test instrument ground lead to the appropriate ground before connection the test instrument positive lead. Always remove the test instrument ground lead last.

Insulation Checking Procedure

Disconnect the attachment plug from the AC outlet and turn the power on. Connect an insulation resistance meter(500V) to the blades of the attachment plug. The insulation resistance between each blade of the attachment plug and accessible conductive parts (Note 1) should be more than 1M-ohm.

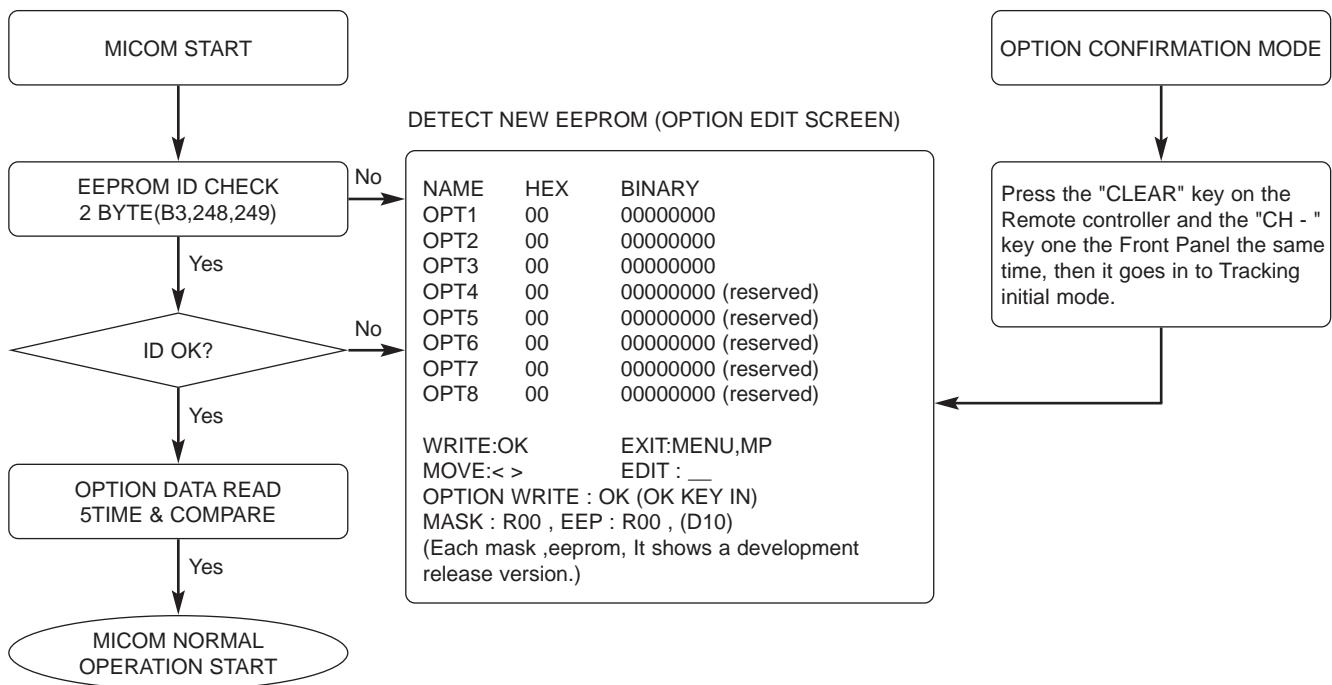
Note 1 : Accessible Conductive Parts including Metal panels, Input terminals, Earphone jacks, etc.

Electrostatically Sensitive (ES) Devices

Some semiconductor (solid state) devices can be damaged easily by static electricity. Such components commonly are called Electrostatically Sensitive (ES) Devices. Examples of typical ES devices are integrated circuits and some field effect transistors and semiconductor chip components. The following techniques should be used to help reduce the incidence of component damage caused by static electricity.

1. Immediately before handling any semiconductor component or semiconductor-equipped assembly, drain off any electrostatic charge on your body by touching a known earth ground. Alternatively, obtain and wear a commercially available discharging wrist strap device, which should be removed for potential shock reasons prior to applying power to the unit under test.
2. After removing an electrical assembly equipped with ES devices, place the assembly on a conductive surface such as aluminum foil, to prevent electrostatic charge buildup or exposure of the assembly.
3. Use only a grounded-tip soldering iron to solder or unsolder ES devices.
4. Use only an anti-static solder removal device. Some solder removal devices not classified a "anti-static" can generate electrical charges sufficient to damage ES devices.
5. Do not use freon-propelled chemicals. These can generate electrical charge sufficient to damage ES devices.
6. Do not remove a replacement ES device from its protective package until immediately before you are ready to install it. (Most replacement ES devices are packaged with leads electrically shorted together by conductive foam, aluminum foil, or comparable conductive material).
7. Immediately before removing the protective material from the leads of a replacement ES device, touch the protective material to the chassis or circuit assembly into which the device will be installed.
- Caution :** Be sure no power is applied to the chassis or circuit, and observe all other safety precautions.
8. Minimize bodily motions when handling unpackaged replacement ES devices. (Normally harmless motion such as the brushing together of your clothes fabric or the lifting of your foot from a carpeted floor can generate static electricity sufficient to damage an ES device.)

SERVICE INFORMATION FOR EEPROM IC SETTING



*** EEPROM INITIAL ***

- SETUP is displayed in the field if pressing the Front ch- & ch+ Key with the Remocon number "clear" key pressed in the status of powering on.
- AUTO SEARCH is done since the initial screen of ACMS is serviced if powering on.

SPECIFICATIONS

• GENERAL

Power requirements	AC 200-240V, 50/60 Hz
Power consumption	35W
Dimensions (approx.)	430 X 54 X 275 mm (w x h x d) without foot
Mass (approx.)	4.1 kg
Operating temperature	5°C to 35°C
Operating humidity	5 % to 90 %
Television system	PAL I, B/G, I/I, SECAM D/K, K1 color system
Recording format	PAL

• RECORDING

Recording format	DVD Video Recording, DVD-VIDEO
Recordable media	HDD (80GB), DVD-ReWritable, DVD-Recordable, DVD+ReWritable, DVD+Recordable, DVD+Recordable (Double Layer)
Recordable time	DVD (4.7GB): Approx. 1 hour (XP mode), 2 hours (SP mode), 4 hours (LP mode), 6 hours (EP mode) DVD+R DL (8.5GB): Approx. 3 hour (XP mode), 3 hours 40 minutes (SP mode), 7 hours 10 minutes (LP mode), 10 hours 30 minutes (EP mode)
	HDD (80GB): Approx. 20 hours (XP mode), 40 hours (SP mode), 74 hours (LP mode), 111 hours (EP mode)

Video recording format

Sampling frequency	27MHz
Compression format	MPEG 2 (VBR support)

Audio recording format

Sampling frequency	48kHz
Compression format	Dolby Digital

• PLAYBACK

Frequency response	DVD (PCM 48 kHz): 8 Hz to 22 kHz, CD: 8 Hz to 20 kHz
Signal-to-noise ratio	DVD (PCM 96 kHz): 8 Hz to 44 kHz
Harmonic distortion	More than 100 dB (AUDIO OUT connector)
Dynamic range	Less than 0.008% (AUDIO OUT connector)
	More than 95 dB (AUDIO OUT connector)

• INPUTS

AERIAL IN	Aerial input, 75 ohms
VIDEO IN	1.0 Vp-p 75 ohms, sync negative, RCA jack x 2 / SCART x 2
AUDIO IN	2.0 Vrms more than 47 kohms, RCA jack (L, R) x 2 / SCART x 2
S-VIDEO IN	(Y) 1.0 V (p-p), 75 Ω, sync negative, Mini DIN 4-pin x 1
DV IN	(C) 0.3 V (p-p) 75 Ω 4 pin (IEEE 1394 standard)

• OUTPUTS

VIDEO OUT	1 Vp-p 75 Ω, sync negative, RCA jack x 1 / SCART x 2
S-VIDEO OUT	(Y) 1.0 V (p-p), 75 Ω, sync negative, Mini DIN 4-pin x 1
COMPONENT VIDEO OUT	(C) 0.3 V (p-p) 75 Ω (Y) 1.0 V (p-p), 75 Ω, sync negative, RCA jack x 1
Audio output (digital audio)	(Pb)/(Pr) 0.7 V (p-p), 75 Ω, RCA jack x 2
Audio output (optical audio)	0.5 V (p-p), 75 Ω, RCA jack x 1
Audio output (analog audio)	3 V (p-p), 75 Ω, Optical connector x 1 2.0 Vrms (1 KHz, 0 dB), 600 Ω, RCA jack (L, R) x 1 / SCART x 2

SECTION 2

CABINET & MAIN CHASSIS

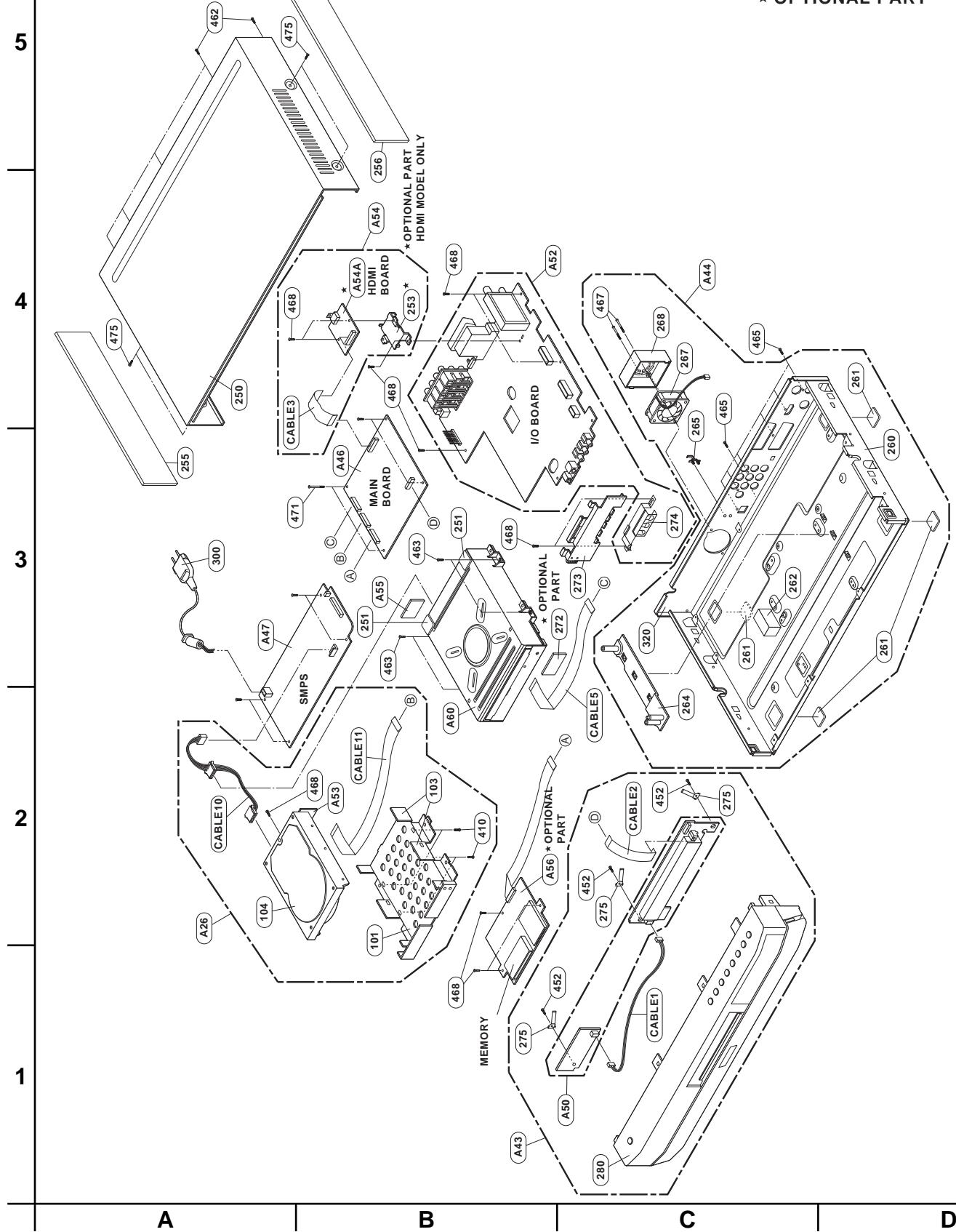
CONTENTS

EXPLODED VIEWS	2-2
1. CABINET AND MAIN FRAME SECTION	2-2
2. DECK MECHANISM SECTION(RS-01A)	2-3
3. PACKING ACCESSORY SECTION	2-4

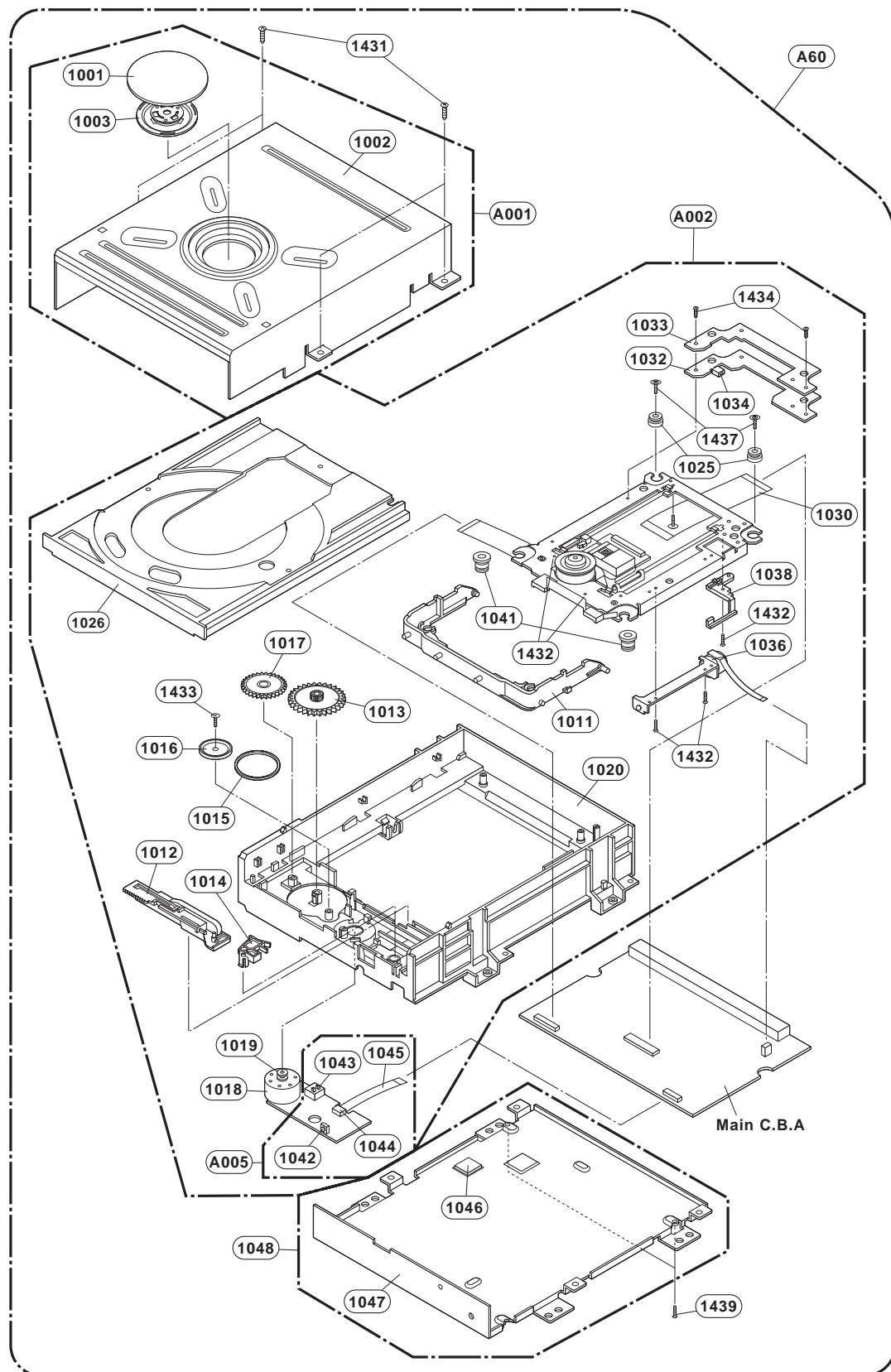
EXPLODED VIEWS

1. CABINET AND MAIN FRAME SECTION

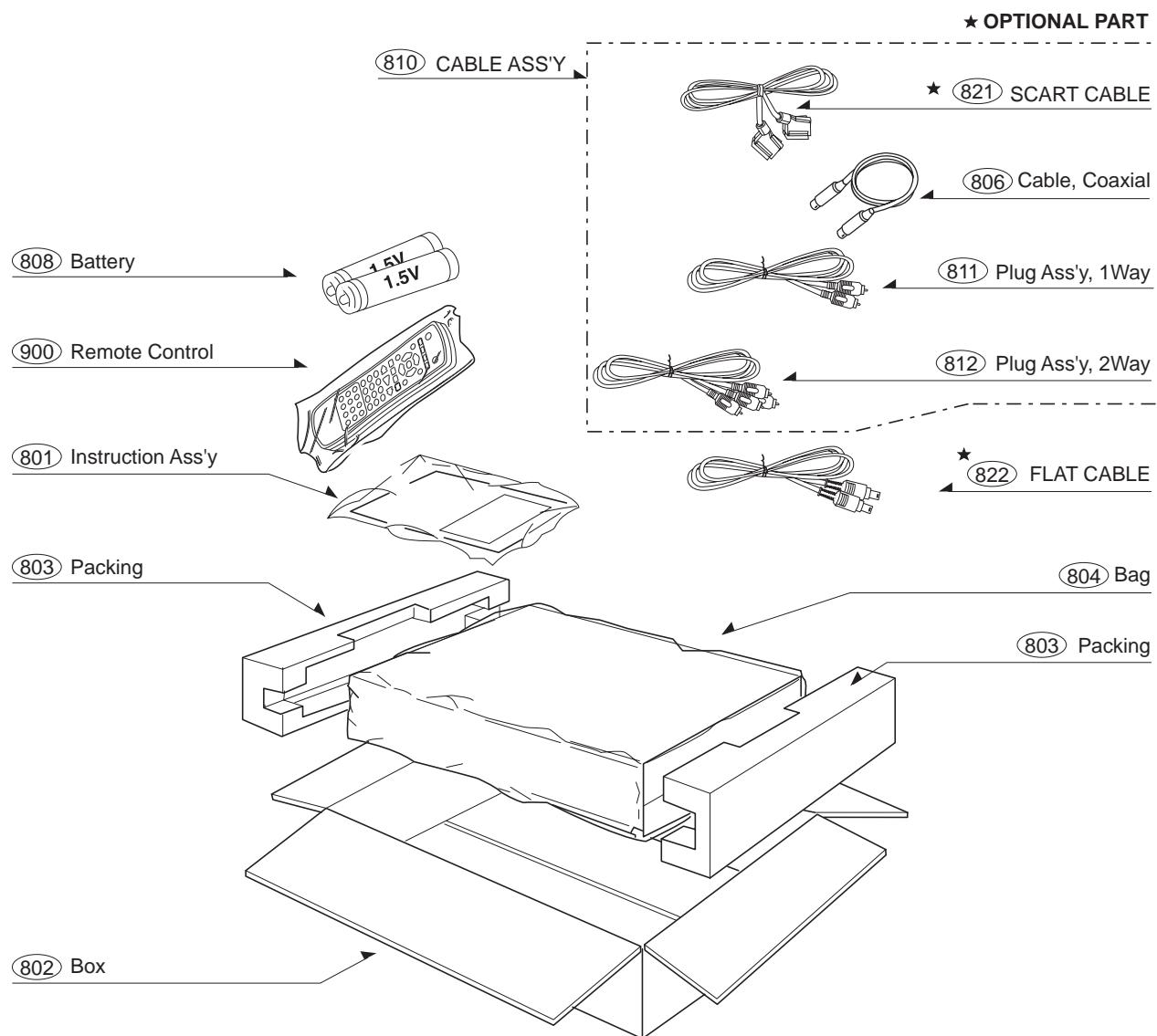
* OPTIONAL PART



2. DECK MECHANISM SECTION(RS-01A)



3. PACKING ACCESSORY SECTION



SECTION 3

ELECTRICAL

CONTENTS

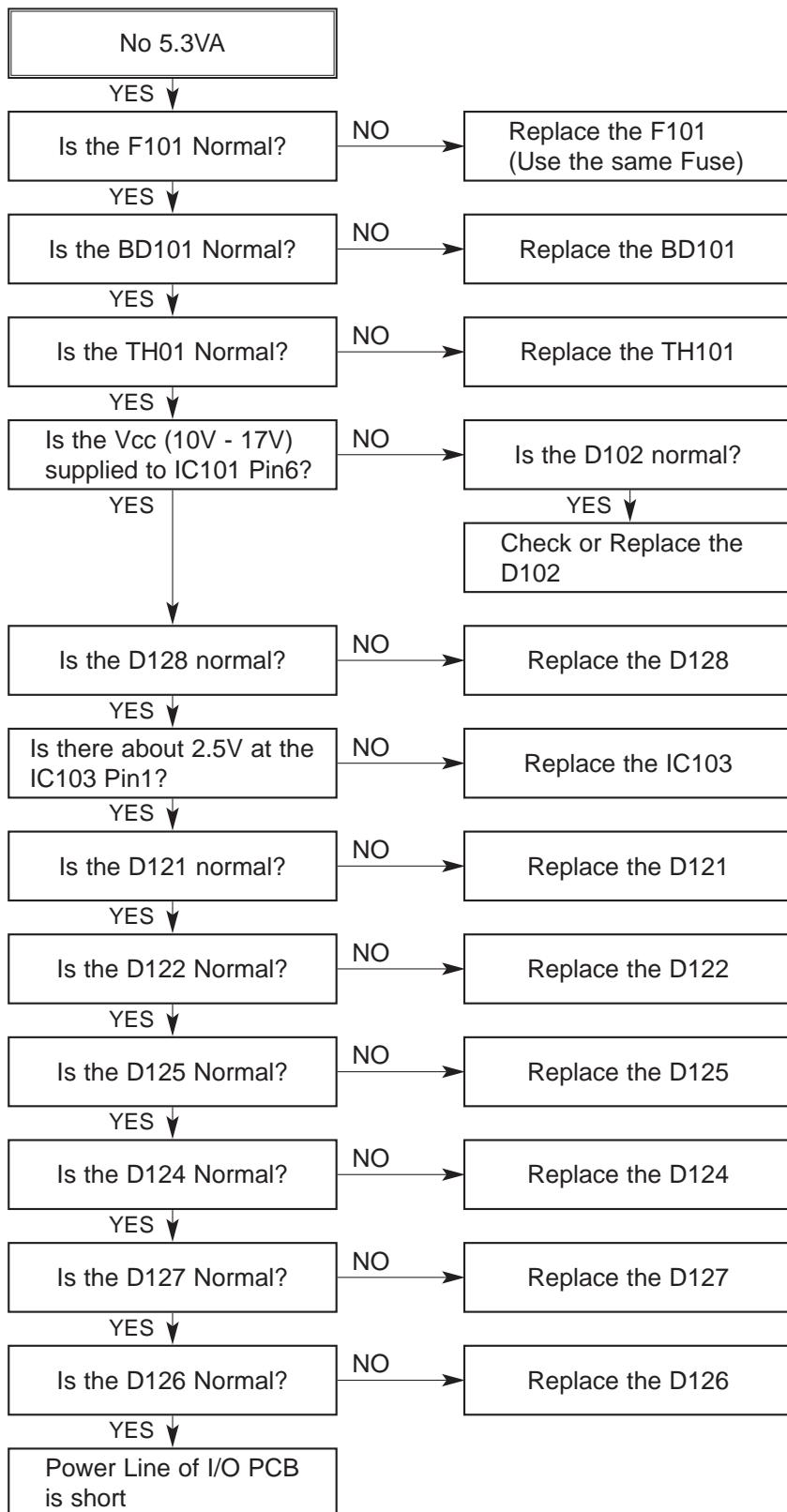
HDR PART

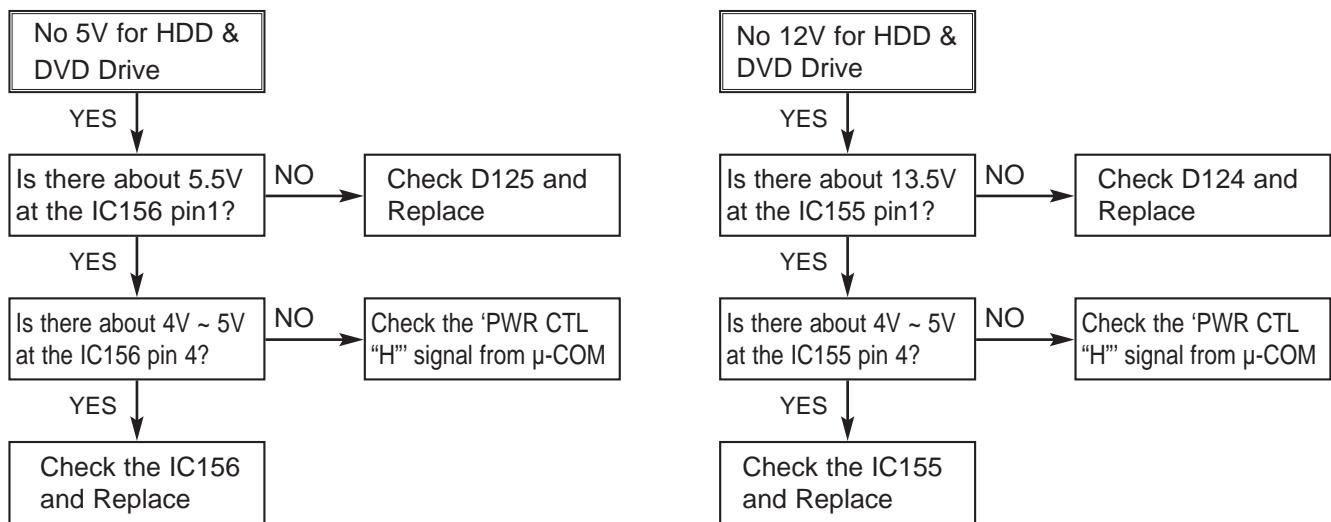
ELECTRICAL TROUBLESHOOTING GUIDE	3-2
1. POWER SUPPLY ON SMPS BOARD	3-2
2. POWER SUPPLY ON I/O BOARD	3-4
3. SYSTEM CIRCUIT PART	3-7
4. DISC NOT RECOGNIZED.....	3-7
5. WHEN PLAYING DISC, NO AUDIO OUTPUT	3-8
6. NO OPTICAL / DIGITAL OUTPUT.....	3-9
7. NO TUNER AUDIO OUTPUT	3-10
8. NO EXTERNAL INPUT AUDIO.....	3-11
9. NO RGB / COMPONENT VIDEO SIGNAL WHEN PLAY DISC	3-12
10. NO COMPOSITE / S-VIDEO SIGNAL WHEN PLAY DISC	3-12
11. NO TV, EXTERNAL INPUT VIDEO SIGNAL	3-13
12. NO DV(IEEE 1394) INPUT (VIDEO/AUDIO) SIGNAL	3-14
 BLOCK DIAGRAMS.....	3-16
1. OVERALL BLOCK DIAGRAM	3-15
2. LAYOUT CONNECTION BLOCK DIAGRAM_1	3-17
3. LAYOUT CONNECTION BLOCK DIAGRAM_2	3-19
4. SMPS BLOCK DIAGRAM.....	3-21
5. POWER : MAIN BOARD BLOCK DIAGRAM	3-23
6. POWER : I/O BOARD BLOCK DIAGRAM	3-25
7. IN/OUT BLOCK DIAGRAM (SCART MODEL ONLY)	3-27
8. VIDEO BLOCK DIAGRAM.....	3-29
9. VIDEO S/W PATH BLOCK DIAGRAM.....	3-31
10. AUDIO BLOCK DIAGRAM.....	3-33
11. AUDIO S/W PATH BLOCK DIAGRAM.....	3-35
12. FLD / μ -COM / TUNER BLOCK DIAGRAM.....	3-37
13. MAIN BLOCK DIAGRAM (SCART MODEL ONLY)	3-39
14. TIMER BLOCK DIAGRAM.....	3-41
 CIRCUIT DIAGRAMS.....	3-43
1. SMPS 1 CIRCUIT DIAGRAM	3-43
2. SMPS 2 CIRCUIT DIAGRAM	3-45
3. MPEG CIRCUIT DIAGRAM	3-47
4. FLASH/DDR CIRCUIT DIAGRAM	3-49
5. RESET & 1394 & JPEG CIRCUIT DIAGRAM	3-51
6. ATAPI & MEMORY CARD CIRCUIT DIAGRAM	3-53
7. I/O, μ -COM CIRCUIT DIAGRAM	3-55
8. TUNER/MPX/ADC/DAC/JACK CIRCUIT DIAGRAM	3-57
9. DECODER CIRCUIT DIAGRAM.....	3-59
10. TIMER CIRCUIT DIAGRAM	3-61
 • WAVEFORMS	3-63
• CIRCUIT VOLTAGE CHART	3-65
 PRINTED CIRCUIT DIAGRAMS.....	3-71
1. MAIN P.C.BOARD.....	3-71
2. I/O P.C.BOARD	3-73
3. POWER P.C.BOARD	3-77
4. TIMER1 P.C.BOARD	3-79
5. TIMER2 P.C.BOARD	3-81
6. HDMI P.C.BOARD	3-83

HDR PART

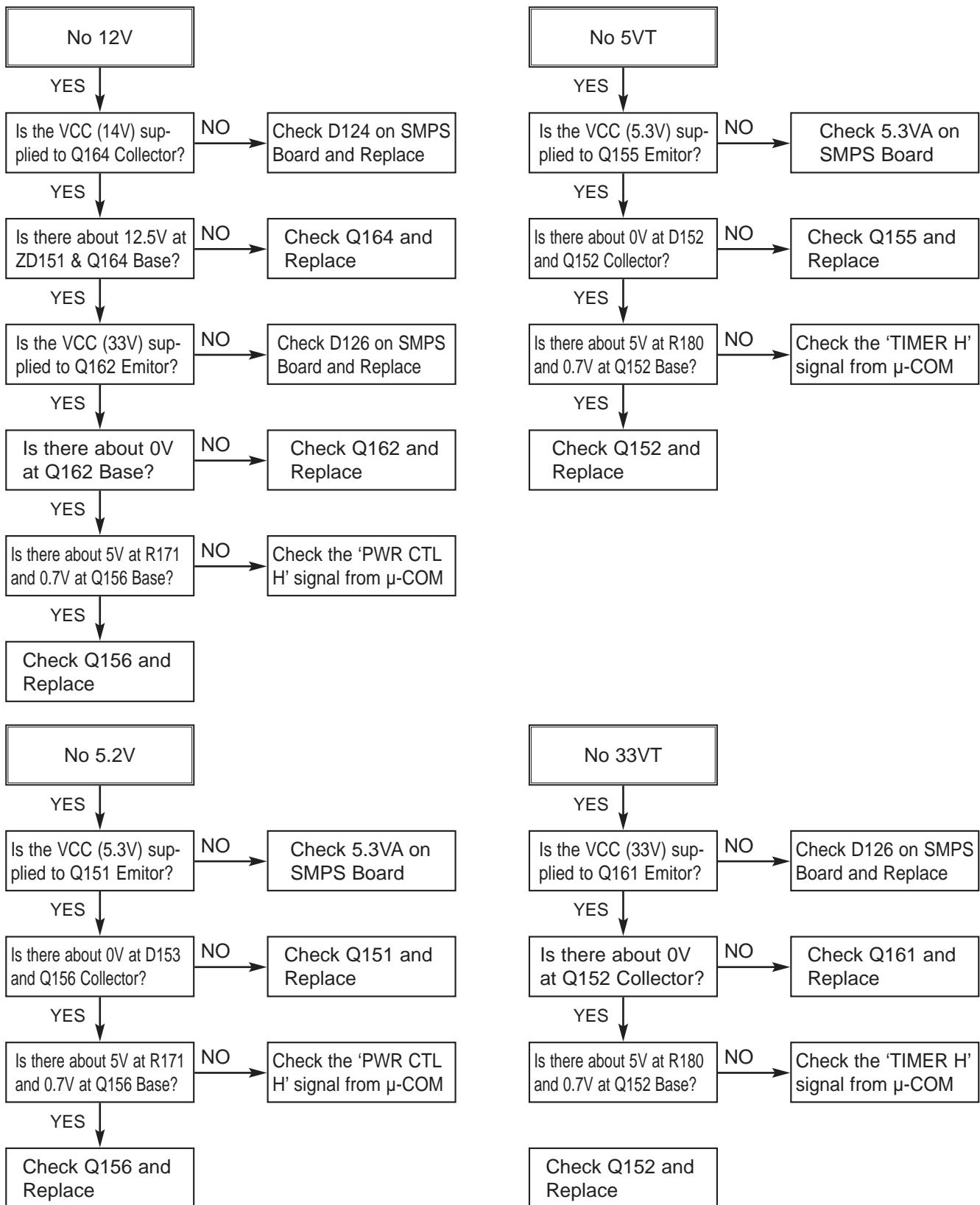
ELECTRICAL TROUBLESHOOTING GUIDE

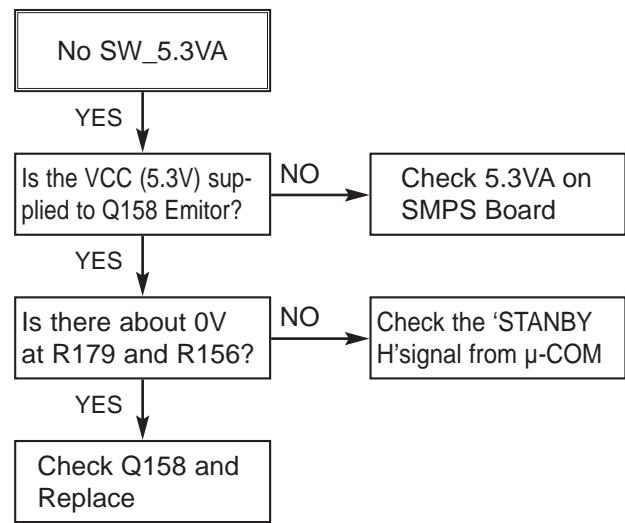
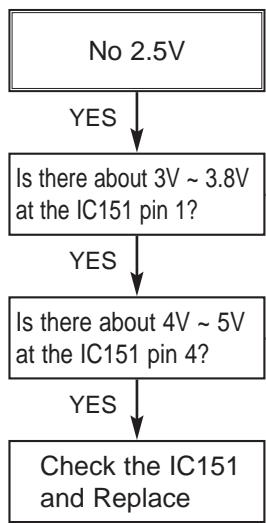
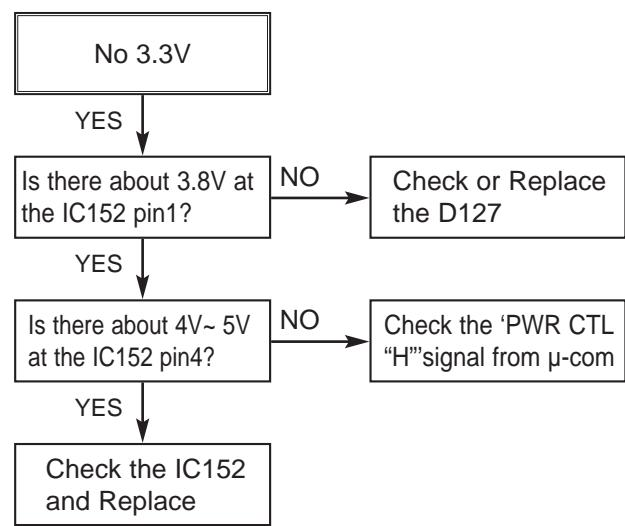
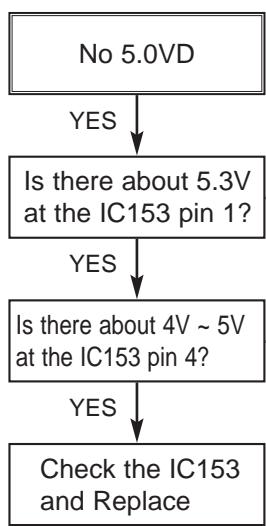
1. POWER SUPPLY ON SMPS BOARD

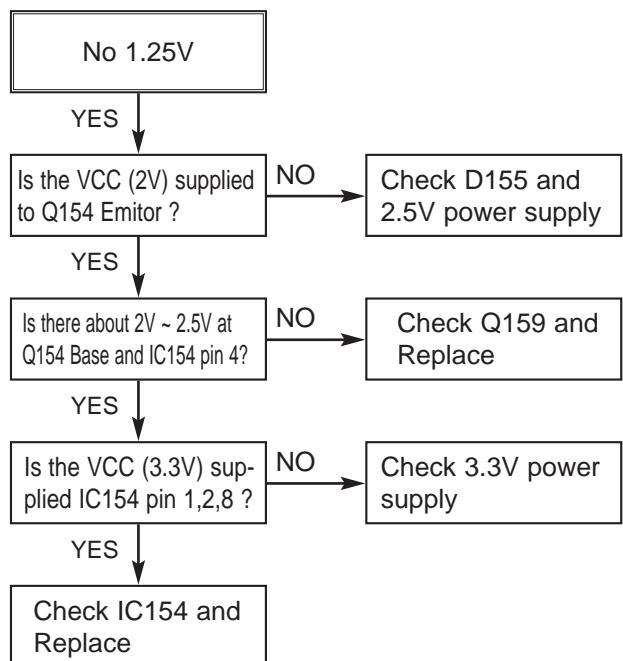
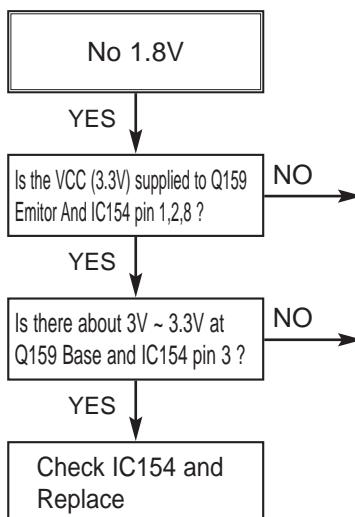
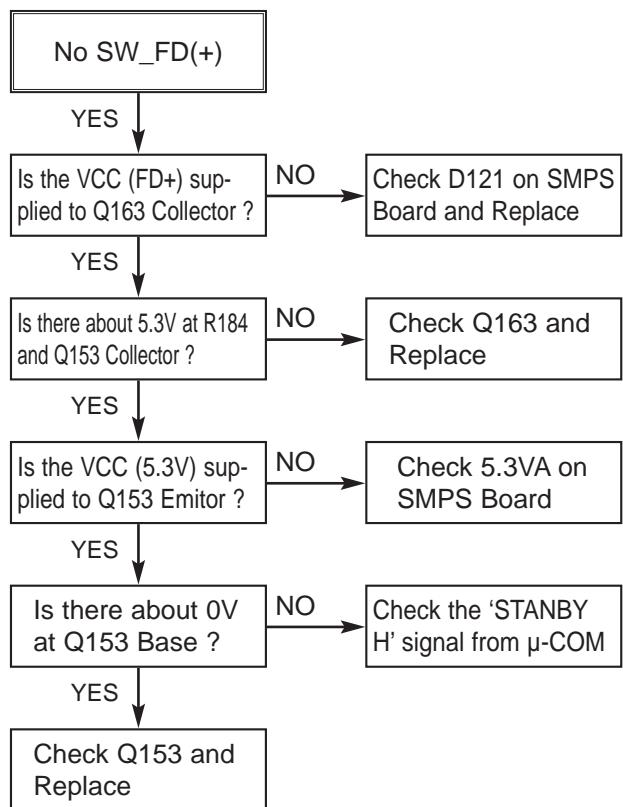
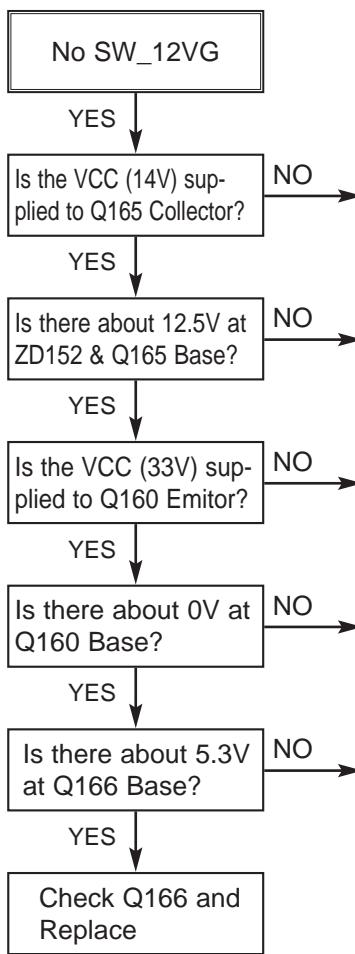




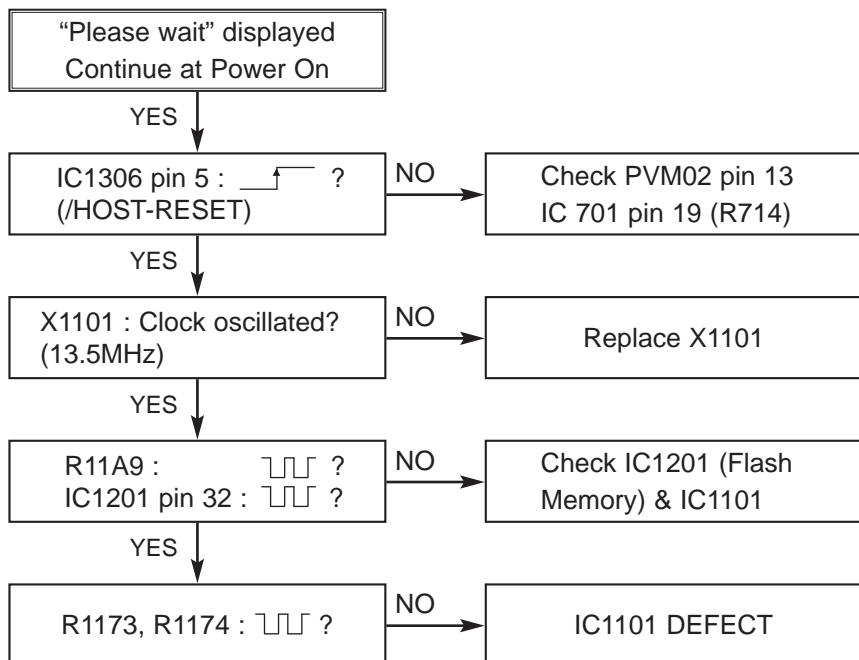
2. POWER SUPPLY ON I/O BOARD



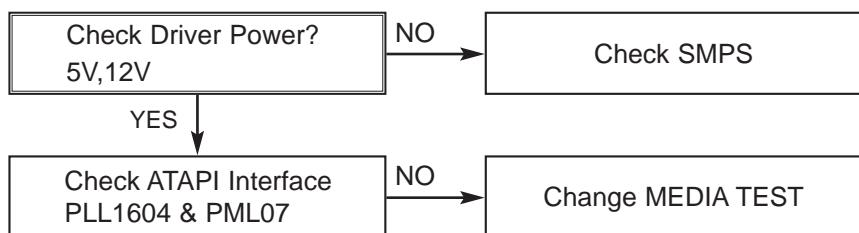




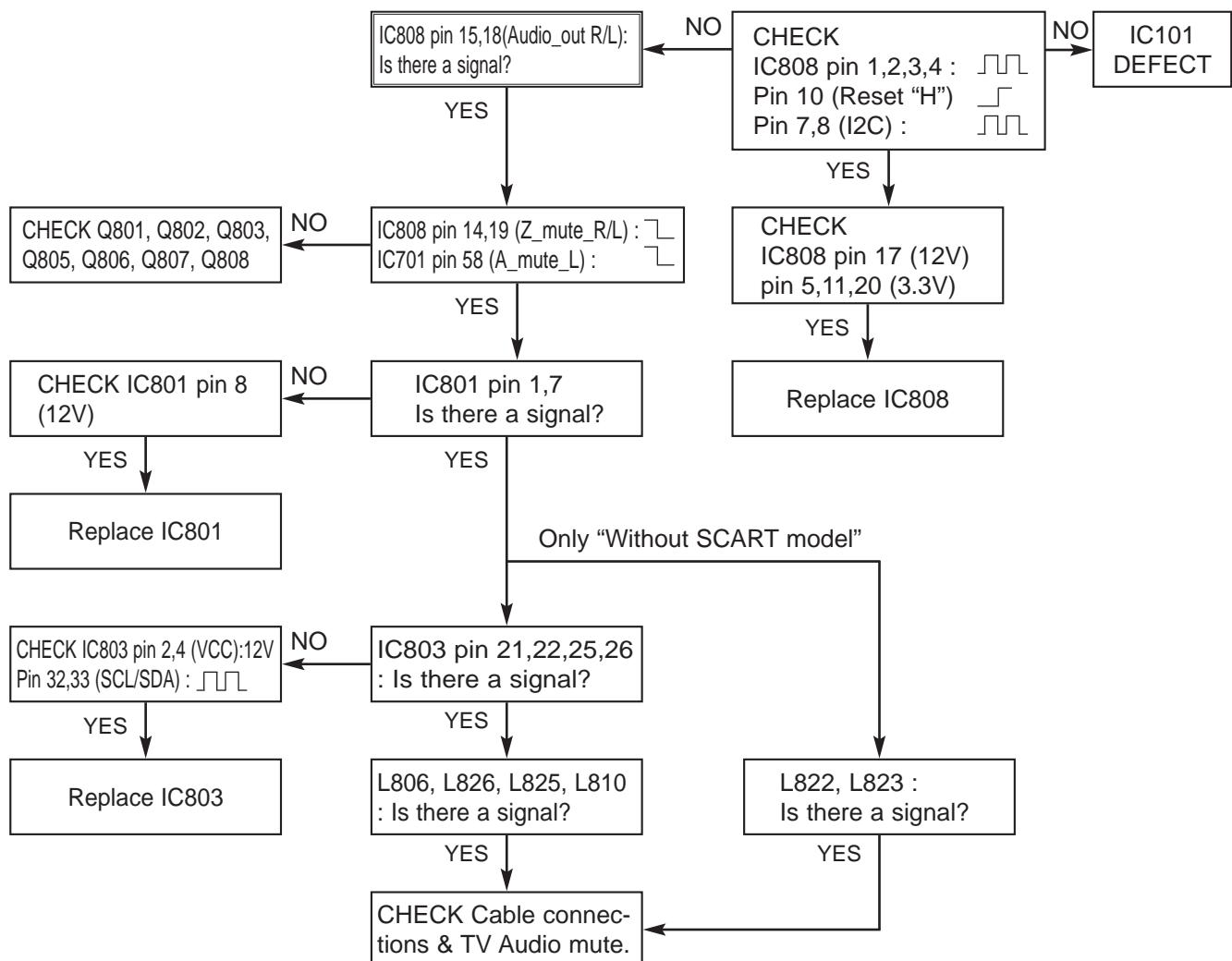
3. SYSTEM CIRCUIT PART



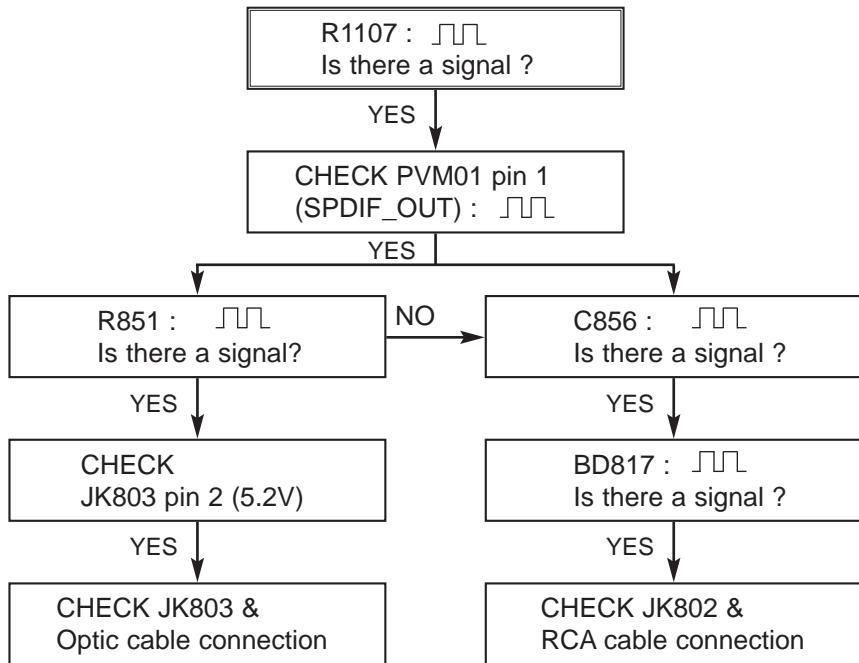
4. DISC NOT RECOGNIZED



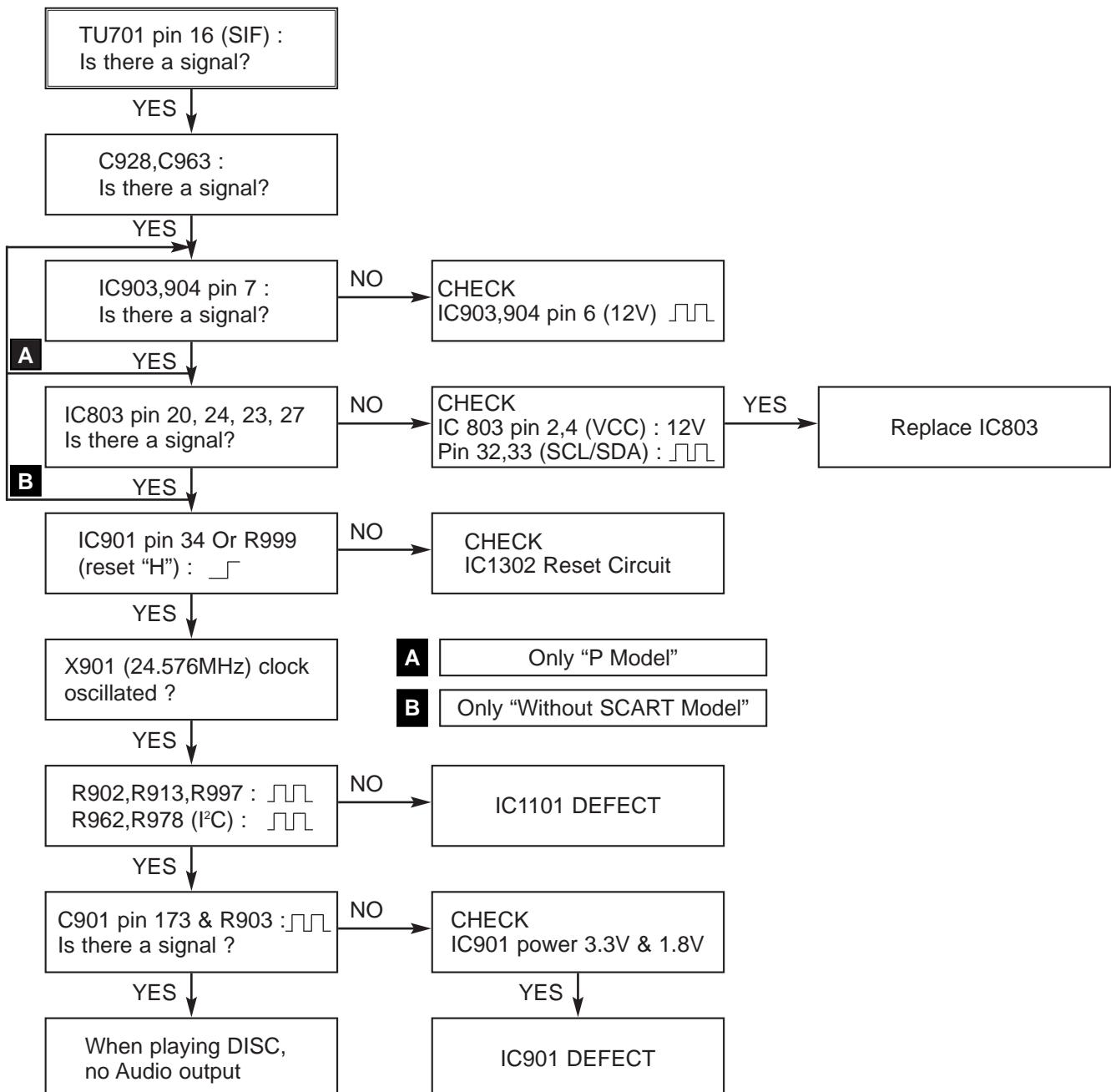
5. WHEN PLAYING DISC, NO AUDIO OUTPUT



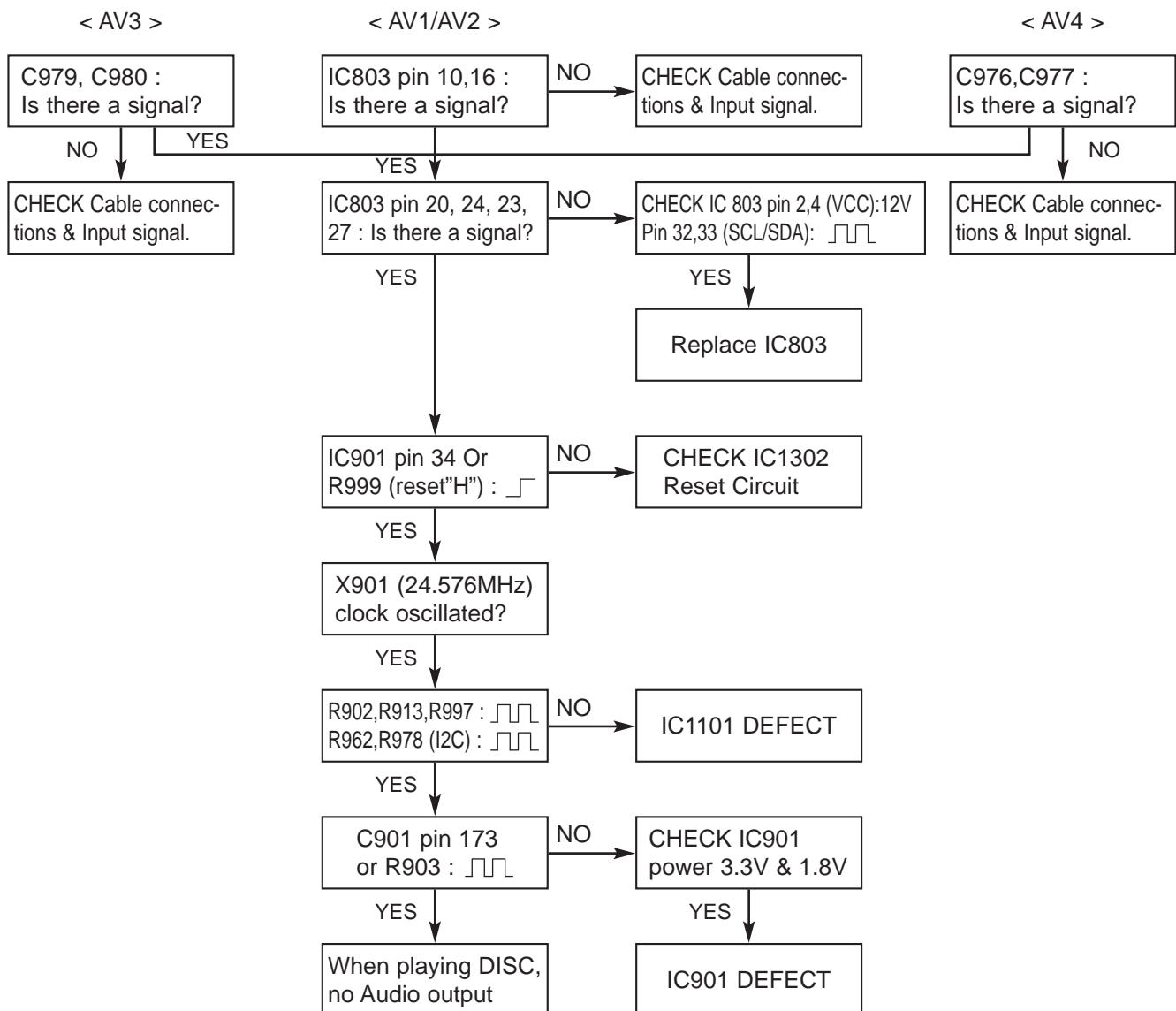
6. NO OPTICAL / DIGITAL OUTPUT



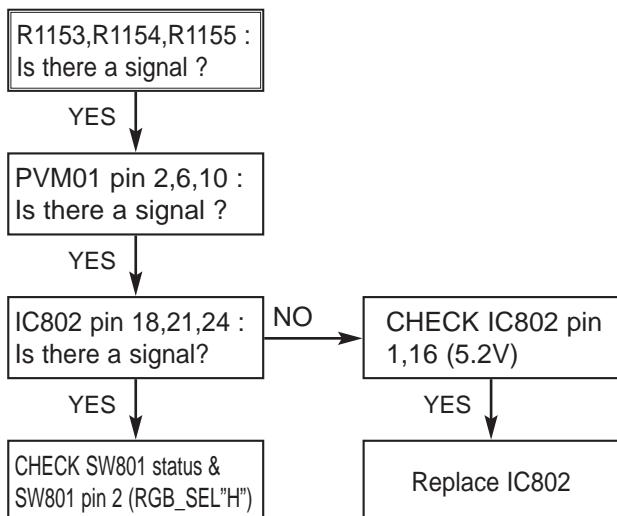
7. NO TUNER AUDIO OUTPUT



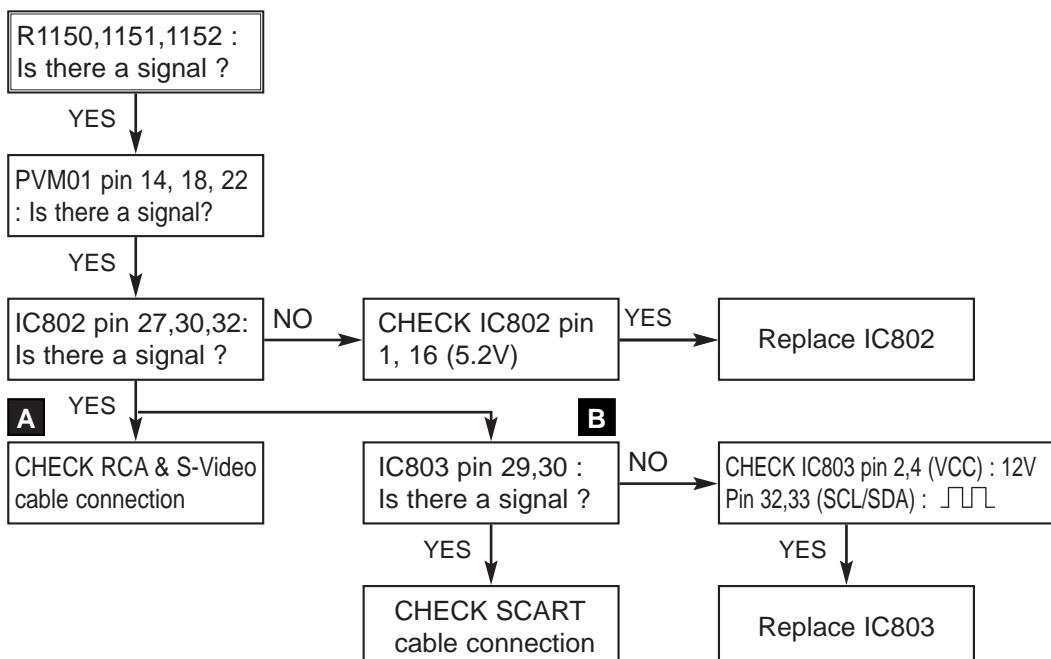
8. NO EXTERNAL INPUT AUDIO



9. NO RGB / COMPONENT VIDEO SIGNAL WHEN PLAY DISC

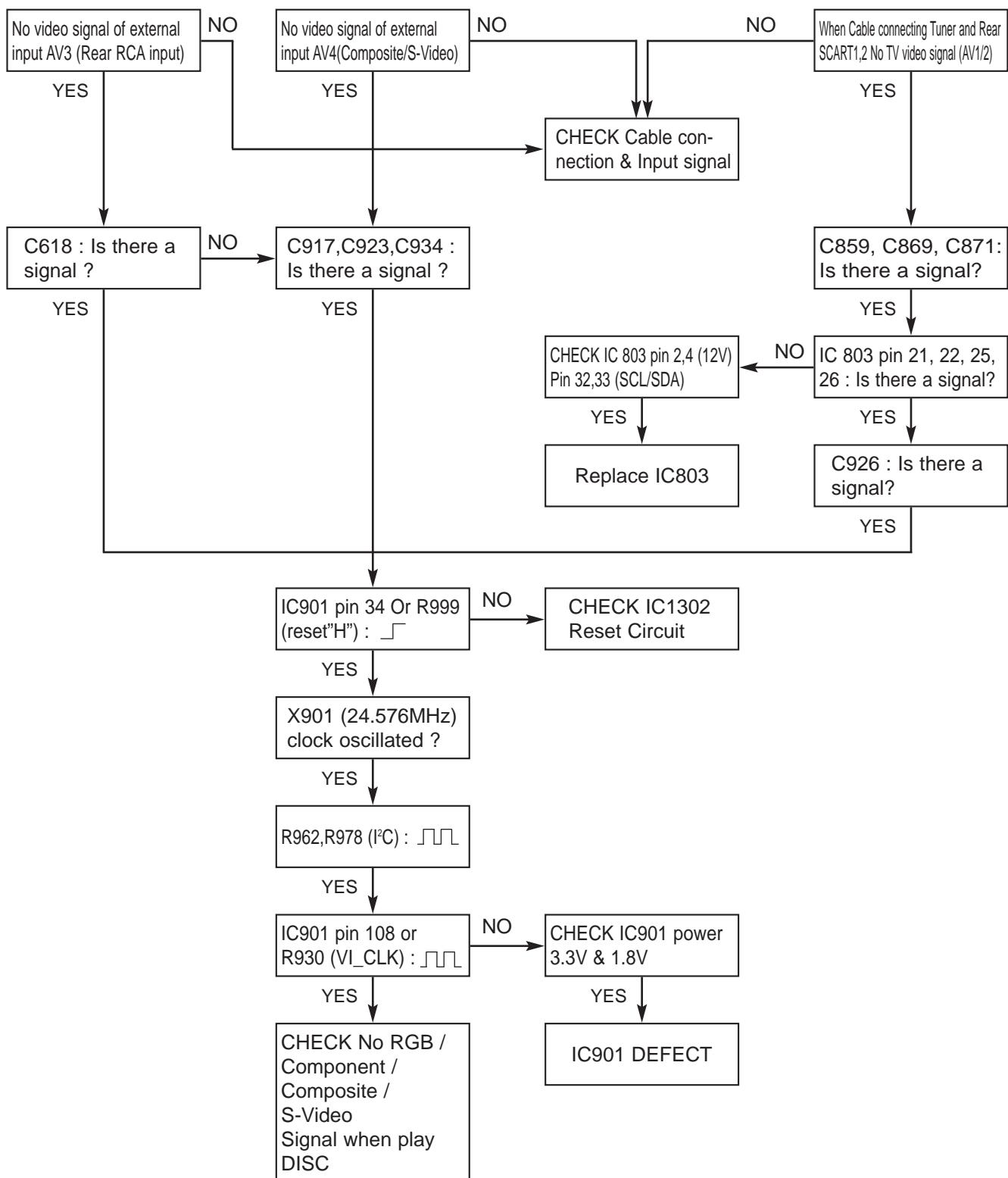


10. NO COMPOSITE / S-VIDEO SIGNAL WHEN PLAY DISC

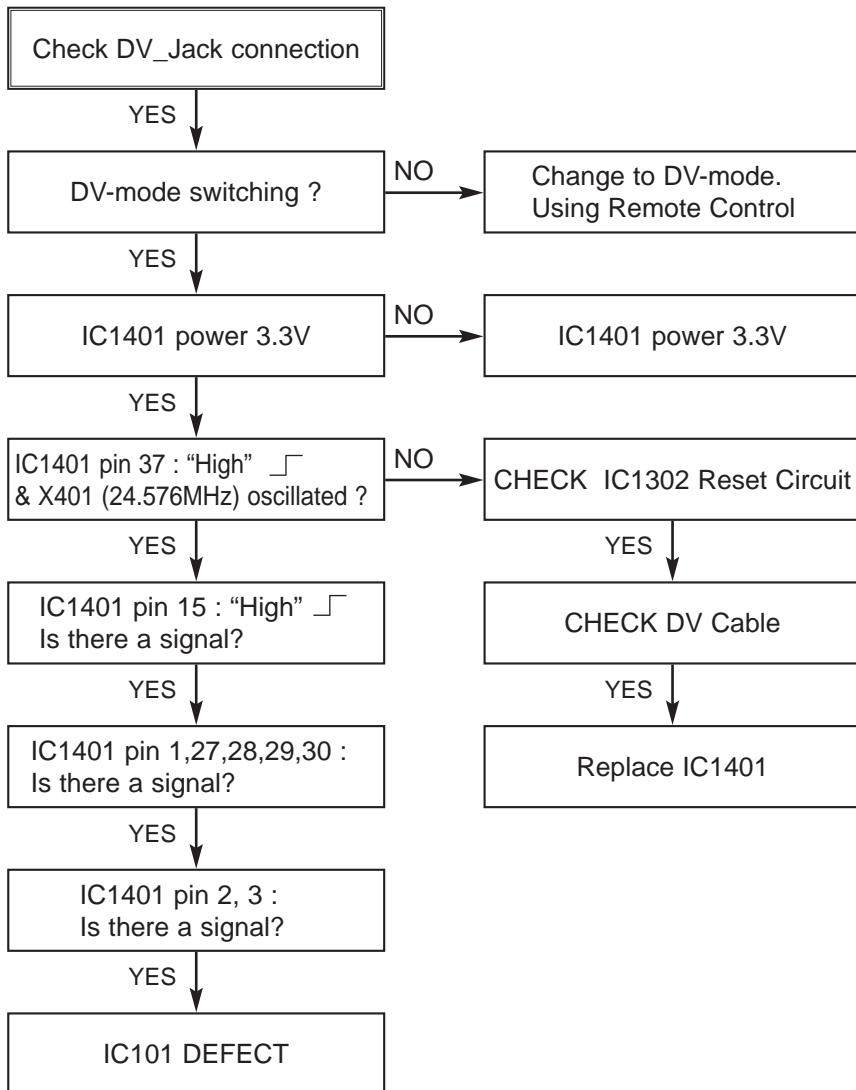


- A** S-Video & RCA
- B** SCART

11. NO TV, EXTERNAL INPUT VIDEO SIGNAL

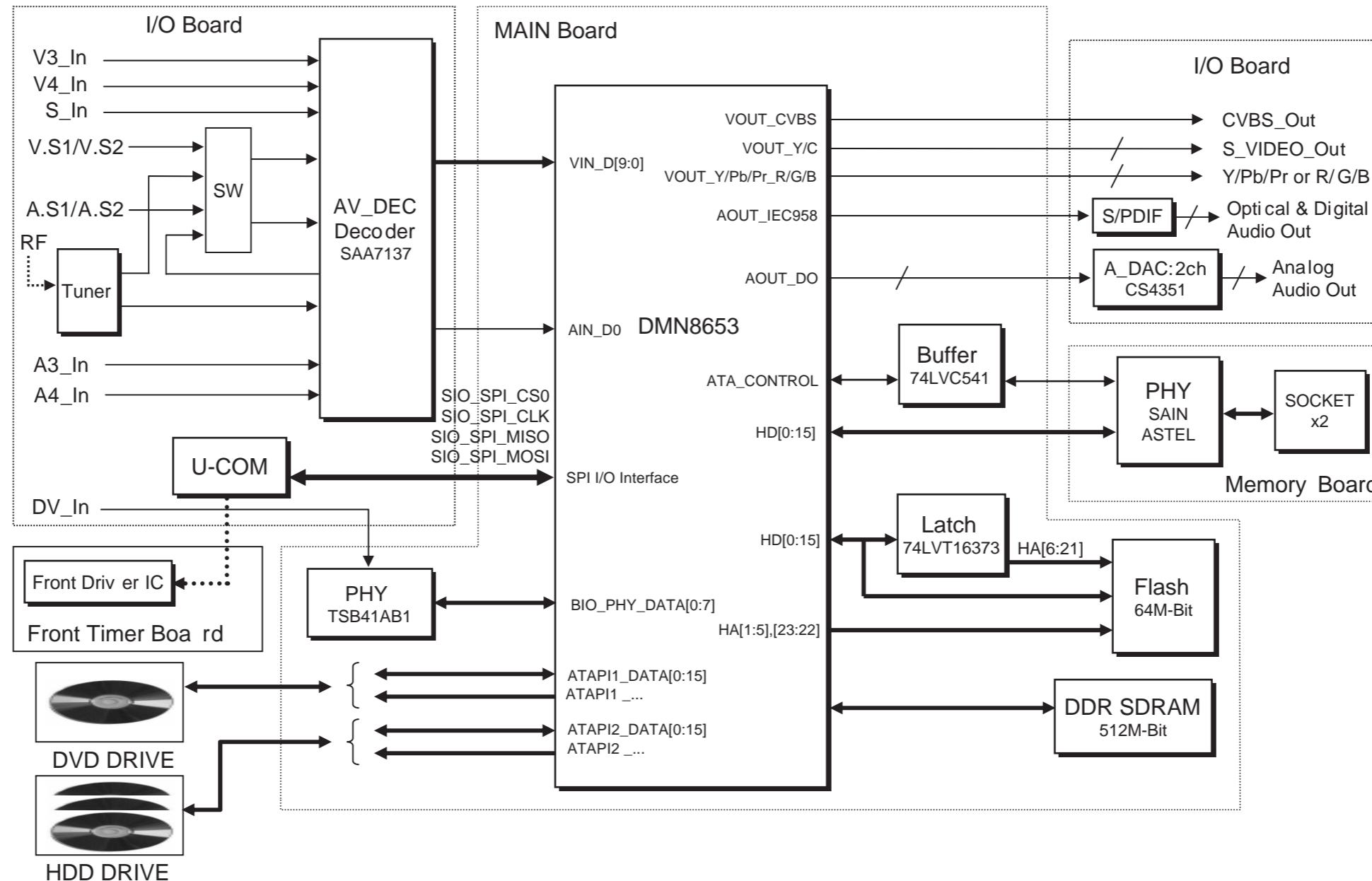


12. NO DV(IEEE 1394) INPUT (VIDEO/AUDIO) SIGNAL

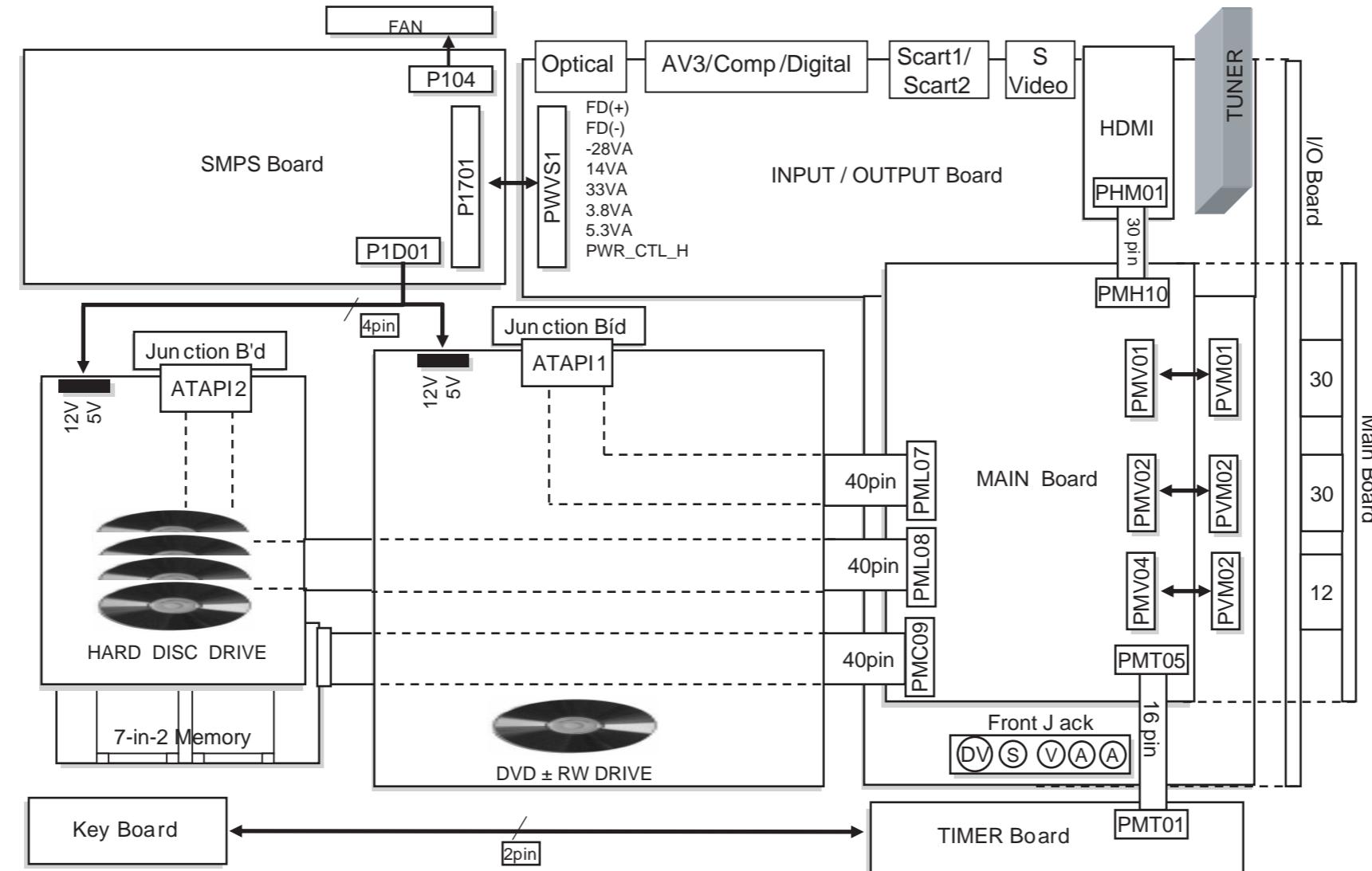


BLOCK DIAGRAMS

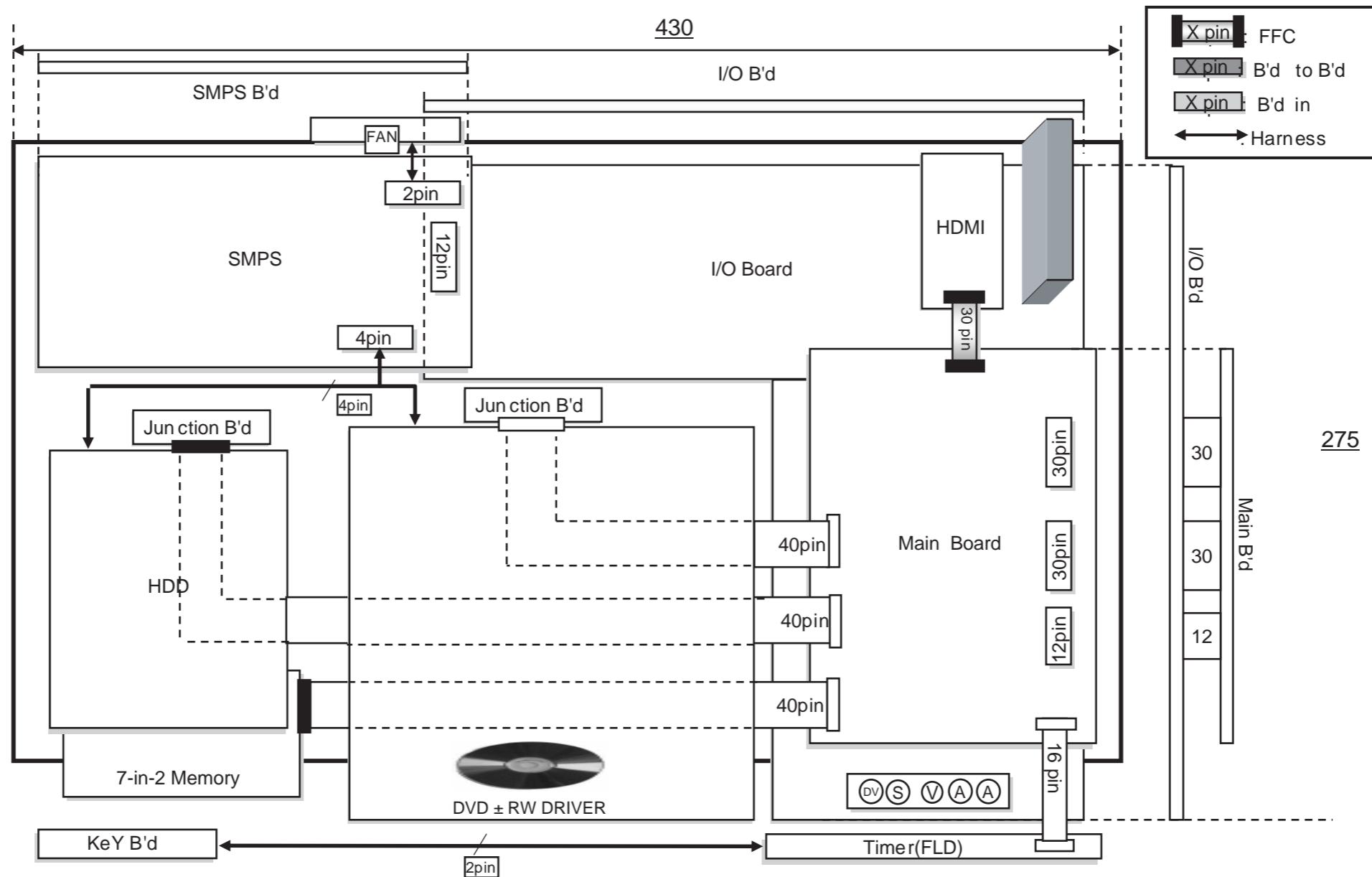
1. OVERALL BLOCK DIAGRAM



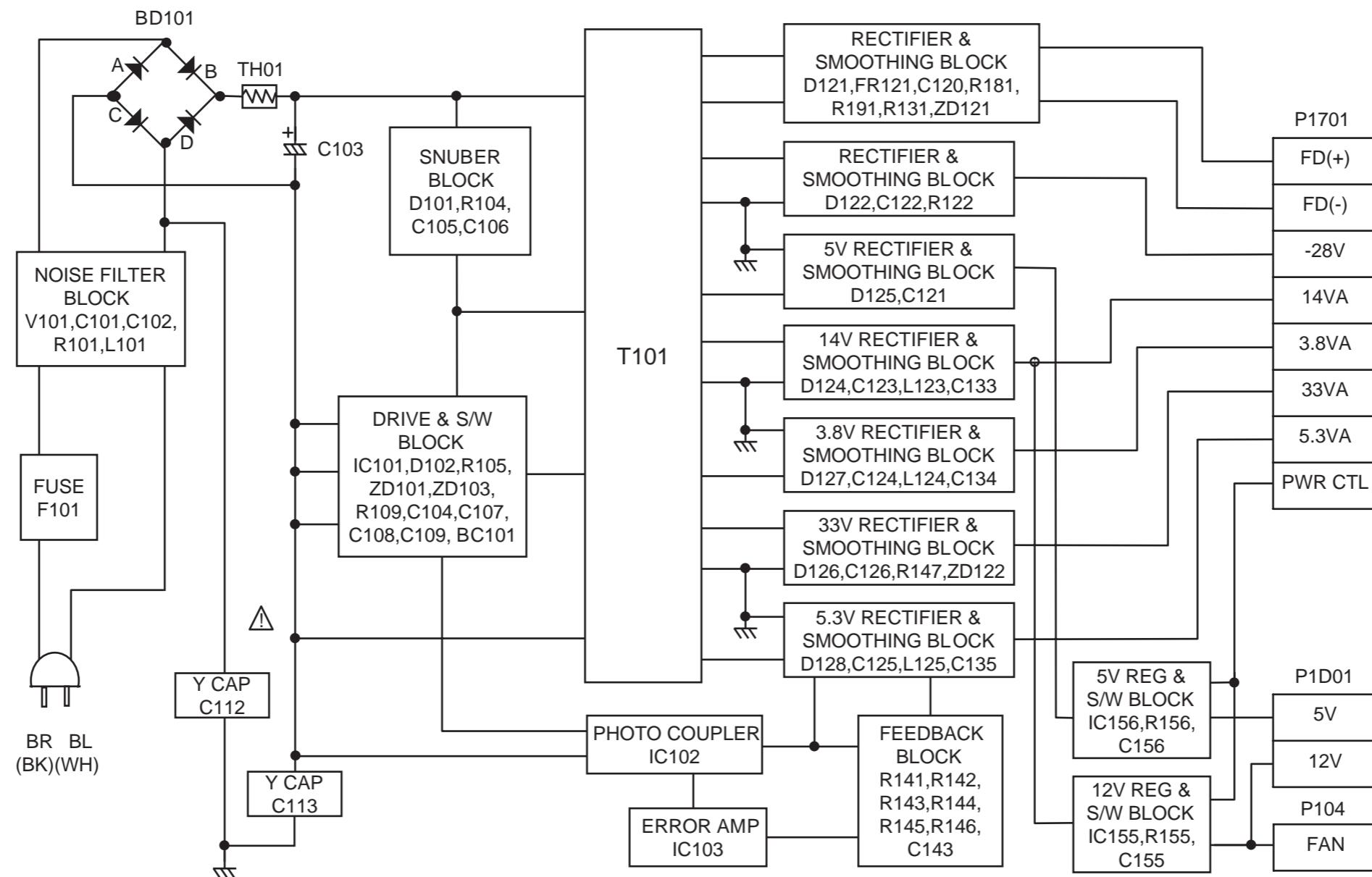
2. LAYOUT CONNECTION BLOCK DIAGRAM_1



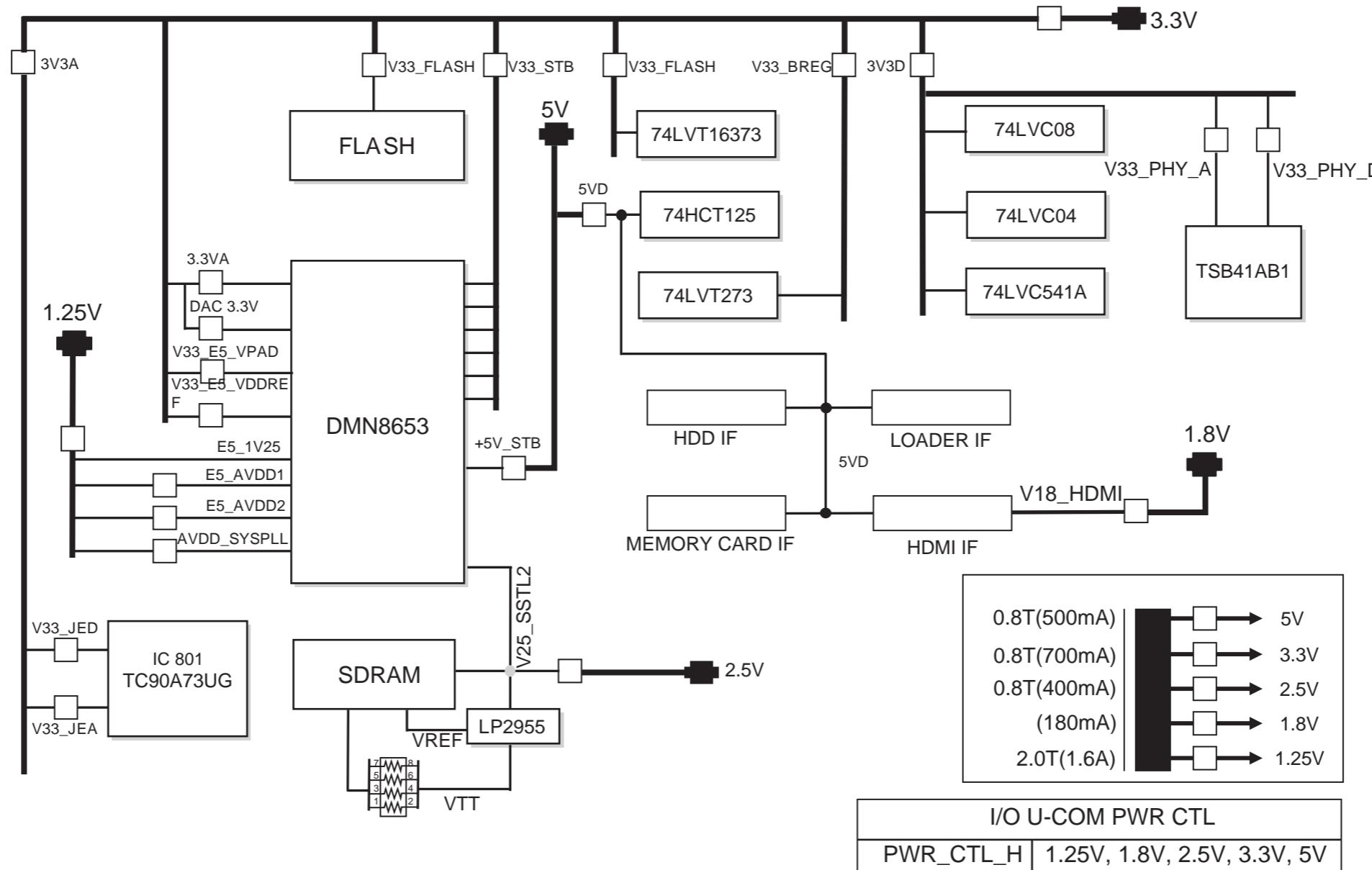
3. LAYOUT CONNECTION BLOCK DIAGRAM_2



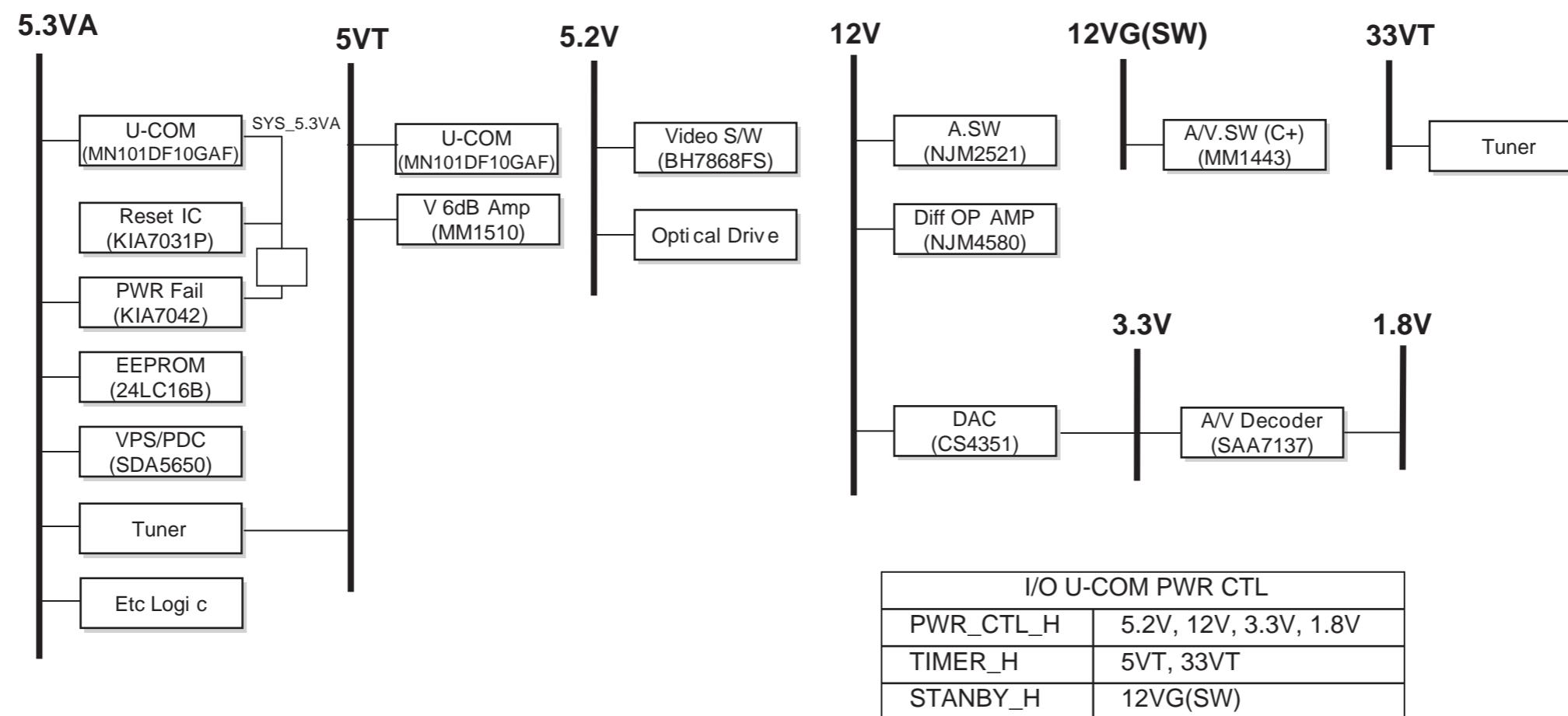
4. SMPS Block Diagram



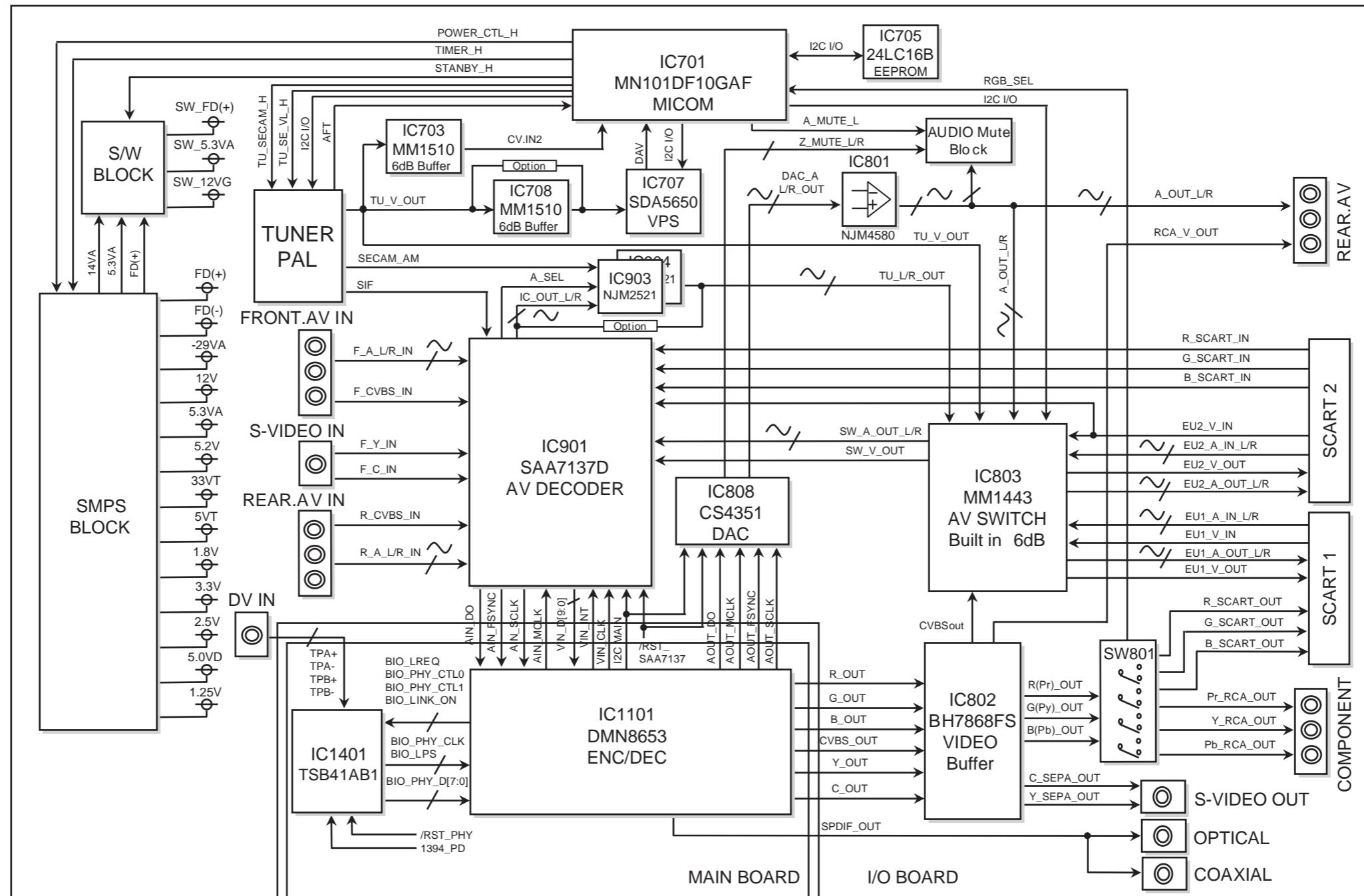
5. Power : Main Board Block Diagram



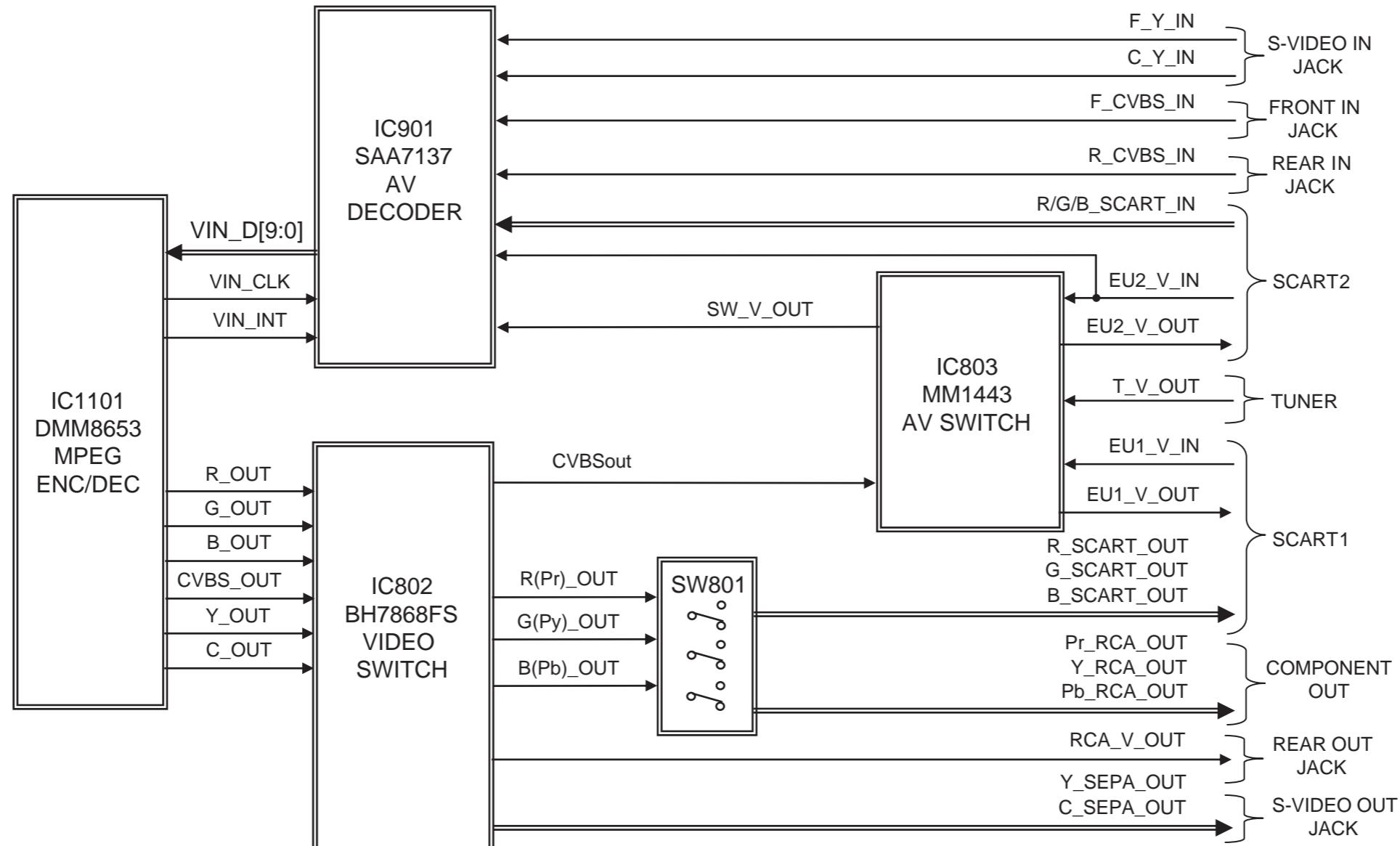
6. Power : I/O Board Block Diagram



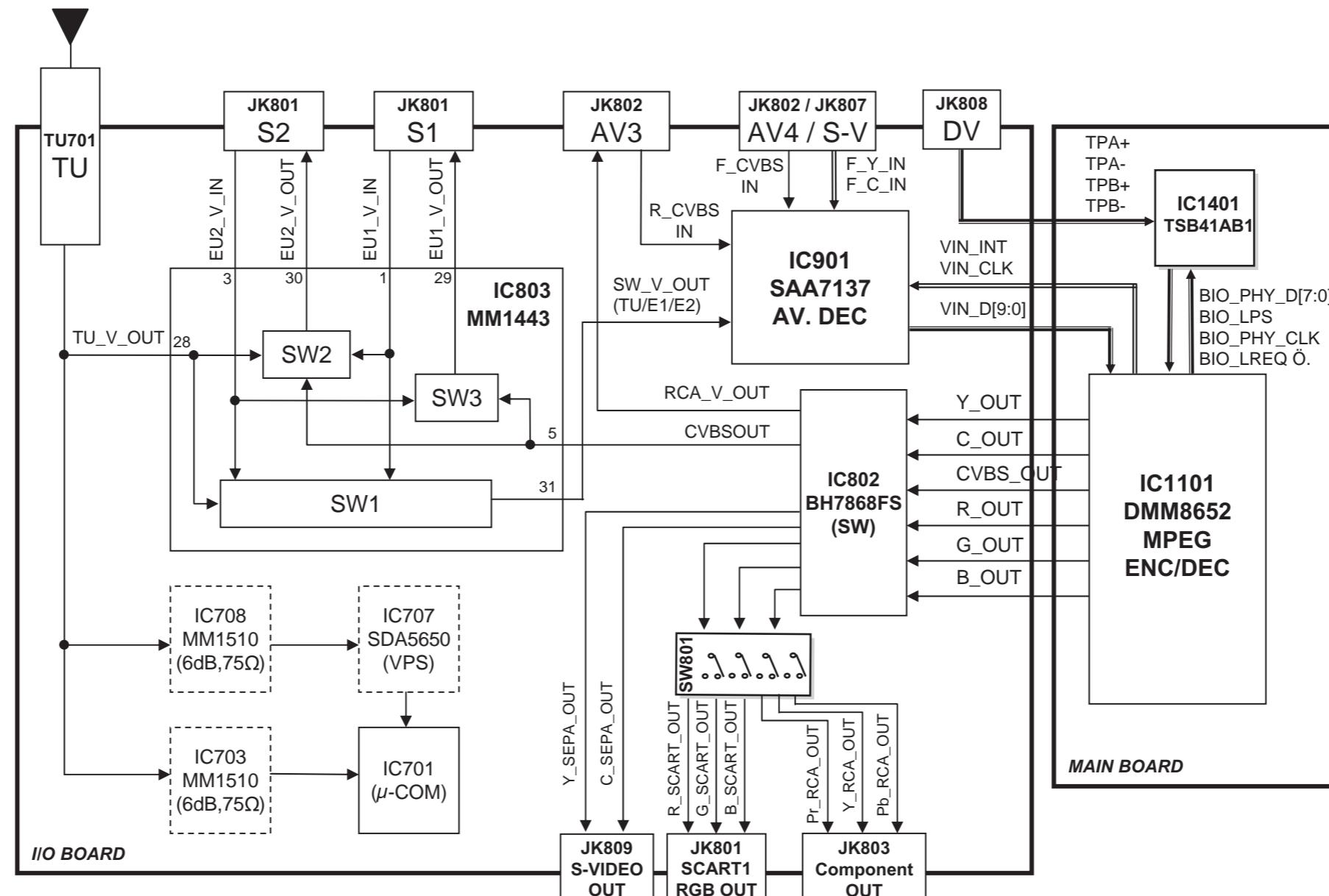
7. IN/OUT BLOCK DIAGRAM (SCART MODEL ONLY)



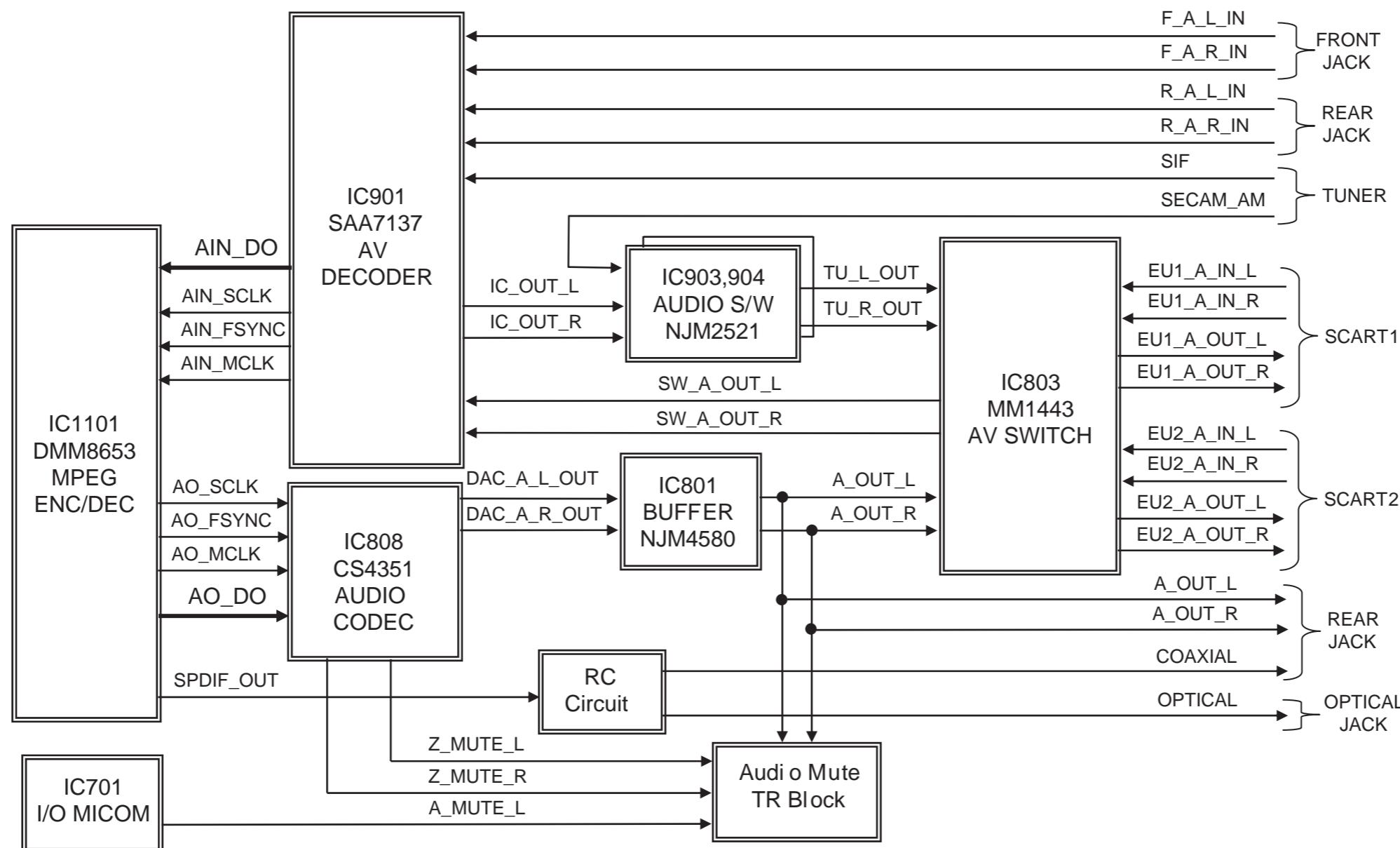
8. VIDEO BLOCK DIAGRAM



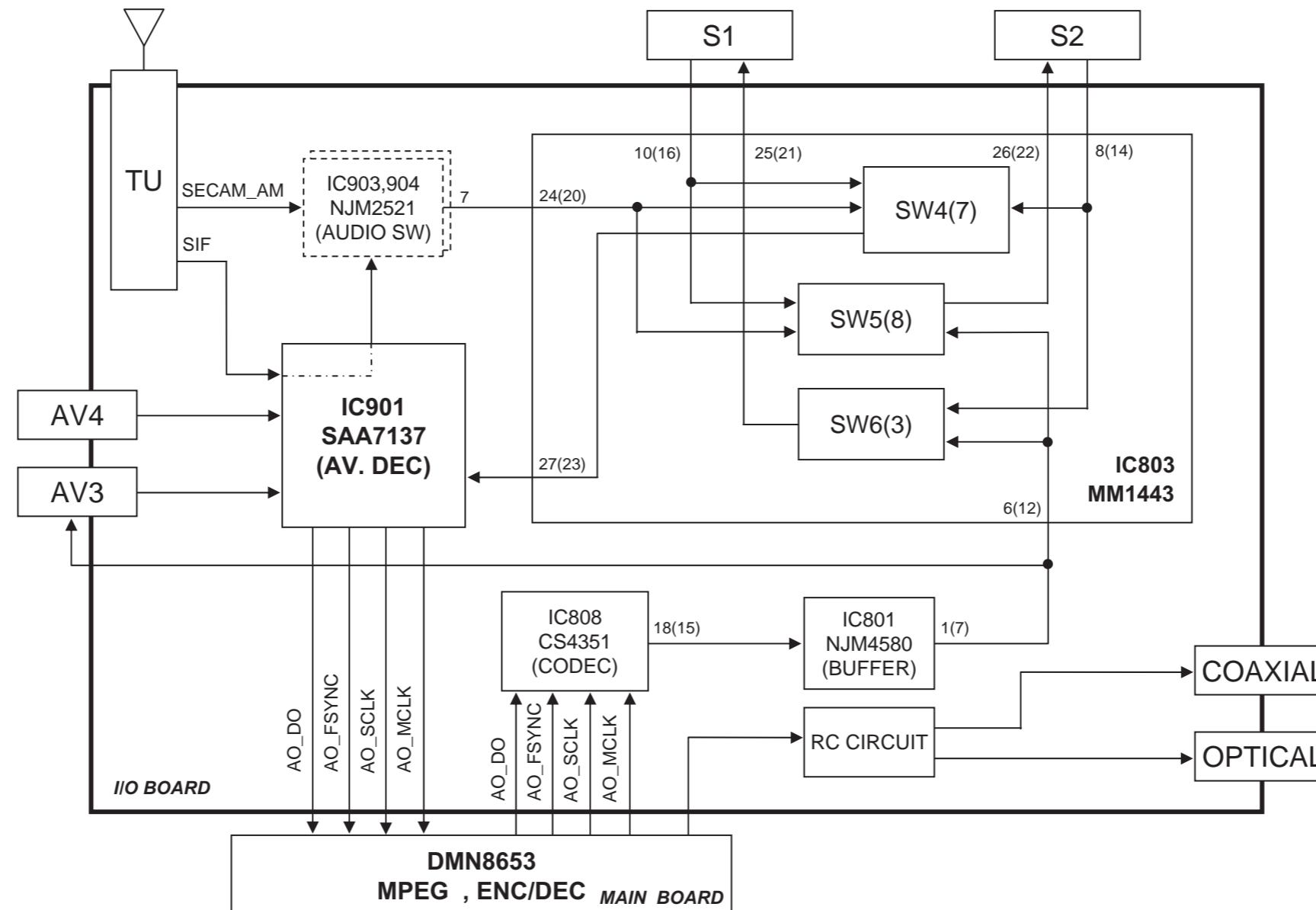
9. VIDEO S/W PATH BLOCK DIAGRAM



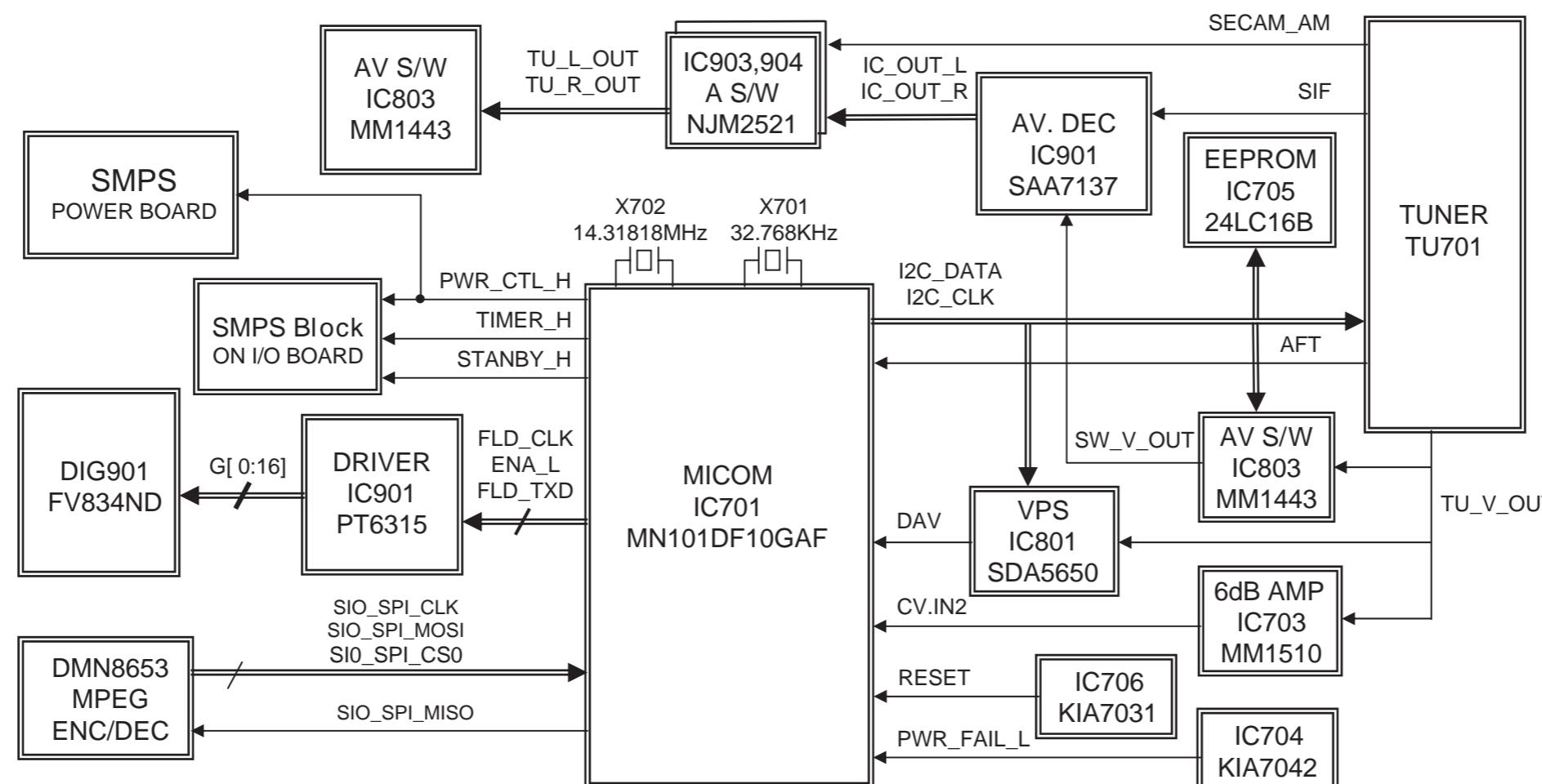
10. AUDIO BLOCK DIAGRAM



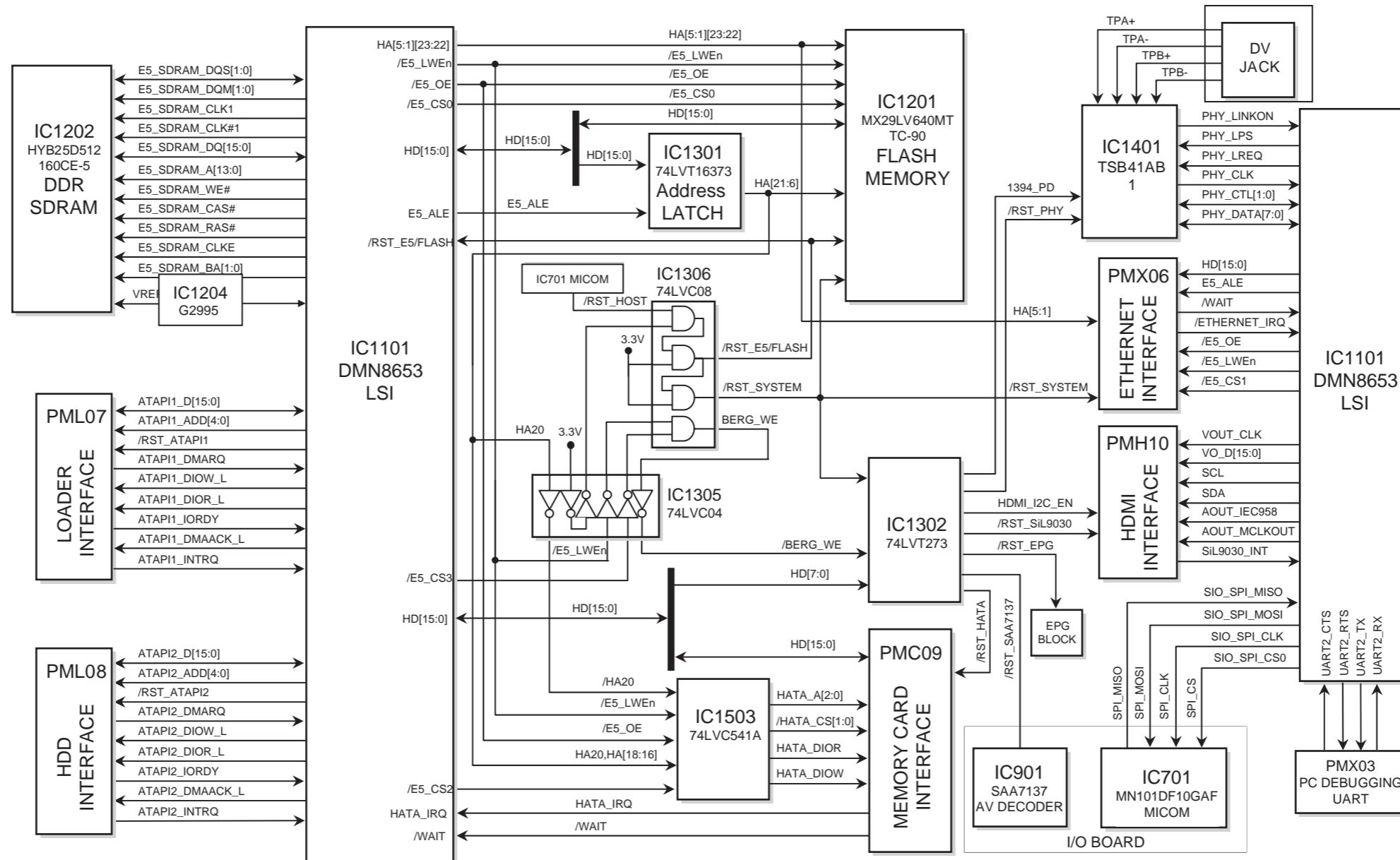
11. AUDIO S/W PATH BLOCK DIAGRAM



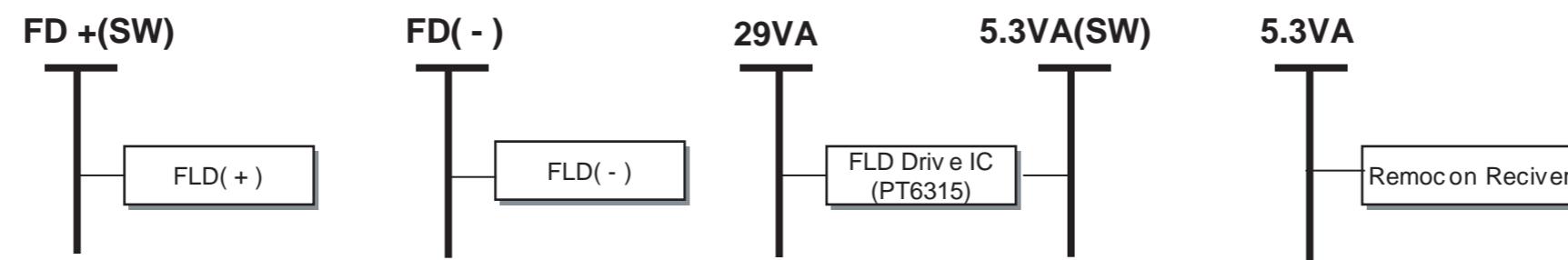
12. FLD / µ-COM / TUNER BLOCK DIAGRAM



13. MAIN BLOCK DIAGRAM (SCART MODEL ONLY)



14. TIMER BLOCK DIAGRAM



I/O U-COM PWR CTL	
STANBY_H	5.3VA(SW), FD+(SW)

CIRCUIT DIAGRAMS

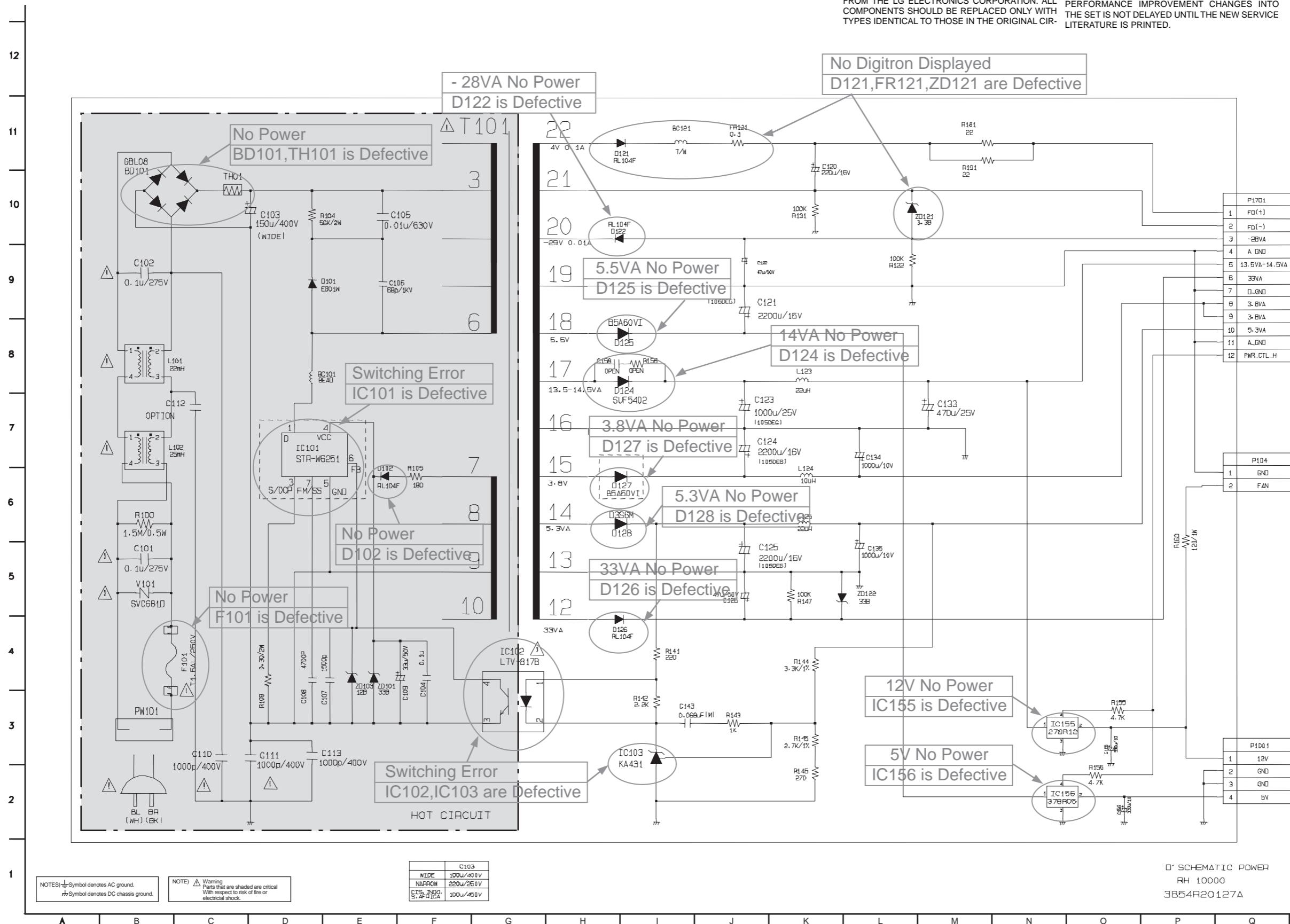
1. SMPS 1 CIRCUIT DIAGRAM

IMPORTANT SAFETY NOTICE

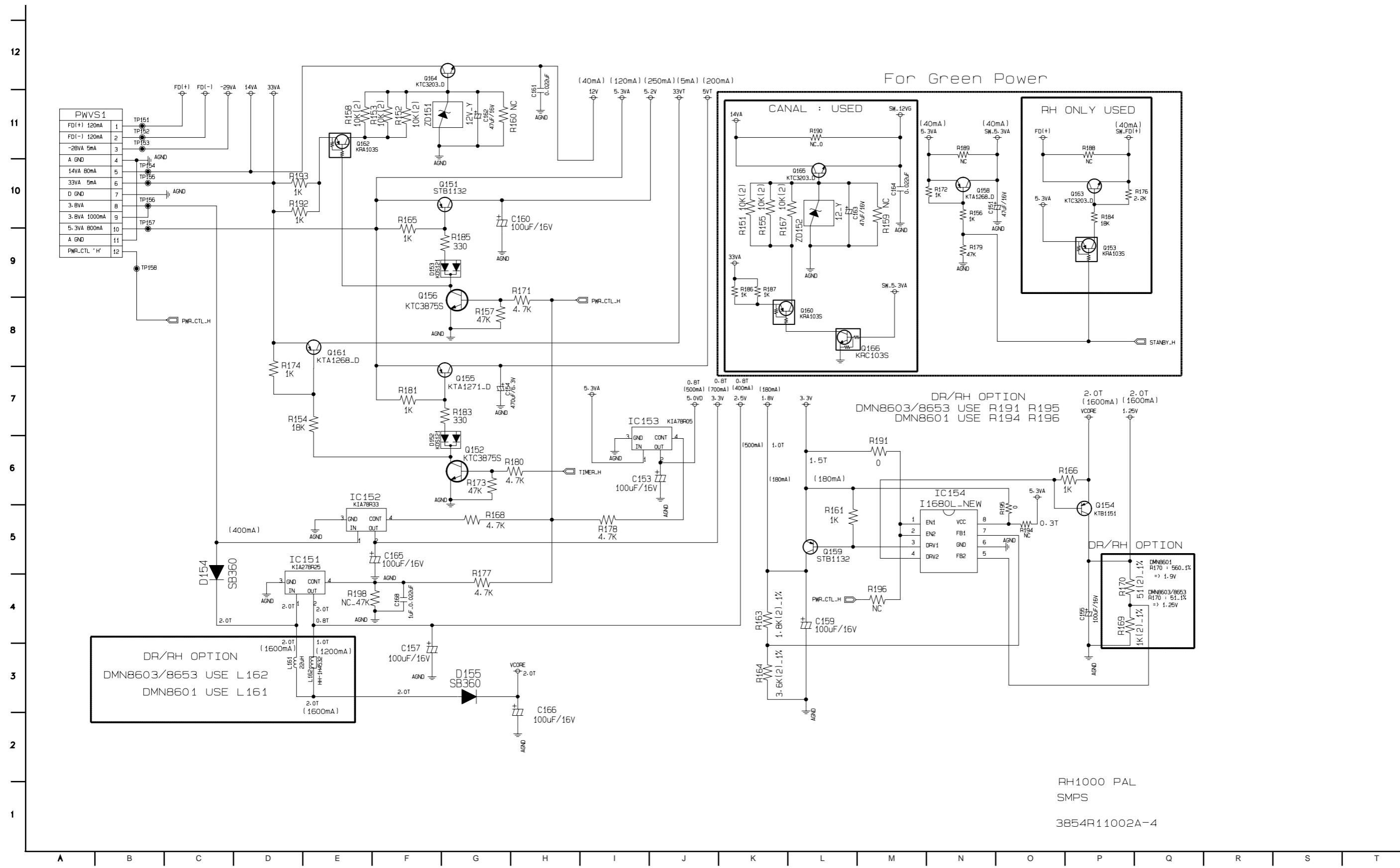
WHEN SERVICING THIS CHASSIS, UNDER NO CIRCUMSTANCES SHOULD THE ORIGINAL DESIGN BE MODIFIED OR ALTERED WITHOUT PERMISSION FROM THE LG ELECTRONICS CORPORATION. ALL COMPONENTS SHOULD BE REPLACED ONLY WITH TYPES IDENTICAL TO THOSE IN THE ORIGINAL CIRCUIT DIAGRAM. THIS CIRCUIT DIAGRAM MAY OCCASIONALLY DIFFER FROM THE ACTUAL CIRCUIT USED. THIS WAY, IMPLEMENTATION OF THE LATEST SAFETY AND PERFORMANCE IMPROVEMENT CHANGES INTO THE SET IS NOT DELAYED UNTIL THE NEW SERVICE LITERATURE IS PRINTED.

NOTE :

1. Shaded(■) parts are critical for safety. Replace only with specified part number.
2. Voltages are DC-measured with a digital voltmeter during Play mode.



2. SMPS 2 CIRCUIT DIAGRAM

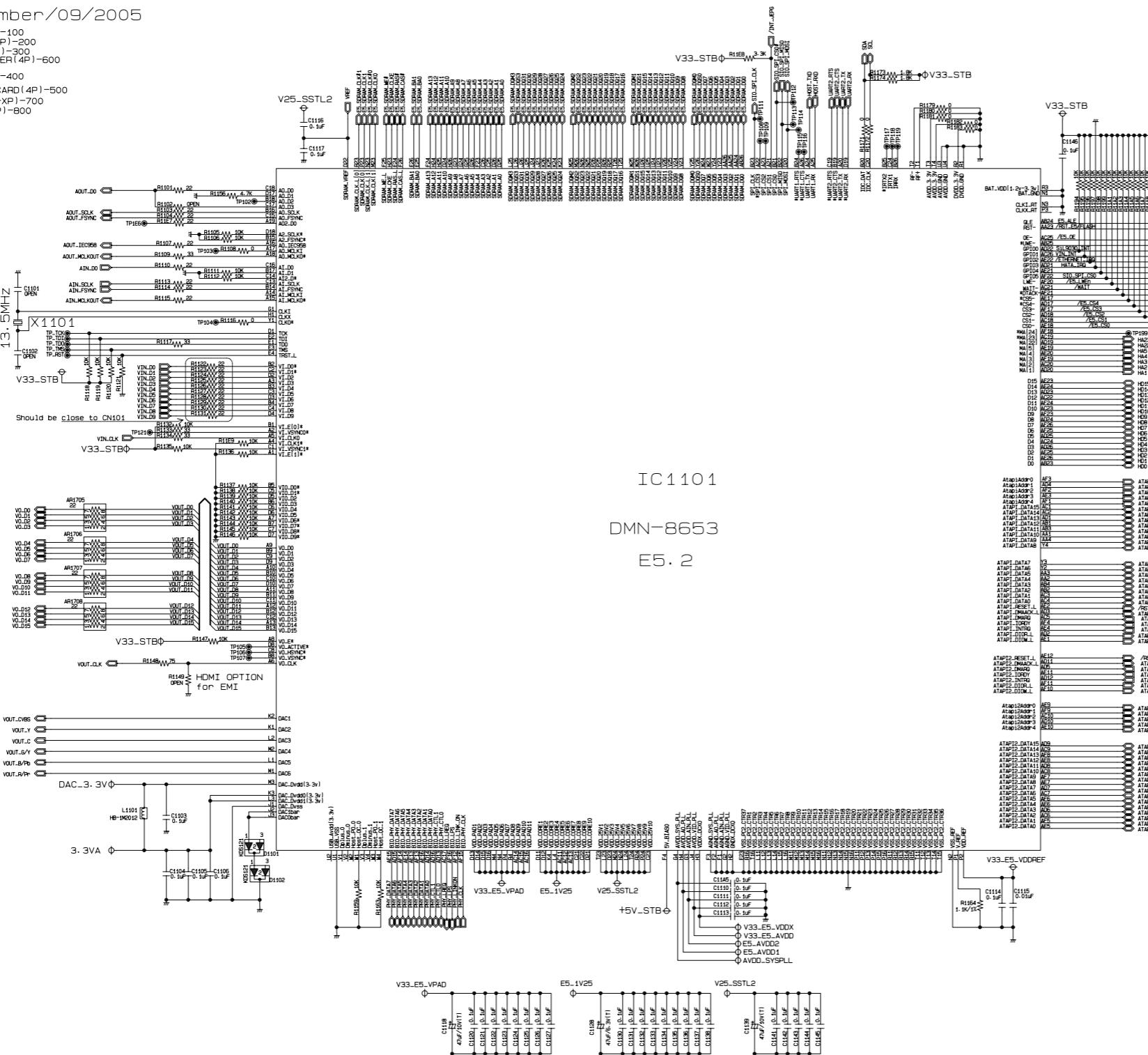


3. MPEG CIRCUIT DIAGRAM

December/09/2005

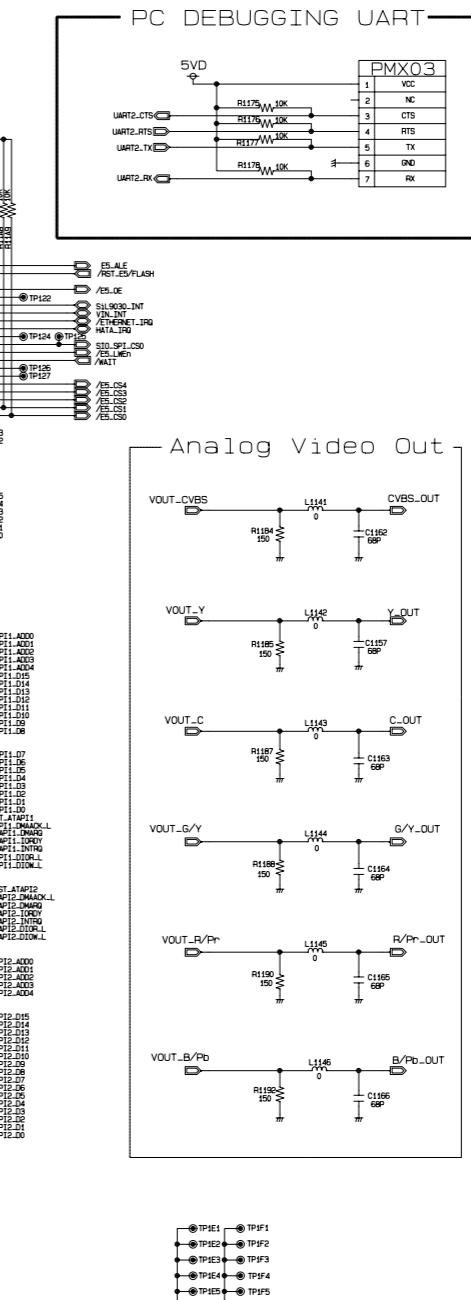
MPEG(1P)-100
MEMORY(2P)-200
RESET(3P)-300
HDDE+LOADFB(4P)-600

1394(3P)-400
MEMORY CARD(4P)-500
HDMI(4P+XP)-700
J_EPG(3P)-800



3-47

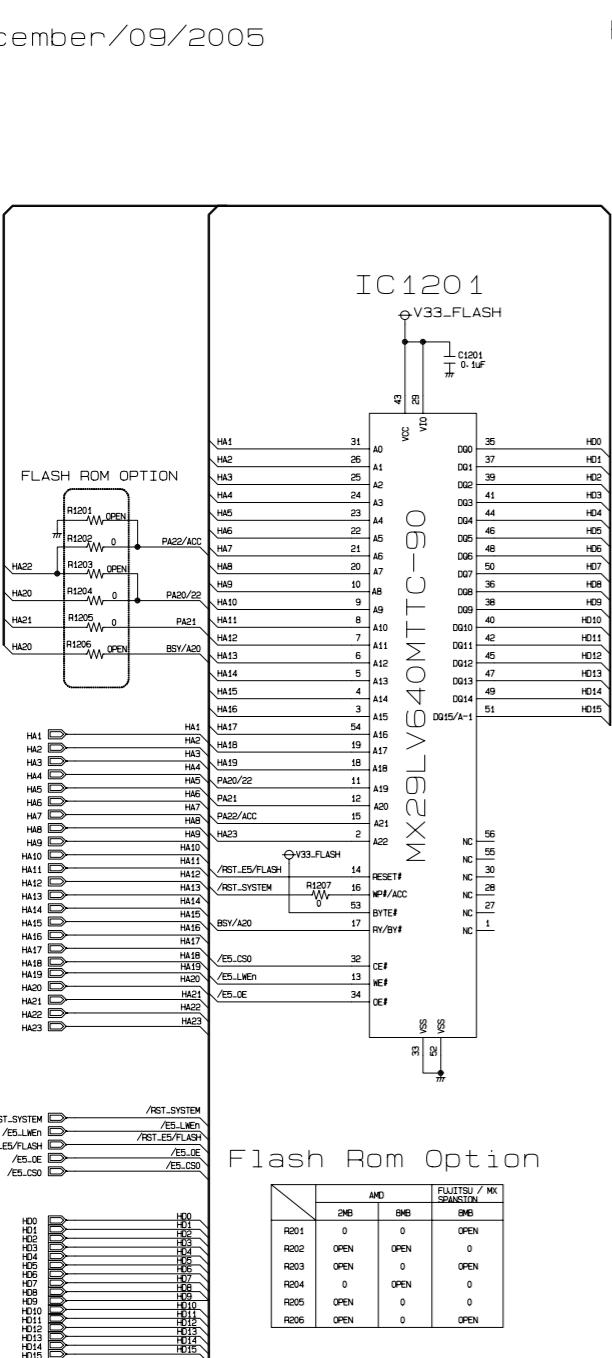
3-48



RH1000
MPEG
8854R17620A
6870R1881AA

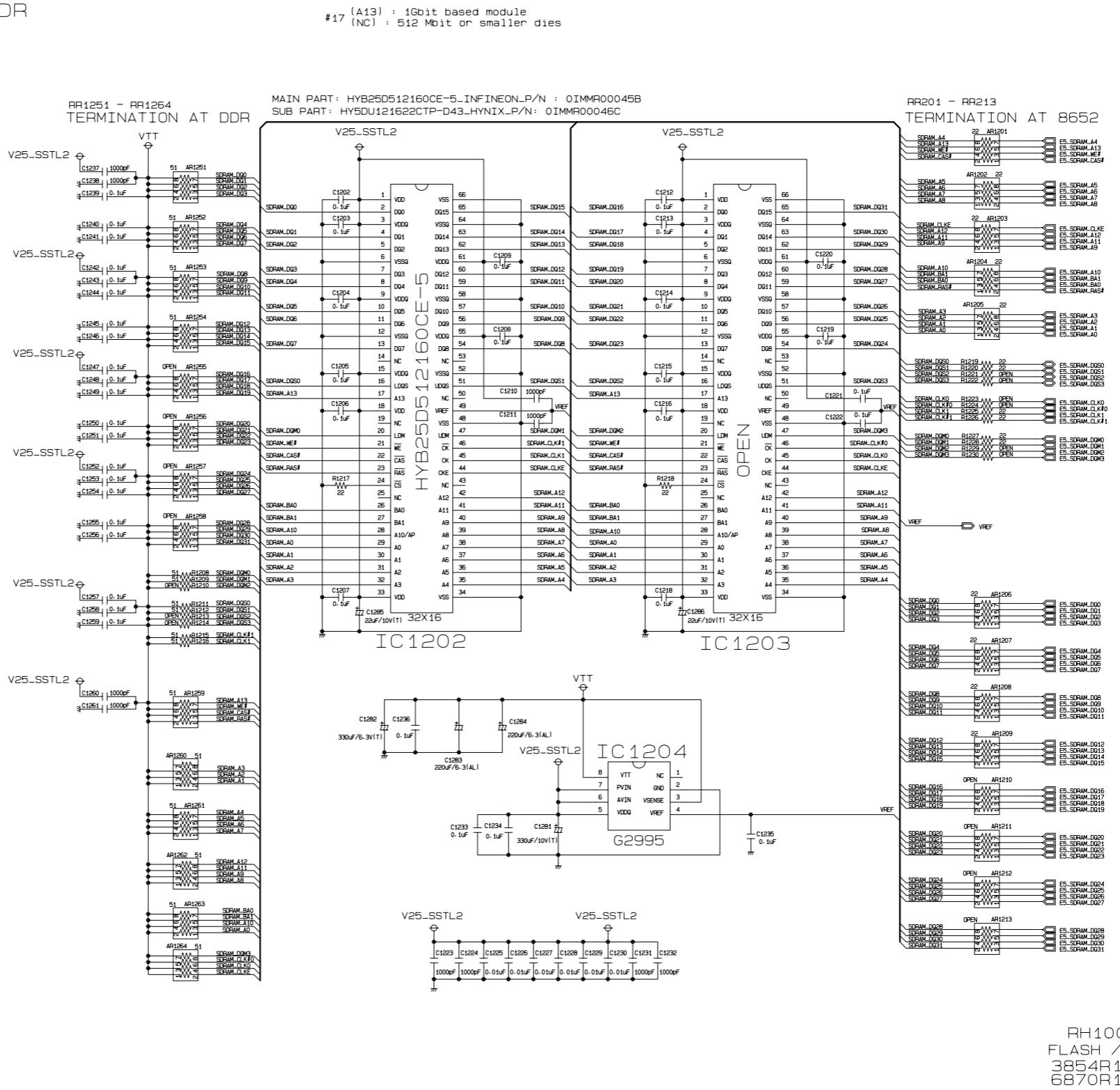
4. FLASH/DDR CIRCUIT DIAGRAM

December/09/2005



FLASHDRB

#17 (A13) : 1Gbit based module
#17 (NC) : 512 Mbit or smaller die

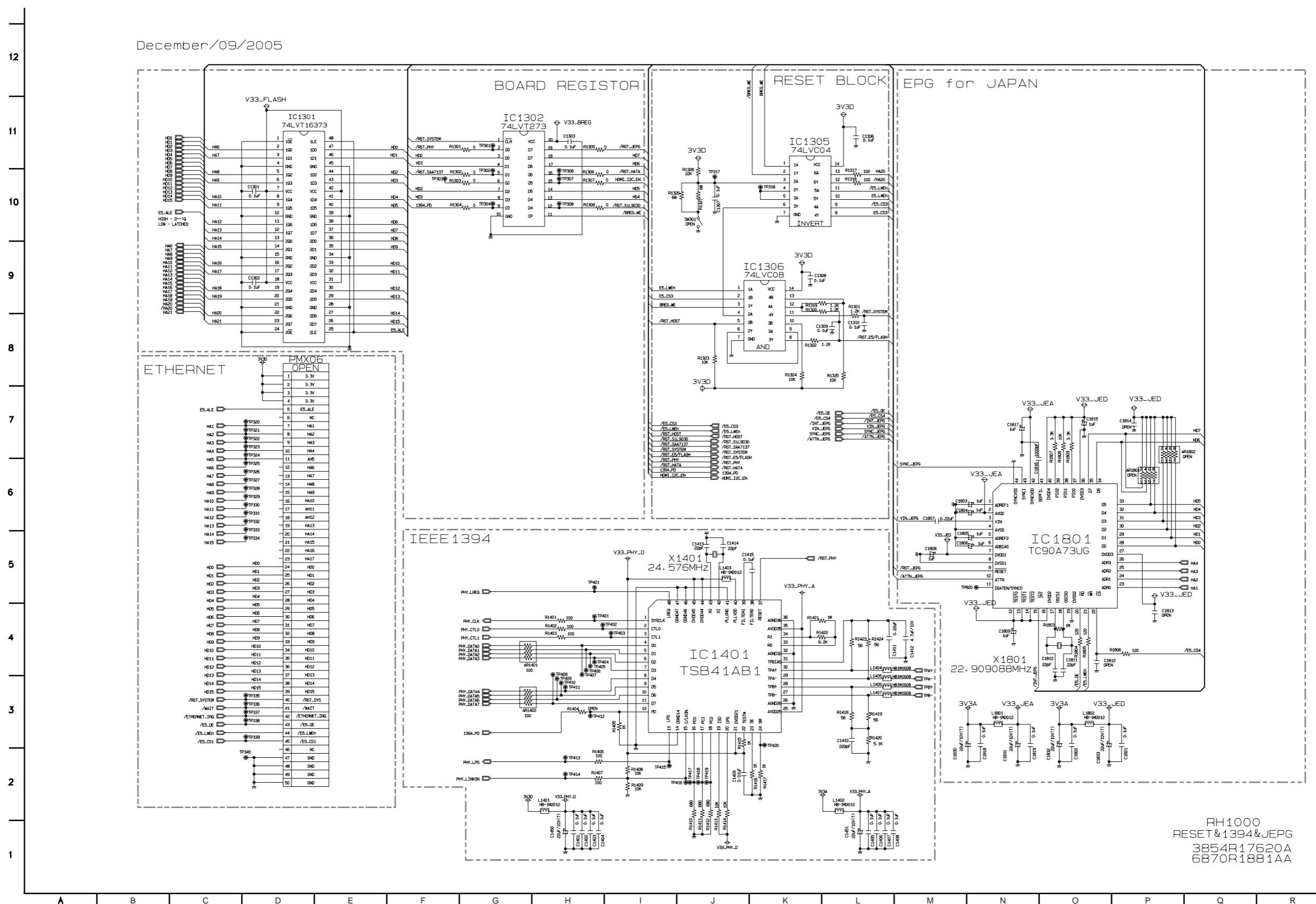


3-49

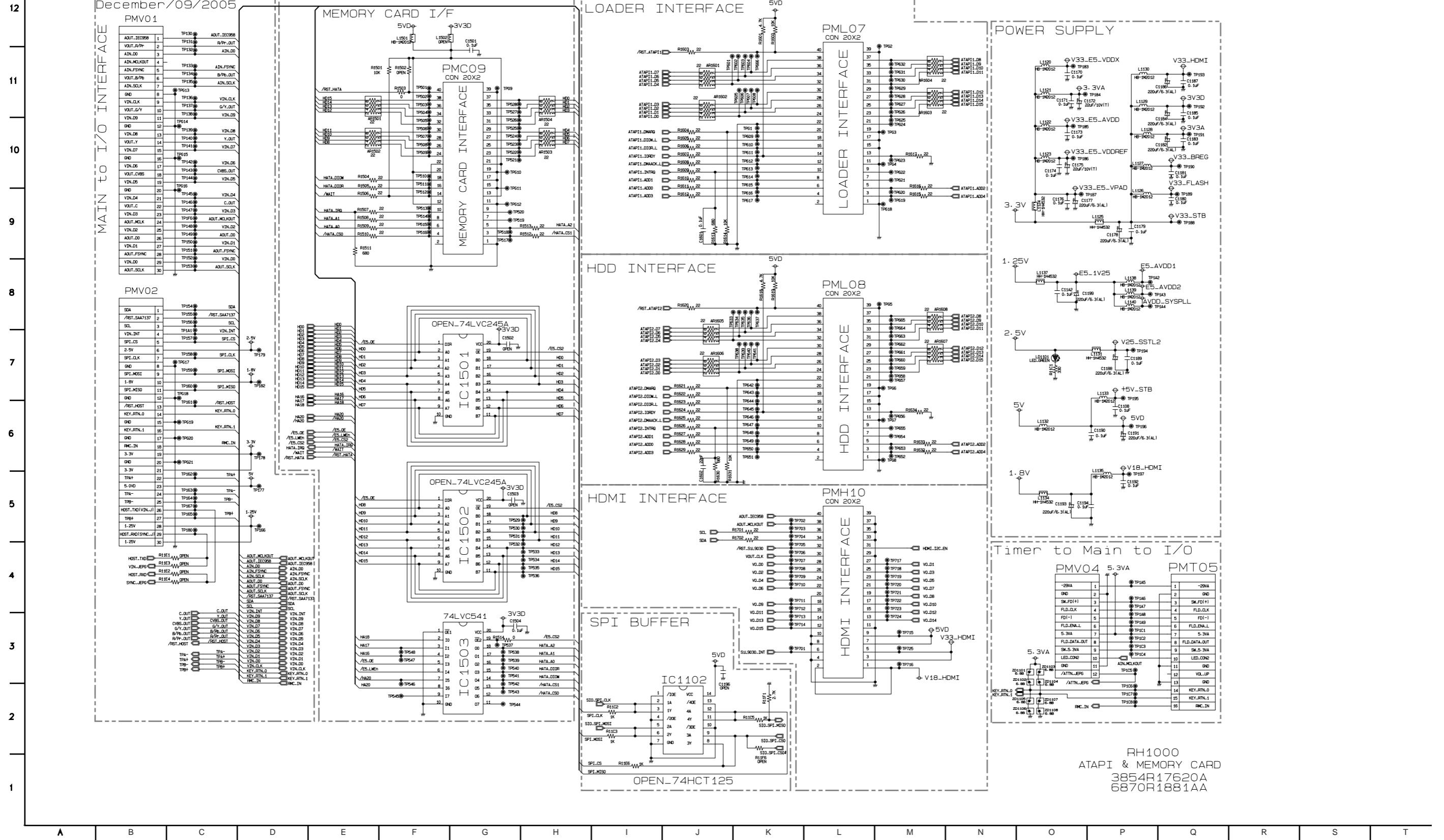
3-50

5. RESET & 1394 & JPEG CIRCUIT DIAGRAM

December/09/2005



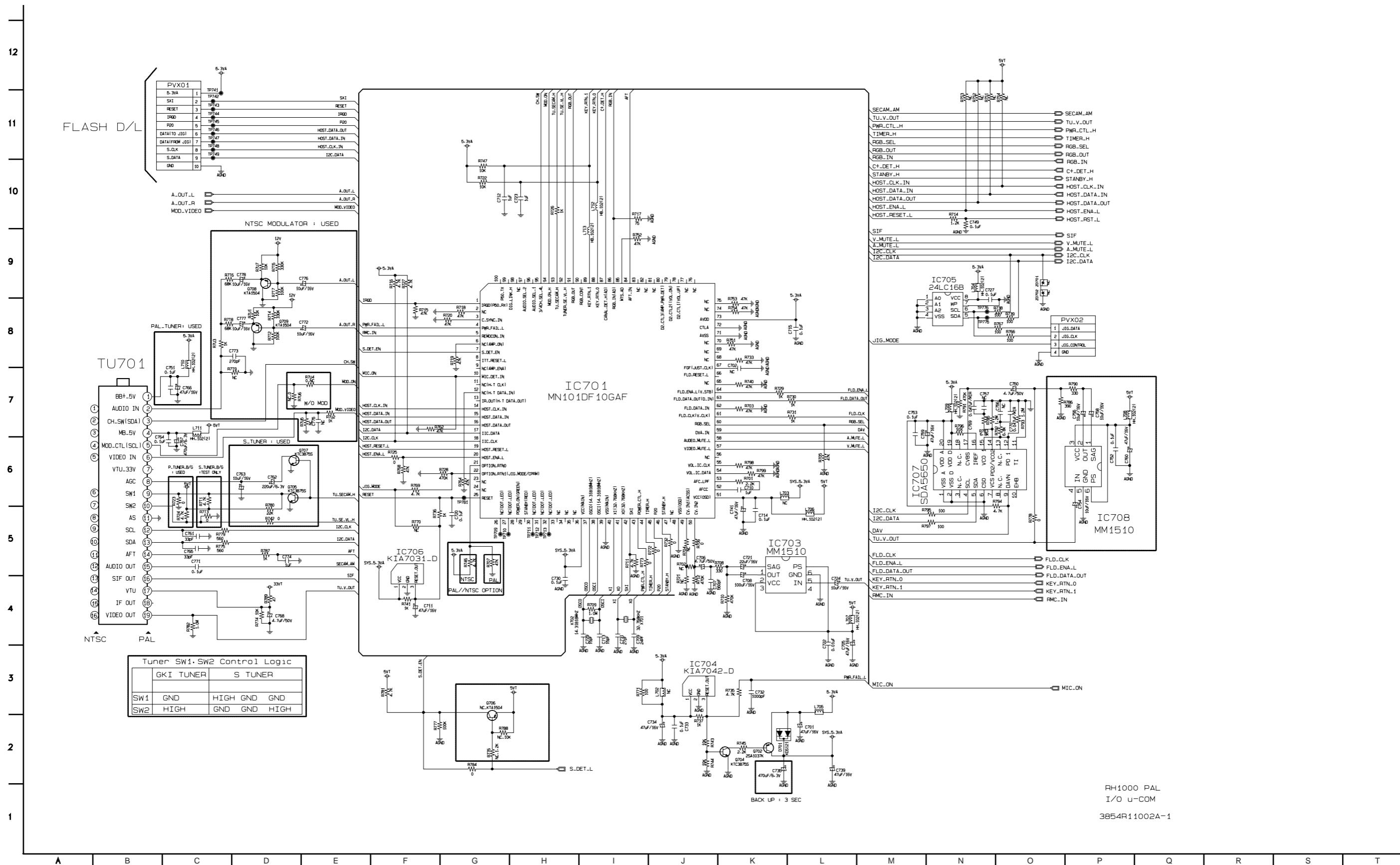
6. ATAPI & MEMORY CARD CIRCUIT DIAGRAM



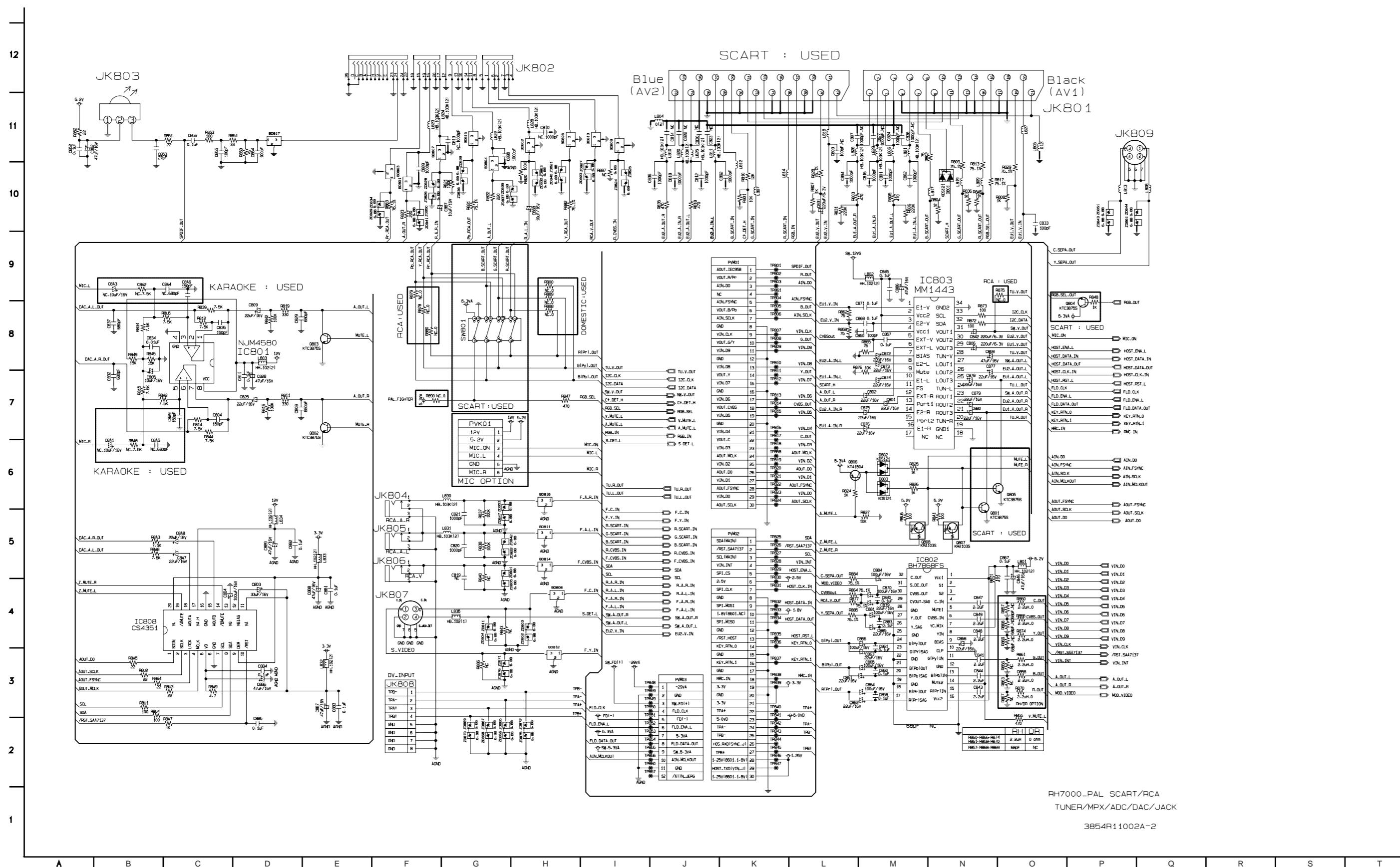
3-53

3-54

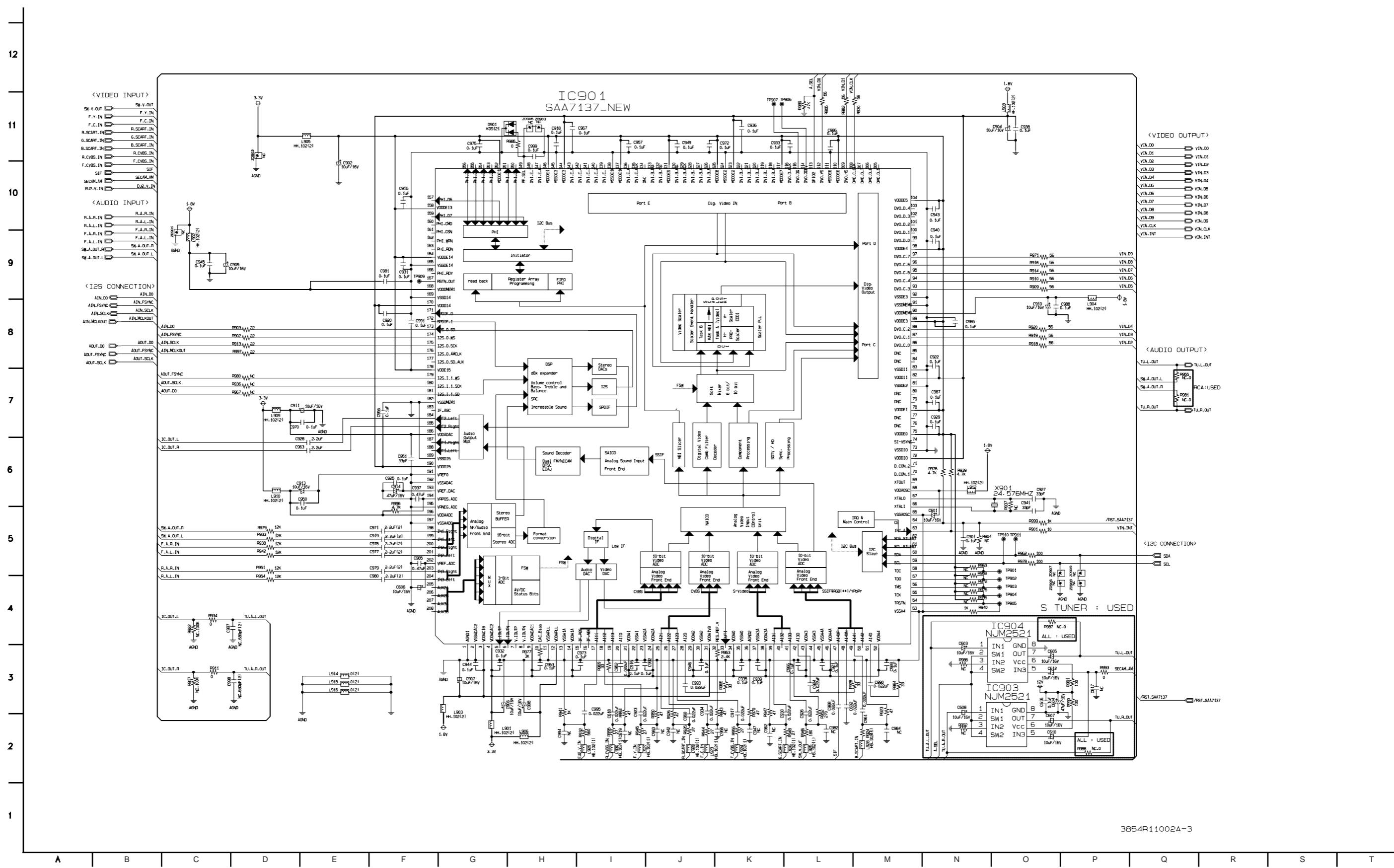
7. I/O, μ-COM CIRCUIT DIAGRAM



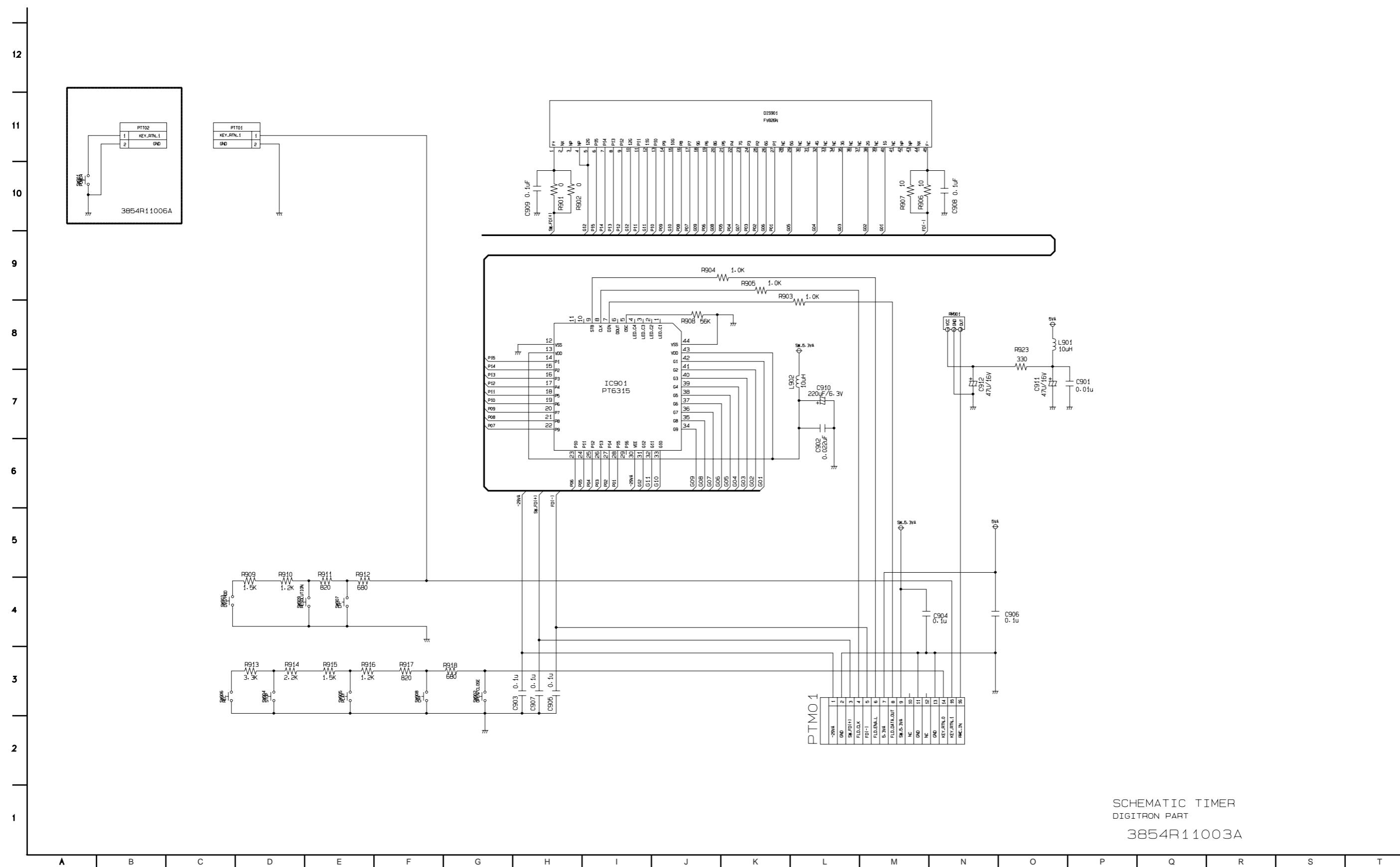
8. TUNER/MPX/ADC/DAC/JACK CIRCUIT DIAGRAM



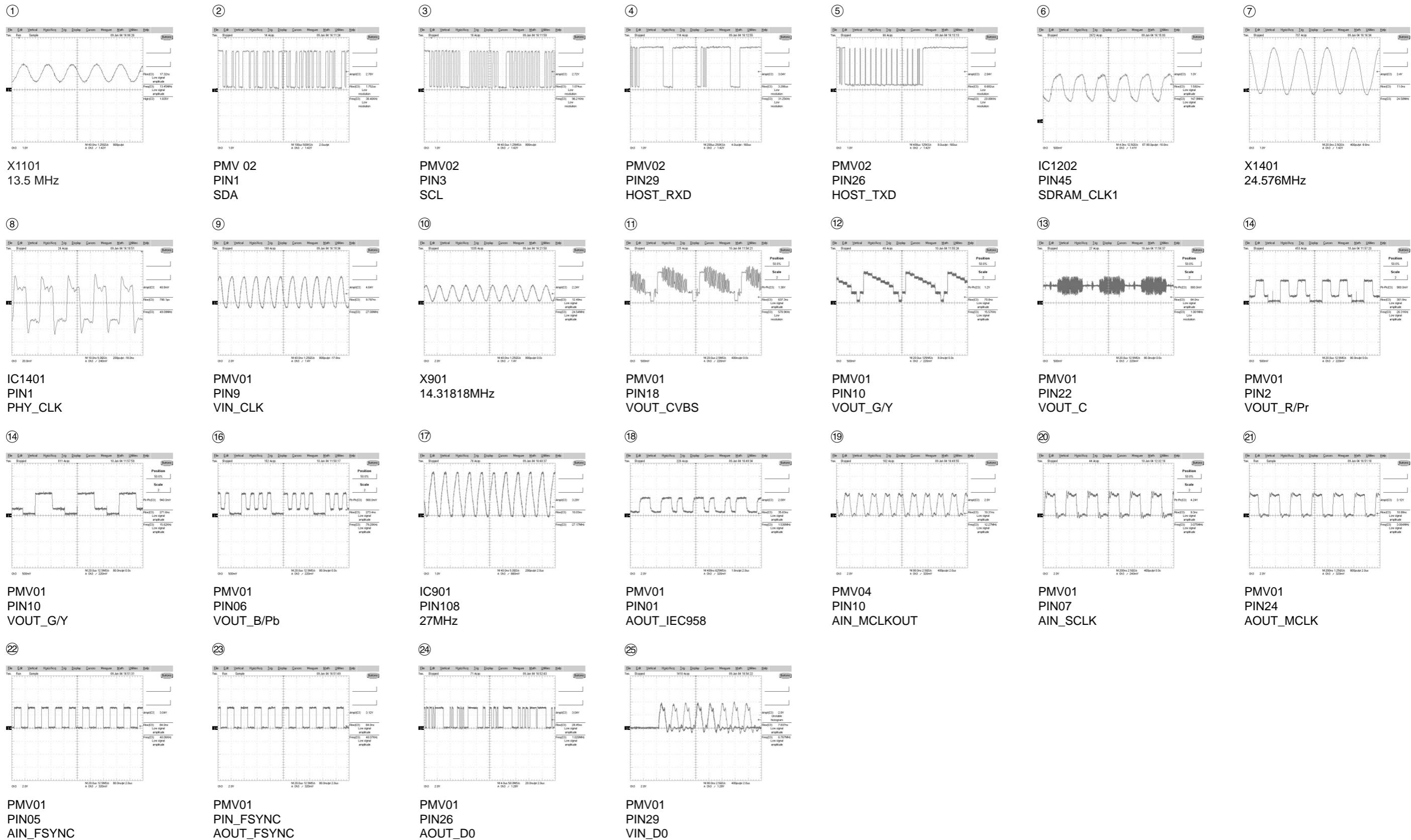
9. DECODER CIRCUIT DIAGRAM



10. TIMER CIRCUIT DIAGRAM



• WAVEFORMS



• CIRCUIT VOLTAGE CHART

MODE PIN NO.	EE	PB	REC
IC101 LSI DMN8653			
A1	1.17	1.19	1.17
A2	0.00	0.00	0.00
A3	1.37	1.36	1.36
A4	0.00	0.00	0.00
A5	1.61	1.64	1.64
A6	1.60	1.62	1.62
A7	1.58	1.61	1.59
A8	3.20	3.23	3.21
A9	1.25	0.67	1.25
A10	1.58	1.62	1.60
A11	1.25	0.67	1.25
A12	1.08	0.94	1.06
A13	1.10	1.08	1.10
A14	NC	NC	NC
A15	3.22	3.23	3.20
A16	1.63	1.64	1.62
A17	1.63	1.64	1.98
A18	1.63	1.64	1.63
A19	0.00	0.00	0.00
A20	3.24	3.24	3.23
A21	0.00	0.00	0.00
A22	0.00	0.00	0.00
A23	0.00	0.00	0.00
A24	3.23	3.24	3.22
A25	2.31	2.32	2.30
A26	3.21	3.21	3.21
B1	3.21	3.22	3.21
B2	0.27	0.27	0.27
B3	1.39	1.39	1.39
B4	1.22	1.22	1.21
B5	0.00	0.00	0.00
B6	1.16	1.21	1.18
B7	1.01	1.07	1.01
B8	3.20	3.20	3.19
B9	1.18	1.22	1.18
B10	1.02	1.07	1.02
B11	1.19	1.22	1.18
B12	1.22	1.25	1.24
B13	2.17	2.14	2.17
B14	1.60	1.61	1.59
B15	1.61	1.62	1.60
B16	1.62	1.62	1.61
B17	0.00	0.00	0.00
B18	0.00	0.00	0.00
B19	4.91	4.91	4.91
B20	3.12	3.14	3.14
B21	2.75	2.80	2.84
B22	4.80	4.90	4.82
B23	3.08	3.11	3.10
B24	3.21	3.21	3.21
B25	3.20	3.23	3.21
B26	3.21	3.23	3.21
C1	2.24	2.25	2.25
C2	2.67	2.67	2.66

MODE PIN NO.	EE	PB	REC
IC101 LSI DMN8653			
C3	1.32	1.32	1.32
E4	2.01	1.97	1.97
E5	1.21	1.18	1.18
E6	1.18	1.18	1.16
F1	0.00	0.00	0.00
F2	0.00	0.00	0.00
F3	0.00	0.00	0.00
F4	1.05	1.05	1.06
F5	1.19	1.16	1.15
F6	1.18	1.06	1.02
F7	2.03	1.98	1.98
F8	2.08	1.93	1.85
G1	1.48	1.49	1.49
G2	0.00	0.00	0.00
G3	1.05	1.05	1.06
G4	2.35	2.26	2.30
G5	1.17	1.16	1.15
G6	1.18	1.16	1.15
G7	1.17	1.16	1.15
G8	1.16	1.16	1.15
H1	1.69	1.72	1.73
H2	0.00	0.00	0.00
H3	3.23	3.24	3.22
H4	1.06	1.05	1.06
H5	2.10	2.05	2.05
H6	1.14	1.09	1.06
H7	1.12	1.08	1.05
H8	1.17	1.14	1.15
J1	0.00	0.00	0.00
J2	1.58	1.58	1.58
J3	1.58	1.58	1.58
J4	3.23	3.23	3.22
J5	1.18	1.18	1.18
J6	1.11	1.11	1.11
J7	1.11	1.11	1.11
J8	1.11	1.11	1.11
J9	1.11	1.11	1.11
J10	1.81	1.78	1.81
J11	1.11	1.11	1.11
J12	1.11	1.11	1.11
K1	0.63	0.61	0.64
K2	0.64	0.62	0.64
K3	3.21	3.22	3.20
K4	1.08	1.05	1.05
K5	1.11	1.11	1.11
K6	1.13	1.13	1.13
K7	1.13	1.13	1.13
K8	1.13	1.13	1.13
L1	0.72	0.72	0.72
L2	0.73	0.73	0.64
L3	3.21	3.22	3.20
L4	1.08	1.05	1.05
L5	0.00	0.00	0.00
L6	0.00	0.00	0.00
L7	0.00	0.00	0.00
L8	0.00	0.00	0.00
L9	0.00	0.00	0.00
L10	0.00	0.00	0.00
L11	0.00	0.00	0.00
L12	0.00	0.00	0.00
L13	0.00	0.00	0.00
L14	0.00	0.00	0.00
L15	0.00	0.00	0.00
L16	0.00	0.00	0.00
L17	0.00	0.00	0.00
L18	0.00	0.00	0.00
L19	0.00	0.00	0.00
L20	0.00	0.00	0.00
L21	0.00	0.00	0.00
L22	0.00	0.00	0.00
L23	0.00	0.00	0.00
L24	0.00	0.00	0.00

MODE PIN NO.	EE	PB	REC
IC101 LSI DMN8653			
E1	0.89	0.40	0.85
E2	2.23	2.24	2.24
E3	2.25	2.26	2.25
E4	0.00	0.00	0.00
E5	2.35	2.29	2.30
E6	1.12	1.08	1.04
E7	1.14	1.09	1.03
E8	1.14	1.09	1.08
E9	0.00	0.00	0.00
E10	0.00	0.00	0.00
E11	0.00	0.00	0.00
E12	0.00	0.00	0.00
E13	0.00	0.00	0.00
E14	0.00	0.00	0.00
E15	0.00	0.00	0.00
E16	0.00	0.00	0.00
E17	0.00	0.00	0.00
E18	0.00	0.00	0.00
E19	0.00	0.00	0.00
E20	0.00	0.00	0.00
E21	0.00	0.00	0.00
E22	0.00	0.00	0.00
E23	0.00	0.00	0.00
E24	0.00	0.00	0.00

MODE PIN NO.	EE	PB	REC
IC101 LSI DMN8653			
L1	0.72	0.72	0.72
L2	0.73	0.73	0.64
L3	3.21	3.22	3.20
L4	1.08	1.05	1.05
L5	0.00	0.00	0.00
L6	0.00	0.00	0.00
L7	0.00	0.00	0.00
L8	0.00	0.00	0.00
L9	0.00	0.00	0.00
L10	0.00	0.00	0.00
L11	0.00	0.00	0.00
L12	0.00	0.00	0.00
L13	0.00	0.00	0.00
L14	0.00	0.00	0.00
L15	0.00	0.00	0.00
L16	0.00	0.00	0.00
L17	0.00	0.00	0.00
L18	0.00	0.00	0.00
L19	0.00	0.00	0.00
L20	0.00	0.00</	

MODE PIN NO.	EE	PB	REC
AF23	0.00	0.00	0.00
AF24	0.00	0.00	0.00
AF25	0.00	0.00	0.00
AF26	0.00	0.00	0.00
C151 KIA78R25			
1	3.25	3.24	3.23
2	2.52	2.52	2.52
3	0.00	0.00	0.00
4	4.88	4.88	4.88
IC152 KIA78R33			
1	3.64	3.63	3.62
2	3.28	3.28	3.28
3	0.00	0.00	0.00
4	4.43	4.42	4.41
IC153 KIA78R05			
1	5.28	5.28	5.28
2	4.97	4.97	4.97
3	0.00	0.00	0.00
4	4.88	4.88	4.88
IC154 IP1680			
1	3.25	3.25	3.25
2	3.25	3.25	3.25
3	2.55	2.56	2.55
4	1.12	1.12	1.12
5	1.71	1.72	1.71
6	0.00	0.00	0.00
7	1.25	1.25	1.25
8	3.25	3.25	3.25
IC701 MN101DF10GAF			
1	0.00	0.00	0.00
2	5.12	5.12	5.12
3	0.00	0.00	0.00
4	4.80	4.80	4.80
5	4.86	4.86	4.87
6	0.01	0.01	0.01
7	5.13	5.13	5.13
8	5.15	5.15	5.16
9	0.01	0.01	0.01
10	0.00	0.00	0.00
11	5.16	5.16	5.16
12	0.00	0.00	0.00
13	5.15	5.15	5.16
14	4.82	4.83	4.89
15	0.04	0.04	0.04
16	0.12	0.11	0.08
17	5.05	5.05	5.05
18	5.06	5.06	5.06
19	5.10	5.10	5.11
20	4.91	4.90	4.90
21	0.00	0.00	0.00
22	0.01	0.01	0.01
23	0.01	0.01	0.01
24	0.33	0.31	0.14
25	5.16	5.16	5.16
26	0.05	0.05	0.05

MODE PIN NO.	EE	PB	REC
27	0.00	0.00	0.22
28	5.16	5.16	5.16
29	0.01	0.01	0.01
30	0.01	0.01	0.01
C151 KIA78R25			
31	0.01	0.01	0.01
32	0.01	0.01	0.01
33	0.31	0.32	0.15
34	0.33	0.32	0.16
35	0.01	0.01	0.01
36	5.16	5.16	5.16
37	2.53	2.52	2.54
38	2.43	2.42	2.42
39	0.00	0.00	0.00
40	1.99	1.95	1.95
41	2.55	2.55	2.55
42	0.00	0.00	0.00
43	4.88	4.89	4.89
44	4.89	4.90	4.90
45	0.01	0.01	0.01
46	0.64	0.63	0.63
IC152 KIA78R33			
47	0.01	0.01	0.01
48	0.00	0.00	0.00
49	1.04	1.04	1.04
50	2.78	2.90	2.89
51	5.13	5.13	5.13
52	3.01	3.04	3.05
53	2.54	2.54	2.54
54	5.10	5.10	5.10
55	5.10	5.10	5.10
56	0.01	0.01	0.01
57	5.09	5.09	5.09
IC703 MM1510			
58	5.00	5.00	5.01
59	5.25	5.02	5.00
60	0.00	0.00	0.00
61	4.94	4.94	4.93
IC704 KIA7042			
62	0.00	0.00	0.00
63	4.80	4.80	4.80
IC706 KIA7031			
64	5.17	5.17	5.17
65	0.00	0.00	0.00
66	5.17	5.16	5.16
IC705 24LC16B			
67	0.00	0.00	0.00
68	0.82	0.81	0.81
69	4.83	4.82	4.82
70	0.00	0.00	0.00
71	0.00	0.00	0.00
72	0.00	0.00	0.00
73	5.25	5.25	5.25
74	0.00	0.00	0.00
75	0.00	0.00	0.00
76	0.01	0.01	0.00
77	0.01	0.01	0.01
78	0.01	0.01	0.01
79	0.01	0.01	0.01
80	0.01	0.01	0.01
81	0.01	0.01	0.01

MODE PIN NO.	EE	PB	REC
82	0.24	0.19	0.14
83	0.24	0.19	0.15
84	1.94	1.92	1.93
85	0.00	0.00	0.00
IC151 KIA78R25			
86	0.00	0.00	0.00
87	0.00	0.00	0.00
88	5.25	5.25	5.25
89	5.25	5.25	5.25
90	0.01	0.01	0.01
91	0.01	0.01	0.01
92	0.02	0.02	0.02
93	0.01	0.01	0.01
94	0.01	0.01	0.01
95	0.01	0.01	0.01
96	5.16	5.16	5.16
IC703 MM1510			
97	0.01	0.01	0.01
98	0.01	0.01	0.01
99	0.01	0.01	0.01
100	5.16	5.16	5.16
IC704 KIA7042			
101	2.24	2.24	2.24
102	2.37	2.37	2.38
103	5.13	5.13	5.13
104	1.87	1.87	1.87
105	0.00	0.00	0.00
106	5.13	5.13	5.13
107	3.01	3.04	3.05
108	2.54	2.54	2.54
109	5.10	5.10	5.10
110	5.10	5.10	5.10
111	0.01	0.01	0.01
112	4.80	4.80	4.80
IC706 KIA7031			
113	5.17	5.17	5.17
114	0.00	0.00	0.00
115	5.17	5.16	5.16
IC705 24LC16B			
116	0.00	0.00	0.00
117	0.00	0.00	0.00
118	0.00	0.00	0.00
119	0.00	0.00	0.00
120	0.00	0.00	0.00
121	0.00	0.00	0.00
122	0.00	0.00	0.00
123	0.00	0.00	0.00
124	0.00	0.00	0.00
125	0.00	0.00	0.00
126	0.00	0.00	0.00
127	0.00	0.00	0.00
128	0.00	0.00	0.00
129	0.00	0.00	0.00
130	0.00	0.00	0.00
131	0.00	0.00	0.00
132	0.00	0.00	0.00
133	0.00	0.00	0.00
134	0.00	0.00	0.00

MODE PIN NO.	EE	PB	REC
113	0.07	0.07	0.07
114	0.26	1.56	1.58
115	0.27	2.49	2.46
116	0.27	1.47	1.45
117	3.19	2.96	2.93
118	0.21	0.33	1.37
119	0.22	0.32	1.35
120	0.27	0.33	1.37
121	0.22	0.34	1.38
122	0.27	0.37	1.37
123	1.88	1.85	1.85
124	0.02	0.02	0.02
125	2.98	2.96	2.93
126	0.43	0.45	1.35
127	0.42	0.44	1.36
128	0.43	0.44	1.38
129	0.43	0.43	1.36
130	0.43	0.44	1.36
131	2.96	2.96	2.93
132	0.51	0.44	1.33
133	0.52	0.35	1.31
134	0.52	0.50	1.33
135	0.41	0.53	1.42
136	1.44	1.44	1.43
137	2.96	2.96	2.93
138	0.02	0.02	0.02
139	1.31	1.31	1.30
140	1.33	1.32	1.36
141	1.48	1.48	1.49
142	1.49	1.52	1.43
143	1.10	1.11	1.10
144	1.85	1.85	1.88
145	0.02	0.02	0.02
146	2.96	2.96	3.20
147	1.13	1.12	0.37
148	1.51	1.53	0.37
149	0.22	0.20	0.23
150	0.49	0.50	0.37
151	0.49	0.45	0.37
152	3.13	2.96	3.20
153	0.52	0.54	0.37
154	0.53	0.38	0.37
155	0.53	0.41	0.35
156	0.53	0.40	1.44
157	0.59	0.52	1.42
158	3.13	2.97	2.93
159	0.51	0.52	1.53
160	0.51	0.52	1.43
161	0.55	0.52	1.40
162	0.59	0.52	1.45
163	0.62	0.52	1.34
164	3.13	2.97	2.93
165	0.02	0.02	0.02
166	0.60	0.54	1.34
167	0.02	0.02	0.02

MODE PIN NO.	EE	PB	REC
168	1.88	1.87	1.05
169	0.02	0.02	0.02
170	1.85	1.85	1.87
171	1.50	1.56	1.88
172	1.35	1.37	1.68
173	1.27	1.32	1.10
174	1.49	1.49	1.49
175	1.51	1.51	1.49
176	2.98	2.99	2.95
177	2.95	2.95	2.92
178	2.96	2.97	2.93
179	1.50	1.50	1.48
180	1.50	1.51	1.49
181	1.52	1.50	1.48
182	0.02	0.02	0.02
183	0.01	0.01	0.01
184	1.45	1.46	1.46
185	1.45	1.46	1.45
186	2.96	2.97	2.93
187	1.46	1.46	1.45
188	1.46	1.46	1.46
189	0.02	0.02	0.02
190	1.85	1.82	1.85
191	0.00	0.00	0.00
192	0.00	0.00	0.00
193	1.45	1.47	1.44
194	2.96	2.97	2.93
195	0.00	0.00	0.00
196	2.96	2.97	2.93
197	0.00	0.00	0.00
198	1.44	1.44	1.43
199	1.44	1.45	1.43
200	1.44	1.44	1.42
201	1.44	1.45	1.43
202	1.48	1.48	1.46
203	1.44	1.44	1.43
204	1.44	1.44	1.43
205	0.56	1.46	1.16
206	0.57	1.45	1.25
207	0.57	1.46	1.24
208	0.57	1.64	1.12

MODE PIN NO.	EE	PB	REC
5	7.22	7.21	7.19
6	10.70	10.69	10.67
7	7.46	7.45	7.43
8	0.00	0.00	0.00

Q151 STB1132 : 5.2V

E	5.27	5.27	5.27
C	5.21	5.21	5.21
B	4.52	4.52	4.52

Q155 KTA1271 : 5VT

E	5.26	5.27	5.27
C	5.18	5.18	5.18
B	4.48	4.49	4.48

Q158 KTA1268 : SW_5.3VA

E	5.26	5.26	5.26
C	5.20	5.20	5.20
B	4.56	4.56	4.55

Q161 KTA1268 : 33VT

E	33.90	33.90	34.00
C	33.80	33.80	33.90
B	33.40	33.30	33.30

Q164 KTC3202 : 12V

E	12.12	12.12	12.12
C	12.99	12.99	12.99
B	12.74	12.74	12.74

Q163 KTC3203 : SW_FD(+)

E	-19.30	-19.30	-19.30
C	-19.20	-19.20	-19.20
B	-18.60	-18.60	-18.60

Q165 KTC3203 : SW_12VG

E	12.16	16.16	12.15
C	12.99	12.98	12.99
B	112.79	12.79	12.80

IC903 NJM2521

1	7.10	7.09	7.08
2	0.01	0.01	0.01
3	7.22	7.20	7.18
4	0.01	0.01	0.01
5	7.22	7.21	7.19
6	10.70	10.69	10.69
7	7.46	7.45	7.43
8	0.00	0.00	0.00

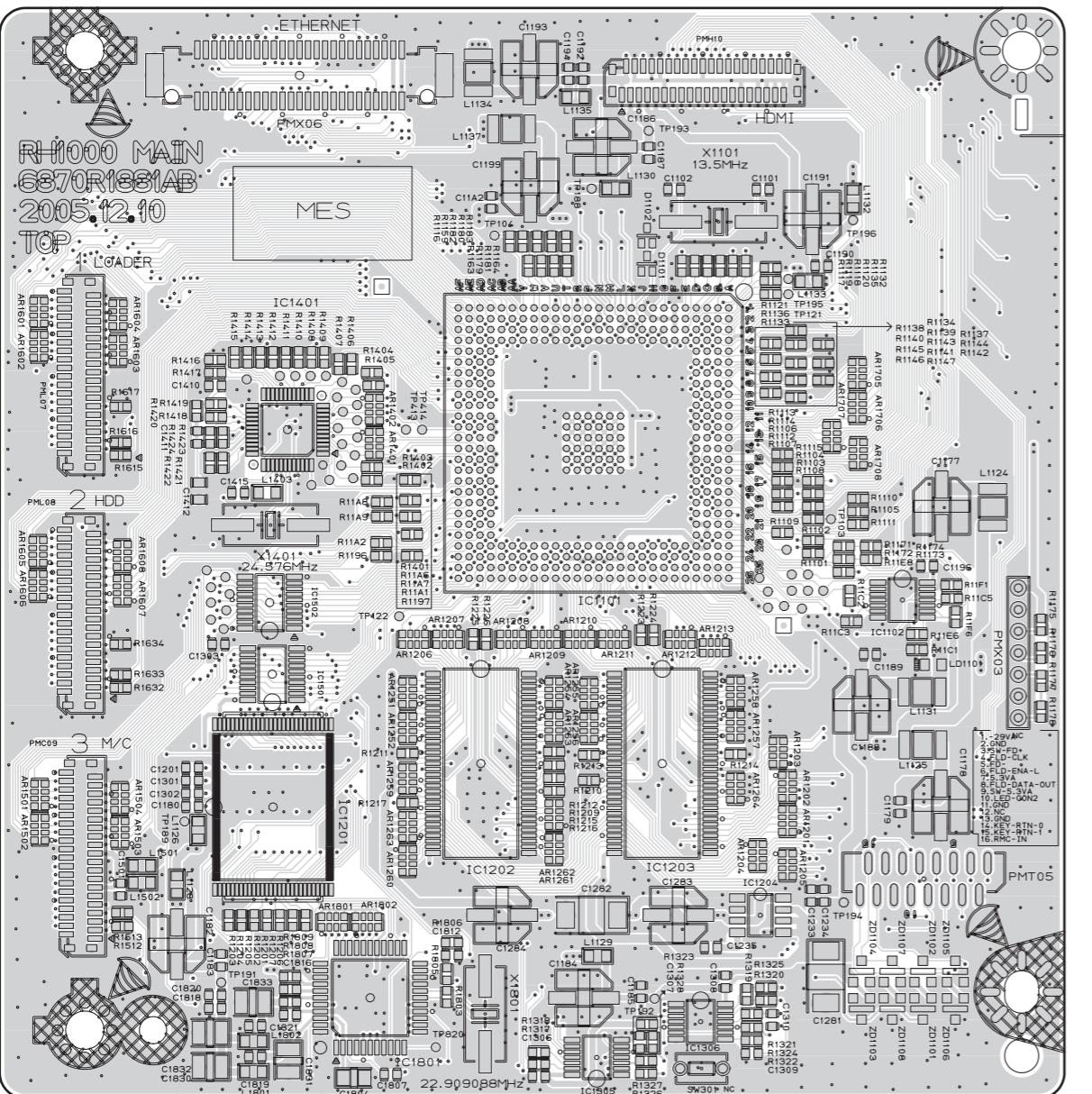
IC904 NJM2521

1	7.10	7.09	7.07
2	0.01	0.01	0.01
3	7.22	7.21	7.19
4	0.01	0.01	0.01

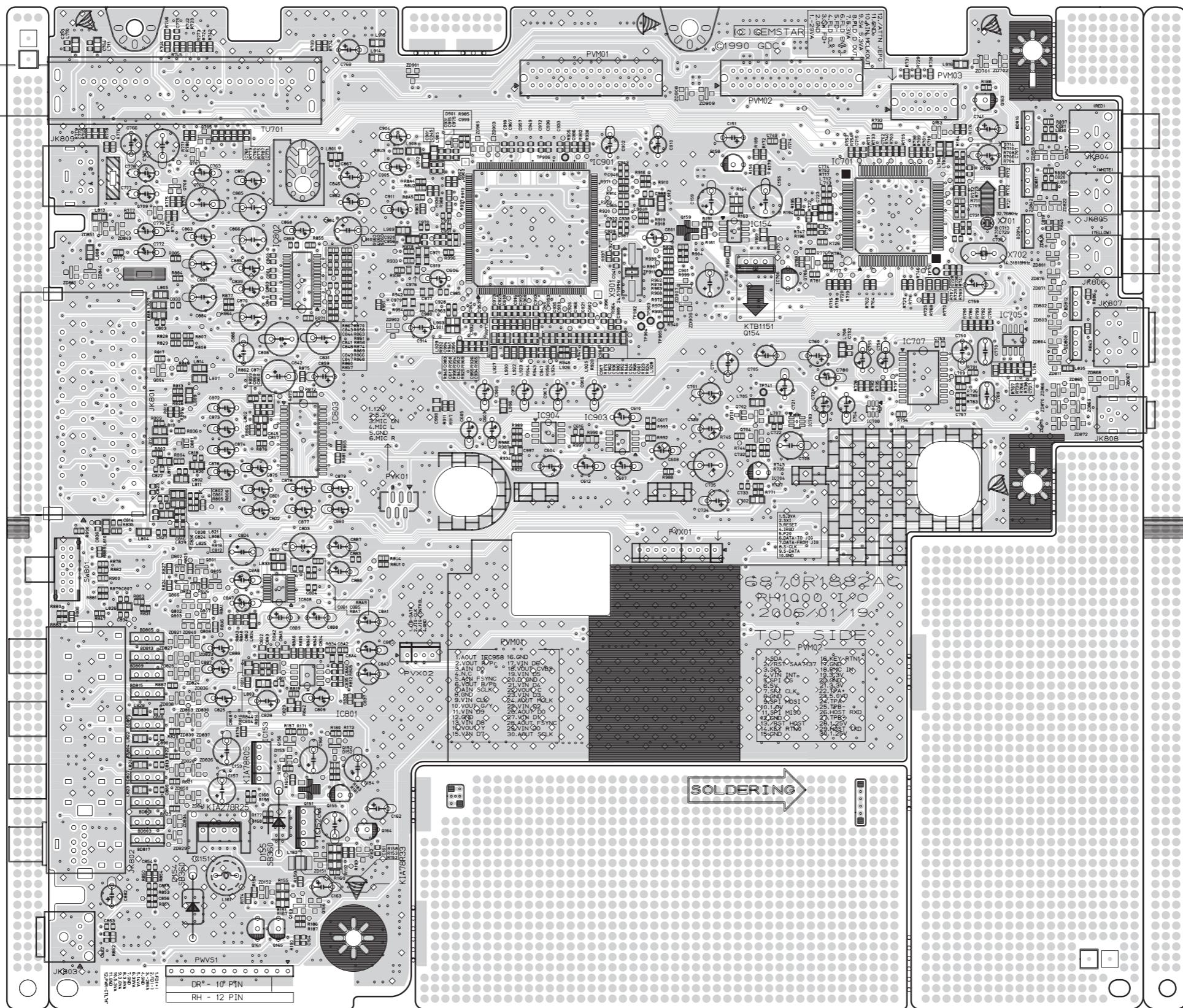
PRINTED CIRCUIT DIAGRAMS

1. MAIN P.C.BOARD

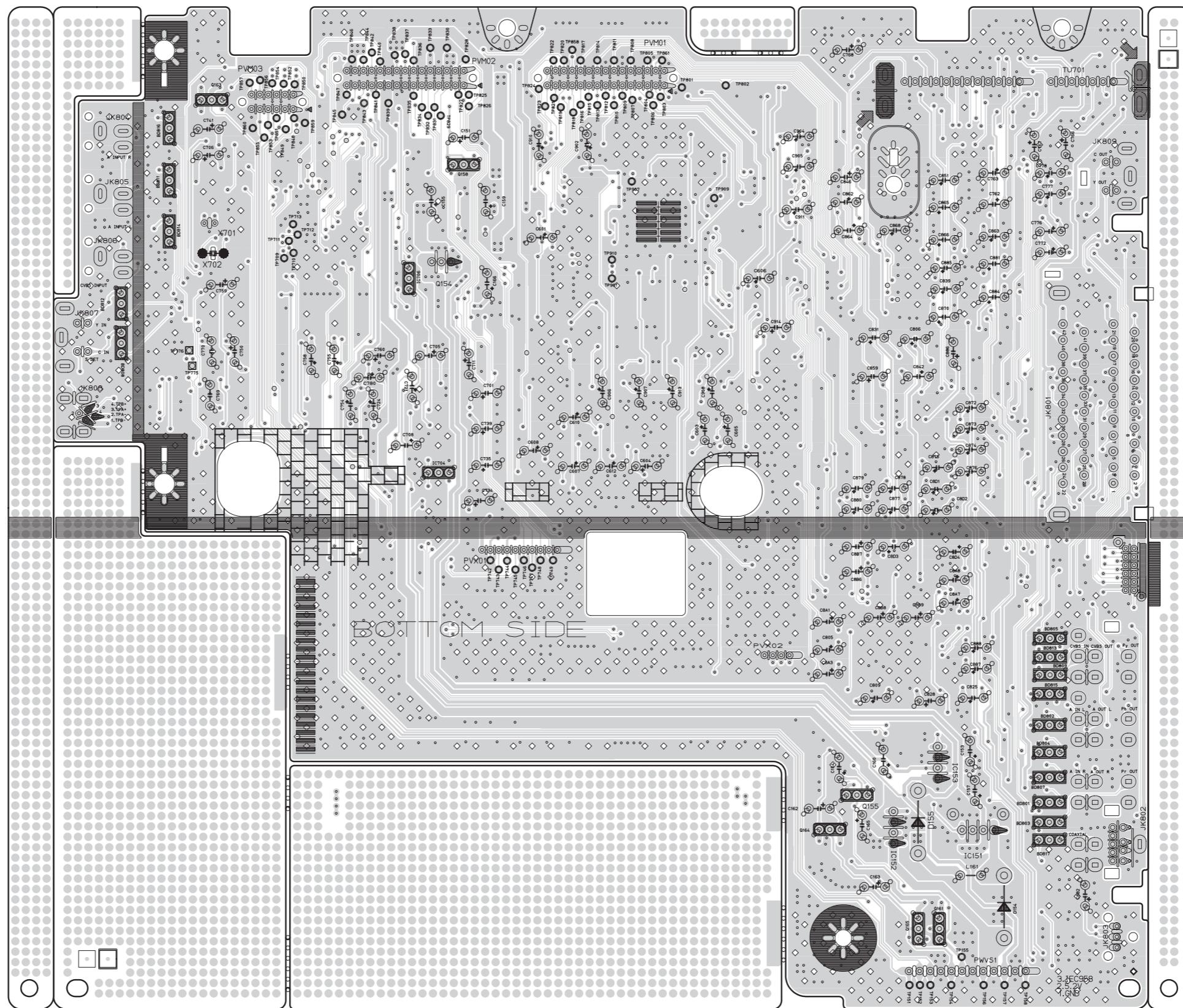
(TOP VIEW)



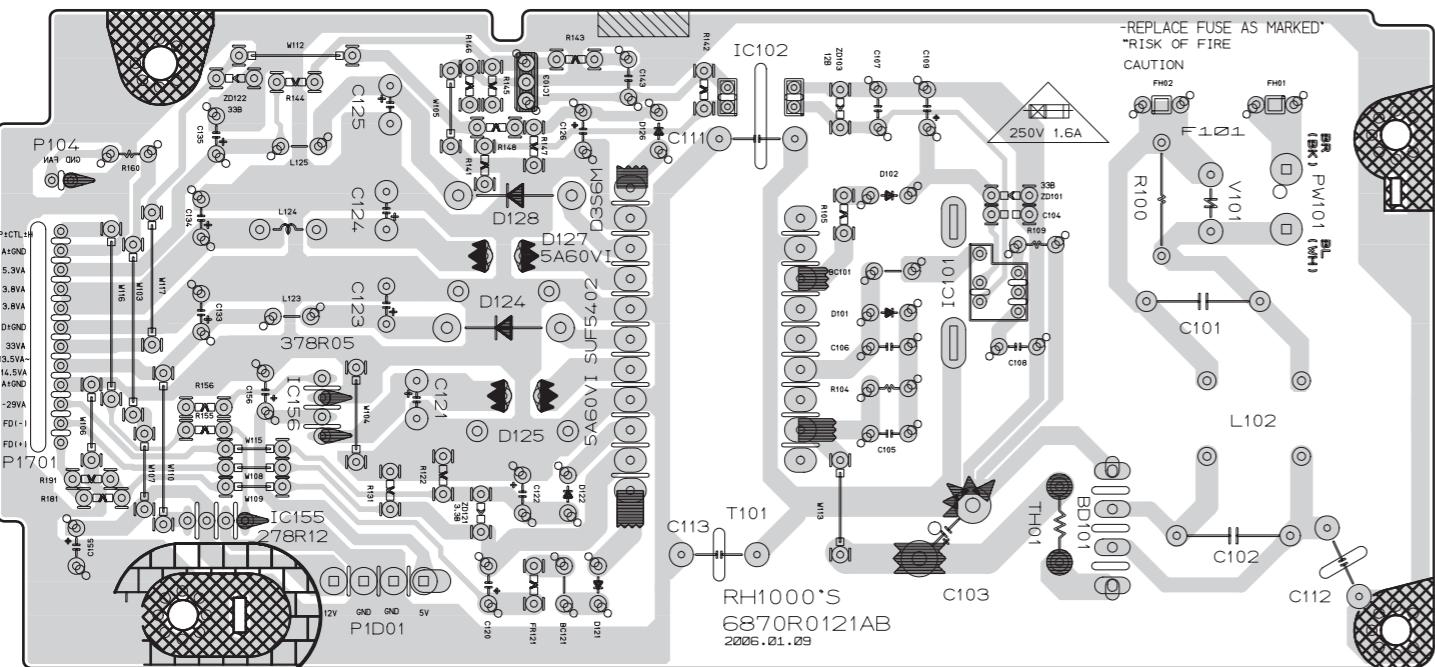
2. I/O P.C.BOARD (TOP VIEW)



**2. I/O P.C.BOARD
(BOTTOM VIEW)**



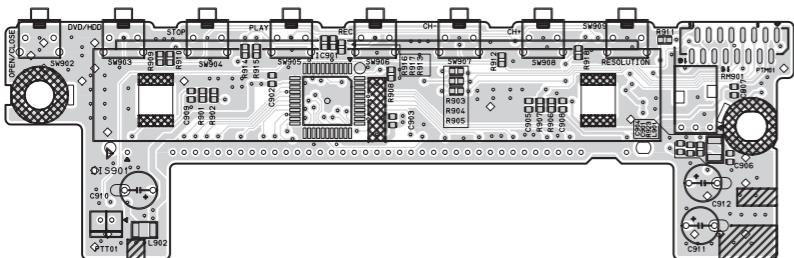
3. POWER P.C.BOARD



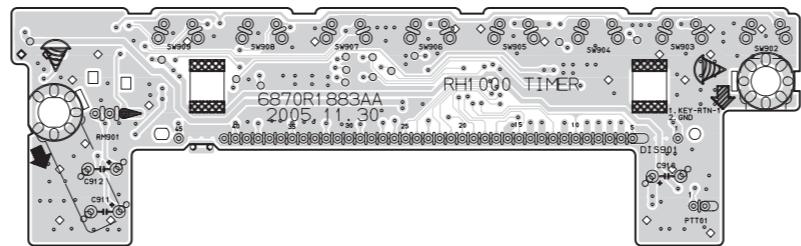
4. TIMER1 P.C.BOARD

(6, 7, 8 TOOL)

(TOP VIEW)

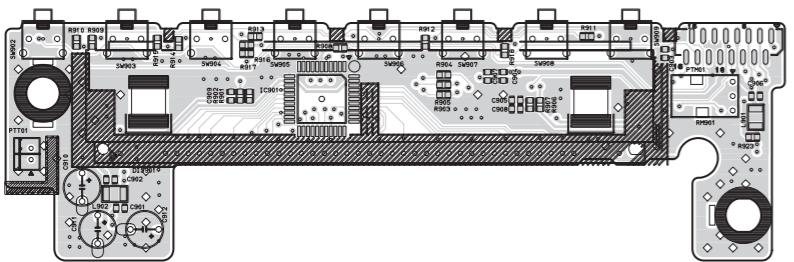


(BOTTOM VIEW)

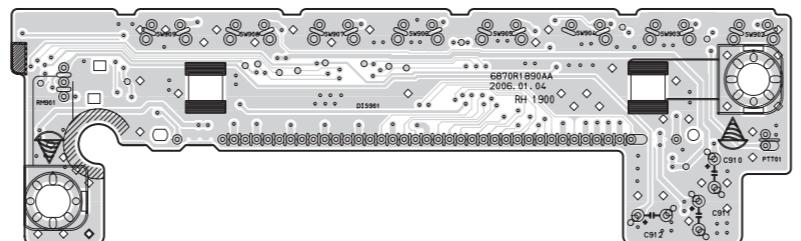


(9TOOL)

(TOP VIEW)

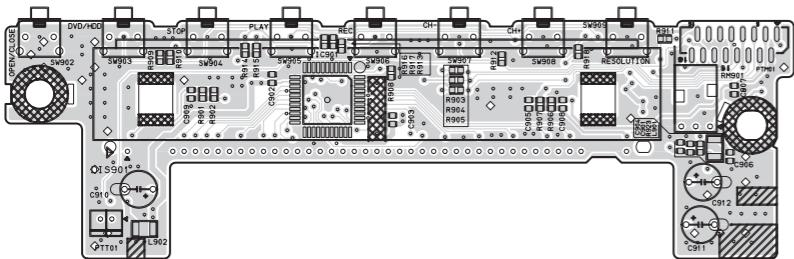


(BOTTOM VIEW)

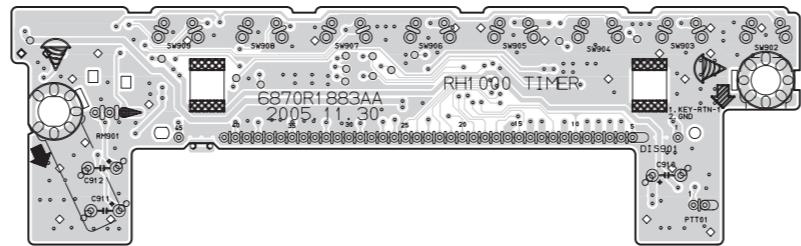


(F TOOL)

(TOP VIEW)



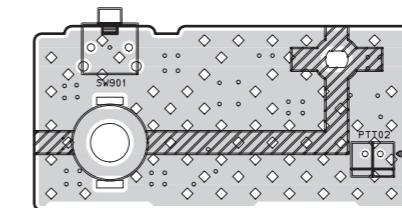
(BOTTOM VIEW)



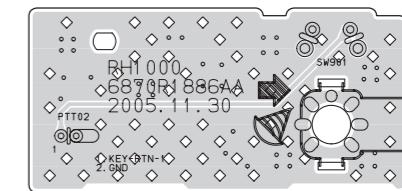
5. TIMER2 P.C.BOARD (6, 7, 8 TOOL)

(6, 7, 8 TOOL)

(TOP VIEW)

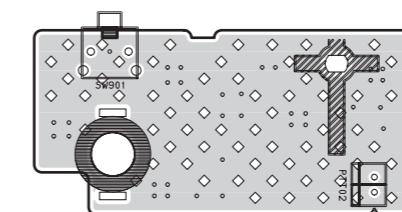


(BOTTOM VIEW)

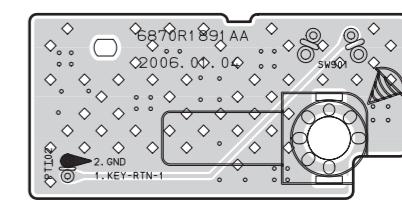


(9TOOL)

(TOP VIEW)

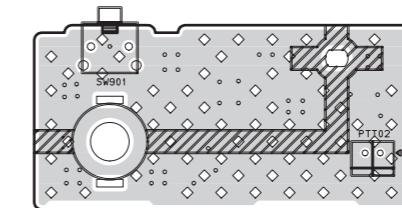


(BOTTOM VIEW)

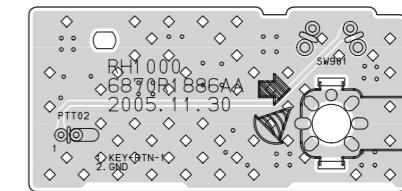


(F TOOL)

(TOP VIEW)

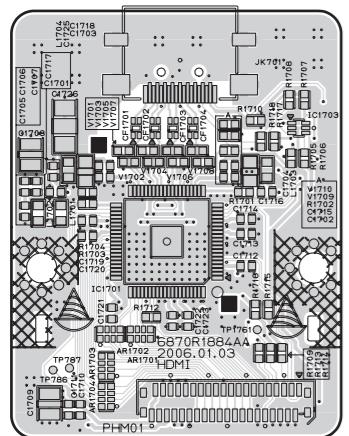


(BOTTOM VIEW)

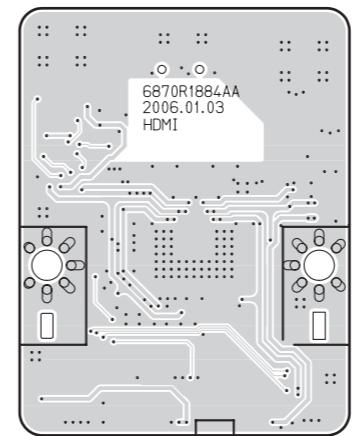


6. HDMI P.C.BOARD(OPTIONAL PARTS)

(TOP VIEW)



(BOTTOM VIEW)



MEMO

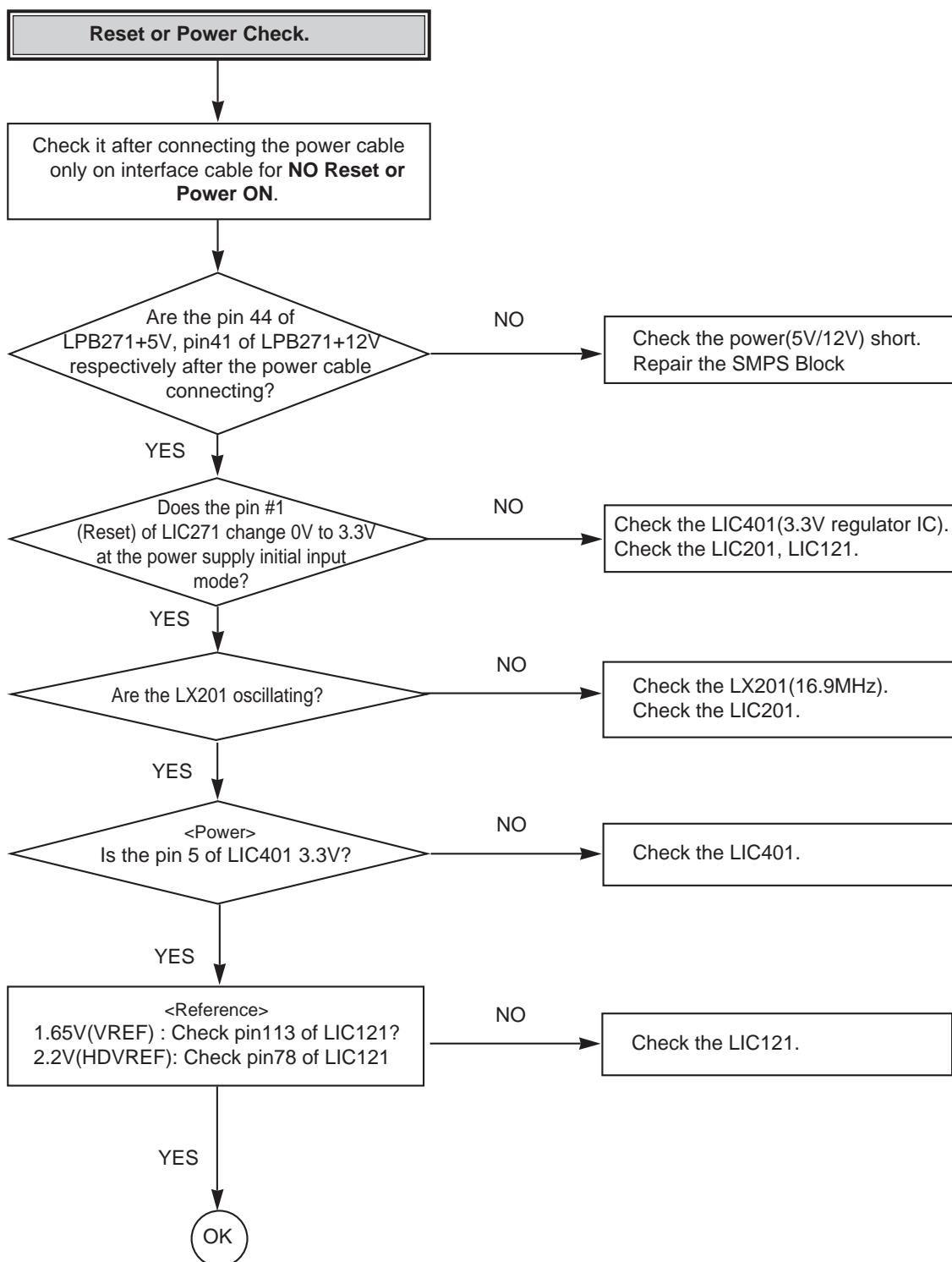
SECTION 4

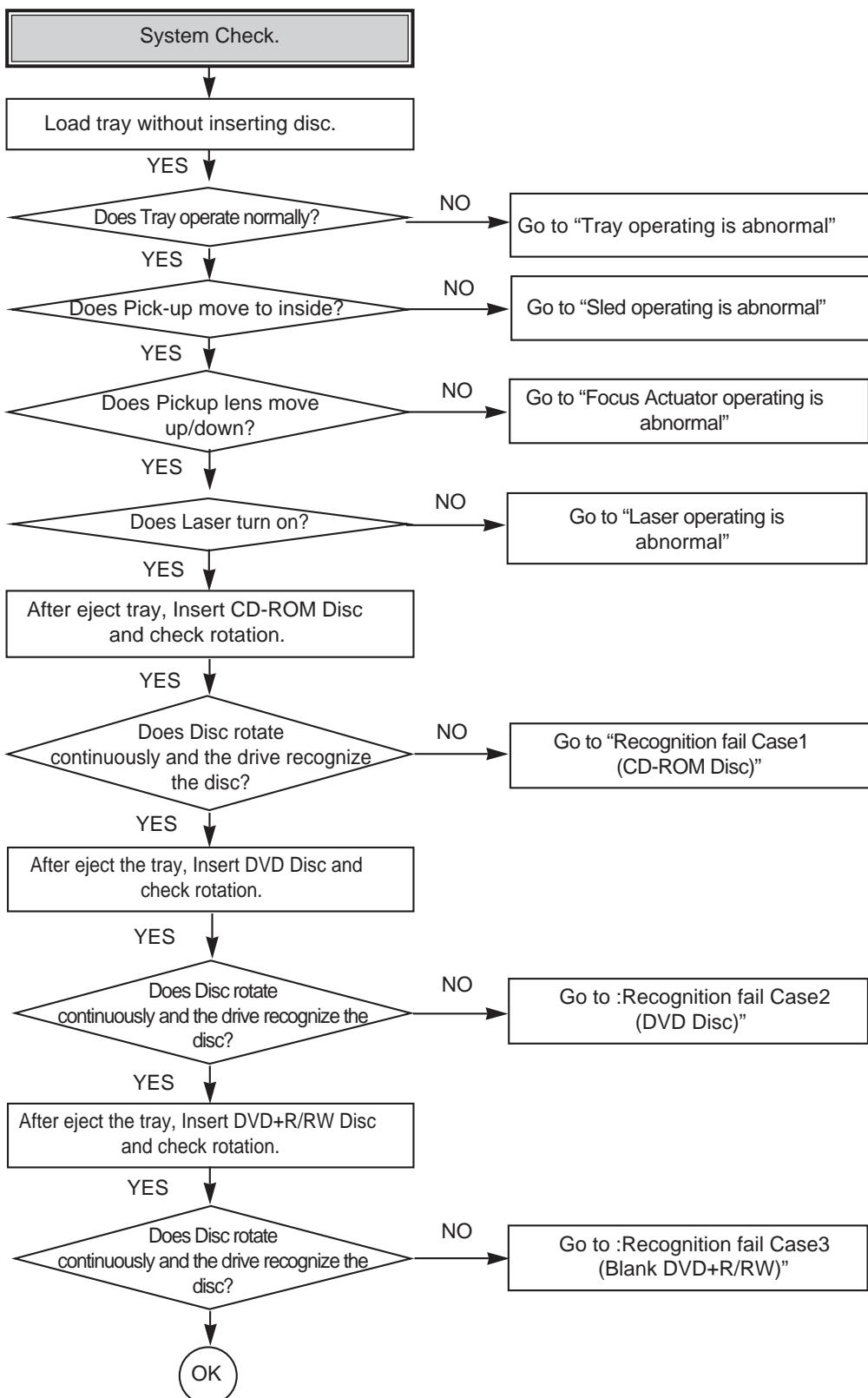
RS-01A LOADER PART

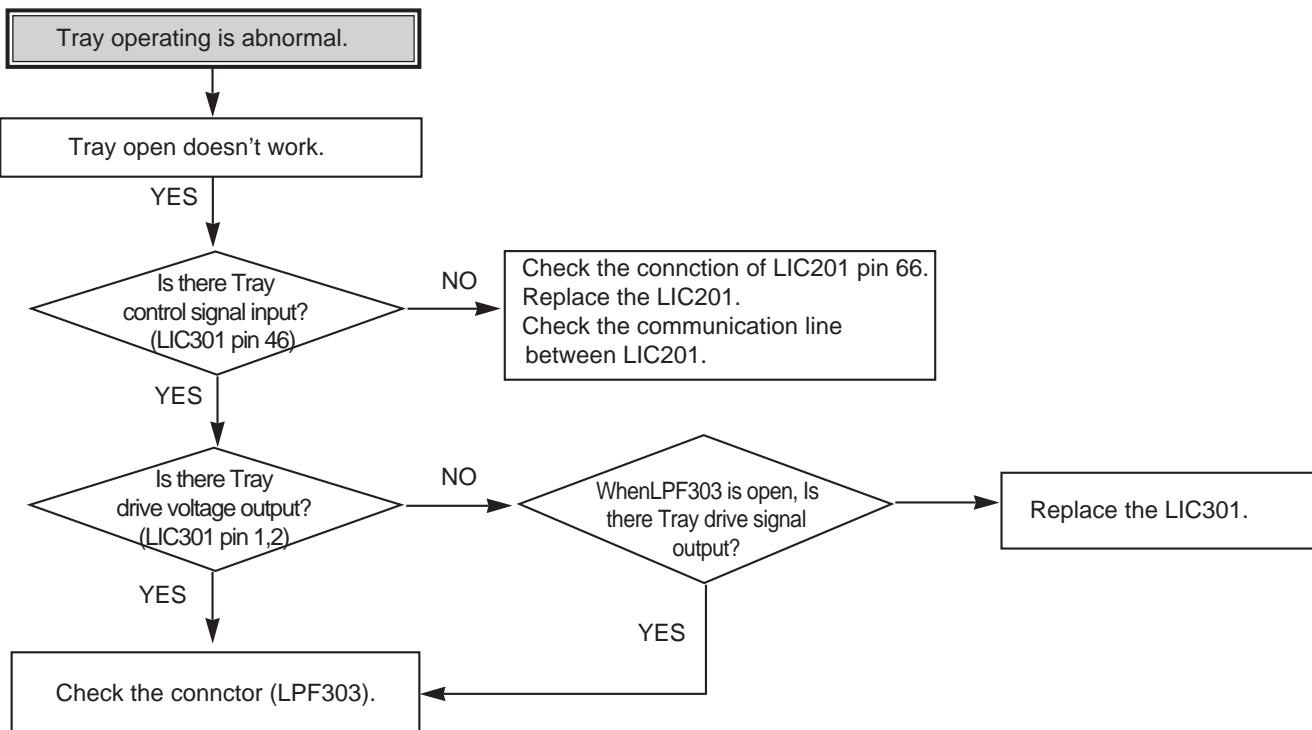
CONTENTS

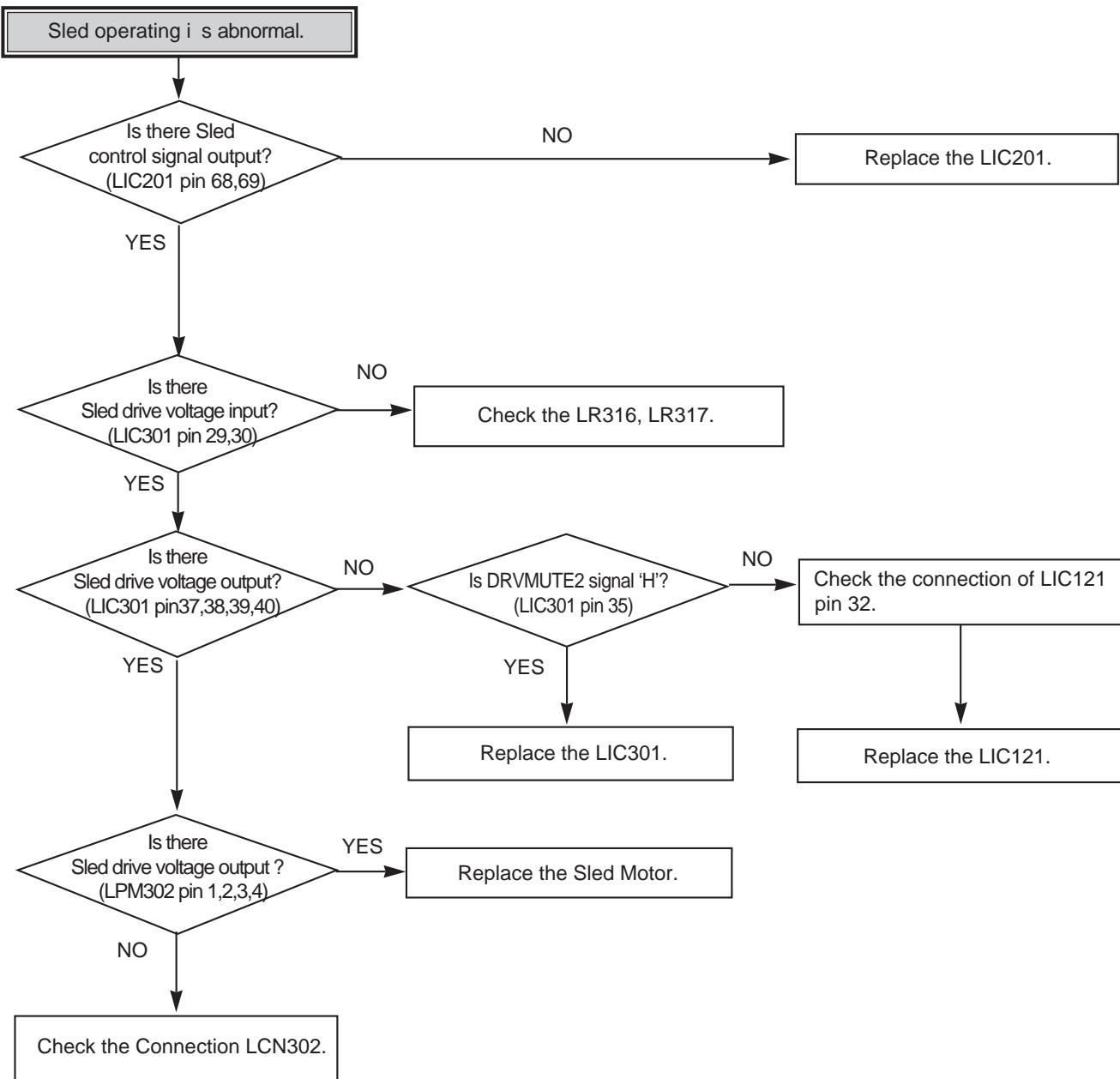
ELECTRICAL TROUBLESHOOTING GUIDE	4-2
THE DIFFERENCE OF DVD-R/RW, DVD+R/RW DISCS AND DVD-ROM	4-15
1. RECORDING LAYER	4-15
2. DISC SPECIFICATION	4-16
3. DISC MATERIALS	4-16
4. ORGANIZATION OF THE INNER DRIVE AREA, OUTER DRIVE AREA, LEAD-IN ZONE AND LEAD-OUT ZONE	4-20
HOW TO USE TEST TOOL	4-24
1. ALPC MEASUREMENT SYSTEM	4-24
2. ALPC PROGRAM	4-24
3. EXECUTE ALPC PROGRAM	4-25
4. OPTICAL POWER SETTING	4-27
5. CONFIRM OPTICAL POWER SETTING PARAMETER	4-28
6. OPTICAL POWER SETTING PARAMETER RANGE	4-29
7. ATTACHMENT. OPTICAL POWER MEASUREMENT	4-30
INTERNAL STRUCTURE OF THE PICK-UP	4-31
1. BLOCK DIAGRAM OF THE PICK-UP(LPC-812R)	4-31
2. PICK UP PIN ASSIGNMENT	4-32
3. SIGNAL DETECTION OF THE P/U	4-33
DESCRIPTION OF CIRCUIT	4-34
1. ALPC (AUTOMATIC LASER POWER CONTROL) CIRCUIT	4-34
2. FOCUS/TRACKING/SLED SERVO CIRCUIT	4-35
3. SPINDLE SERVO CIRCUIT	4-37
MAJOR IC INTERNAL BLOCK DIAGRAM	4-38
LIC121 (AN22113A) : FEP(RF) ANALOG SIGNAL PROCESSOR	4-38
IC301 (BD7956FS) : CD-ROM/DVD-ROM 7CH POWER DRIVER	4-41
CIRCUIT DIAGRAMS	4-42
1. DSP CIRCUIT DIAGRAM	4-42
2. RF CIRCUIT DIAGRAM	4-44
3. DRIVE CIRCUIT DIAGRAM	4-46
CIRCUIT VOLTAGE CHART	4-48
PRINTED CIRCUIT DIAGRAMS	4-50
1. MAIN P.C.BOARD(TOP VIEW)	4-50
2. MAIN P.C.BOARD(BOTTOM VIEW)	4-52

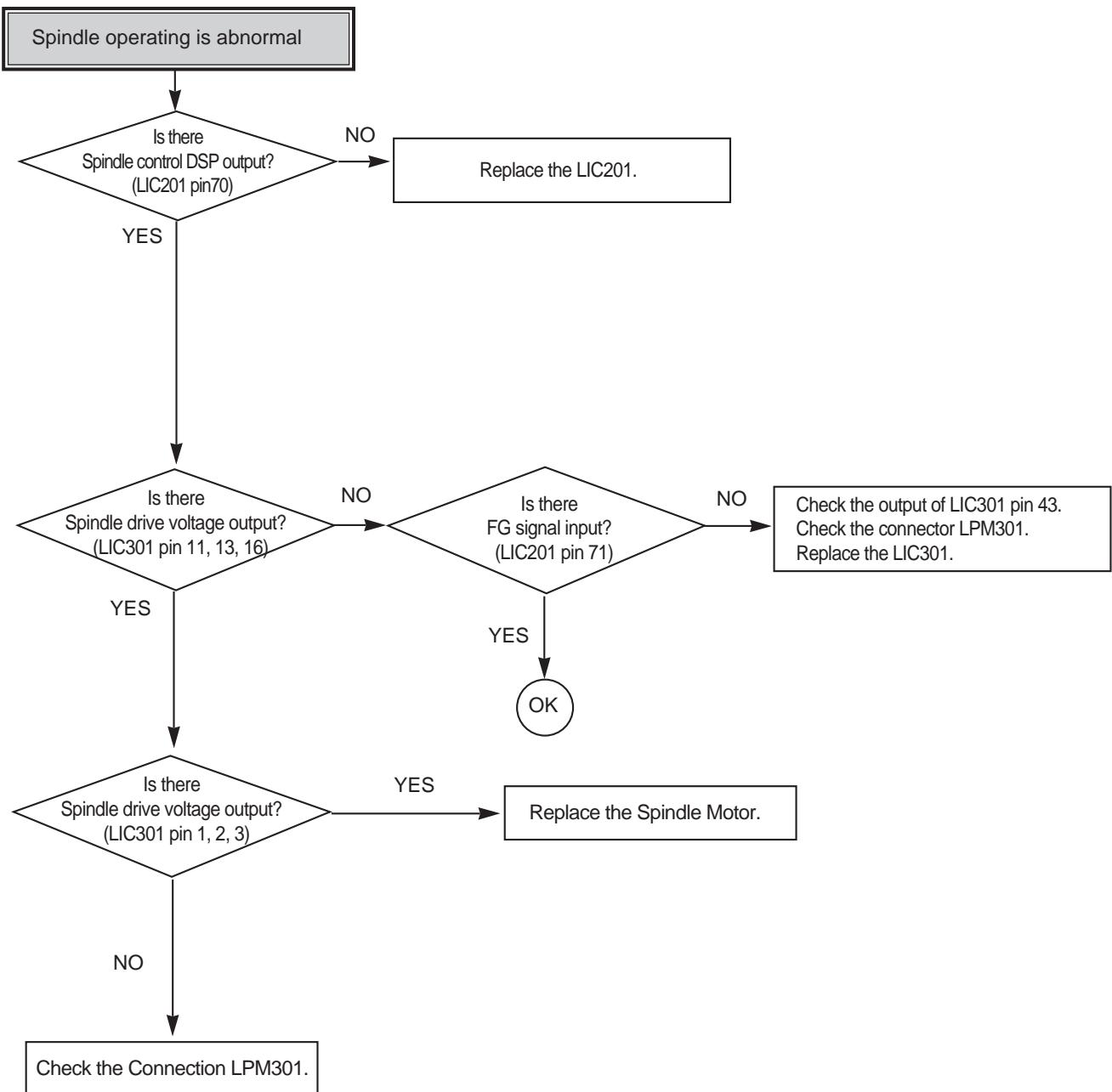
ELECTRICAL TROUBLESHOOTING GUIDE

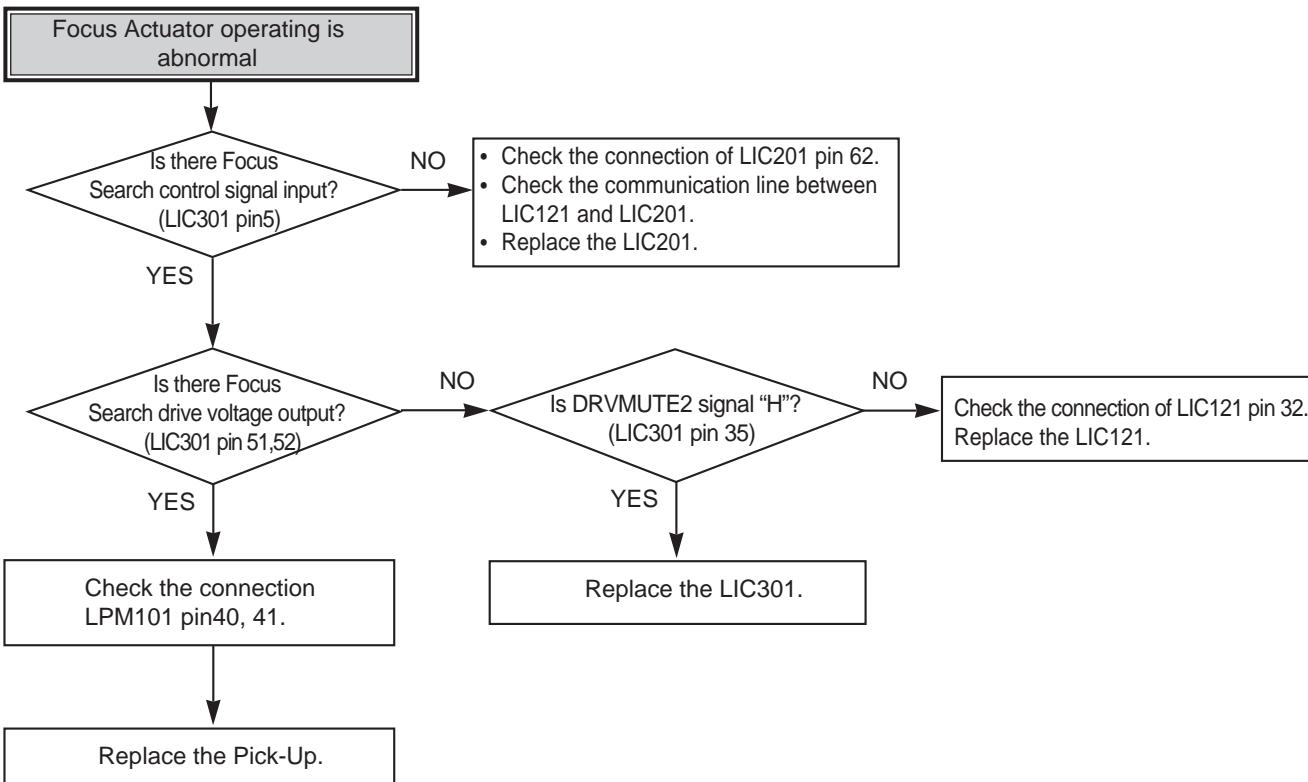
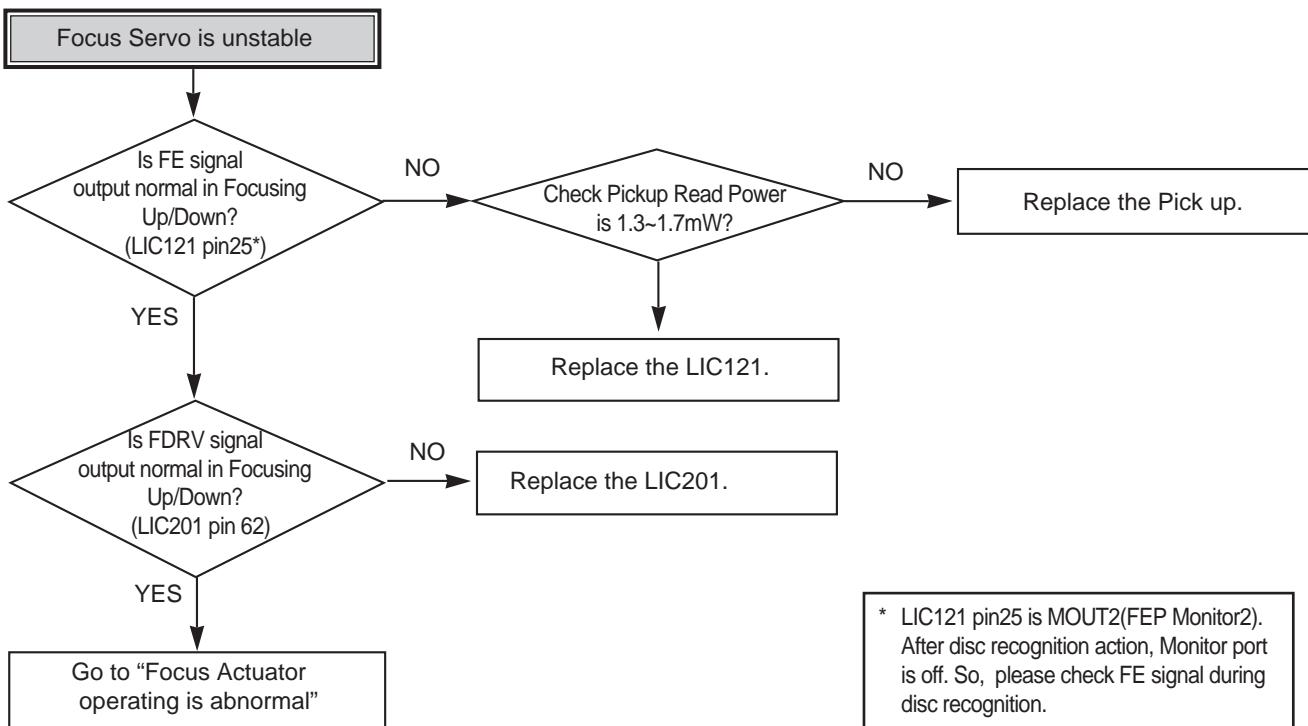


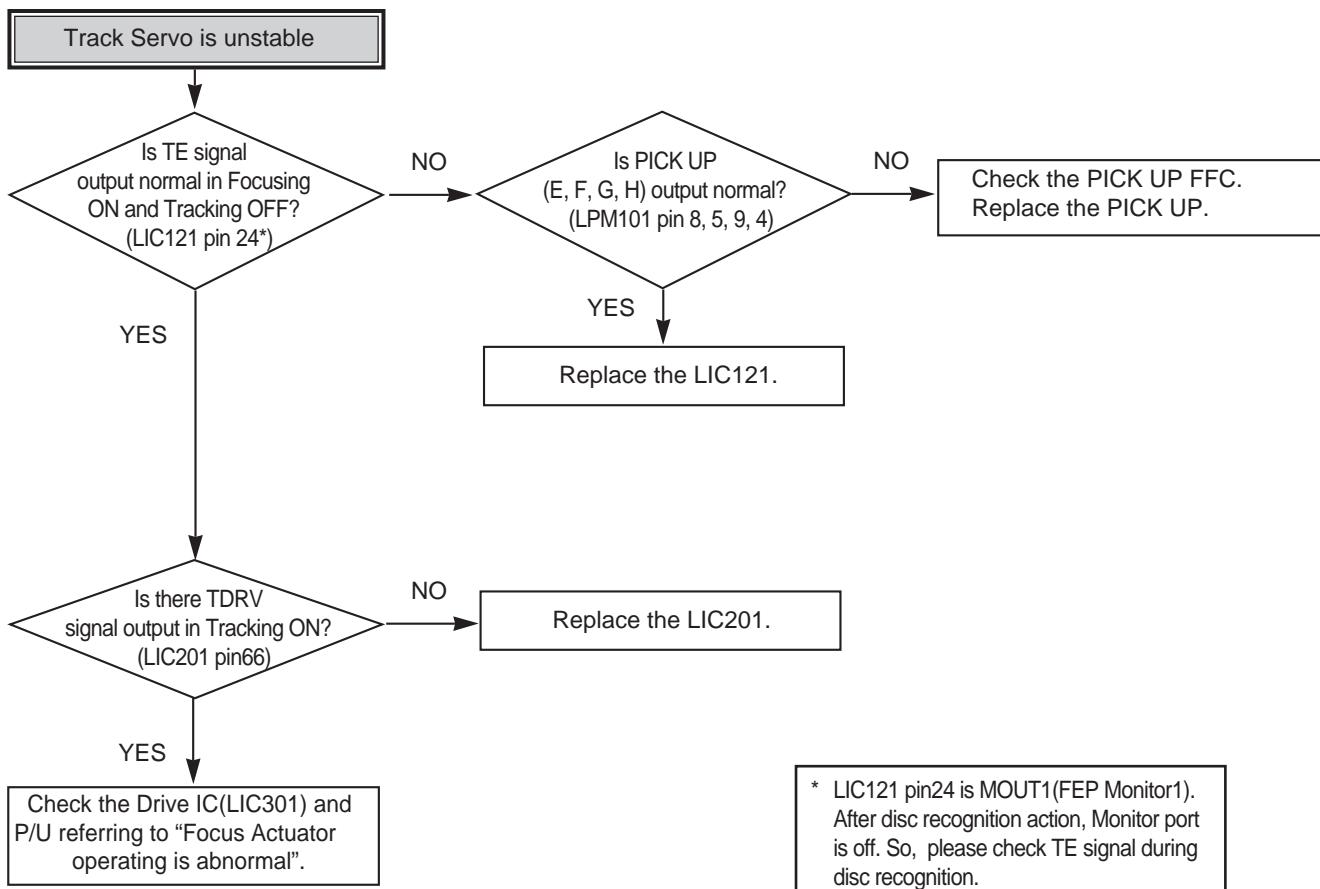




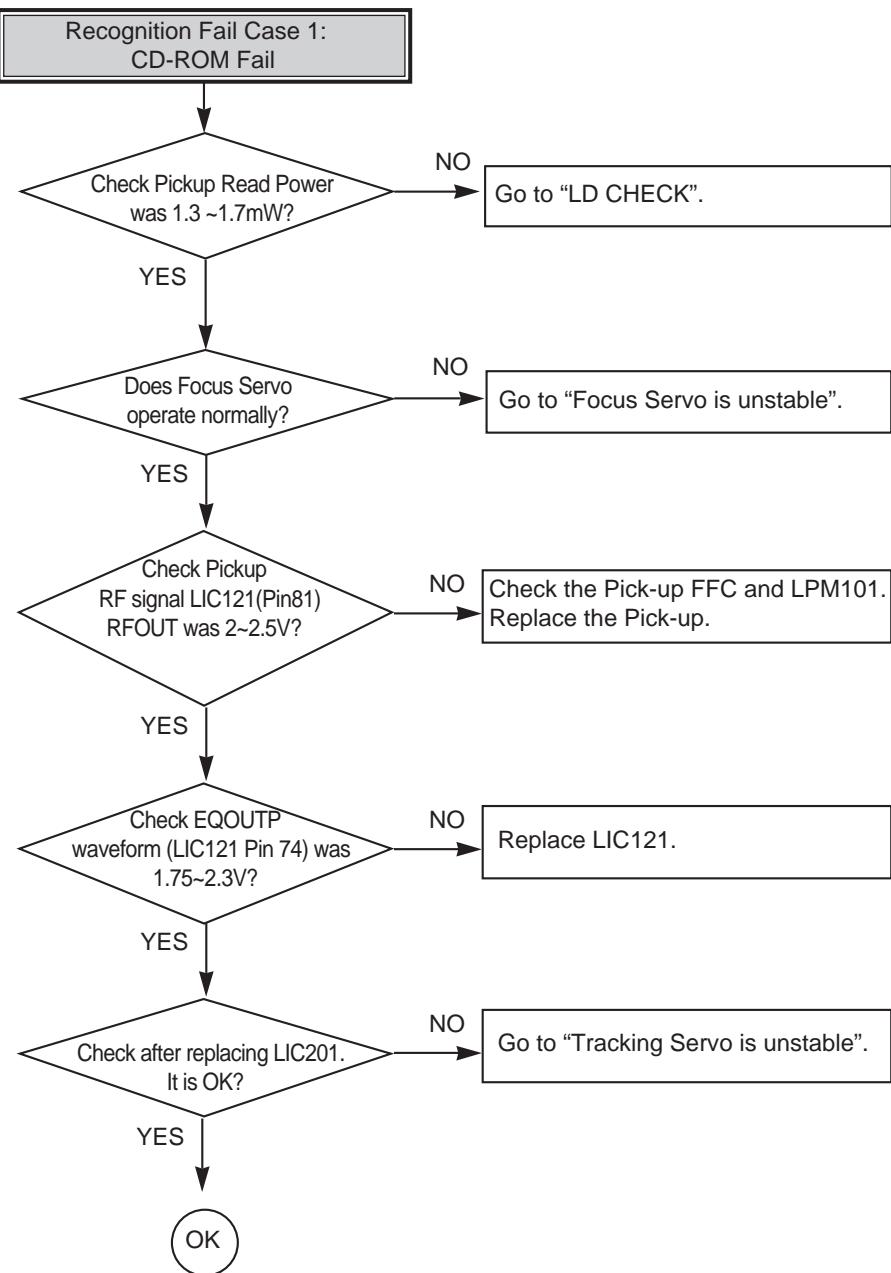


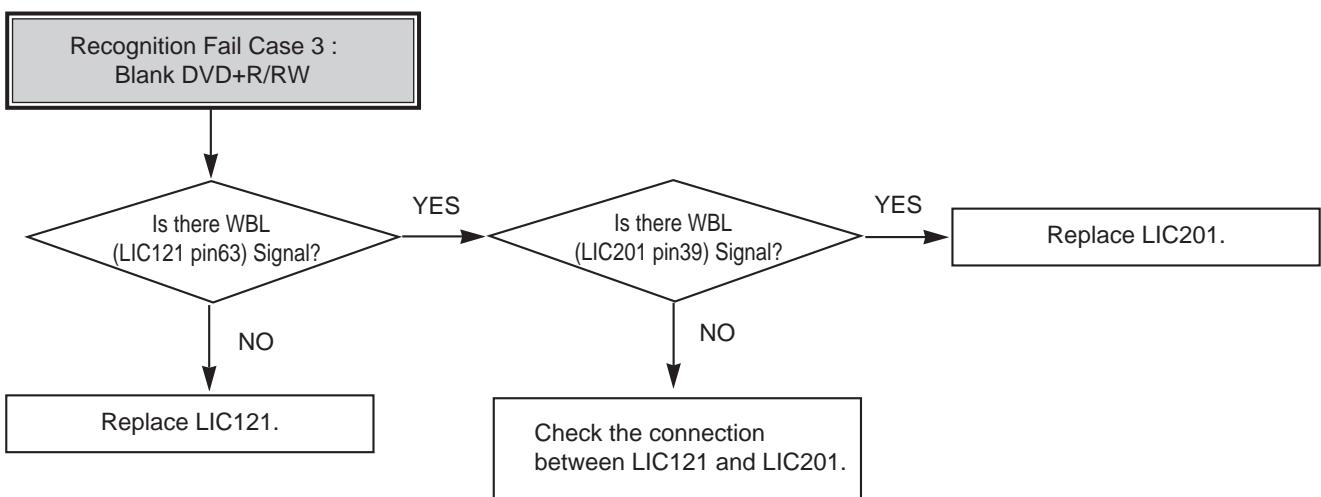
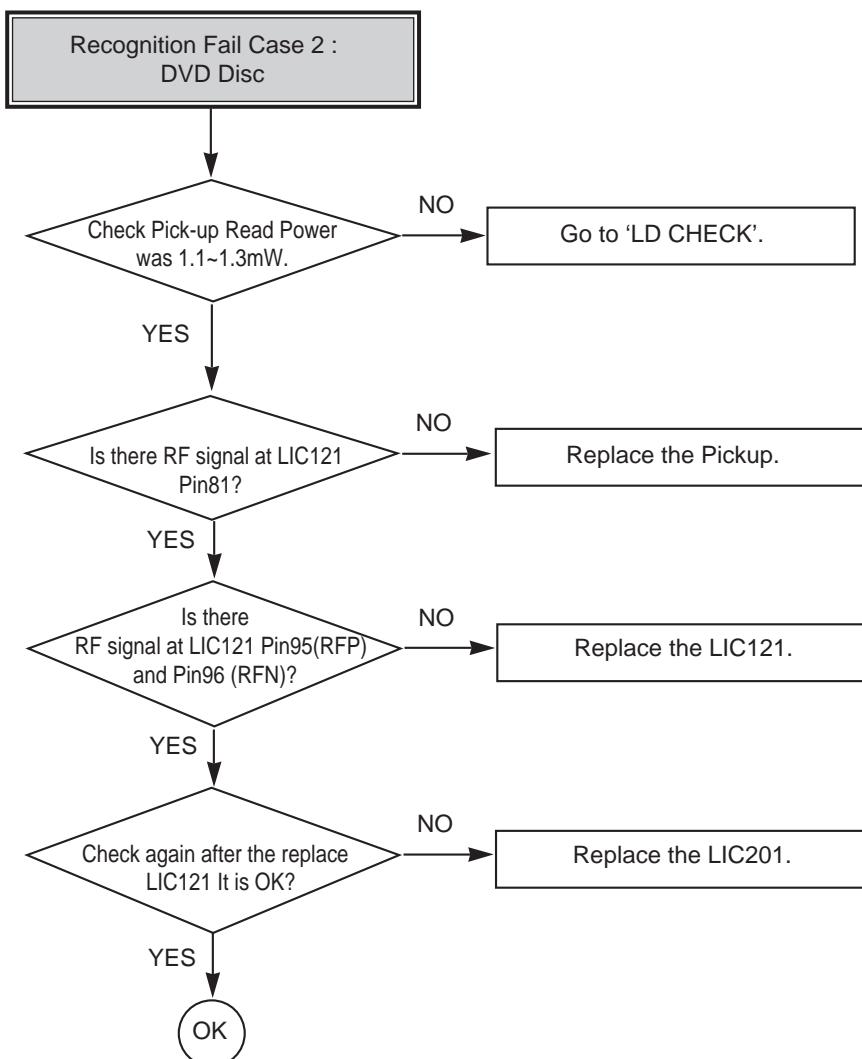


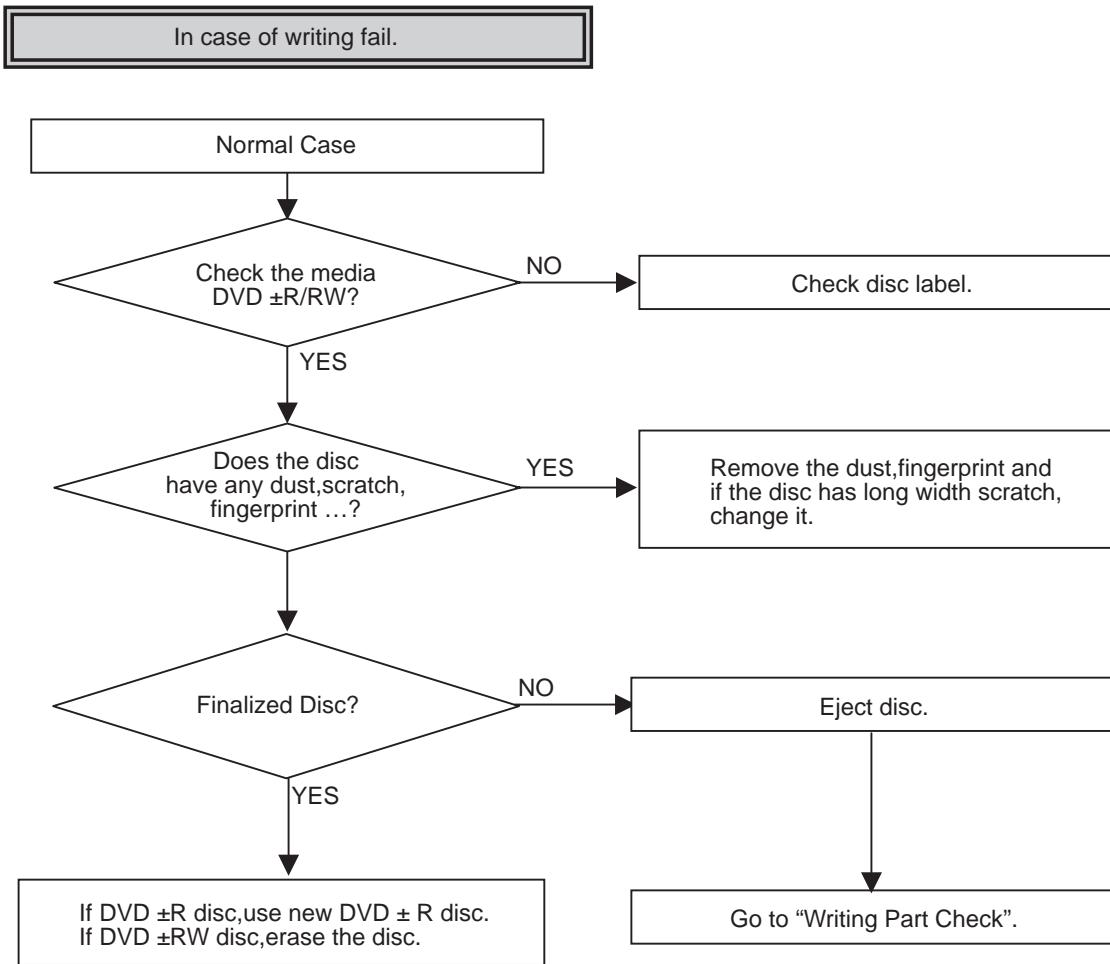


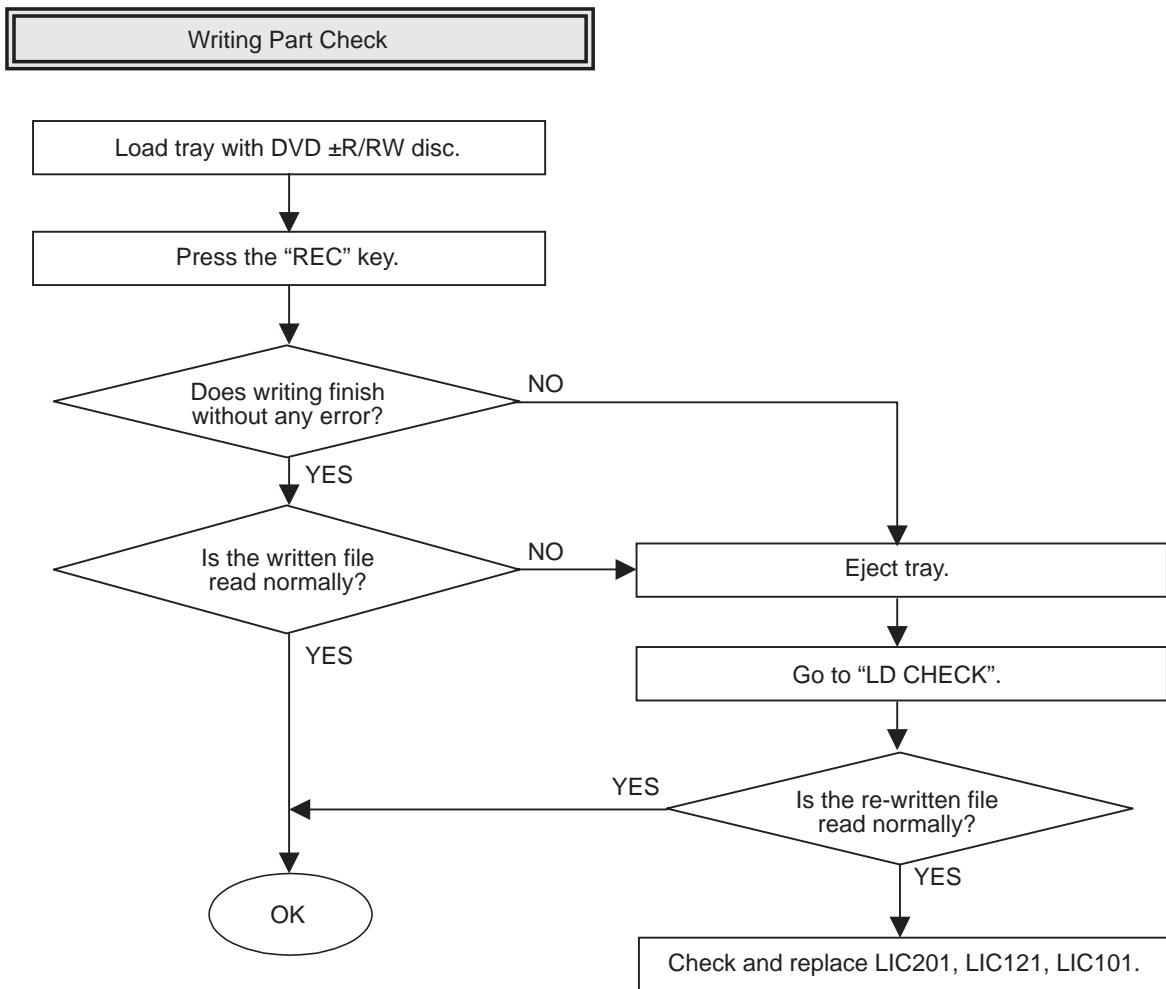


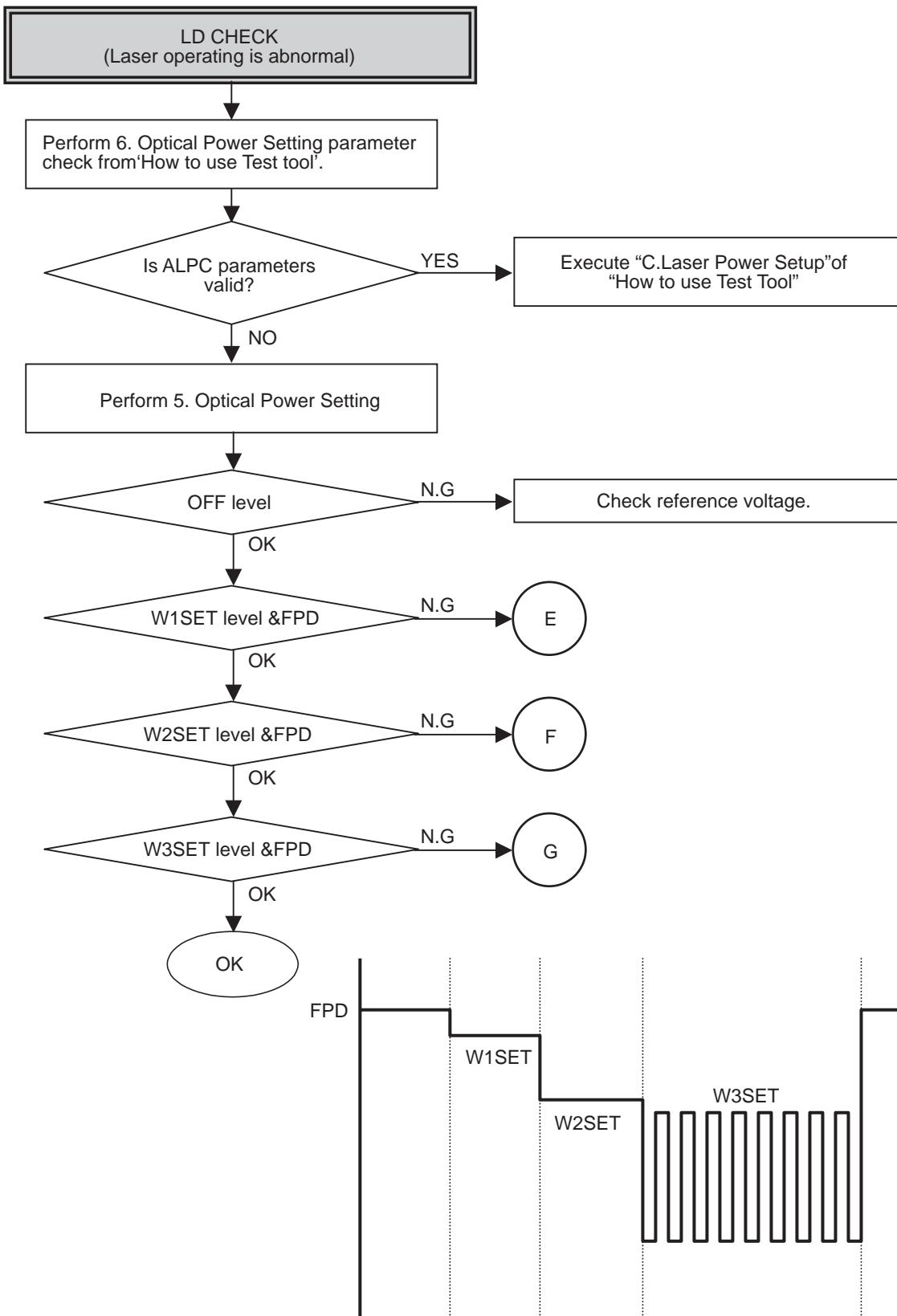
* LIC121 pin24 is MOUT1(FEP Monitor1).
After disc recognition action, Monitor port is off. So, please check TE signal during disc recognition.

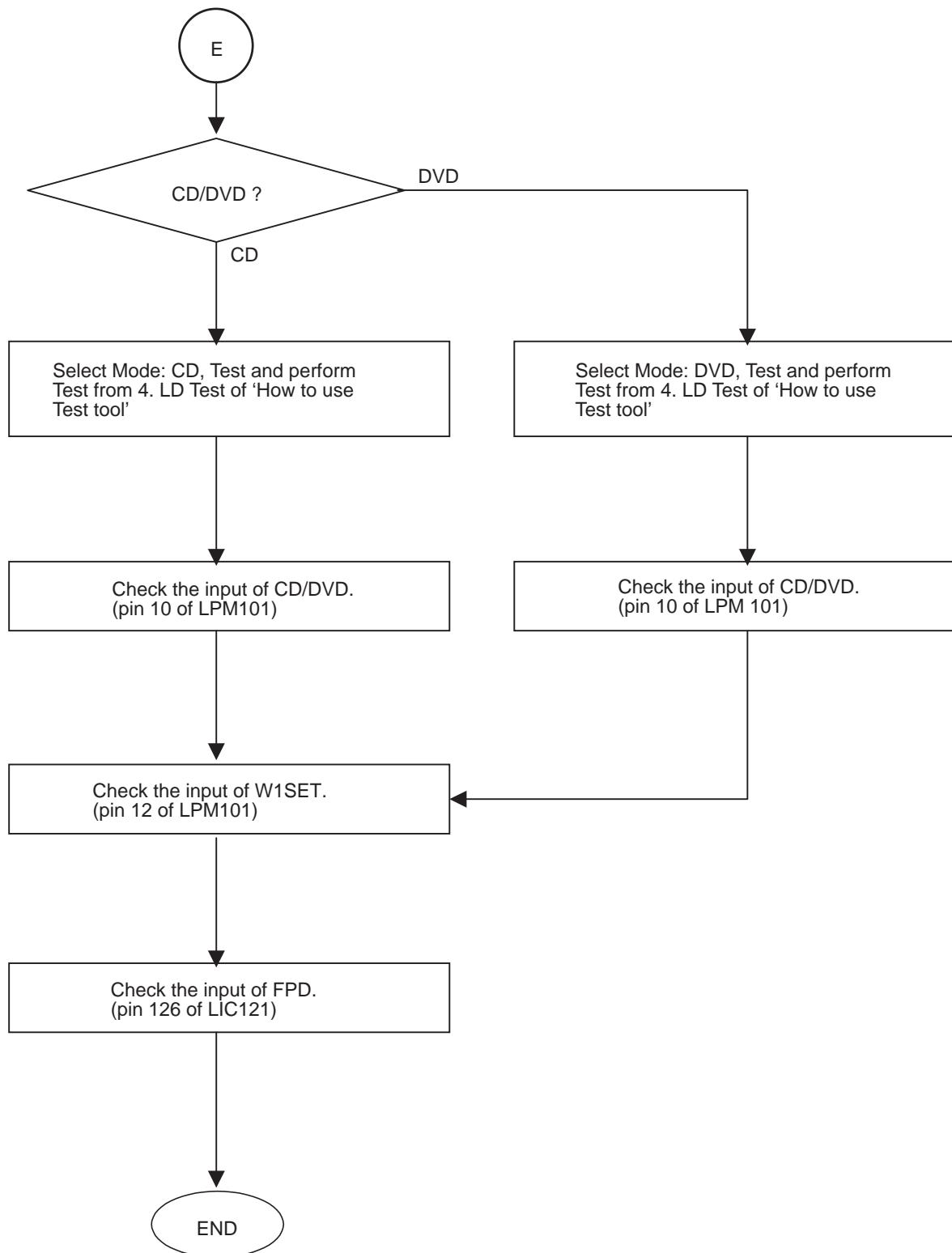


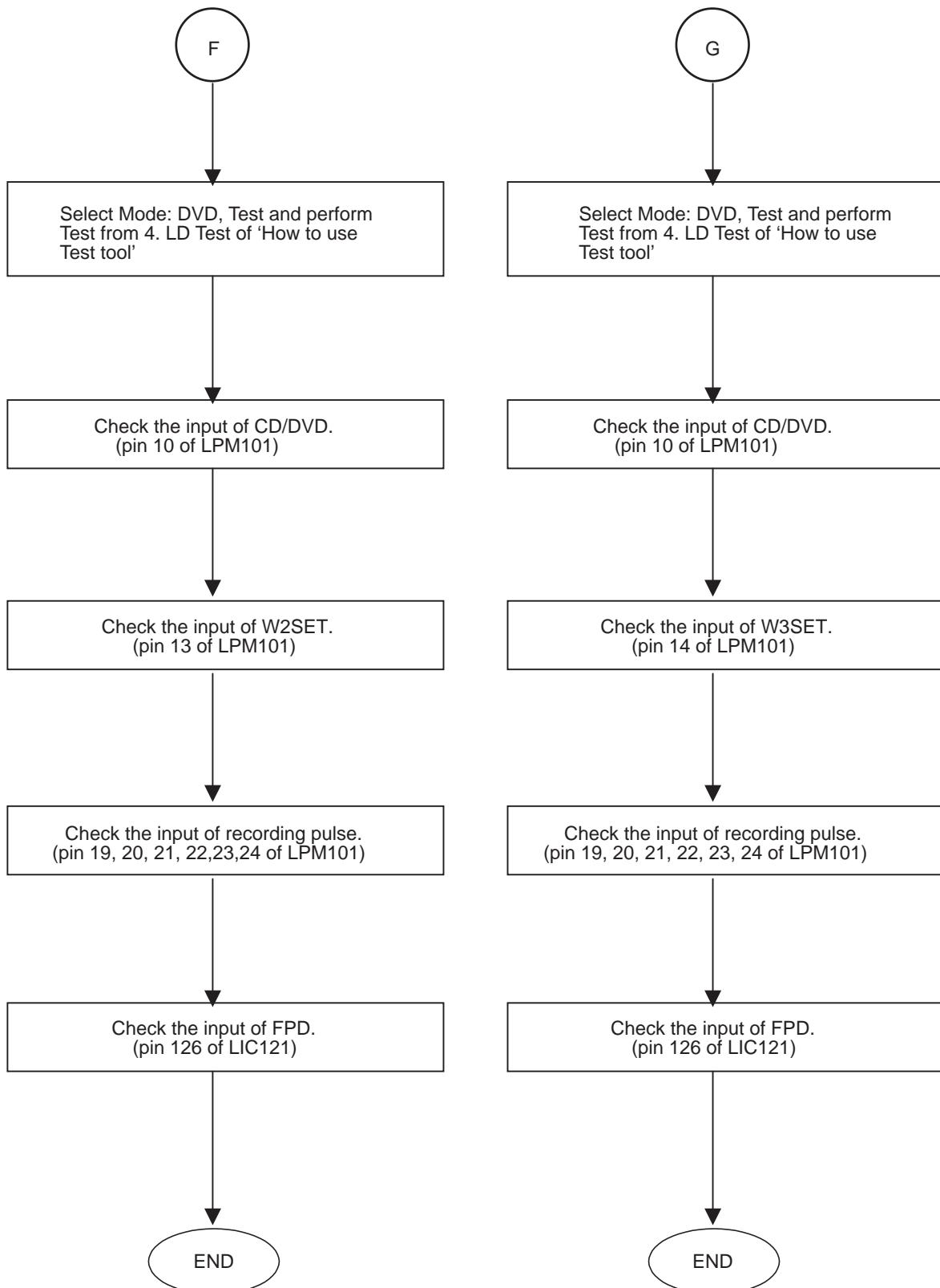








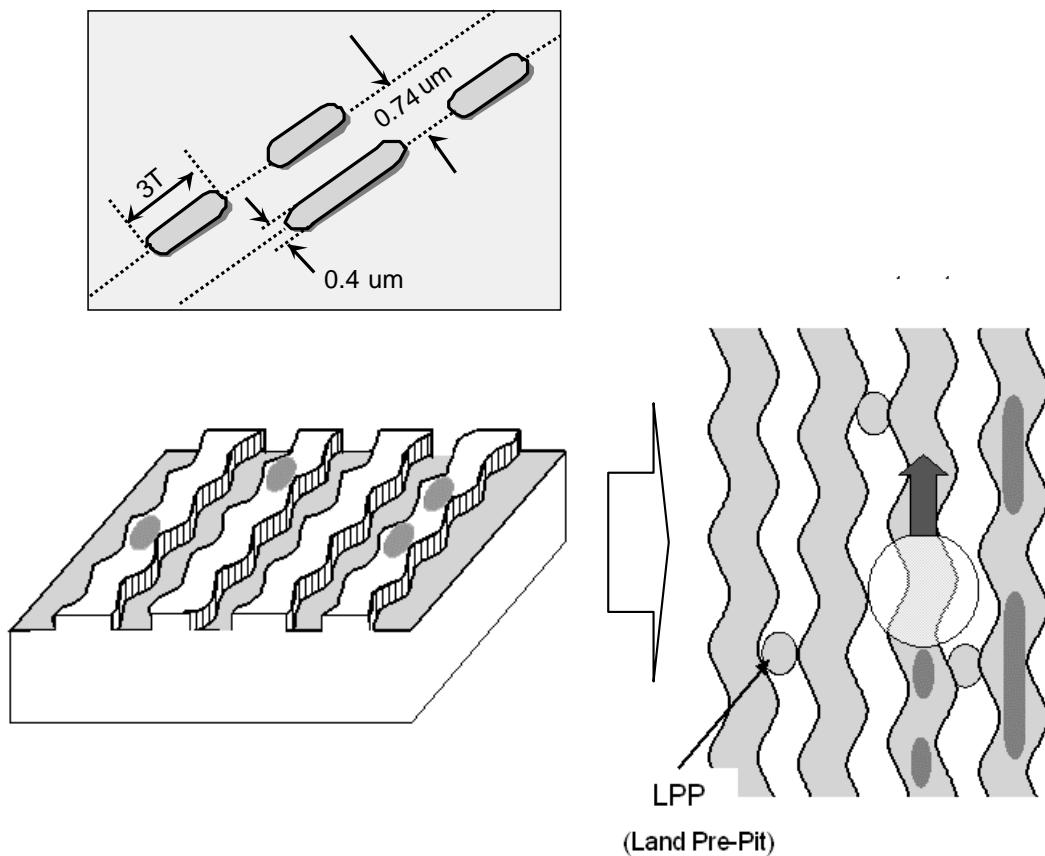




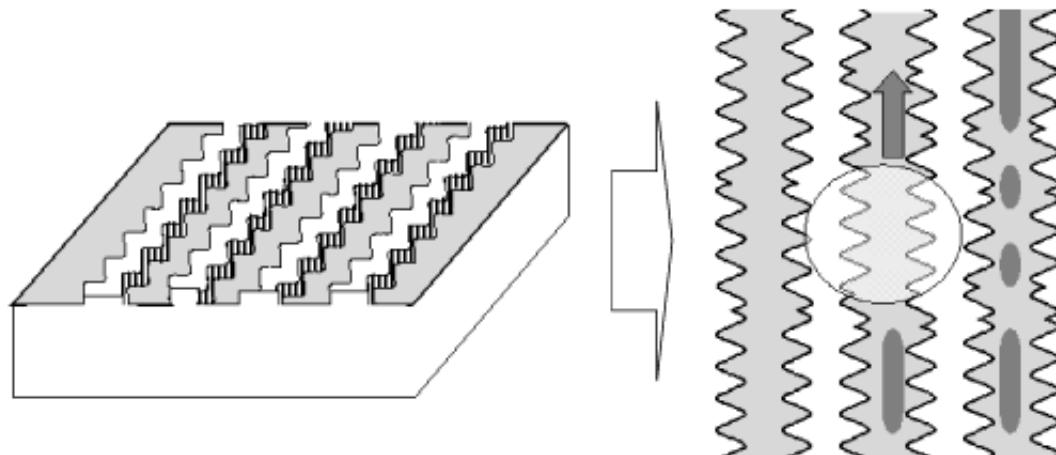
THE DIFFERENCE OF DVD-R/RW, DVD+R/RW DISCS AND DVD-ROM

1. RECORDING LAYER

- DVD-ROM (Read Only Disc)



- DVD+R/RW Disc



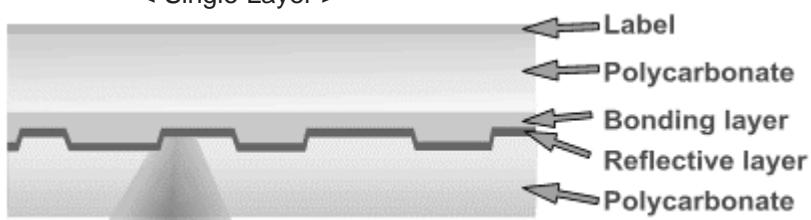
2. DISC SPECIFICATION

	DVD-ROM		DVD-R	DVD-RW	DVD+R	DVD+RW
	Single-Layer	Dual-Layer				
Media Type	Read Only	Read Only	Dye	Phase change	Dye	Phase change
User data capacity	4.7GB	8.54GB	4.7GB	4.7GB	4.7GB	4.7GB
Wavelength	650nm	650nm	650nm	650nm	650nm	650nm
Reflectivity	45~85%	18~30%	45~85%	18~30%	45~85%	18~30%
Track pitch	0.74μm	0.74μm	0.74μm	0.74μm	0.74μm	0.74μm
Minimum pit length	0.4μm	0.4μm	0.4μm	0.4μm	0.4μm	0.4μm
Modulation	>0.6	>0.6	>0.6	>0.6	>0.6	>0.6
Channel bit-rate	26.16MHz	26.16MHz	26.16MHz	26.16MHz	26.16MHz	26.16MHz
Wobble Frequency	–	–	140KHz	140KHz	817.4KHz	817.4KHz
Addressing	26.16MHz	26.16MHz	Wobble & LPP	Wobble & LPP	Wobble(ADIP)	Wobble(ADIP)
Read Power (mW)					0.7 ± 0.1	0.7 ± 0.1
Write Power (mW)	–					
Jitter	<8%	<8%	<8%	<8%	<9%	<9%

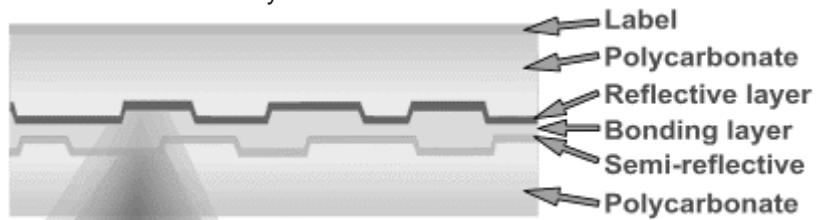
3. DISC MATERIALS

1) DVD-ROM

< Single Layer >



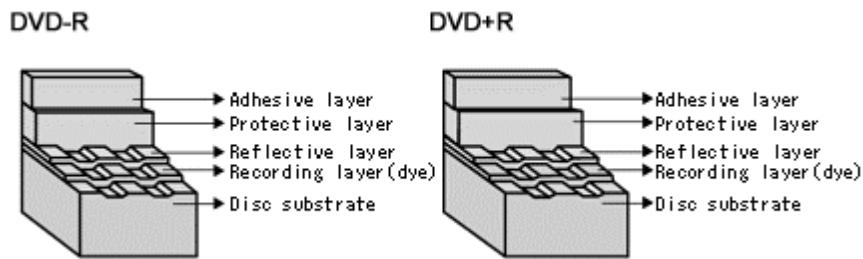
< Dual Layer >



2) Recording format using organic dye material (DVD-R / DVD+R)

The format that records data through the creation of recorded marks by changing the organic dye material with a laser beam.

► Disc structure



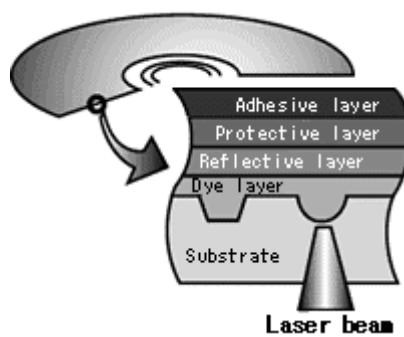
► Recording principles

[Recording]

Recording is done by changing the organic dye layer and the substrate with a laser. When a strong laser is applied to a disc, the temperature of the organic dye material goes up, the dye is decomposed and the substrate changes at the same time. At this time, a durable bit is created as is the case with a CD-ROM.

[Playback]

Signals are read with the differences of the reflection of a laser from pits.

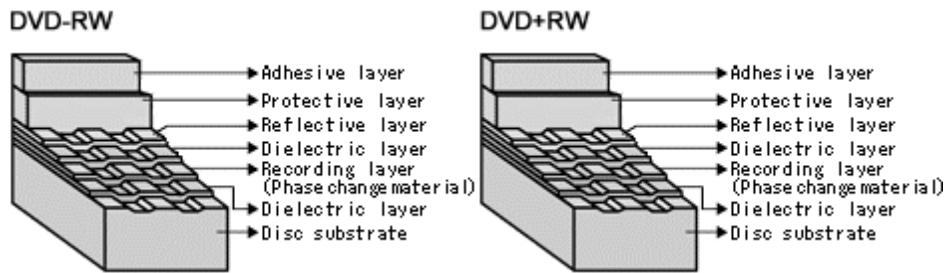


3) Recording format using phase-change recording material (DVD-RW / DVD+RW)

- Data is recorded by changing the recording layer from the amorphous status to the crystalline status, and played back by reading the difference of the reflection coefficient.

Amorphous: Non-crystalline.

► Disc structure



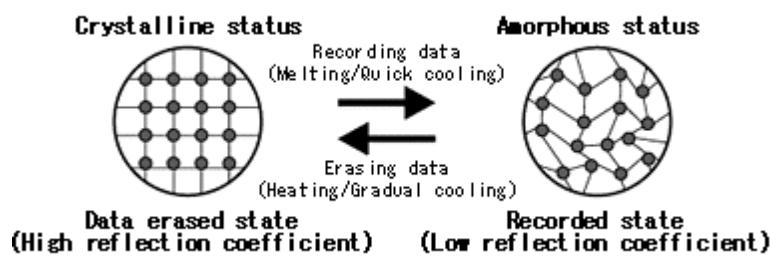
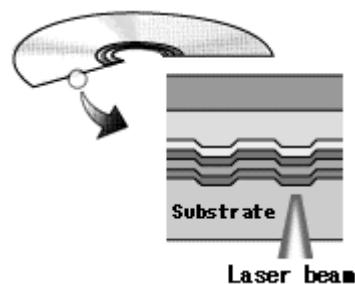
► Recording principles

[Recording]

When a high-power laser is applied to the recording material, it melts and then becomes amorphous with a low reflection coefficient when it quickly cools off. When a mid-power laser is applied to heat gradually the recording material and then gradually cools it off, it becomes crystal with a high reflection coefficient.

[Playback]

A low-power laser is used for playback. The amount of reflected light depends on the status (amorphous or crystalline) of the recording material. This is detected by an optical sensor.

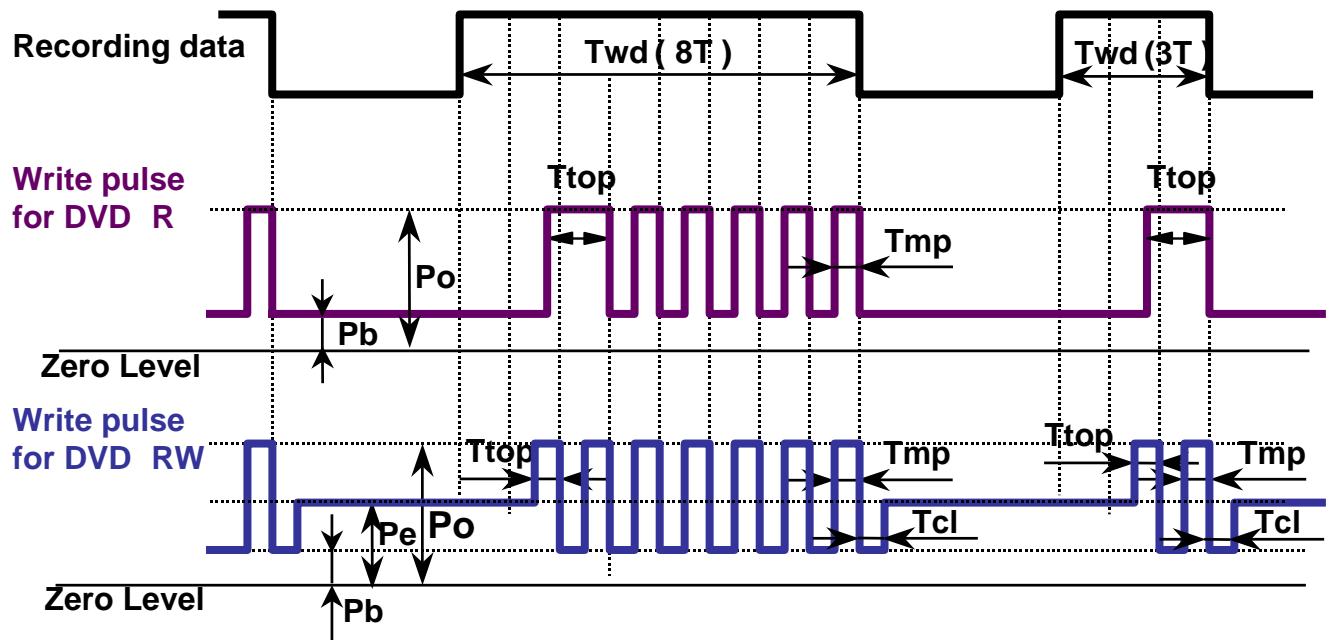


To make recordings, it is necessary to modulate the write pulse, which is called "Write Strategy".

There can be many types in Write Strategy. Typically Write Strategy for DVD ±R has NMP(Non Multi-Pulse) type and MP(Multi-Pulse) type. In NMP type each single mark is created by subsequent separated short pulses. In MP type each single mark is created by one continuous pulse.

Write Strategy for DVD ±RW has Type 1 and Type2. In Type 1 the mark with nT width is created by one top pulse and $(n-2)$ multi-pulses. Thus mark 3T is made by one top pulse and one multi-pulse. In Type 2 the mark with nT width is created by one top pulse and $(n-3)$ multi-pulses. Thus mark 3T is made by one top pulse only.

RL-02A uses MP type Write Strategy for DVD ±R and Type 1 for DVD ±RW as shown below.



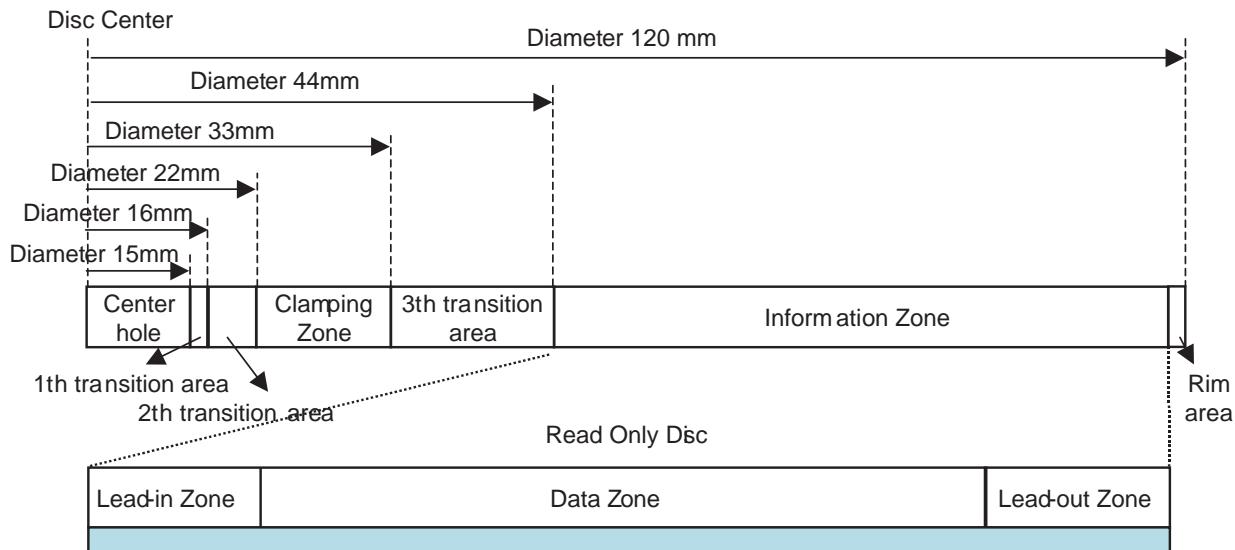
Po :Write Power (Peak Power)

Pe :Erase Power

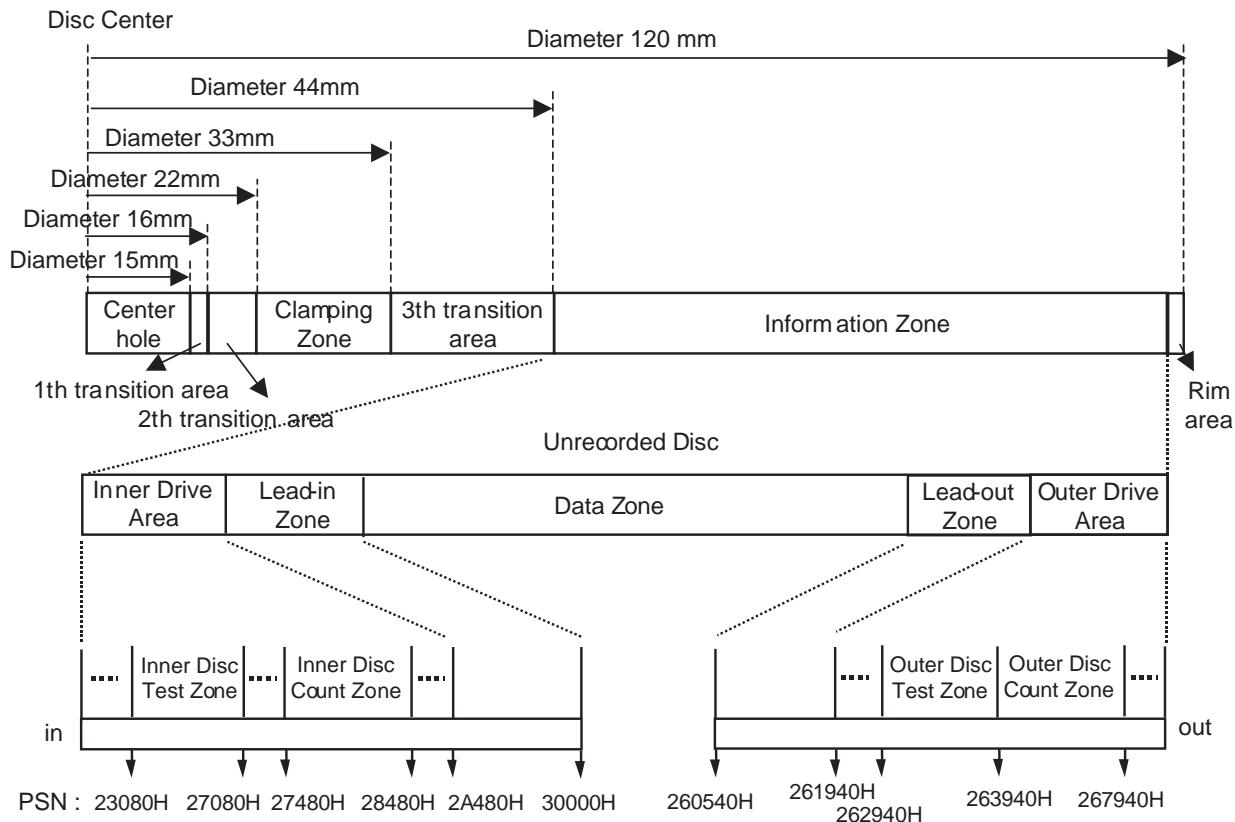
Pb :Bias Power

4. ORGANIZATION OF THE INNER DRIVE AREA, OUTER DRIVE AREA, LEAD-IN ZONE AND LEAD-OUT ZONE

1) Layout of DVD-ROM disc



2) Layout of DVD+R disc



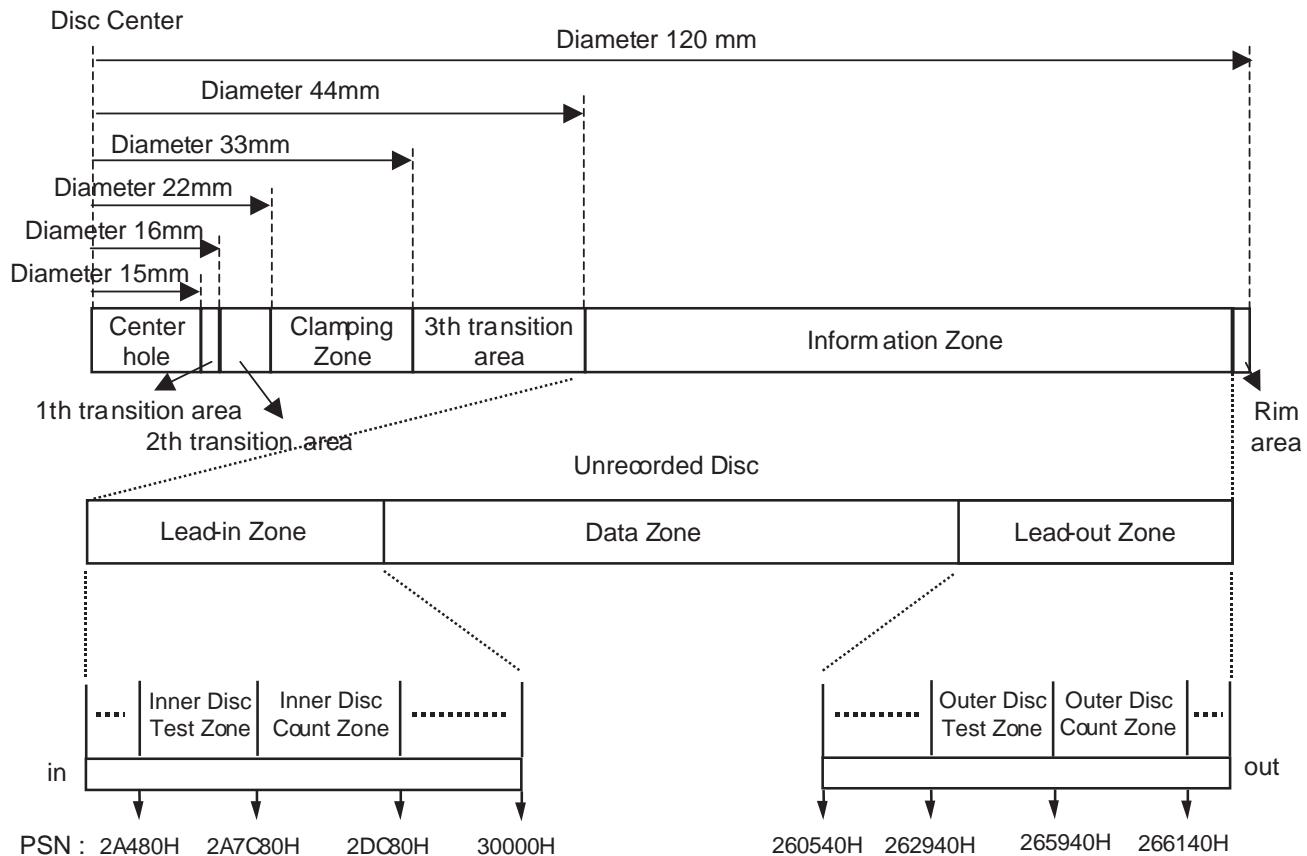
Inner Disc Test Zone : for performing OPCprocedures.

Inner Disc Count Zone : For counting the number of OPCalgorithm performed in IDT Zone.

Outer Disc Test Zone : for performing OPC procedures.

Outer Disc Count Zone : For counting the number of OPC algorithm performed in IDT Zone.

3) Layout of DVD+RW disc



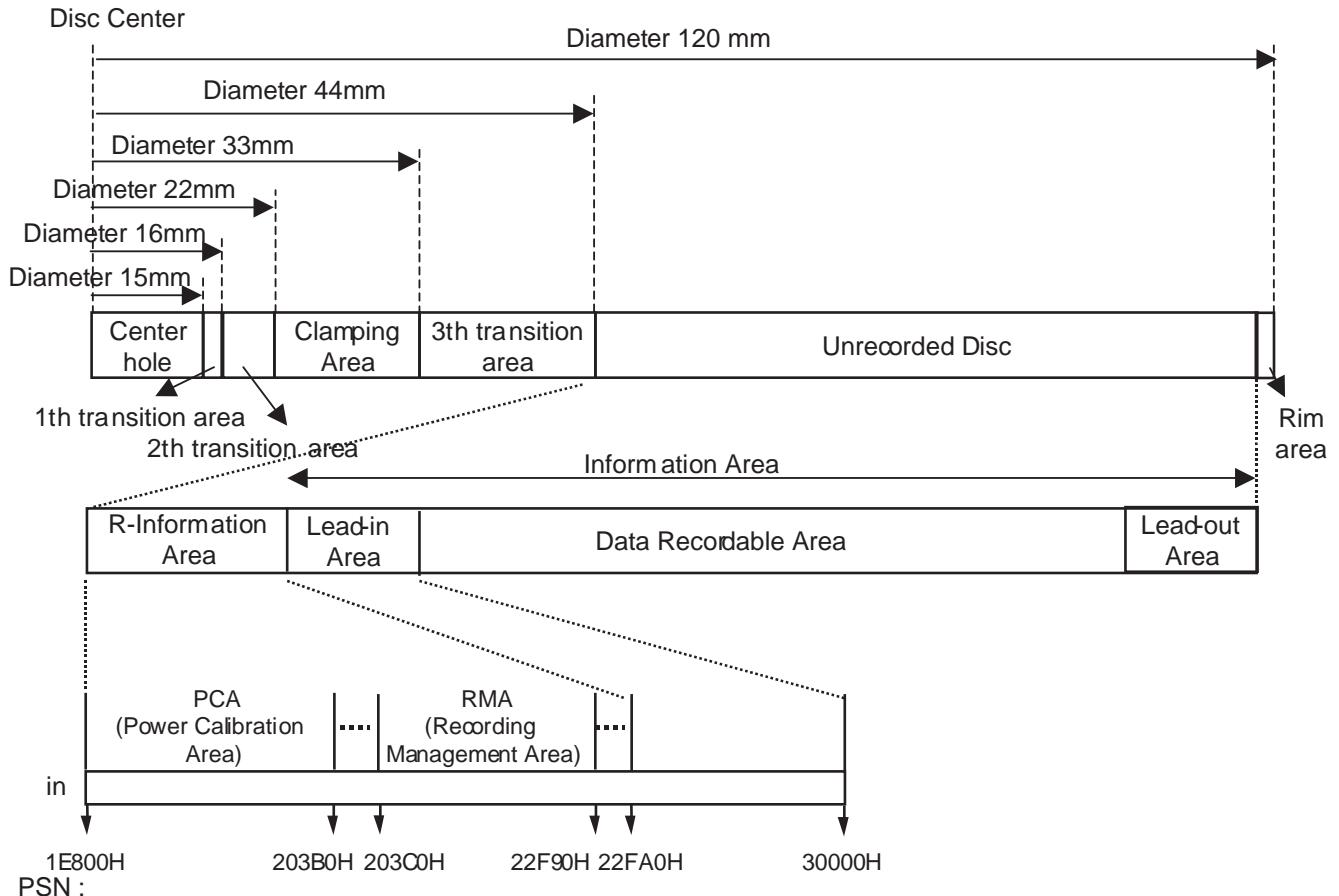
Inner Disc Test Zone : for performing OPC procedures.

Inner Disc Count Zone : For counting the number of OPC algorithm performed in IDT Zone.

Outer Disc Test Zone : for performing OPC procedures.

Outer Disc Count Zone : For counting the number of OPC algorithm performed in IDT Zone.

4) Layout of DVD-R/RW disc



HOW TO USE TEST TOOL

1. ALPC MEASUREMENT SYSTEM

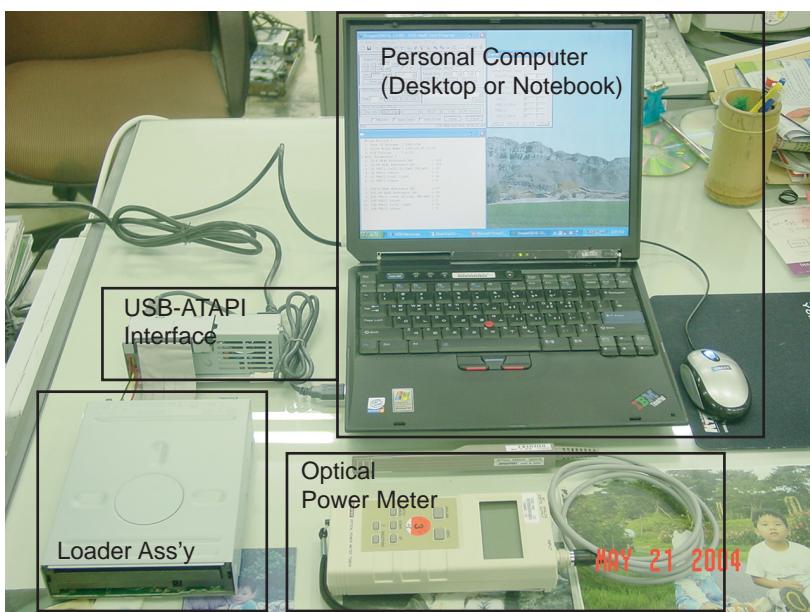
We need basically several measurement instrument to adjust Optical Power of CD and DVD Disc

• ESSENTIAL INSTRUMENT

- 1) Optical Power meter & Sensor (ADVANTEST, TQ8230/Q82014A)
- 2) Personal Computer
- 3) Adjustment Program (Dragon or ALPC) --> being recommended ALPC Program in case of SVC

• OPTIONAL INSTRUMENT

- 1) USB-ATAPI Interface (if you don't have Notebook which has ATAPI Interface or use PC USB Port)
- 2) Connector-ATAPI Interface Board



2. ALPC PROGRAM

Use the ALPC program in Dragon tool for Optical power setting. It is consist of total 4 files.

Dragon_JW3P.exe
dragon.cfg
blue.dat
WNASPI32.DLL

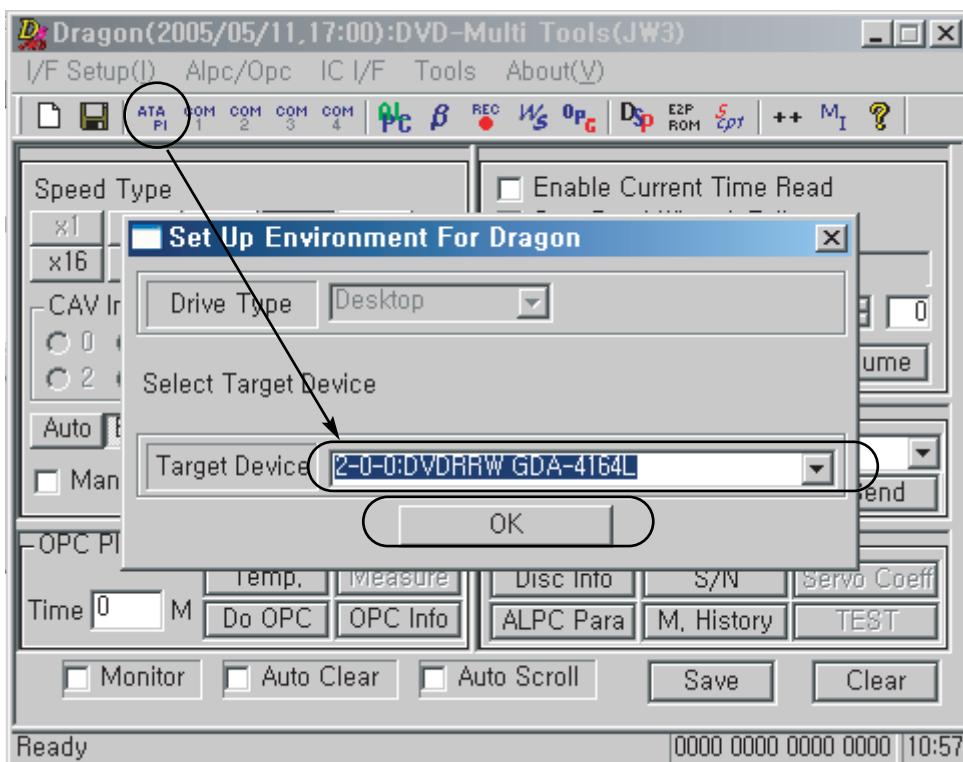
Four files must exist in same Directory.

3. EXECUTE ALPC PROGRAM

- 1) Execute Dragon_JW3P.exe file.

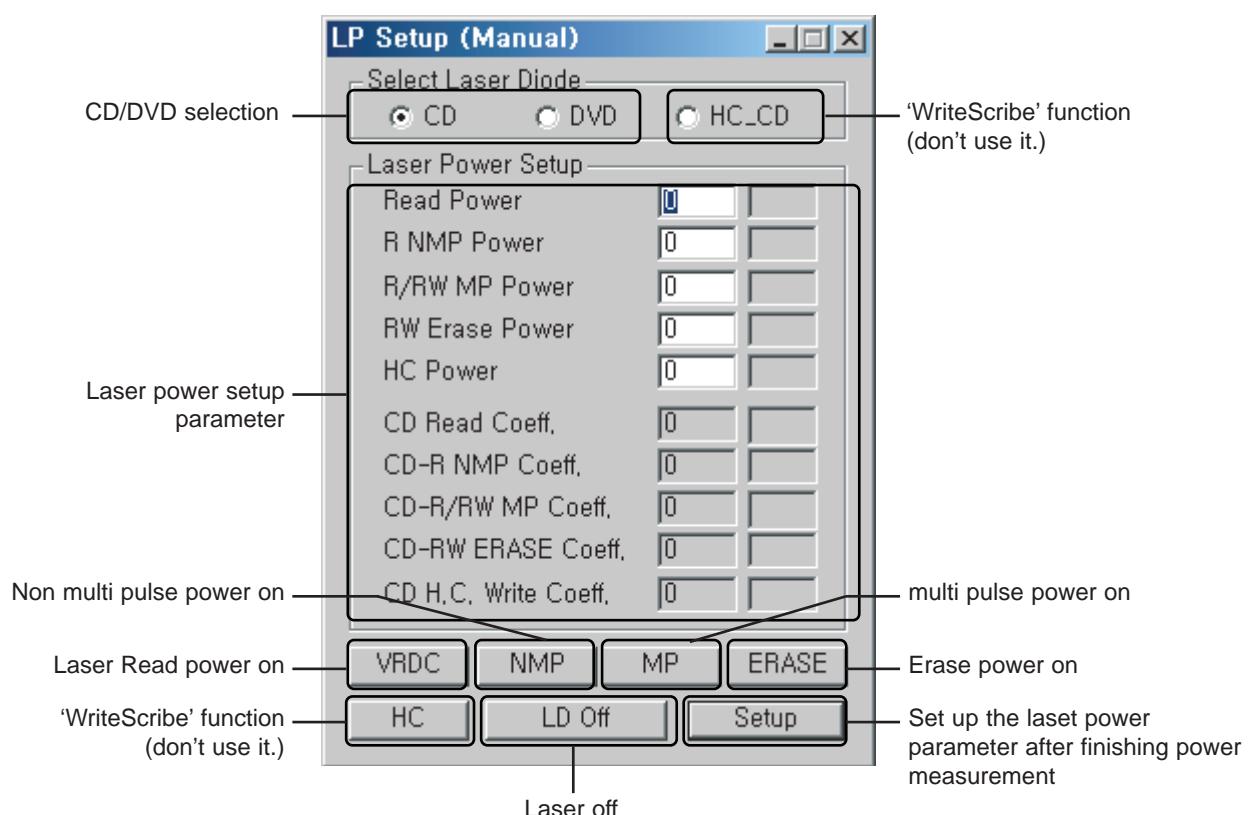
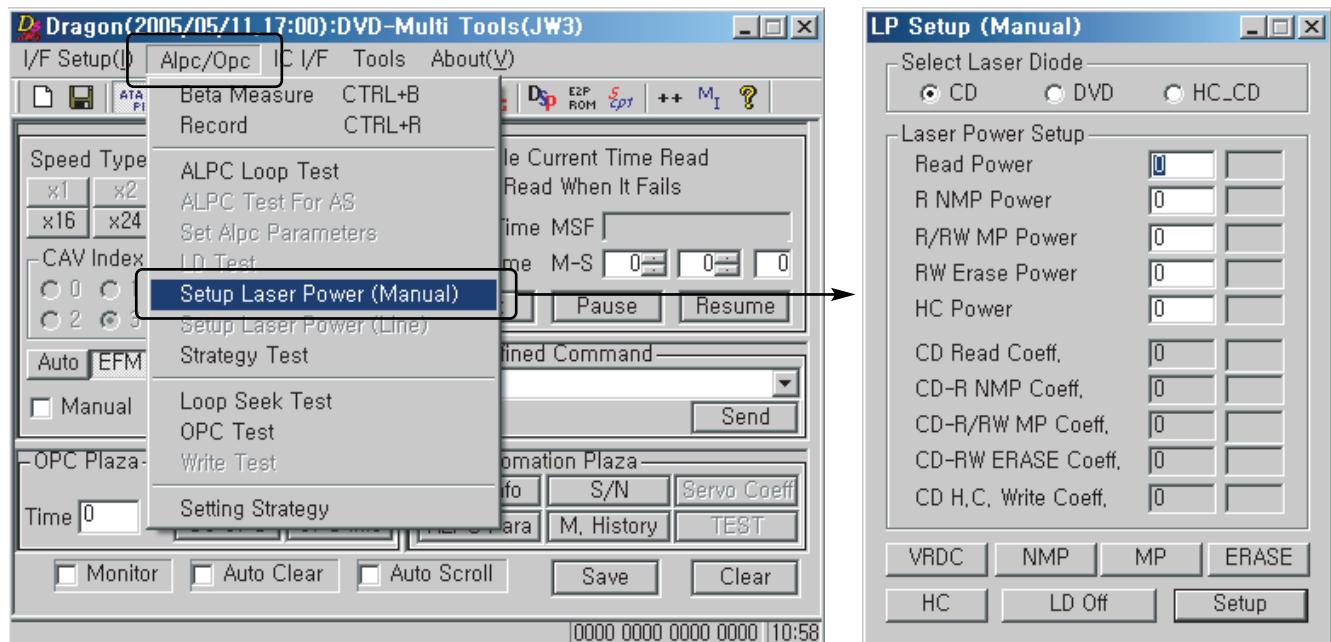


- 2) Enter the password. It is 'qaz'.
When you enter the password, turn off the 'Caps lock' in your keyboard.



- 3) Set up the target device.
Press 'ATAPI' button on the main dialog of Dragon tool. And find the target device which is GDA-4164L.

4) If the target device setting is completed, execute the 'Setup Laser Power(Manual)' in the 'Alpc/Opc' menu.



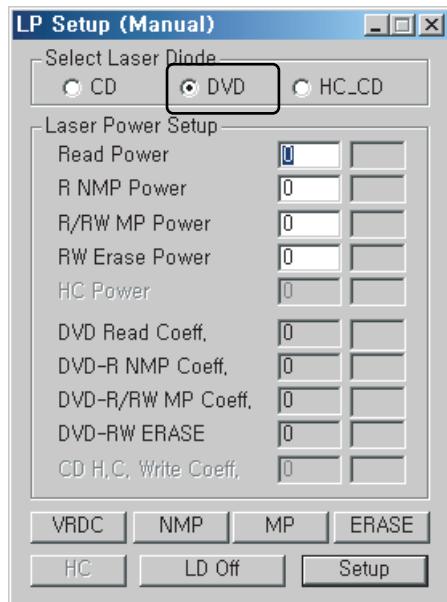
4. OPTICAL POWER SETTING

<Test for checking DVD LD and CD LD>

When you change the Travers ass°Øy(including pick-up) or loader PCB, you must do the laser power setting to match pick-up and loader PCB.

1) DVD LD power setting

- Select the DVD in the 'Select Laser Diode'
- Press **VRDC** (Read Power On, Strong Read light)
- Measure optical read Power.
- Write read power value.
- In case of **NMP** **MP** **ERASE** ,
you are able to measure the power through same procedure.
- (caution) Don't watch light directly.**
- When you finish optical power measurement,
press **LD Off** button(LD Off).
- Press **Setup** button.(save to EPROM)

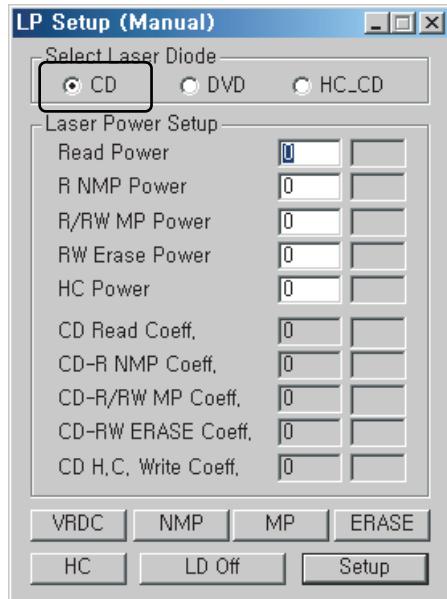


2) CD LD Power Setting

- Select the CD in the 'Select Laser Diode'
- Press **VRDC** .
- Measure optical read Power.
- Write Read Power value.
- Press **Setup** button(save to EPROM)

*** In case of CD power setting of RS-01A, loader don't need to set up write power.
Although NMP, MP,Erase and HC power is N.G when you press setup, please ignore the N.G message.
Because of RS-01A only support reading function about CD-R/RW.

* Look at reference sheet to test Optical Power.
**Power value is β— unit. Value is read power X 100.



5. CONFIRM OPTICAL POWER SETTING PARAMETER

LD Test result is ok, but Loader performance is bad.

1. Check ALPC parameter value

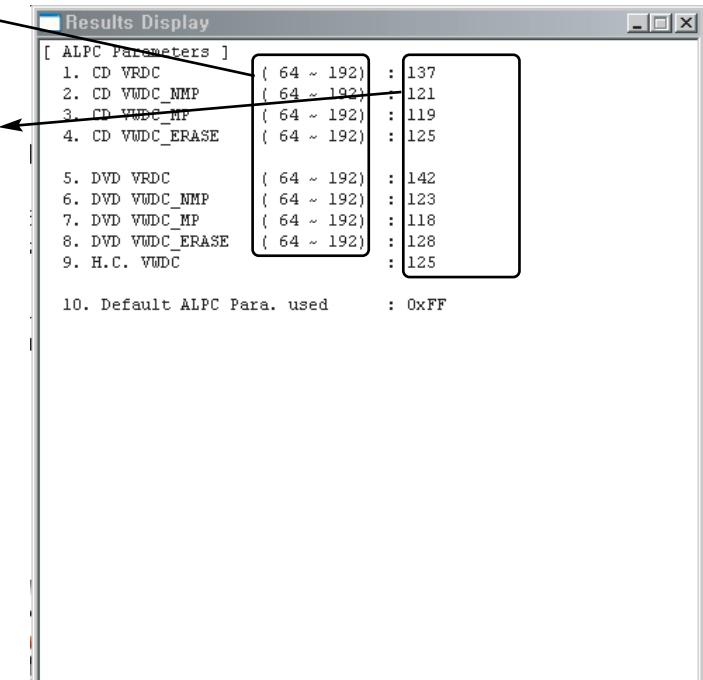
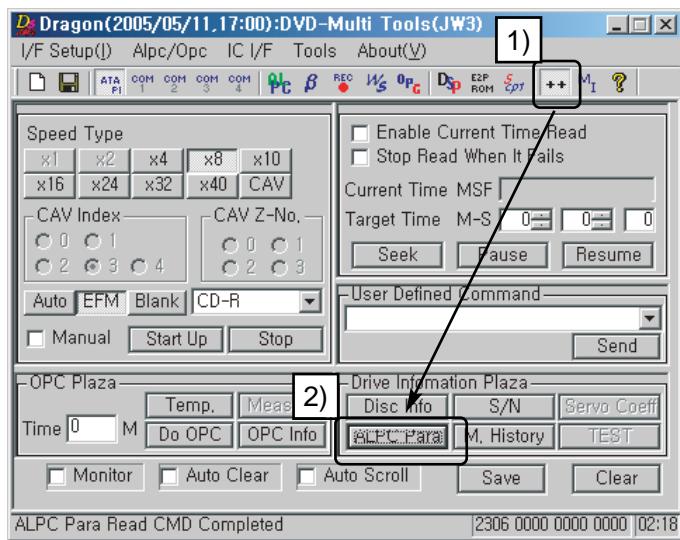
1) Press  button to open 'Results Display' dialog.

2) Press  button.

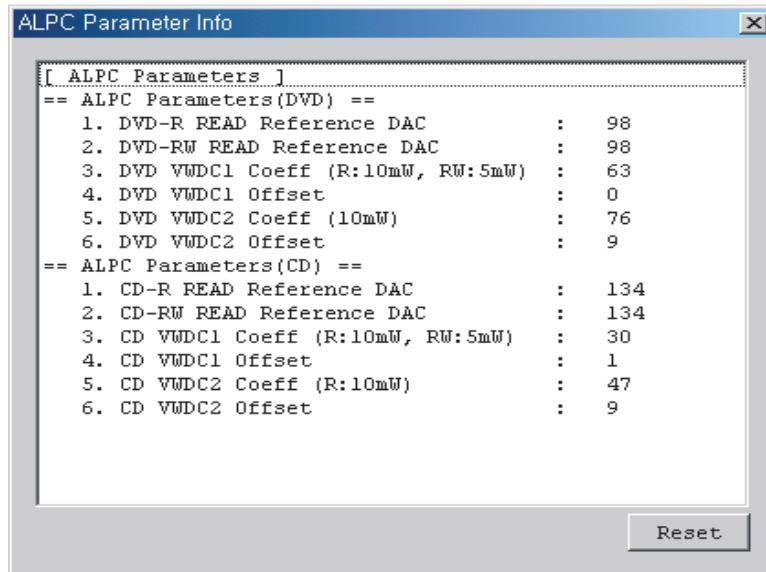
- We can see optical power setting value.
- Write optical Power Setting value to paper.
- Adjust power setting again.
- Compare original parameter to new parameter.
- if parameter value is different highly, original value is wrong or optical power may change.
- But pick-up LD test is all ok , just adjust optical power setting again.

Normal range of ALPC parameter

Optical power value which has been saved in the EEPROM



6. OPTICAL POWER SETTING PARAMETER RANGE



[VALID ALPC Parameters]

< CD >

1) CD-R READ Reference DAC	: 30 ~150
2) CD-RW READ Reference DAC	: 80 ~ 250
3) VWDC1	: 10 ~ 39
4) VWDC1 Offset	: 0 ~ 20
5) VWDC2	: 20 ~ 57
6) VWDC2 Offset	: 0 ~ 20

<DVD>

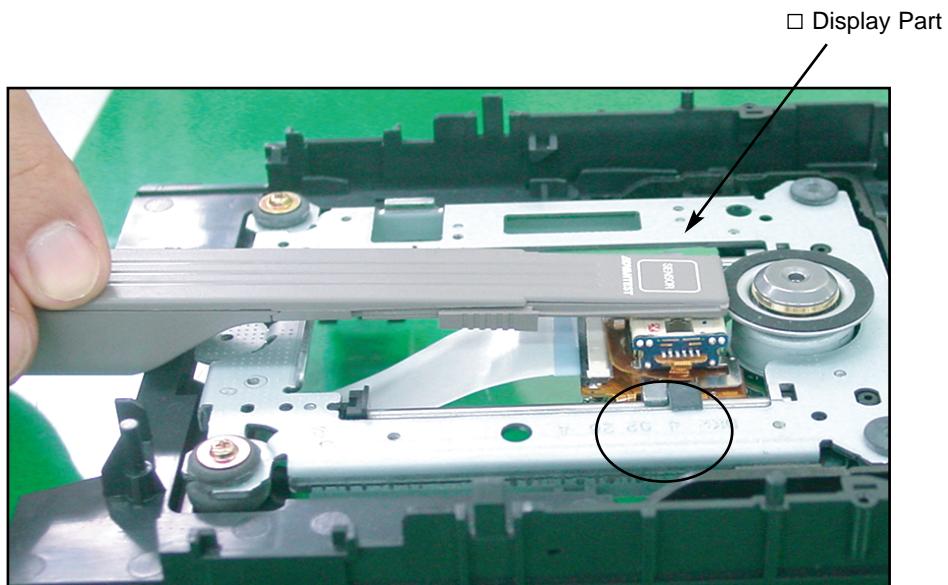
1) DVD-R READ Reference DAC	: 40 ~145
2) DVD-RW READ Reference DAC	: 40 ~145
3) VWDC1	: 30 ~ 100
4) VWDC1 Offset	: 0 ~ 20
5) VWDC2	: 40 ~ 140
6) VWDC2 Offset	: 0 ~ 20

7. ATTACHMENT. OPTICAL POWER MEASUREMENT

Optical Power measurement is to adjust LD power from Pick-up
To measure optical power, LD status is on. Other light affects to optical power.
Avoid other light to measure exact power
Generally headlight power is about 50 μ W, Sun power is about 100mW.

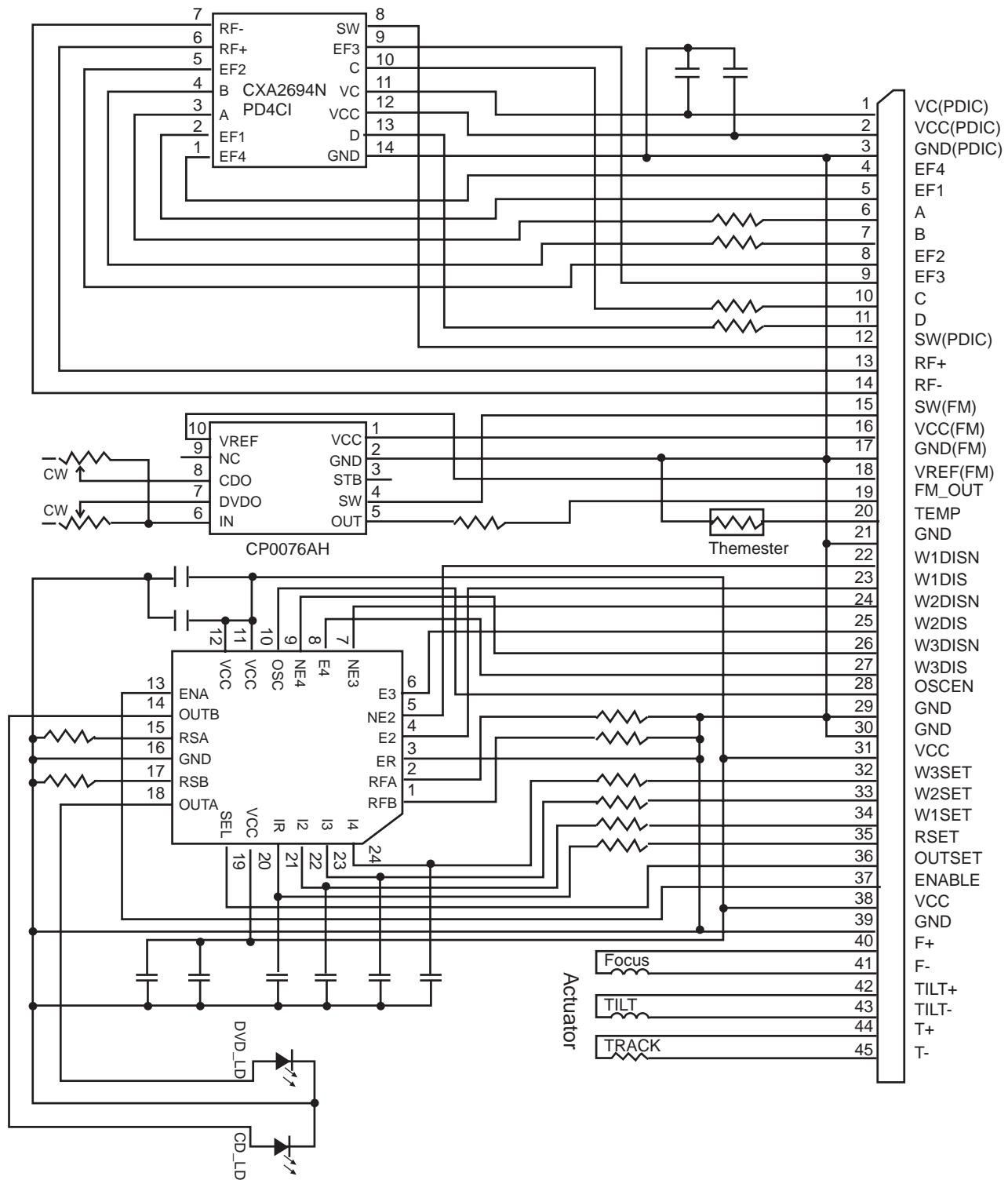
Optical Power measurement method

1. Fit optical Power Meter λ (wavelength) value to DVD.(generally 660nm)
2. DVD LD On.
3. Approach power sensor to Pick-up Lens about 3mm vertically. Fix Lens and Sensor \square mark position.
4. Read Monitor value. (move sensor read just a little and read max value.)
(caution) unit is mW.
5. Write monitoring value \times 100. Only an integer.
6. Fit opticcal Power Meter λ (wavelength) value to CD.(generally 780nm)
7. CD LD On.
8. 3 ~ 5 recheck.



INTERNAL STRUCTURE OF THE PICK-UP

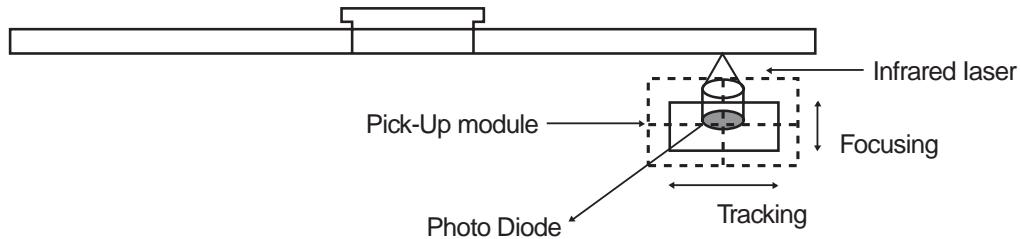
1. BLOCK DIAGRAM OF THE PICK-UP(LPC-812R)



2. PICK UP PIN ASSIGNMENT

No.	Pin Name	Signal Description
45	T-	Tracking Actuator drive signal-
44	T+	Tracking Actuator drive signal+
43	Tilt-	Tilting Actuator drive signal-
42	Tilt+	Tilting Actuator drive signal+
41	F-	Focusing Actuator drive signal-
40	F+	Focusing Actuator drive signal+
39	GND(LDD)	Ground connection for LDD
38	VCC(LDD)	Power supply for LDD
37	ENABLE	Disables output current regardless of OUTEN(ENABLE Low:No Iout)
36	OUTSEL	High:selects DVD LD, Low:CD LD
35	RSET	Input voltage for current amplifier
34	W1SET	Input voltage for current amplifier
33	W2SET	Input voltage for current amplifier
32	W3SET	Input voltage for current amplifier
31	VCC(LDD)	Power supply for LDD
30	GND(LDD)	Ground connection for LDD
29	GND(LDD)	Ground connection for LDD
28	OSCEN	TTL control for Oscillator Enable (High Enable)
27	W3DIS	LVDS control for output current (High Enable)
26	W3DISN	LVDS control for output current (Low Enable)
25	W2DIS	LVDS control for output current (High Enable)
24	W2DISN	LVDS control for output current (Low Enable)
23	W1DIS	LVDS control for output current (High Enable)
22	W1DISN	LVDS control for output current (Low Enable)
21	GND(FPD)	Ground connection for PDIC, FPD, TEMP
20	TEMP	Output voltage for controlling temperature
19	FPD-OUT	APC amplifier output
18	VREF(FPD)	APC amplifier reference voltage output
17	GND(TEMP)	Ground connection for PDIC, FPD, TEMP
16	VCC(FPD)	Power supply for FPD
15	SW2(FPD)	FPD output gain Select (High : CD, Low:DVD)
14	RF-	Signal PDIC RF negative differential output
13	RF+	Signal PDIC RF positive differential output
12	SW1(PDIC)	PDIC output gain Select (L/M/H)
11	D	Signal PDIC output D
10	C	Signal PDIC output C
9	EF3	Signal PDIC output EF3
8	EF2	Signal PDIC output EF2
7	B	Signal PDIC output B
6	A	Signal PDIC output A
5	EF1	Signal PDIC output EF1
4	EF4	Signal PDIC output EF4
3	GND(PDIC)	Ground connection for PDIC, FPD, TEMP, LDD
2	VCC(PDIC)	Power supply for PDIC(+5V)
1	VC(PDIC)	Reference voltage input for PDIC

3. SIGNAL DETECTION OF THE P/U



1) Focus Error Signal ==> $(A+C)-(B+D)$

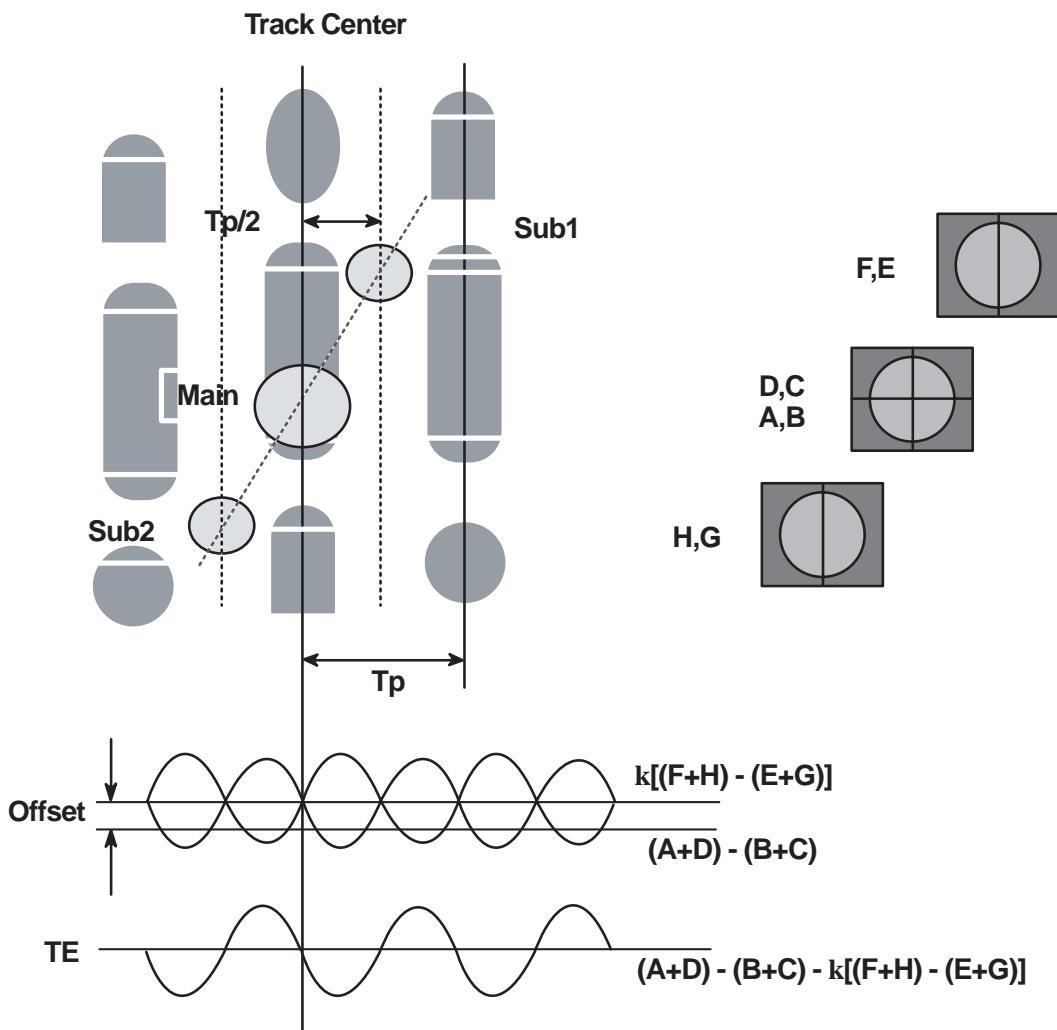
This signal is generated in RF IC (LIC121 : AN22113A) and controls the pick-up's up and down to focus on Disc.

2) Tracking Error Signal (DPP Method) ==> $\{(A+D)-(B+C)\} - k \times \{(EF_1+EF_4)-(EF_2+EF_3)\}$

This signal is generated in RF IC (LIC121 : AN22113A) and controls the pick-up's left and right shift to find to track on Disc.

3) RF Signal ==> $(A+B+C+D)$

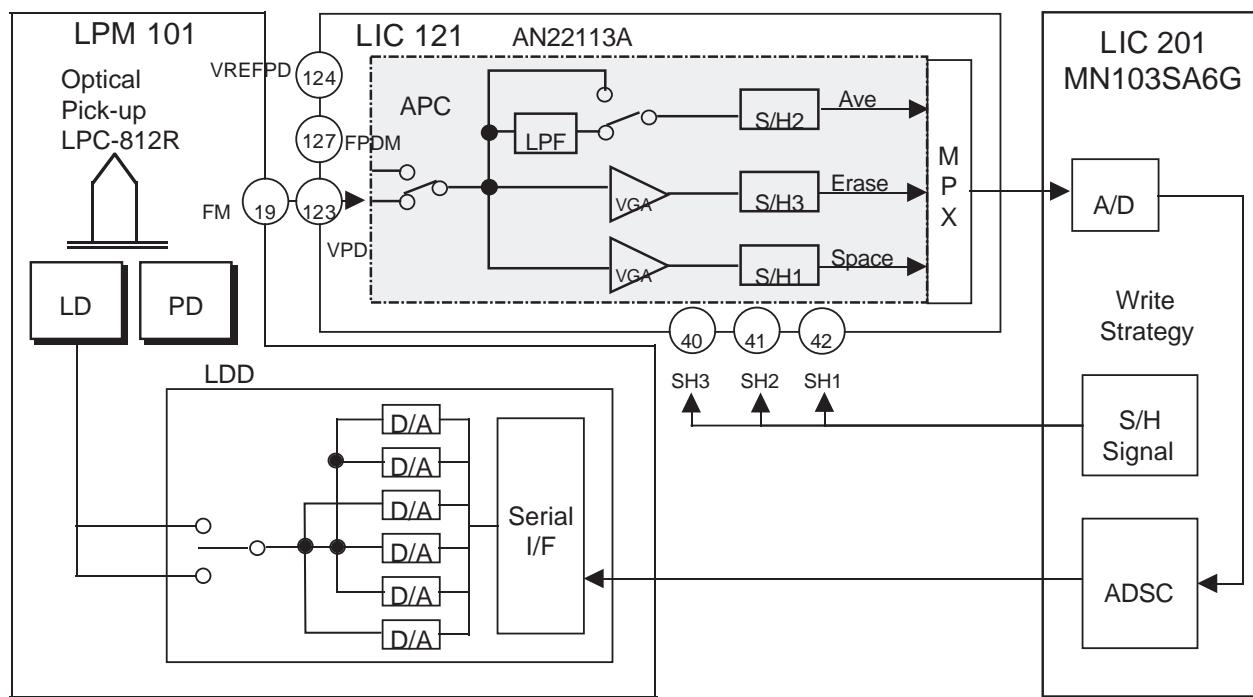
This signal is converted to DATA signal in DSP IC (LIC201 : MN103SA6G).



DESCRIPTION OF CIRCUIT

1. ALPC (AUTOMATIC LASER POWER CONTROL) CIRCUIT

1-1. BLOCK DIAGRAM

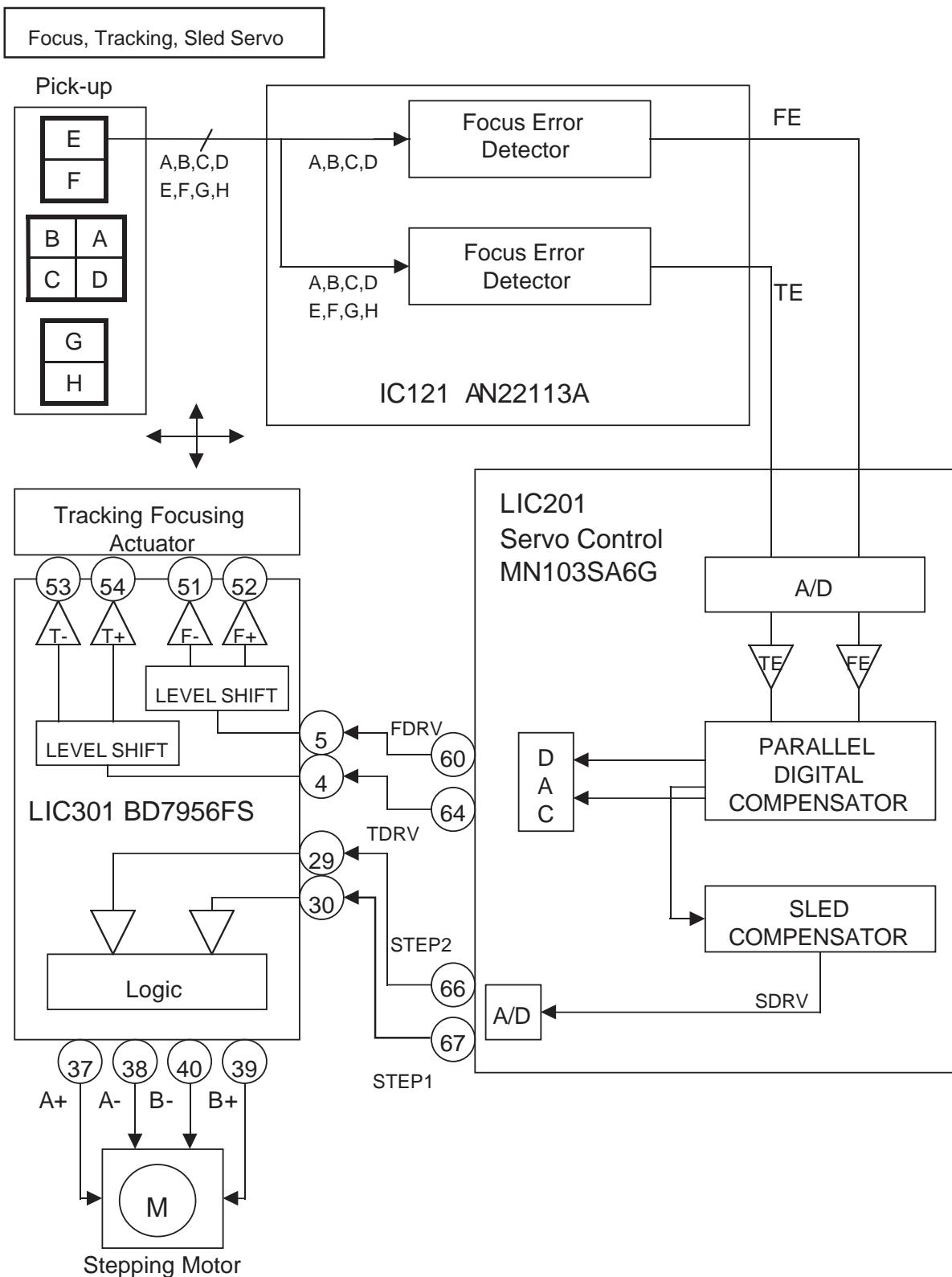


1-2. ALPC (AUTOMATIC LASER POWER CONTROL) CIRCUIT OPERATION

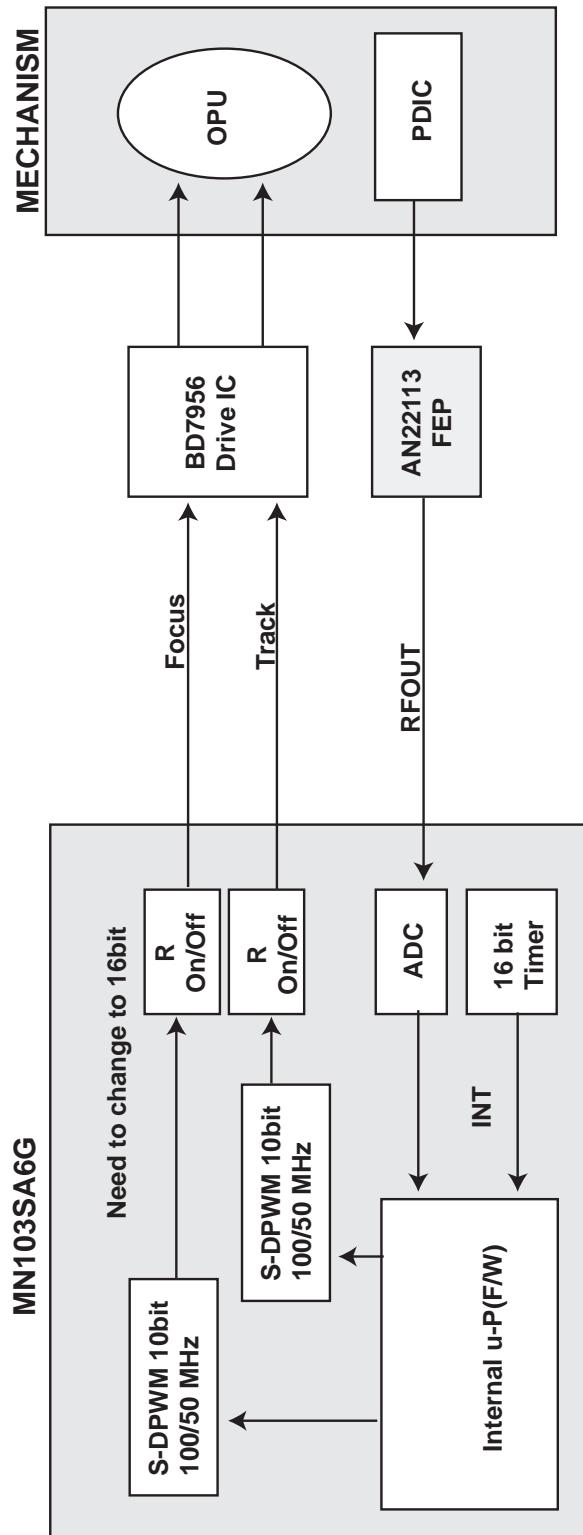
THE ALPC BLOCK DETECTS THE LASER OUTPUT POWER OF THE FRONT MONITOR. THE POWER SIGNAL DETECTED WITH THE PD FOR FRONT MONITOR
DETECTION IS INPUT THE VOLTAGE FROM THE VPD PIN(123PIN) OR THE FPDM PIN(127PIN), THE REFERENCE SIGNAL OF THE INPUT SIGNAL IS
INPUT FROM THE VREFPD PIN(124PIN). THE ALPC BLOCK GENERATES THE SINGALS FROM THE INPUT LASER POWER SIGNALS IN THE
FOLLOWING DETECTION SYSTEMS. THIS BLOCK HAS FOUR DETECTION PATHS: ALL AVERAGE VALUE
PATH, MULTI PULSE AVERAGE/PEAK VALUE
DETECTION PATH, ERASE/BOTTOM VALUE DETECTION PATH, SPACE/PLAYBACK POWER VALUE
DETECTION PATH.

2. FOCUS/TRACKING/SLED SERVO CIRCUIT

2-1. FOCUS, TRACKING & SLED SERVO PROCESS

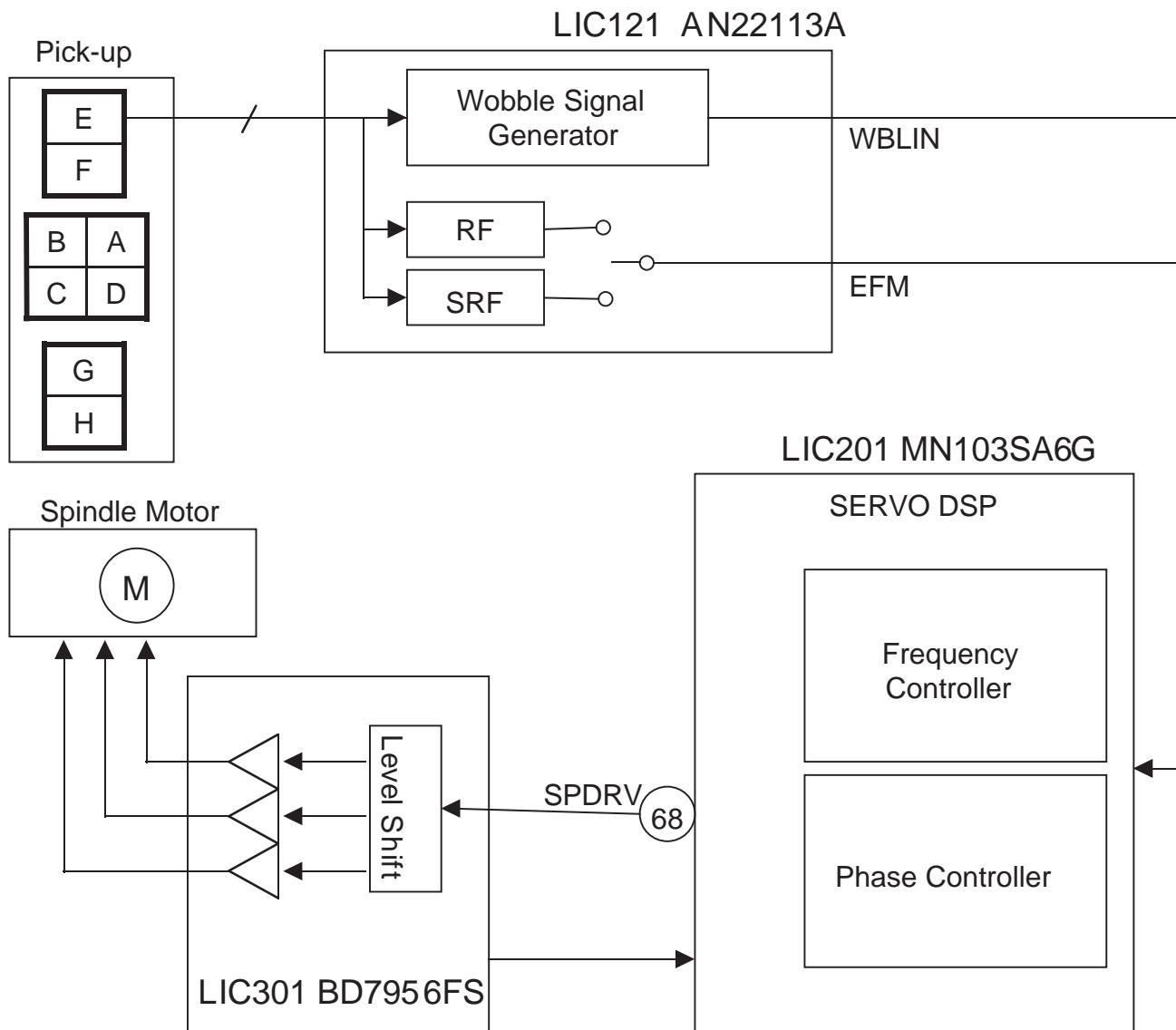


2-1. FOCUS, TRACKING & SLED SERVO PROCESS



3. SPINDLE SERVO CIRCUIT

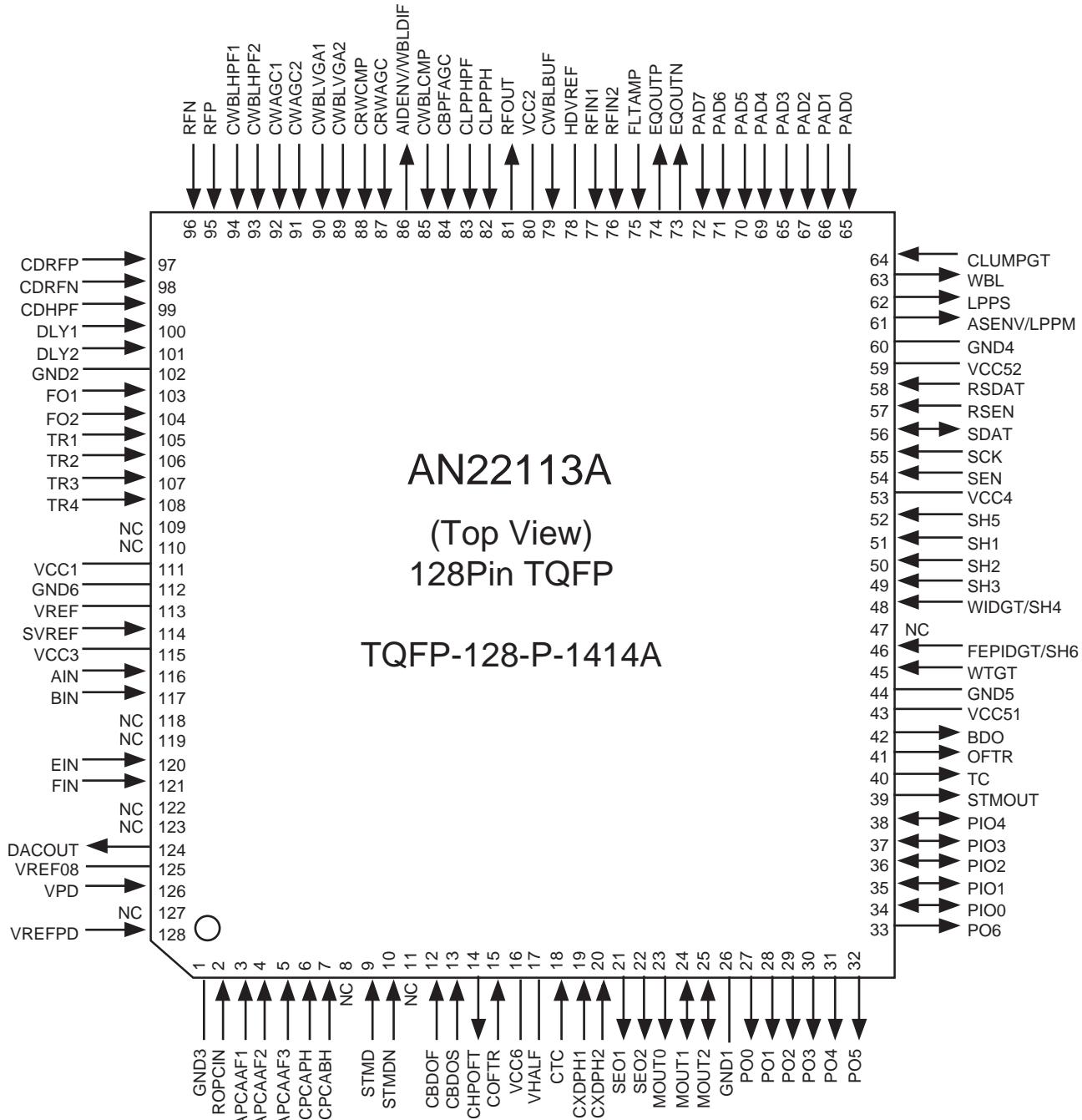
3-1. SPINDLE SERVO PROCESS



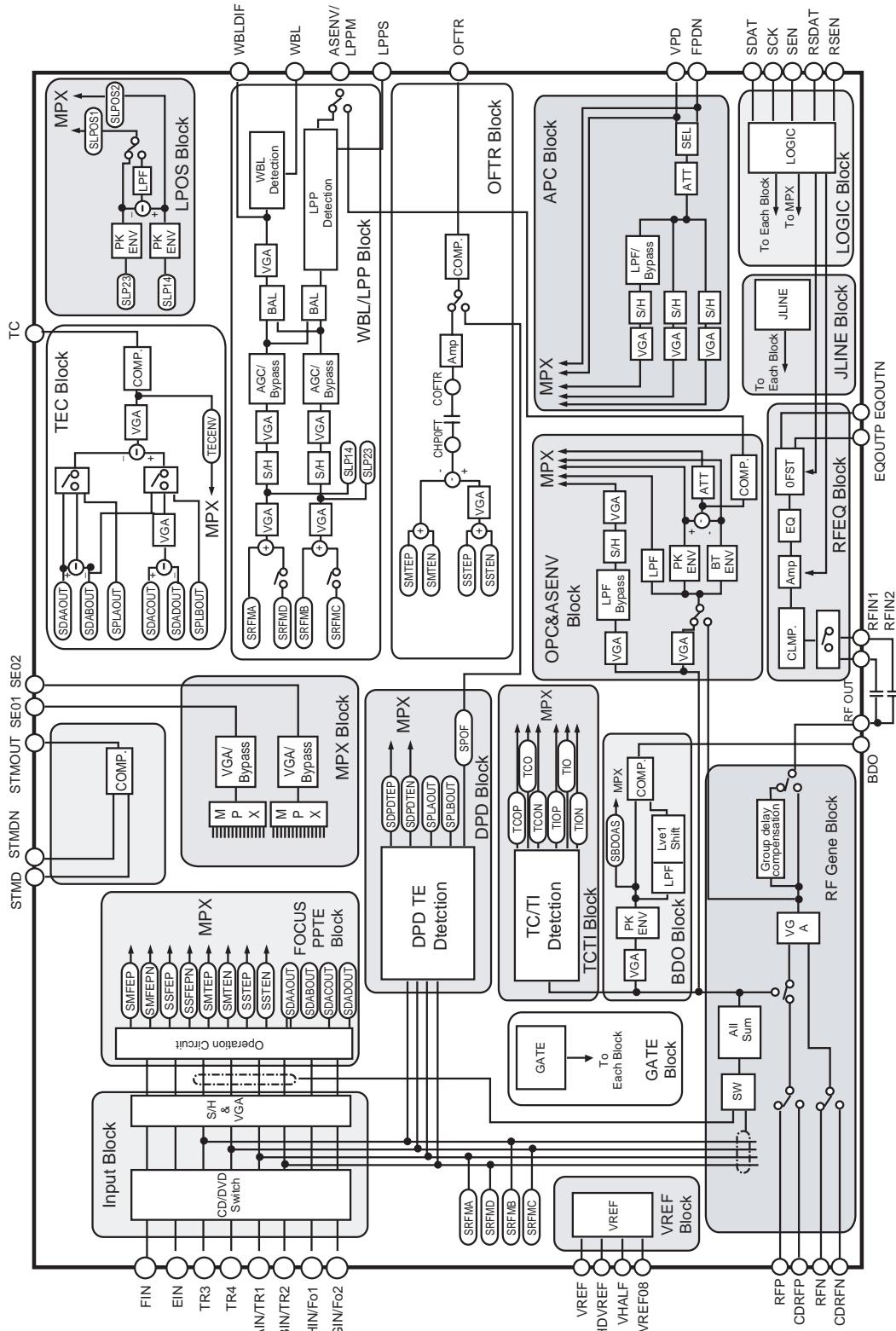
MAJOR IC INTERNAL BLOCK DIAGRAM

LIC121 (AN22113A) : FEP(RF) ANALOG SIGNAL PROCESSOR

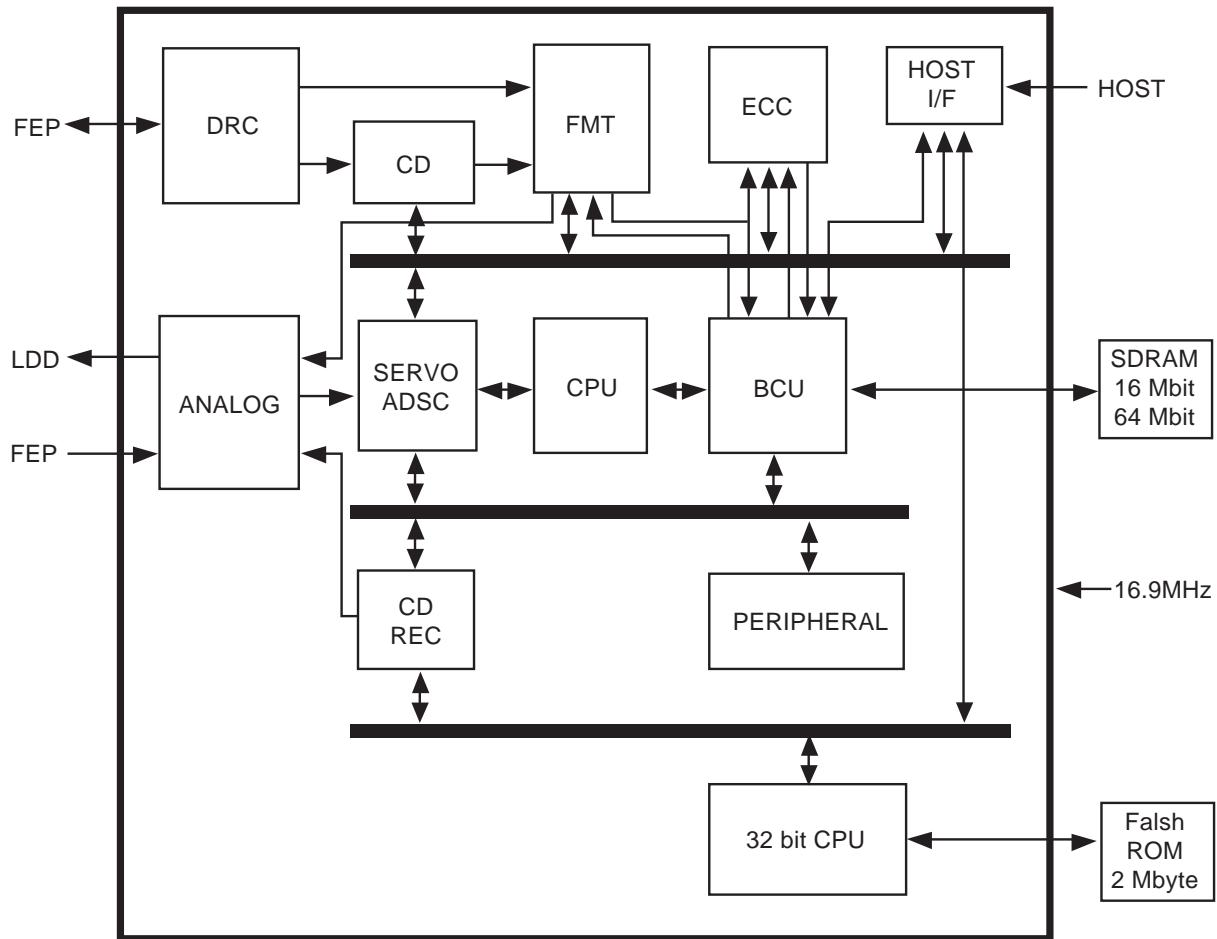
- PIN ASSIGNMENT



- **BLOCK DIAGRAM**

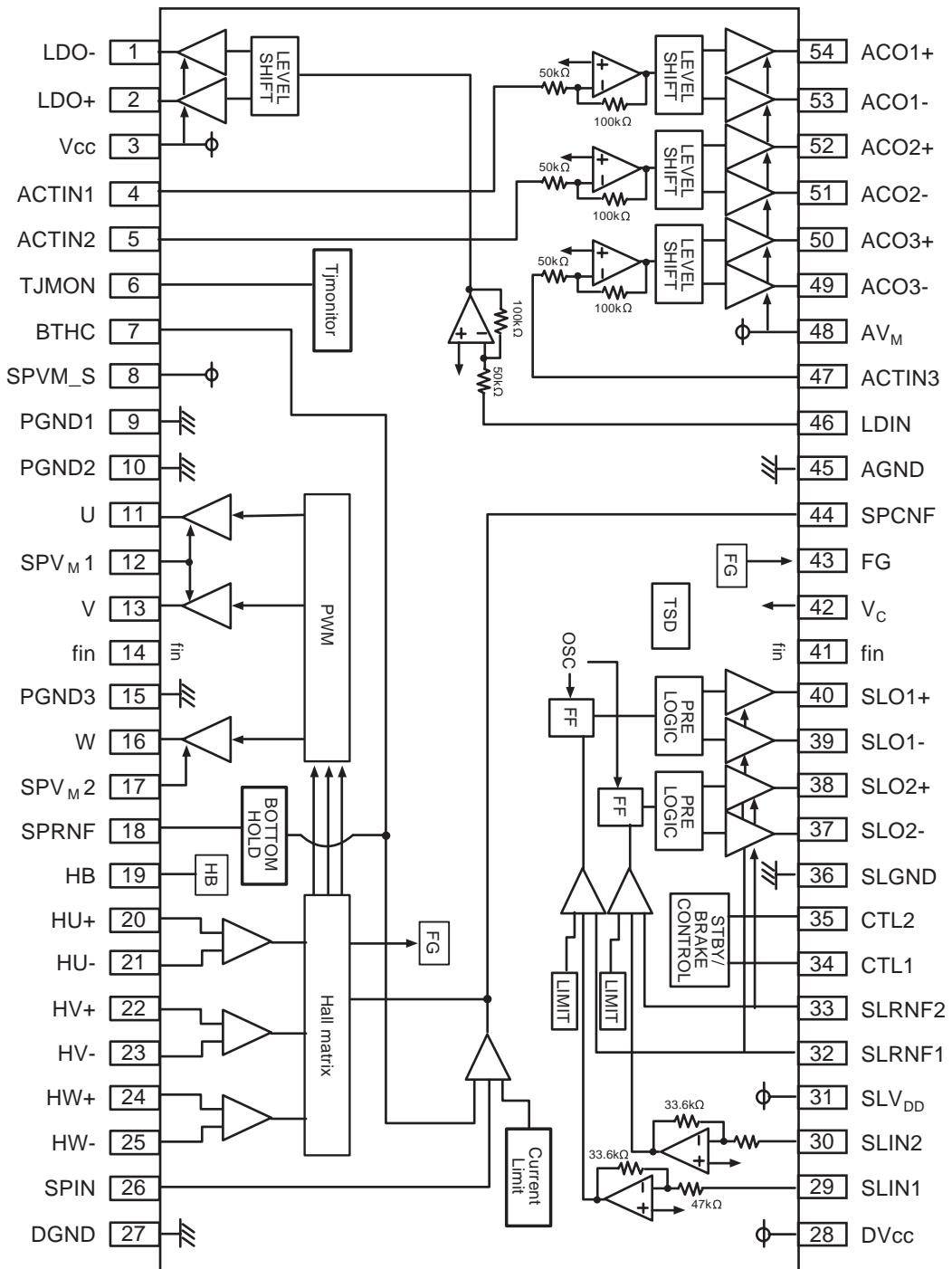


• BLOCK DIAGRAM



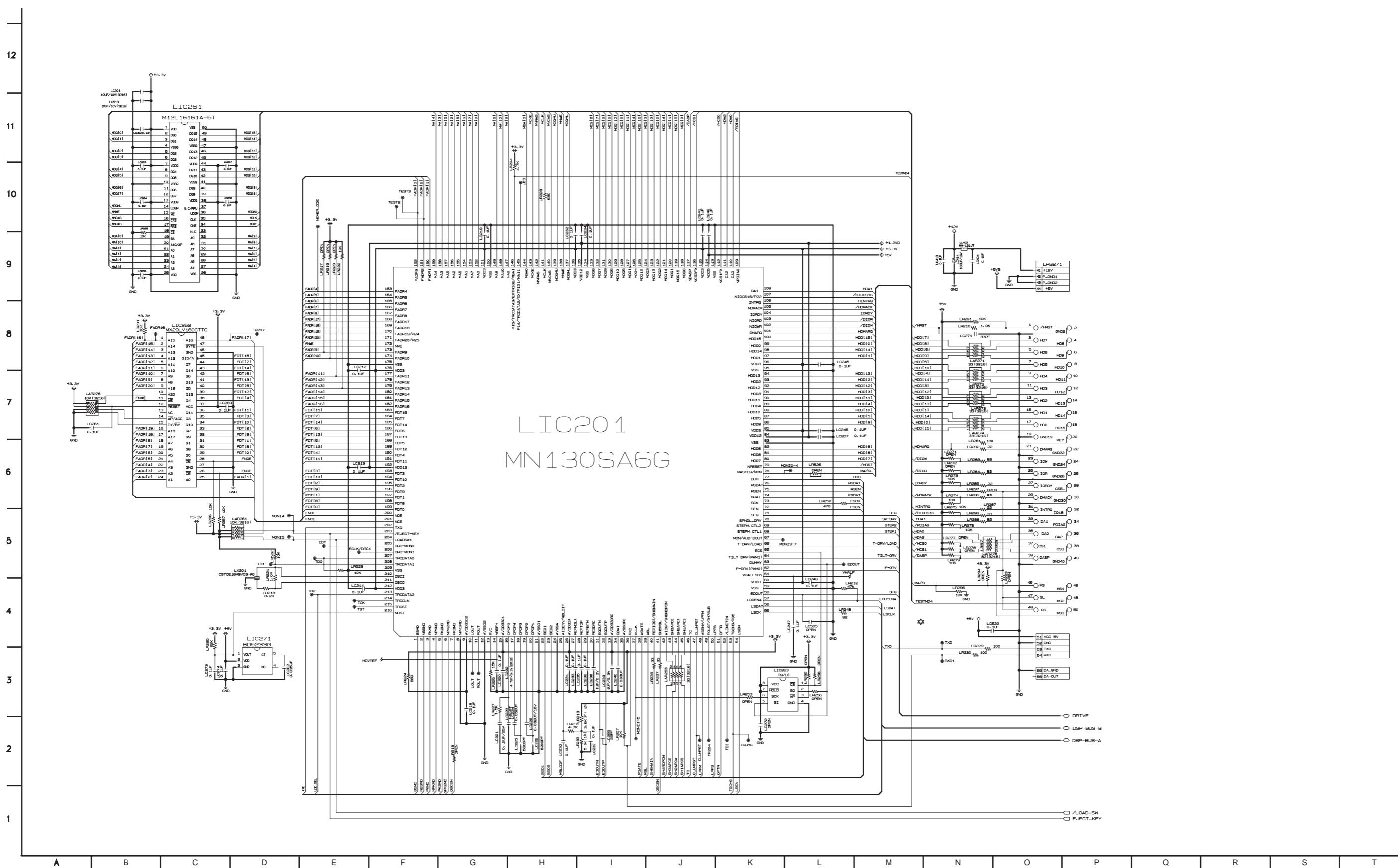
IC301 (BD7956FS) : CD-ROM/DVD-ROM 7CH POWER DRIVER

- BLOCK DIAGRAM

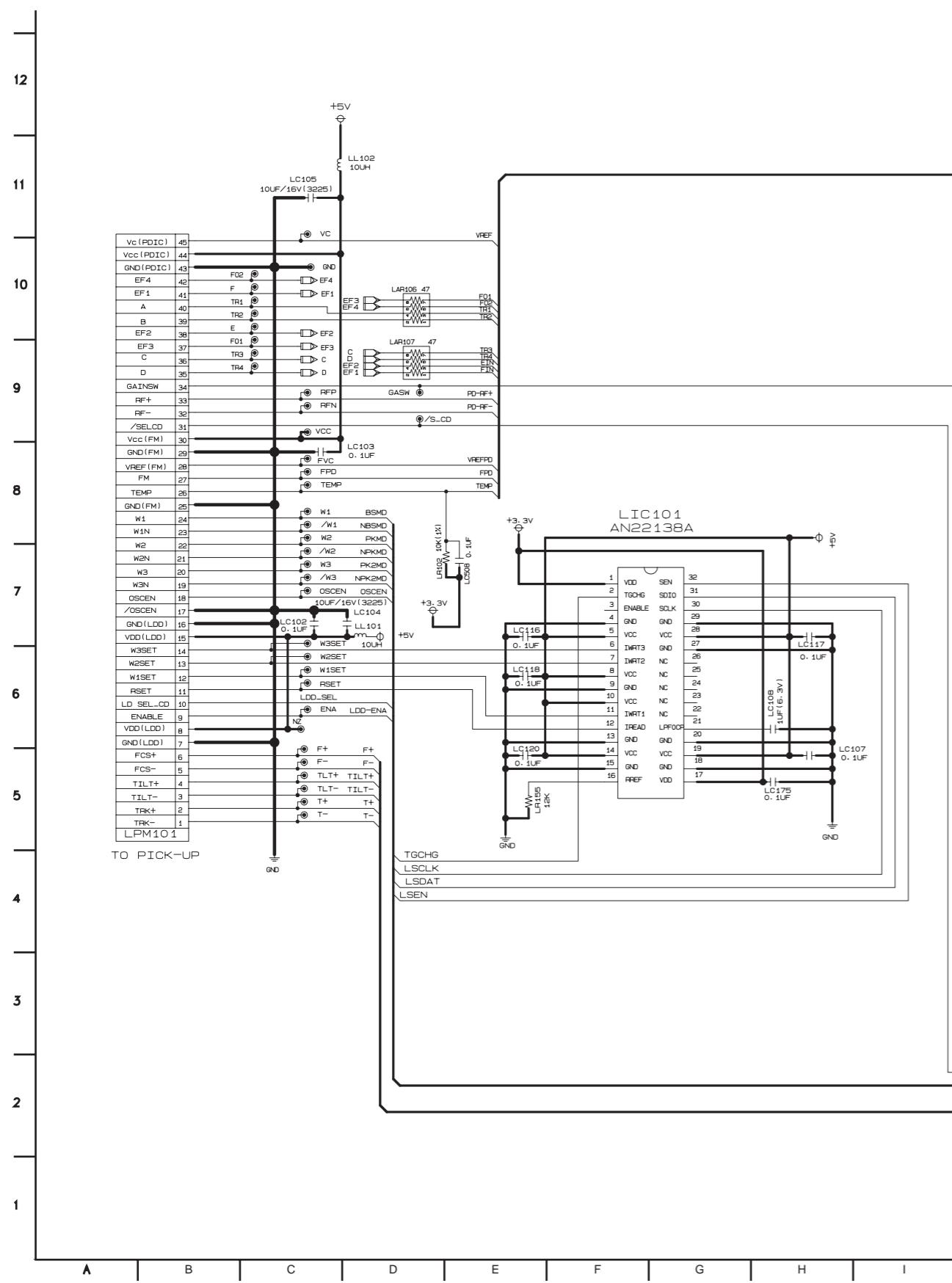


CIRCUIT DIAGRAMS

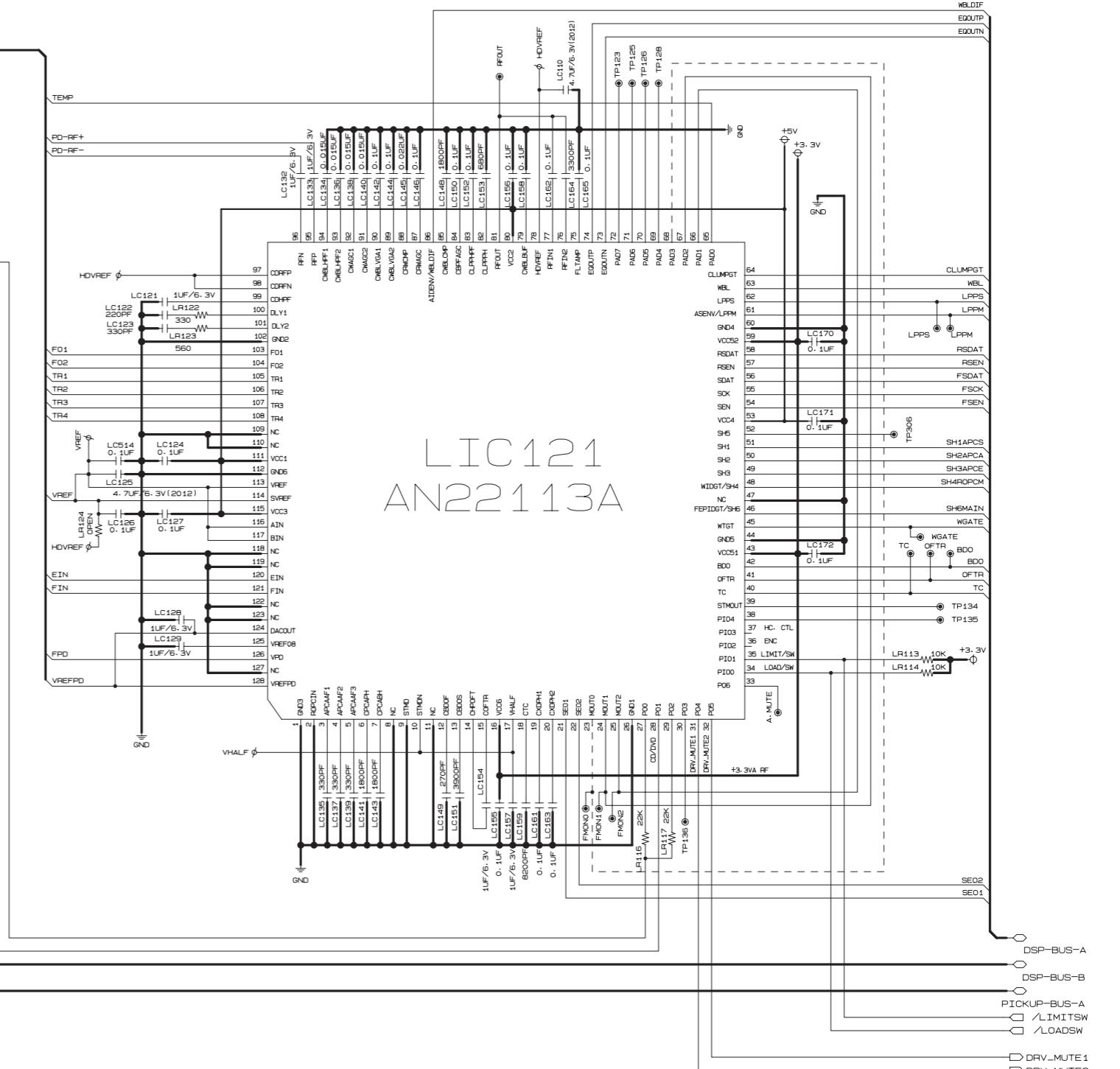
1. DSP CIRCUIT DIAGRAM



2. RF CIRCUIT DIAGRAM

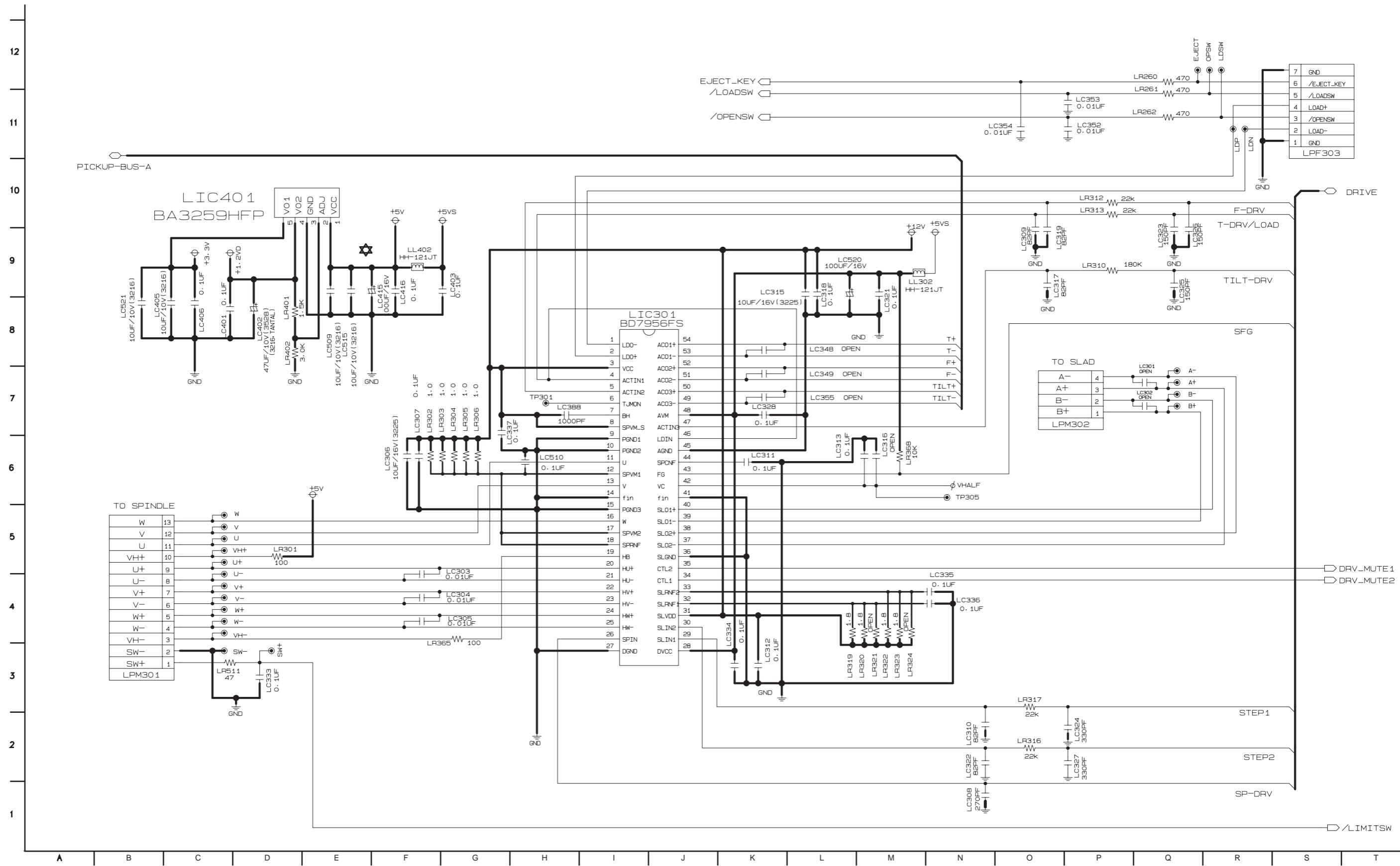


4-44



4-45

3. DRIVE CIRCUIT DIAGRAM

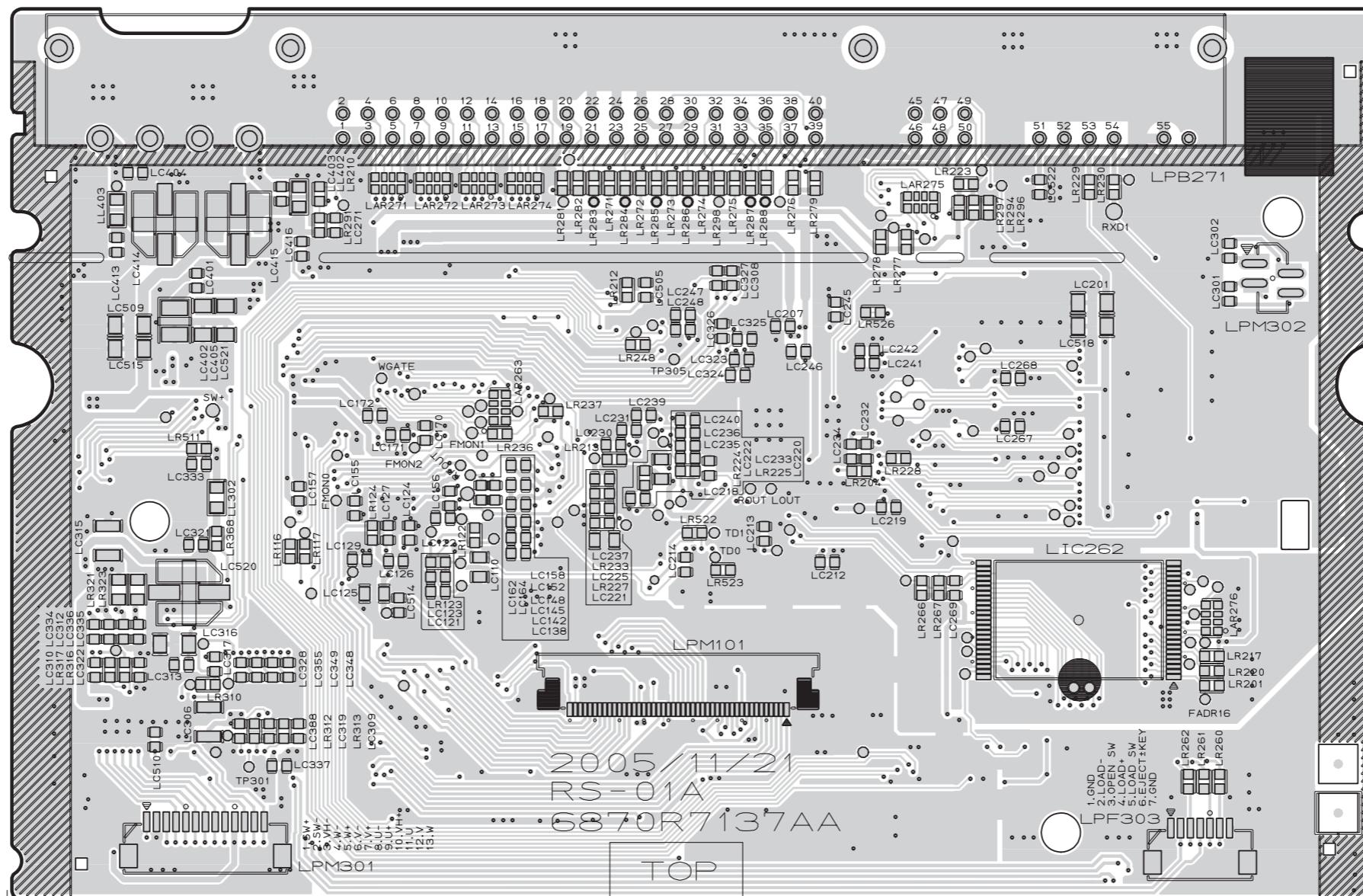


CIRCUIT VOLTAGE CHART

MODE PIN NO.	STATE
LIC101	
1	3.29
2	0.00
3	3.28
4	0.00
5	4.99
6	0.00
7	0.00
8	4.98
9	0.00
10	4.99
11	0.00
12	0.00
13	0.00
14	4.99
15	0.00
16	1.09
17	3.28
18	0.00
19	4.99
20	0.00
21	0.00
22	0.00
23	1.33
24	1.10
25	1.33
26	0.00
27	0.00
28	3.30
29	0.00
30	0.00
31	0.00
32	0.00
33	0.00
34	0.00
35	3.20
36	3.29
37	0.00
38	0.00
39	0.00
40	0.00
41	1.61
42	3.29
43	3.30
44	0.00
45	0.00
46	0.00
47	0.00
48	0.00
49	0.00
50	0.00
51	0.00
52	0.00
53	0.36
54	0.00
55	1.23
56	1.65
57	1.62
58	1.65
59	0.61
60	3.29
61	1.44
62	0.00
63	1.65
64	0.00
65	1.65
66	0.00
67	1.21
68	1.34
69	1.34
70	1.35
71	0.50
72	2.51
73	0.00
74	2.50
75	0.00
76	2.03
77	0.00
78	1.64
79	2.19
80	2.16
81	4.98
82	2.18
83	2.96
84	2.79
85	2.21
86	2.49
87	2.85
88	2.20
89	2.16
90	2.19
91	2.19
92	2.19
93	1.68
94	1.67
95	2.37
96	2.37
97	2.19
98	2.19
99	1.62
100	1.49
101	1.44
102	0.00
103	1.65
104	1.65
105	1.29
106	2.02
107	2.24
108	1.73
109	1.29
110	1.65
111	0.00
112	1.65
113	0.00
114	1.65
115	1.65
116	0.00
117	1.65
118	0.00
119	1.65
120	1.65
121	1.66
122	0.00
123	1.66
124	0.00
125	1.66
126	0.00
127	1.65
128	0.00
129	1.65
130	0.00
131	1.65
132	0.00
133	1.65
134	0.00
135	1.65
136	0.00
137	1.65
138	0.00
139	1.65
140	0.00
141	1.65
142	0.00
143	1.65
144	0.00
145	1.65
146	0.00
147	1.65
148	0.00
149	1.65
150	0.00
151	1.65
152	0.00
153	1.65
154	0.00
155	1.65
156	0.00
157	1.65
158	0.00
159	1.65
160	0.00
161	1.65
162	0.00
163	1.65
164	0.00
165	1.65
166	0.00
167	1.65
168	PULSE
169	PULSE
170	PULSE
171	PULSE
172	PULSE
173	PULSE
174	PULSE
175	PULSE
176	PULSE
177	PULSE
178	PULSE
179	PULSE
180	PULSE
181	PULSE
182	PULSE
183	PULSE
184	PULSE
185	PULSE
186	PULSE
187	PULSE
188	PULSE
189	PULSE
190	PULSE
191	PULSE
192	PULSE
193	PULSE
194	PULSE
195	PULSE
196	PULSE
197	PULSE
198	PULSE
199	PULSE
200	PULSE
201	PULSE
202	PULSE
203	PULSE
204	PULSE
205	PULSE
206	PULSE
207	PULSE
208	PULSE
209	PULSE
210	OSC
211	OSC
212	PULSE
213	PULSE
214	PULSE
215	PULSE
216	PULSE
217	PULSE
218	PULSE
219	PULSE
220	PULSE
221	PULSE
222	PULSE
223	PULSE
224	PULSE
225	PULSE
226	PULSE
227	PULSE
228	PULSE
229	PULSE
230	PULSE
231	PULSE
232	PULSE
233	PULSE
234	PULSE
235	PULSE
236	PULSE
237	PULSE
238	PULSE
239	PULSE
240	PULSE
241	PULSE
242	PULSE
243	PULSE
244	PULSE
245	PULSE
246	PULSE
247	PULSE
248	PULSE
249	PULSE
250	PULSE
251	PULSE
252	PULSE
253	PULSE
254	PULSE
255	PULSE
256	PULSE
257	PULSE
258	PULSE
259	PULSE
260	PULSE
261	PULSE
262	PULSE
263	PULSE
264	PULSE
265	PULSE
266	PULSE
267	PULSE
268	PULSE
269	PULSE
270	PULSE
271	PULSE
272	PULSE
273	PULSE
274	PULSE
275	PULSE
276	PULSE
277	PULSE
278	PULSE
279	PULSE
280	PULSE
281	PULSE
282	PULSE
283	PULSE
284	PULSE
285	PULSE
286	PULSE
287	PULSE
288	PULSE
289	PULSE
290	PULSE
291	PULSE
292	PULSE
293	PULSE
294	PULSE
295	PULSE
296	PULSE
297	PULSE
298	PULSE
299	PULSE
300	PULSE
301	PULSE
302	PULSE
303	PULSE
304	PULSE
305	PULSE
306	PULSE
307	PULSE
308	PULSE
309	PULSE
310	PULSE
311	PULSE
312	PULSE
313	PULSE
314	PULSE
315	PULSE
316	PULSE
317	PULSE
318	PULSE
319	PULSE
320	PULSE
321	PULSE
322	PULSE
323	PULSE
324	PULSE
325	PULSE
326	PULSE
327	PULSE
328	PULSE
329	PULSE
330	PULSE
331	PULSE
332	PULSE
333	PULSE
334	PULSE
335	PULSE
336	PULSE
337	PULSE
338	PULSE
339	PULSE
340	PULSE
341	PULSE
342	PULSE
343	PULSE
344	PULSE
345	PULSE
346	PULSE
347	PULSE
348	PULSE
349	PULSE
350	PULSE
351	PULSE
352	PULSE
353	PULSE
354	PULSE
355	PULSE
356	PULSE
357	PULSE
358	PULSE
359	PULSE
360	PULSE
361	PULSE
362	PULSE
363	PULSE
364	PULSE
365	PULSE
366	PULSE
367	PULSE
368	PULSE
369	PULSE
370	PULSE
371	PULSE
372	PULSE
373	PULSE
374	PULSE
375	PULSE
376	PULSE
377	PULSE
378	PULSE
379	PULSE
380	PULSE
381	PULSE
382	PULSE
383	PULSE
384	PULSE
385	PULSE
386	PULSE
387	PULSE
388	PULSE
389	PULSE
390	PULSE
391	PULSE
392	PULSE

PRINTED CIRCUIT DIAGRAMS

1. MAIN P.C.BOARD (TOP VIEW)



2. MAIN P.C.BOARD (BOTTOM VIEW)

