

Service Manual

General Description
Adjustment Procedures
Block / Schematic Diagrams
Exploded Views / Parts List

DVD-Recorder
LOEWE



MODELS

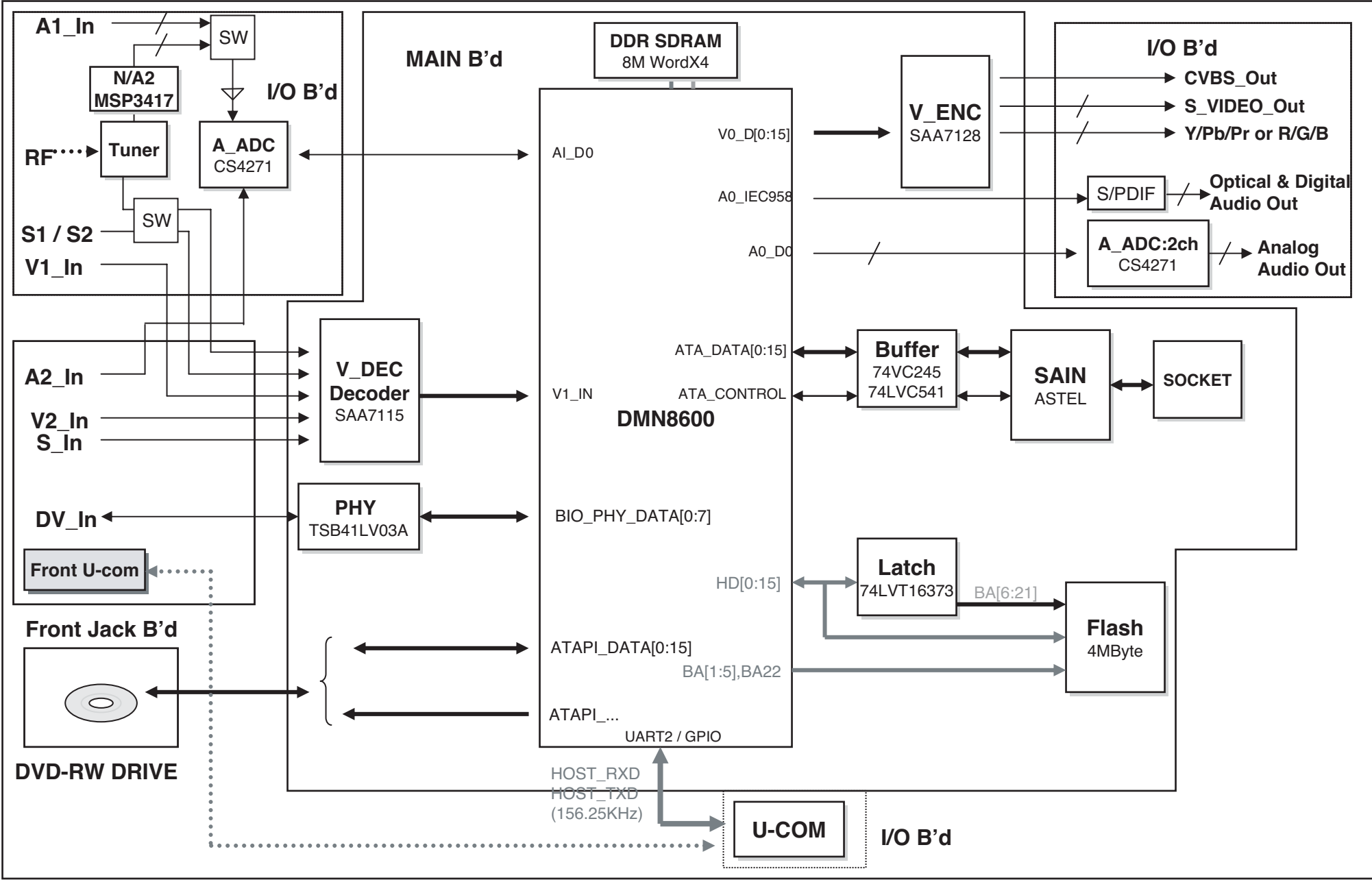
CENTROS 1102
Art.-Nr. 64501

CENTROS 1172
Art.-Nr. 64511

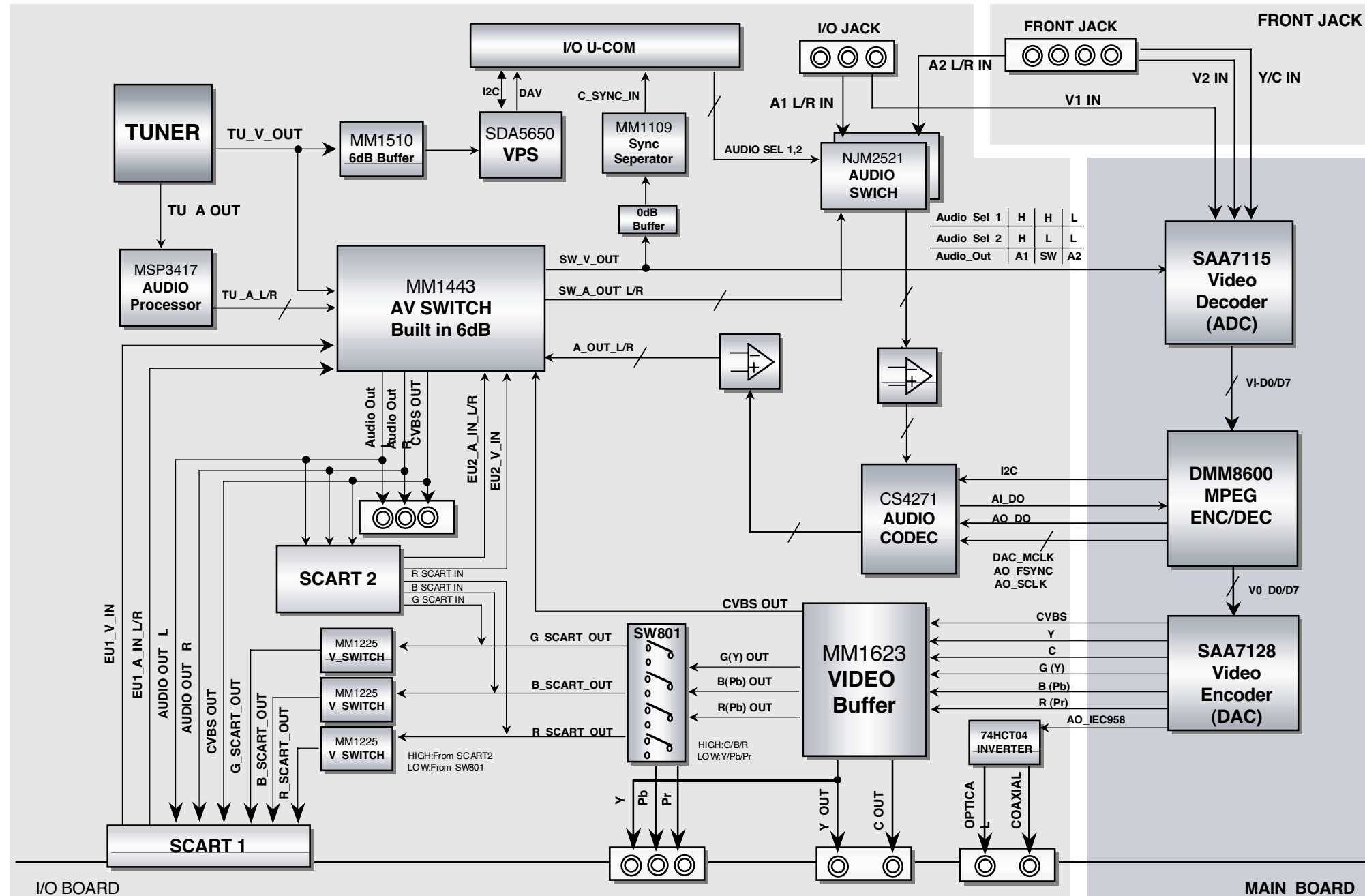
LOEWE.

BLOCK DIAGRAMS

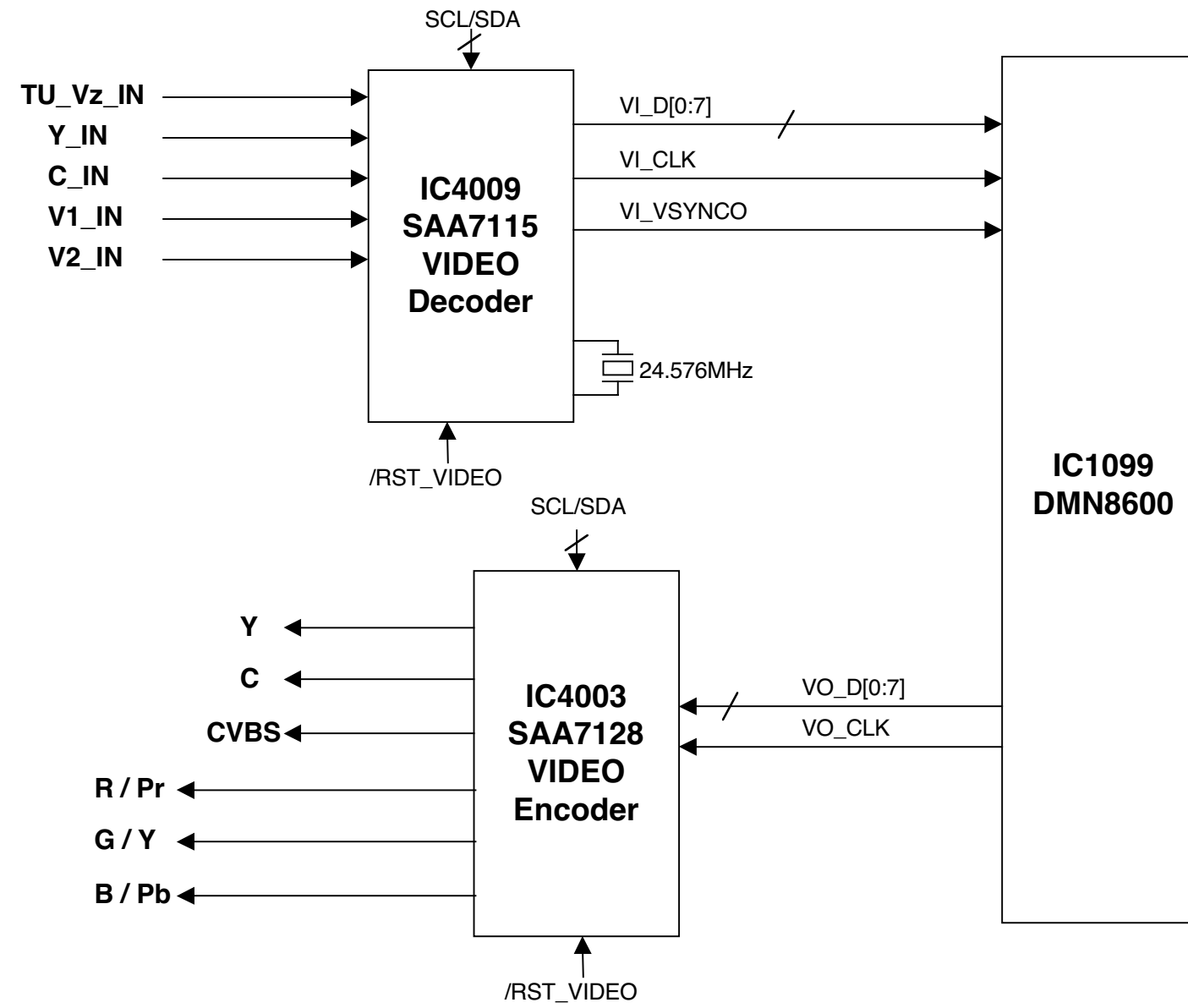
1. LSI Overall Block Diagram



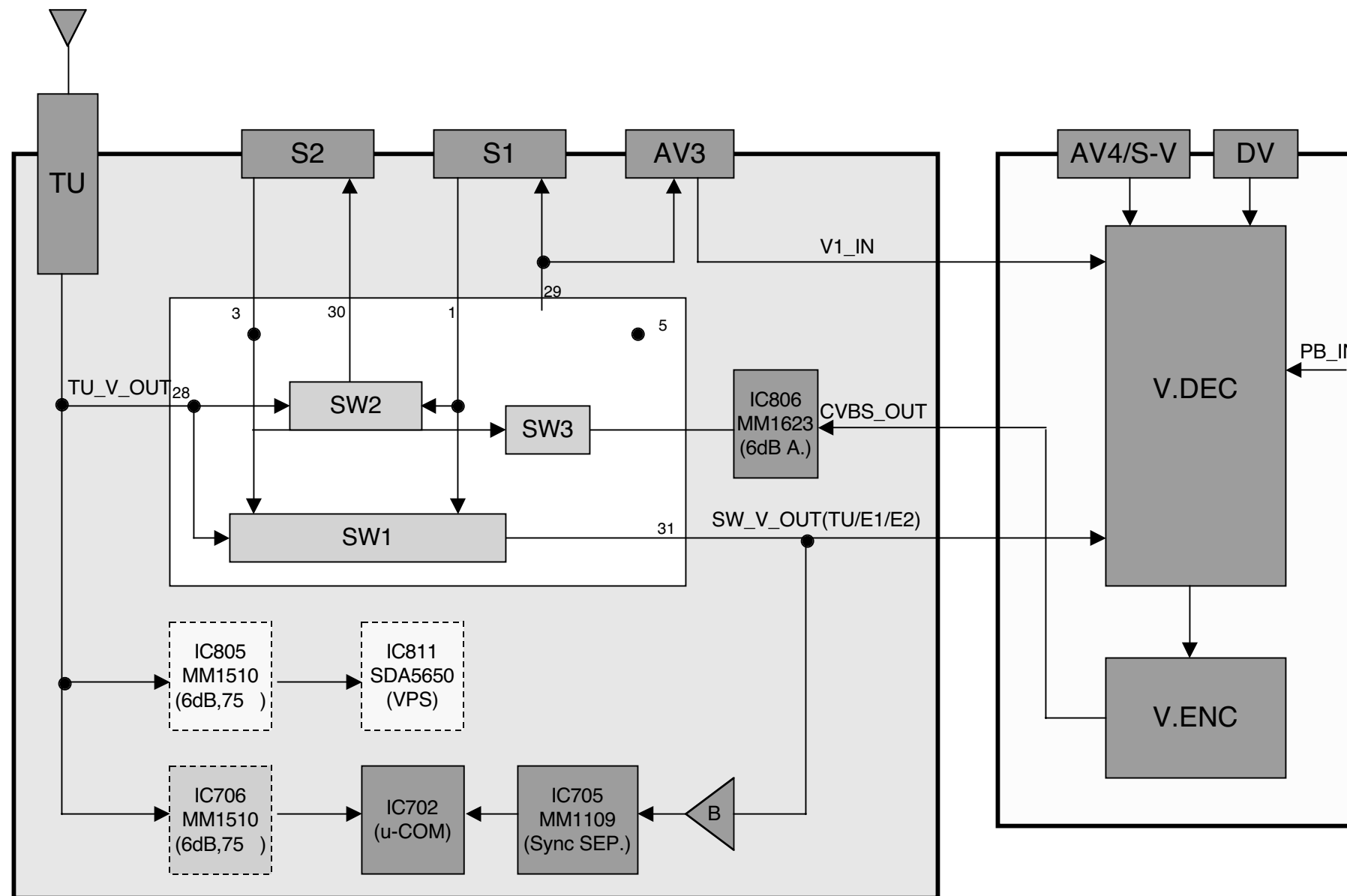
2. In/Out Block Diagram



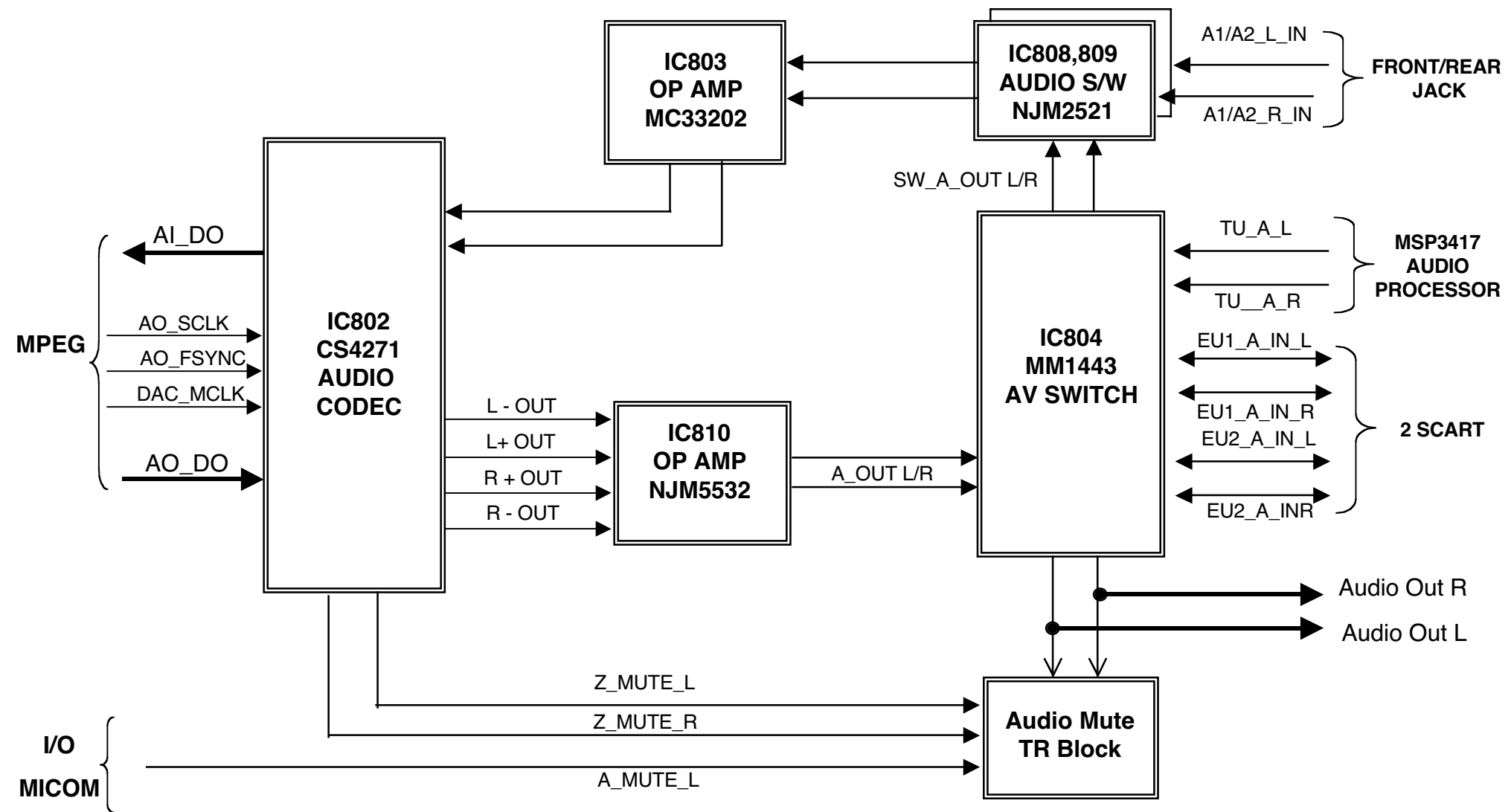
3. Video In/Out Block Diagram



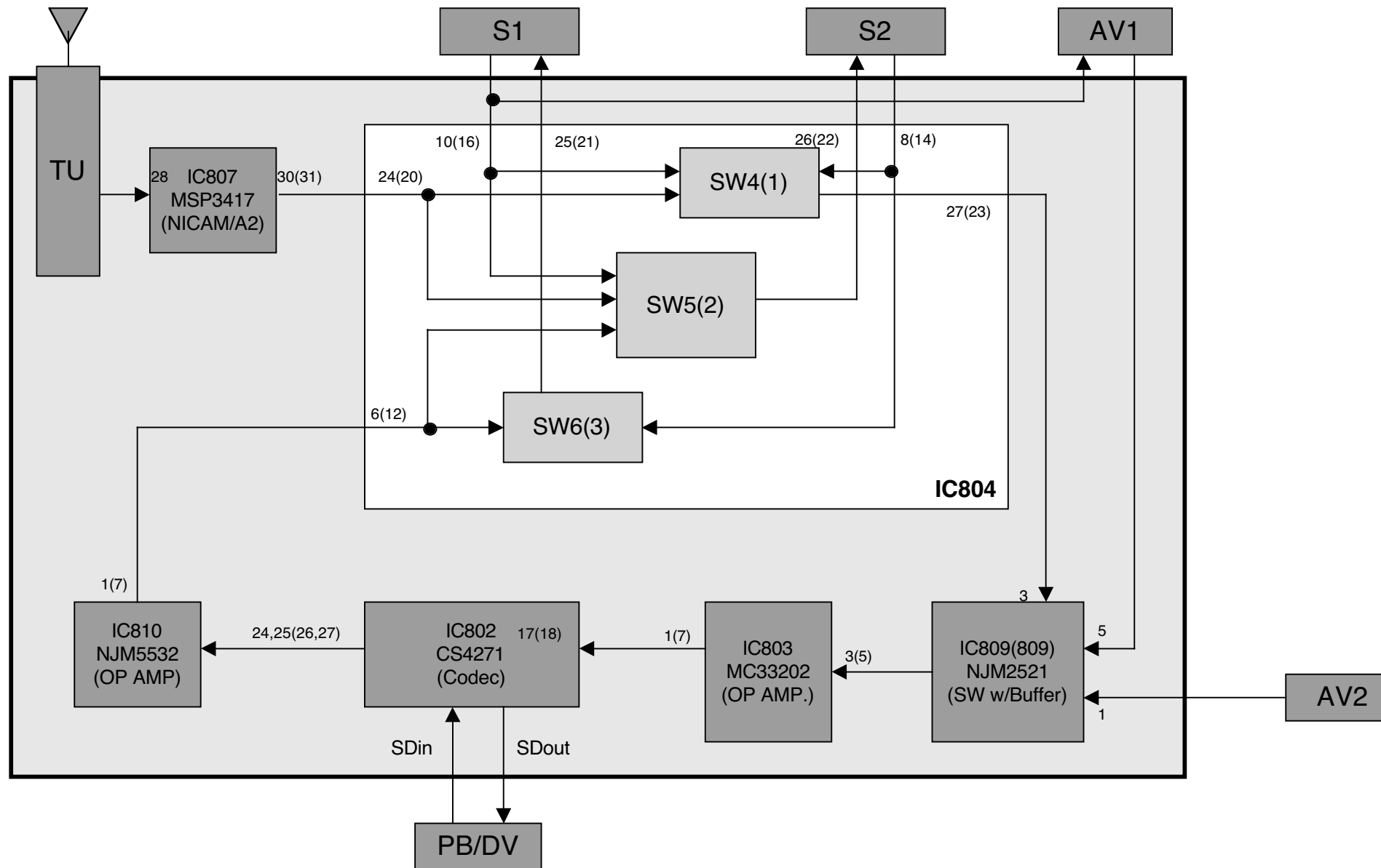
4. Video SW Path Block Diagram



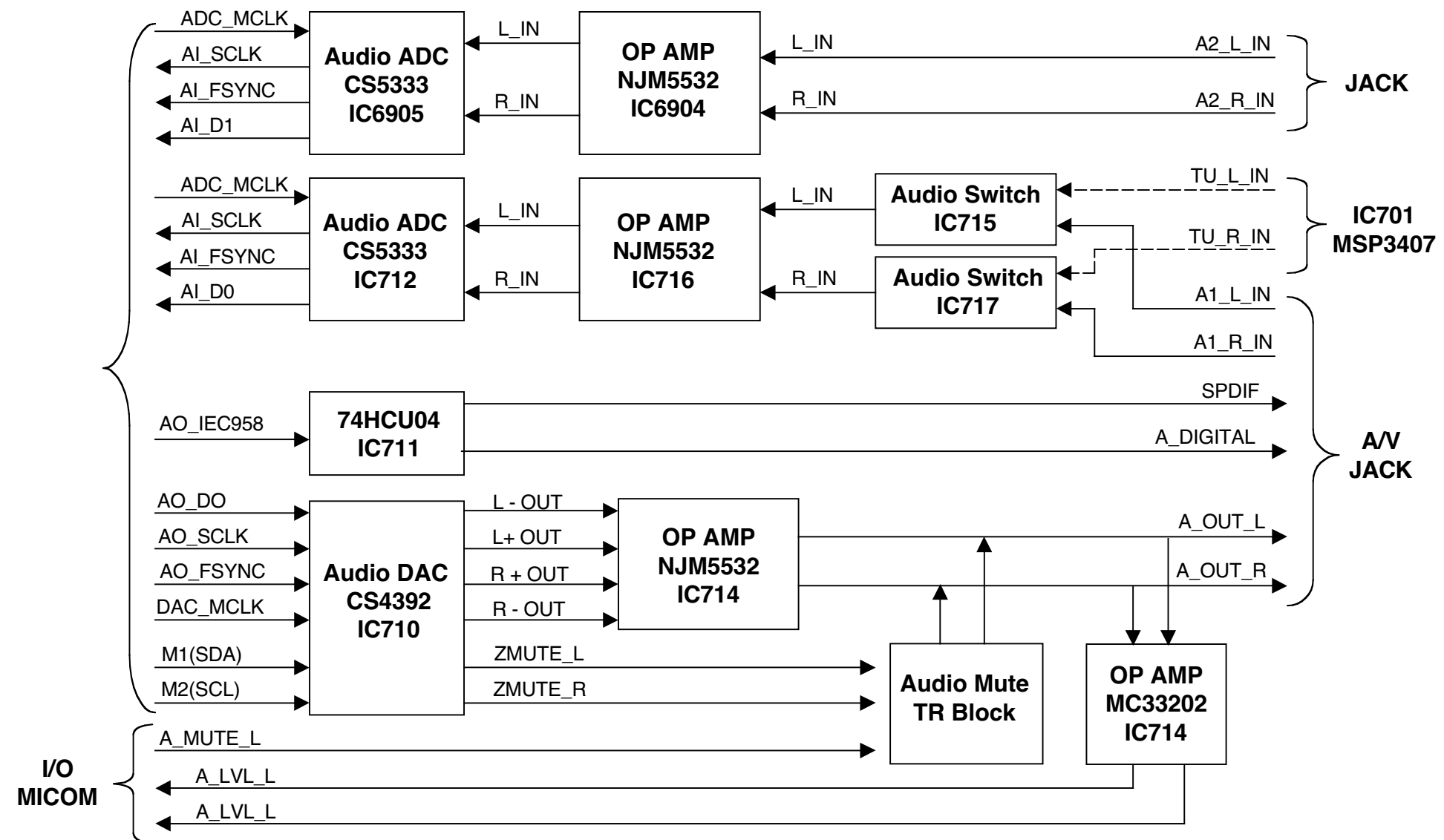
5. Audio Block Diagram



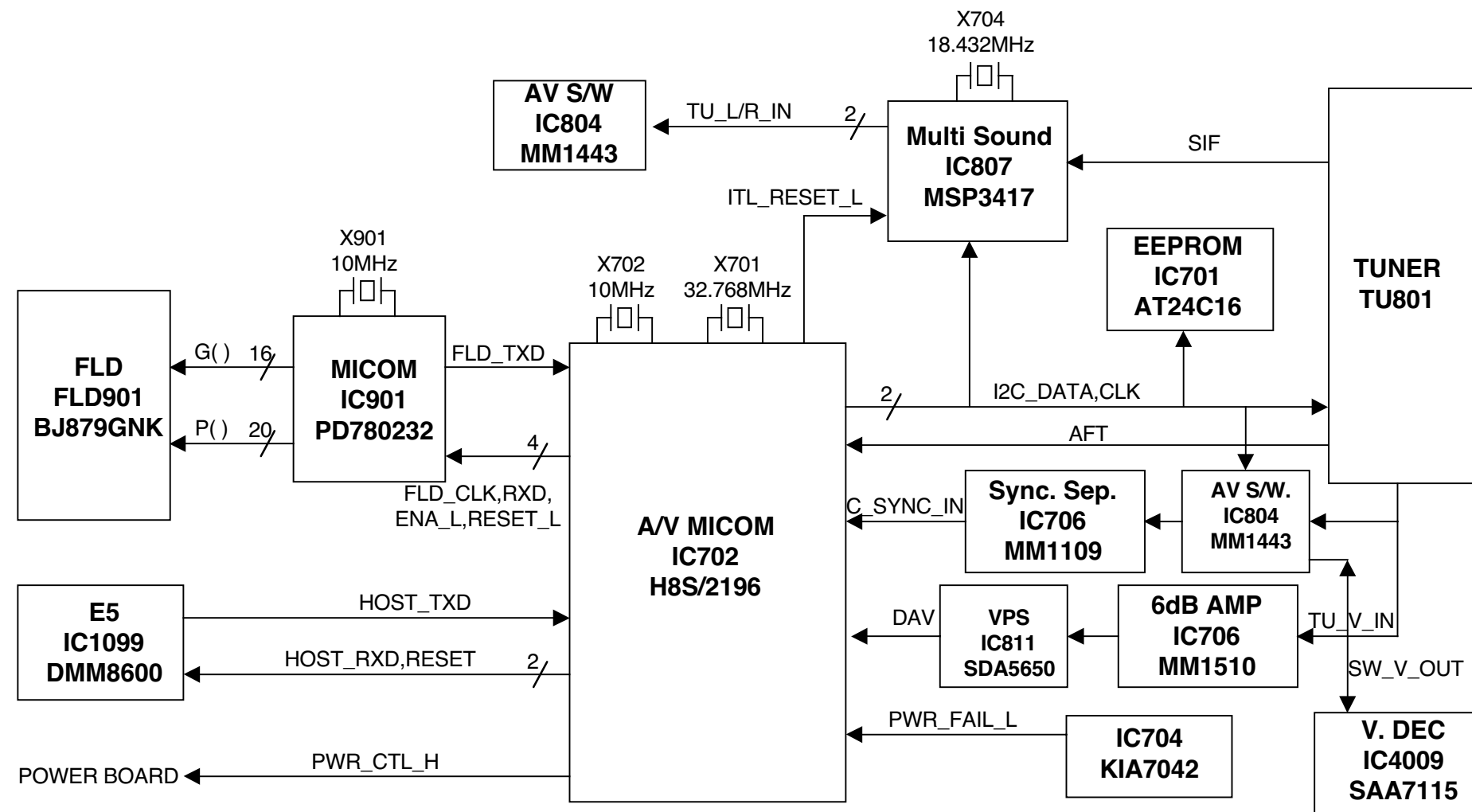
6. Audio SW Path Block Diagram



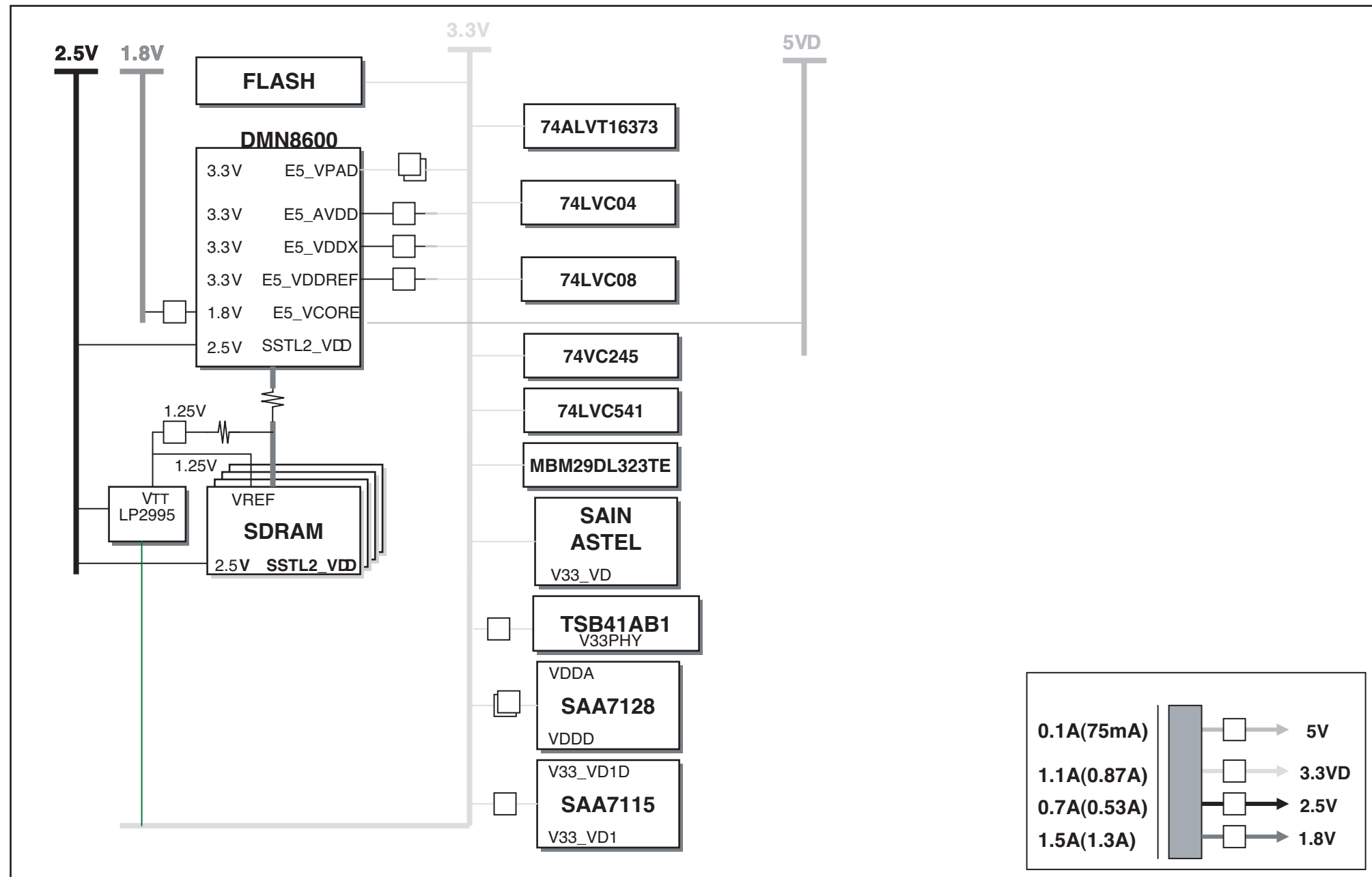
7. Audio In/Out Block Diagram



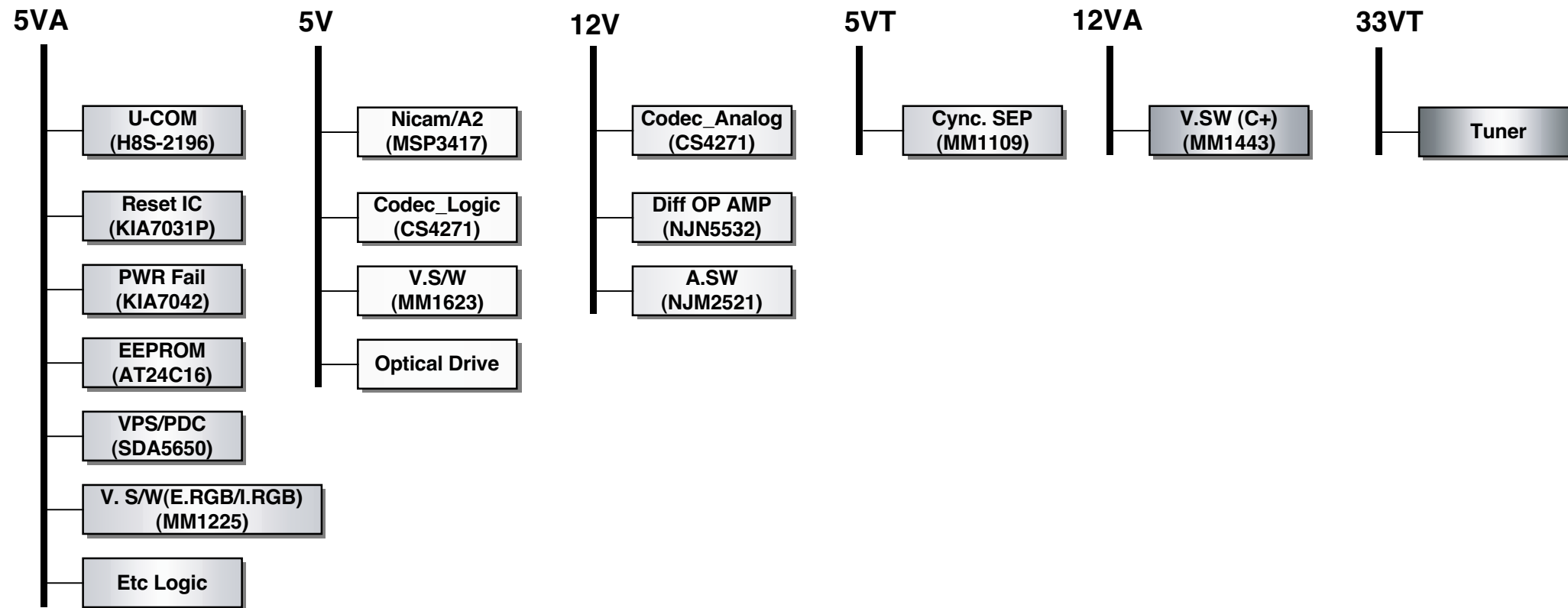
8. FLD/ μ -COM/Tuner Block Diagram



9. Power : Main Board Block Diagram

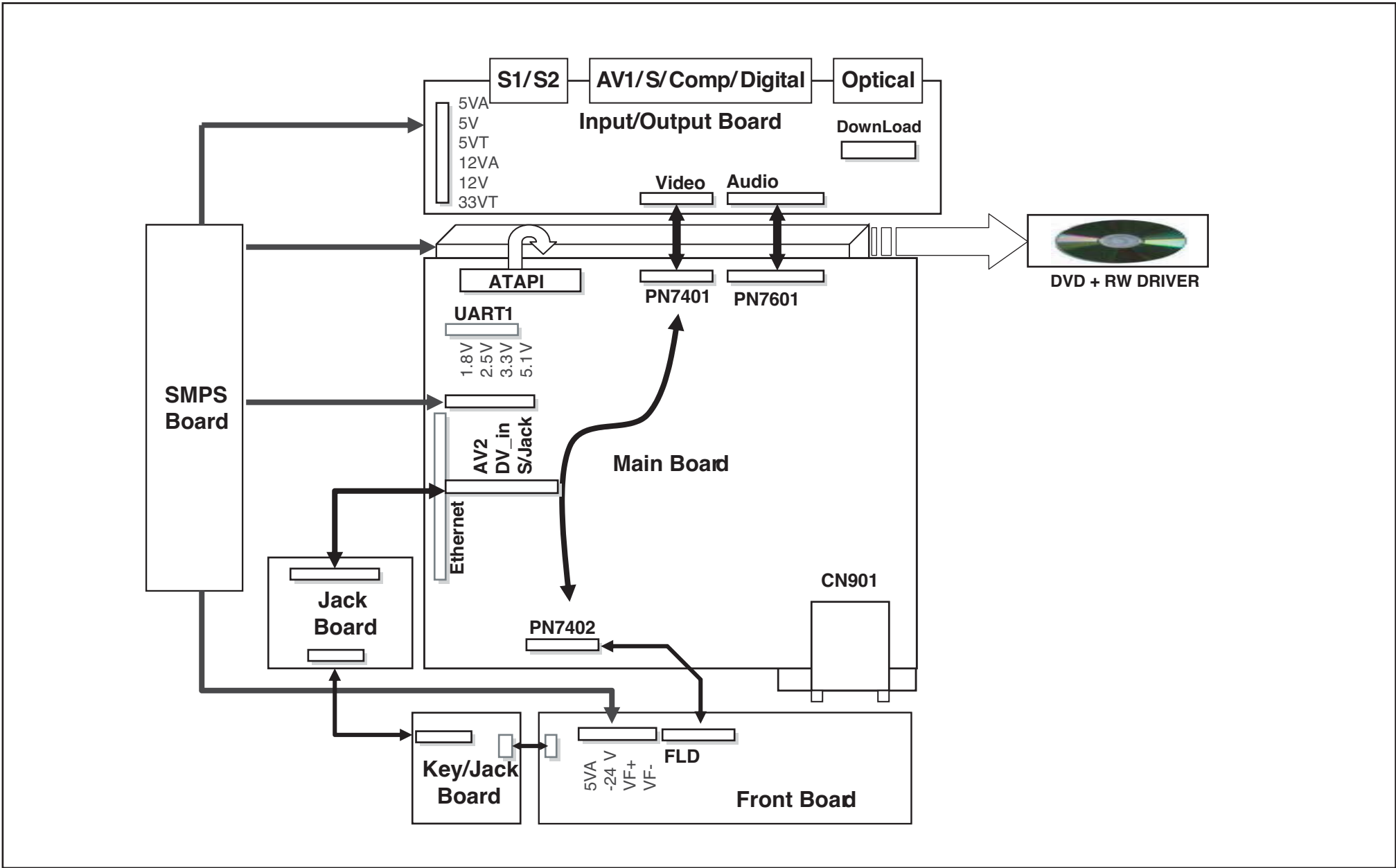


10. Power : I/O Board Block Diagram

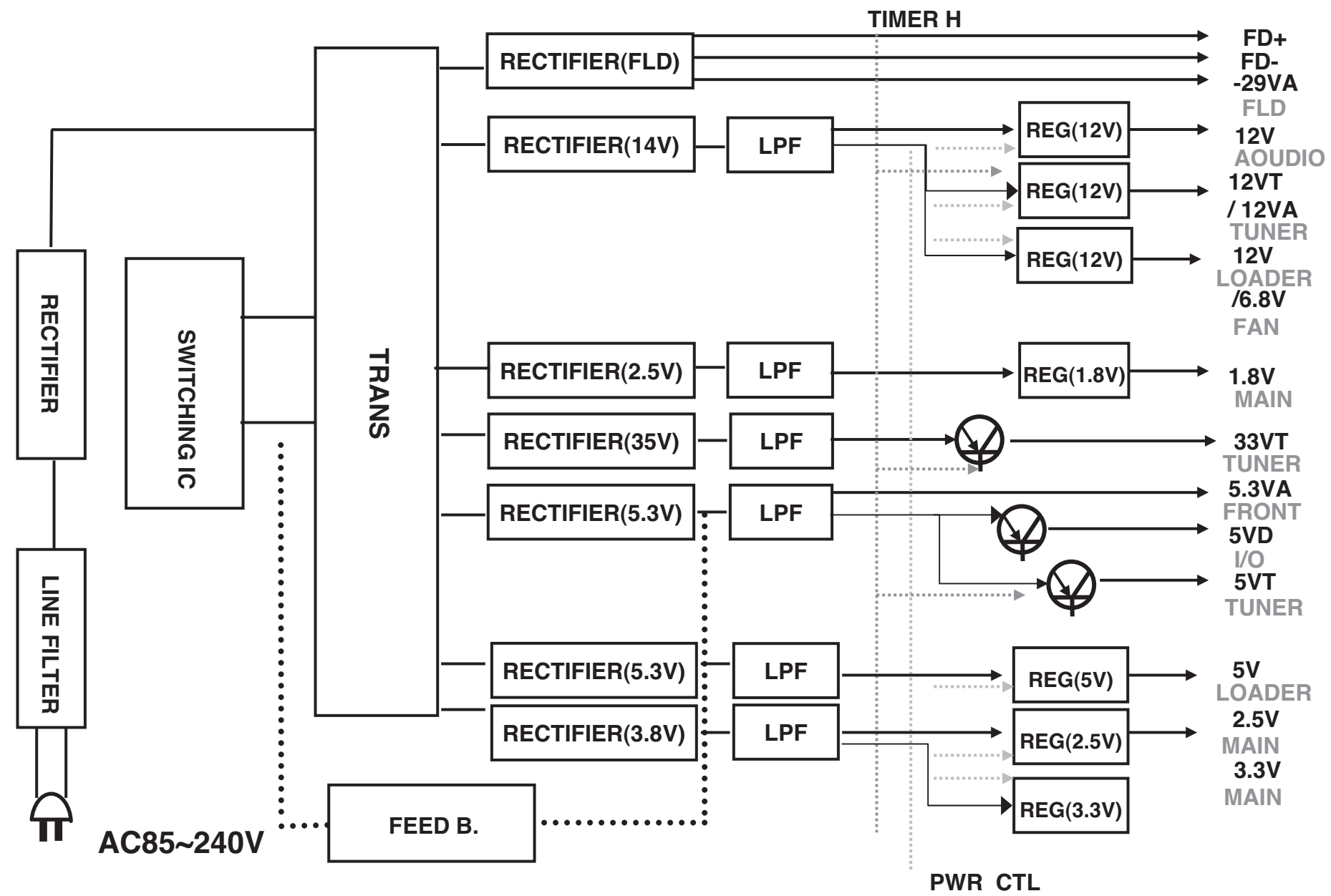


I/O Ucom PWR CTL SIGNAL	
PWR_CTL_H	5V, 12V CONTROL
TIMER_H	5VT, 33VT

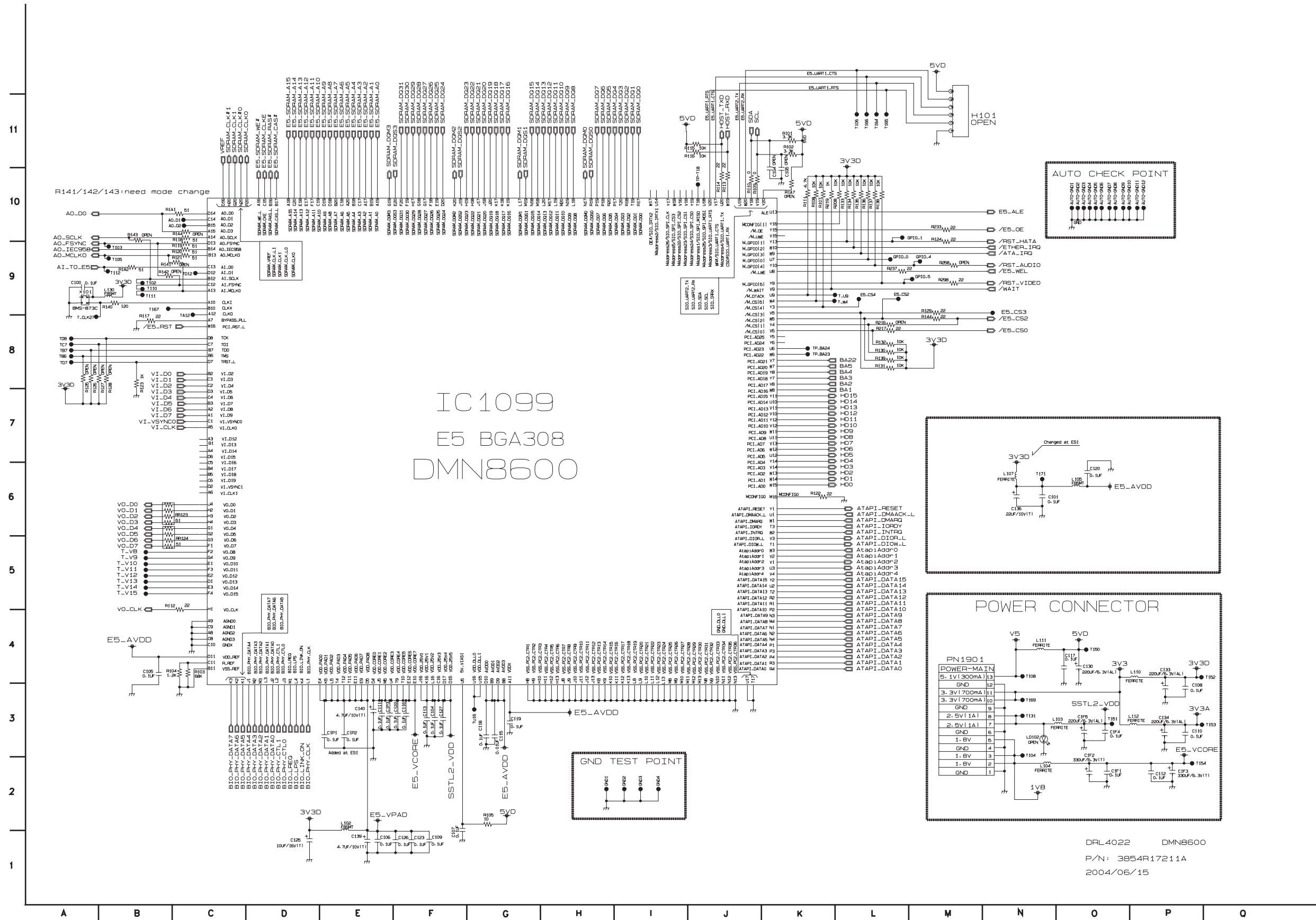
11. Power : Layout Connection Block Diagram



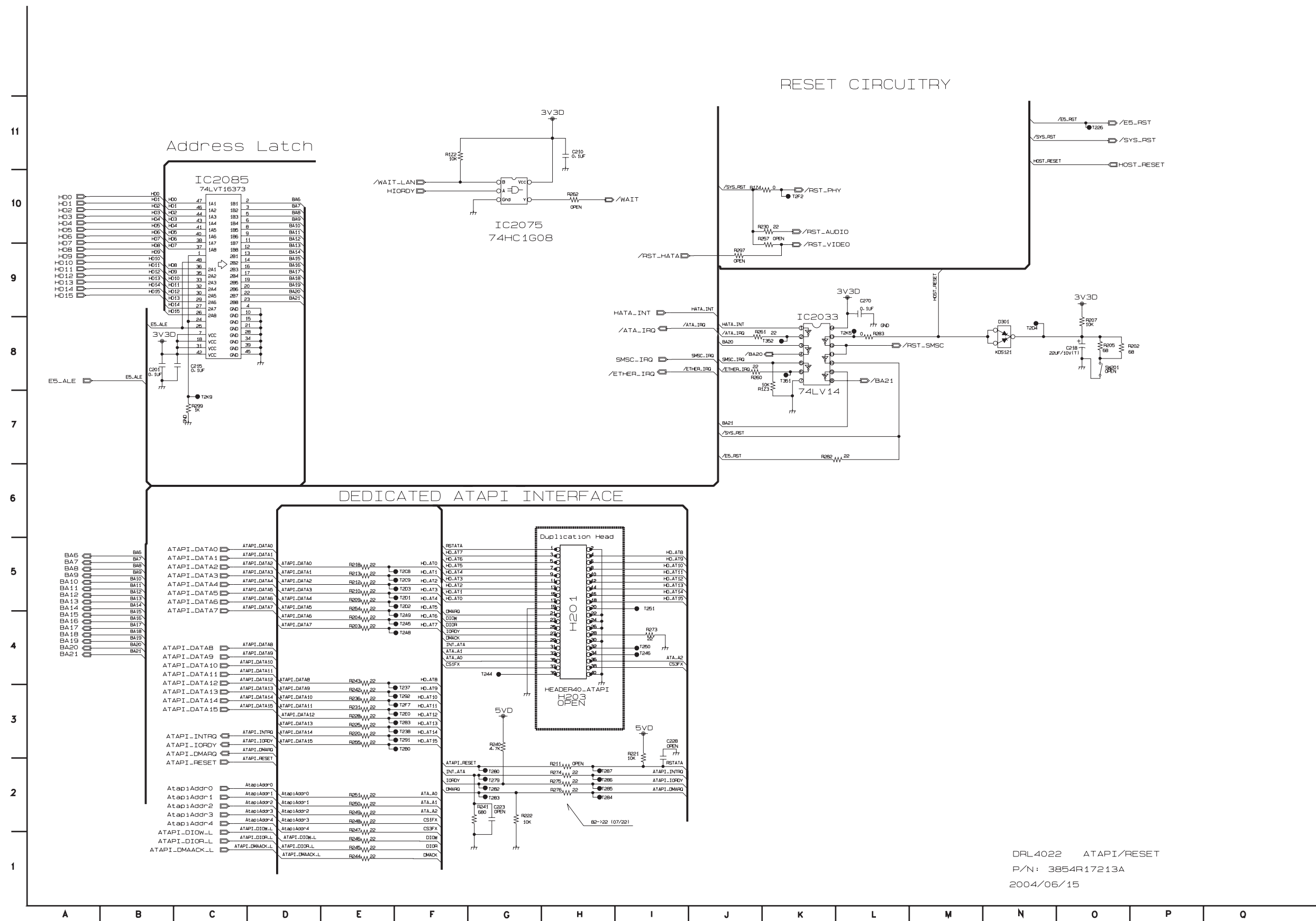
12. SMPS Block Diagram



2. E5 BGA, POWER, UART2 CIRCUIT DIAGRAM

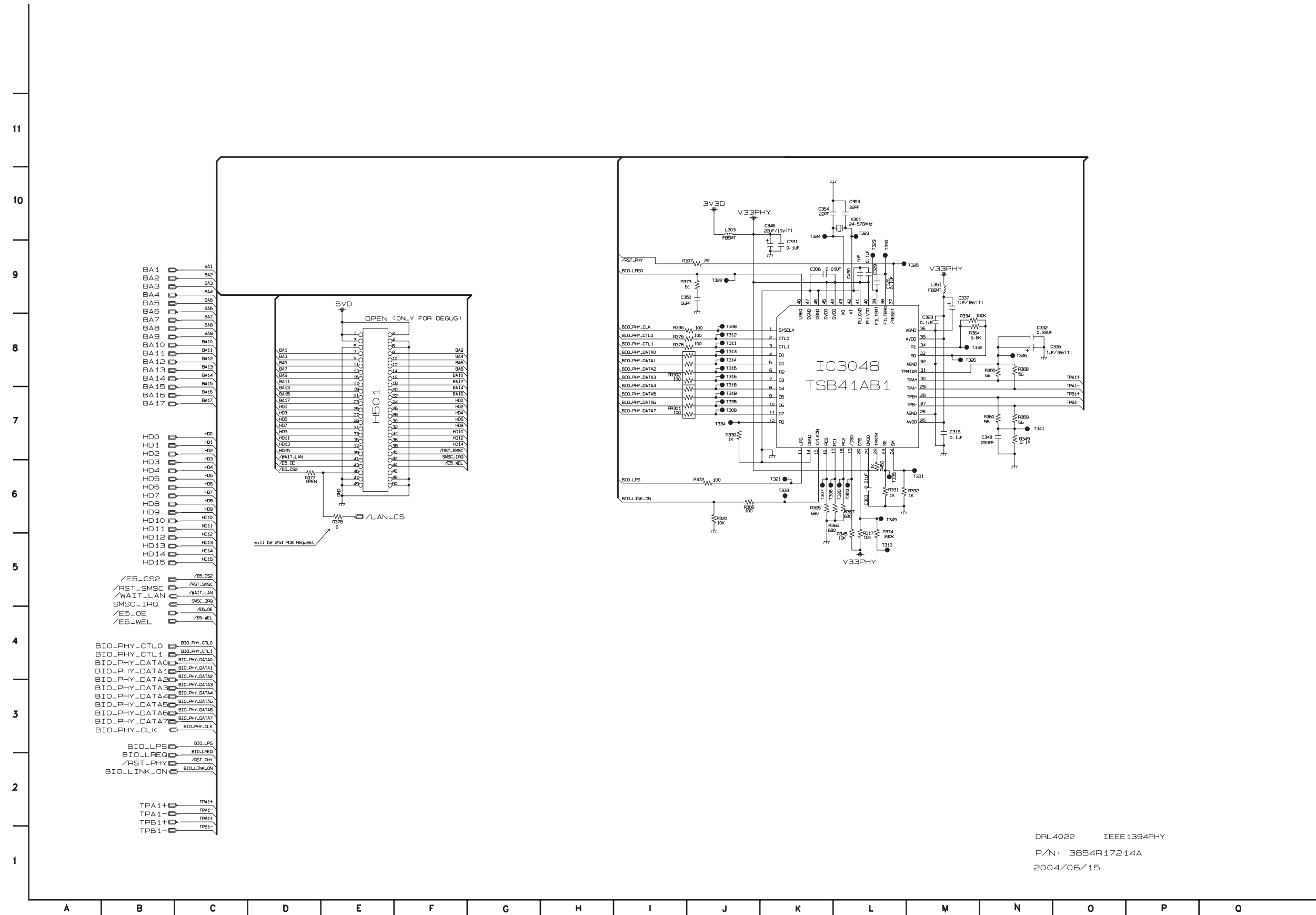


4. RST, CONTROL/STATUS REG, ATAPI, HOST CPLD, LATCH CIRCUIT DIAGRAM



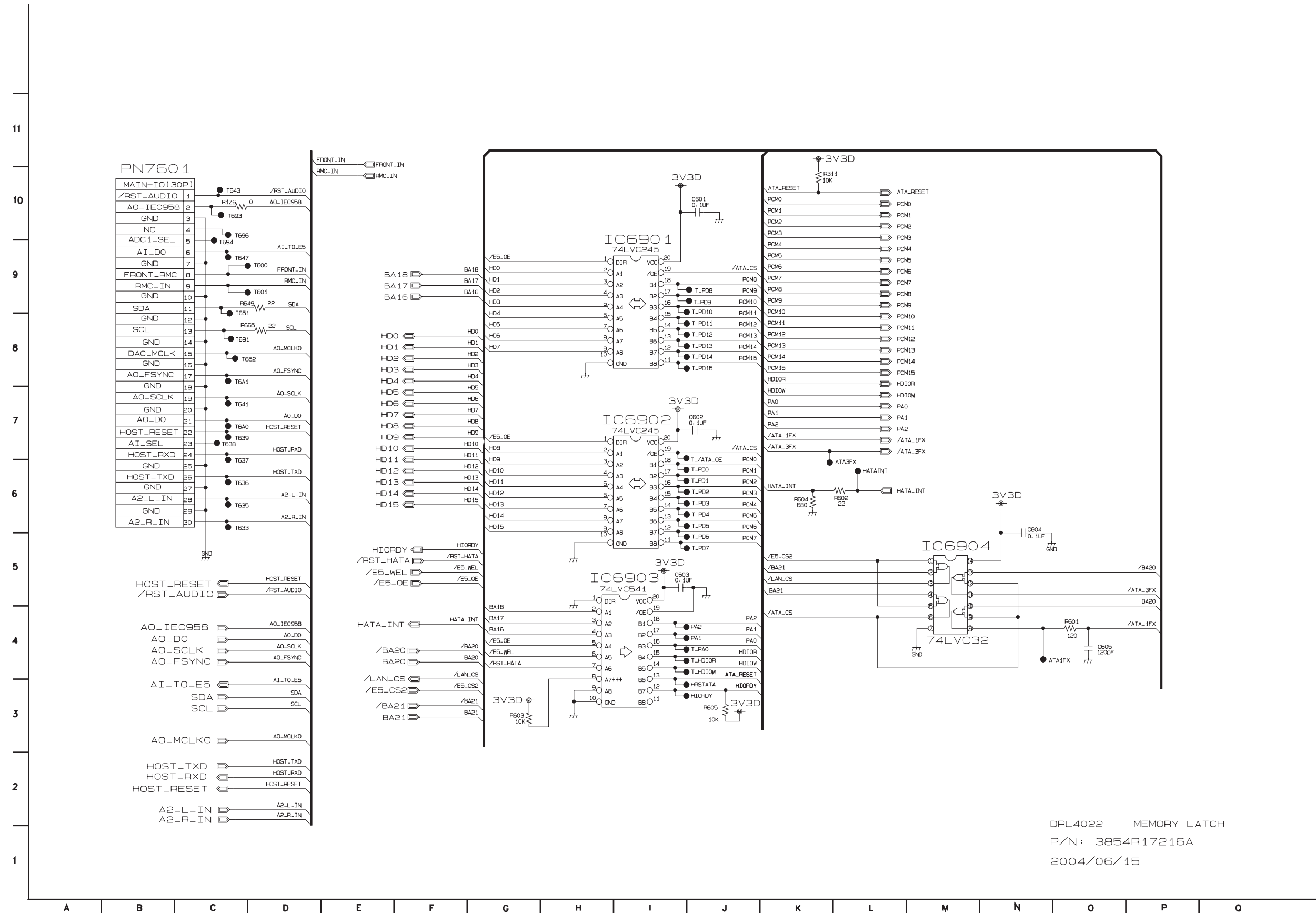
DRL4022 ATAPI/RESET
P/N: 3854R17213A
2004/06/15

5. 1394, ETHERNET CONNECTOR CIRCUIT DIAGRAM



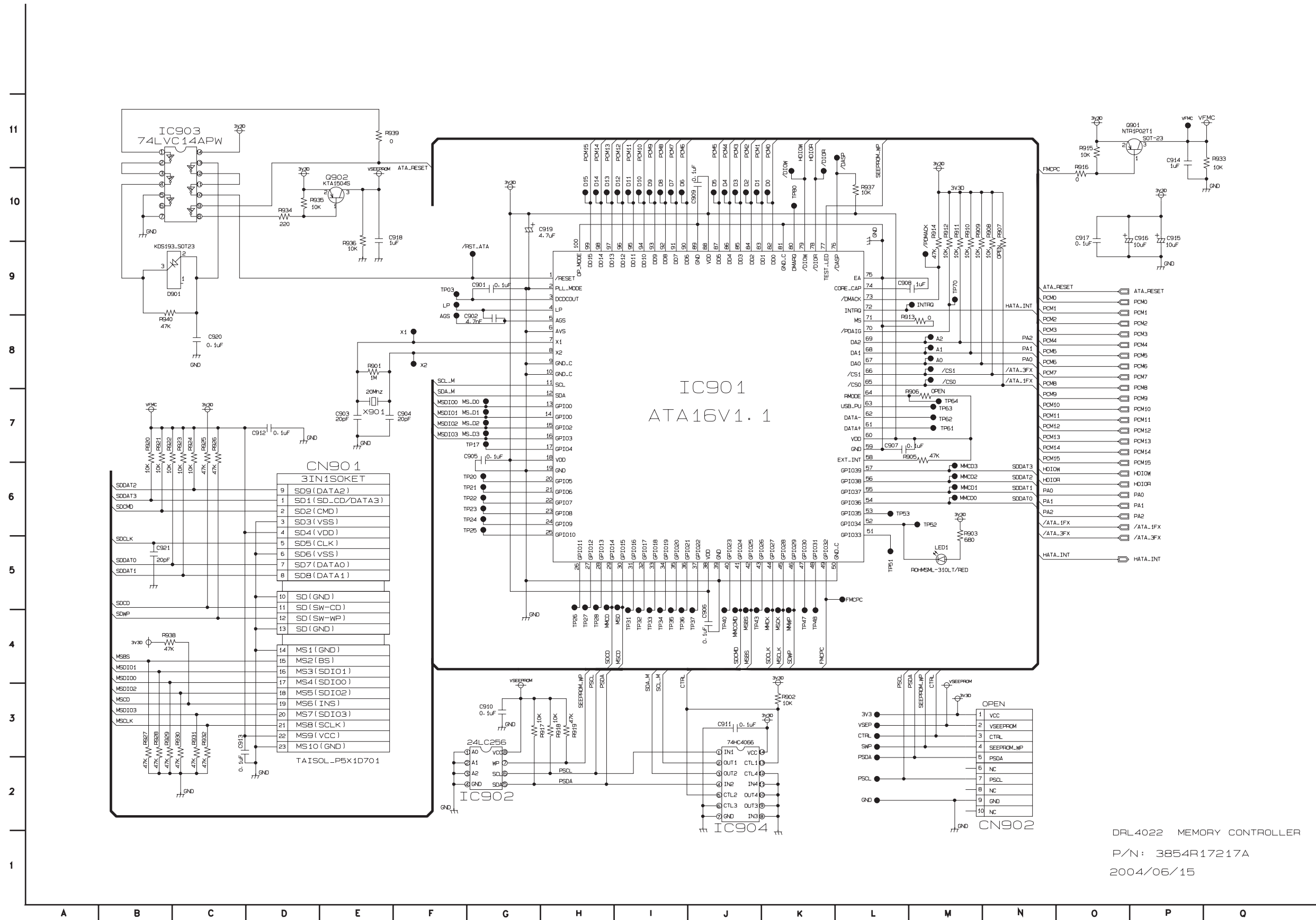
DRL4022 IEEE 1394PHY
P/N: 3854R17214A
2004/06/15

7. AUDIO IN/OUT NON-STD VIDEO CIRCUIT DIAGRAM



DRL4022 MEMORY LATCH
P/N: 3854R17216A
2004/06/15

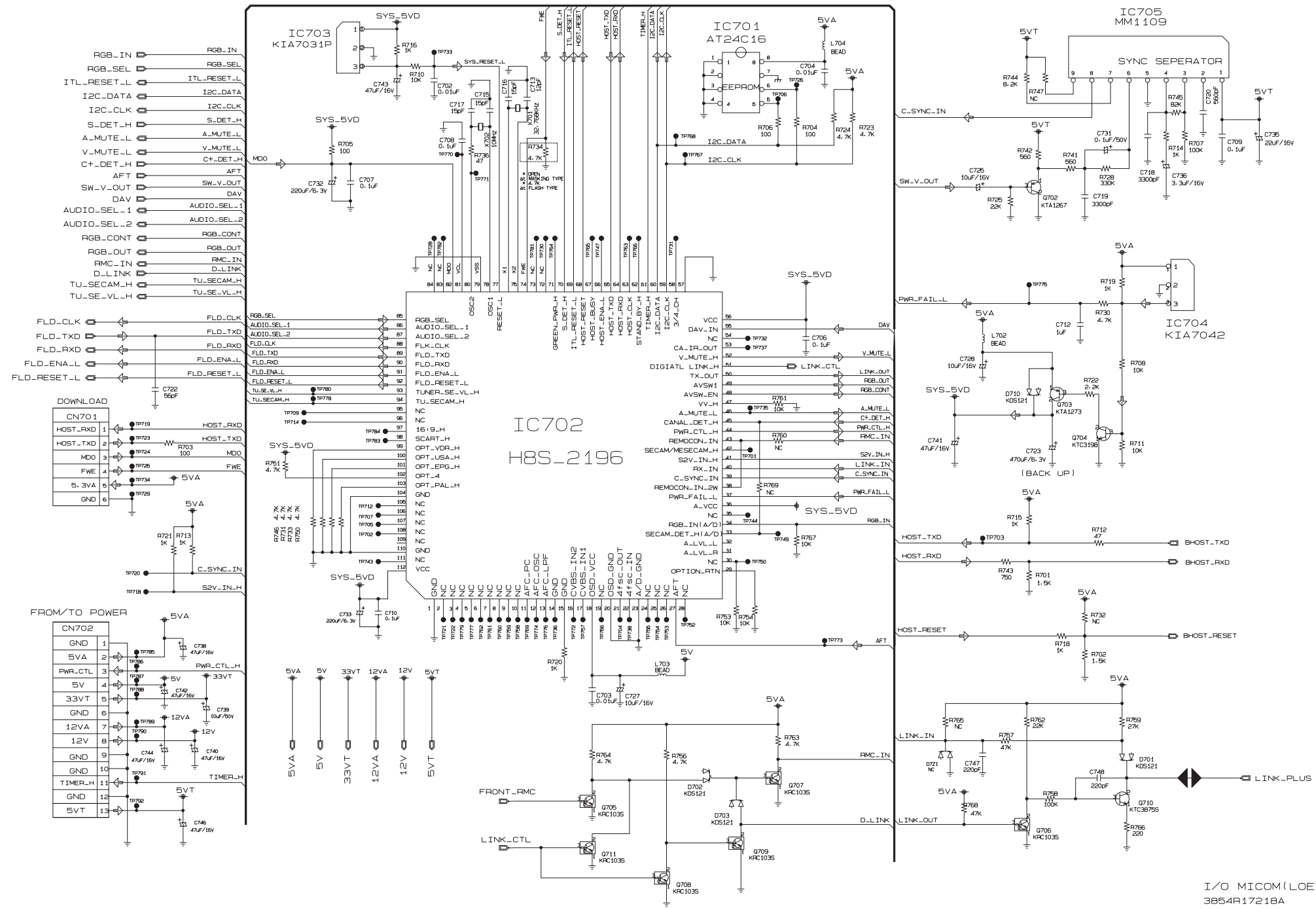
8. 3-IN-1 MEMORY CIRCUIT DIAGRAM



DRL4022 MEMORY CONTROLLER
P/N: 3854R17217A
2004/06/15

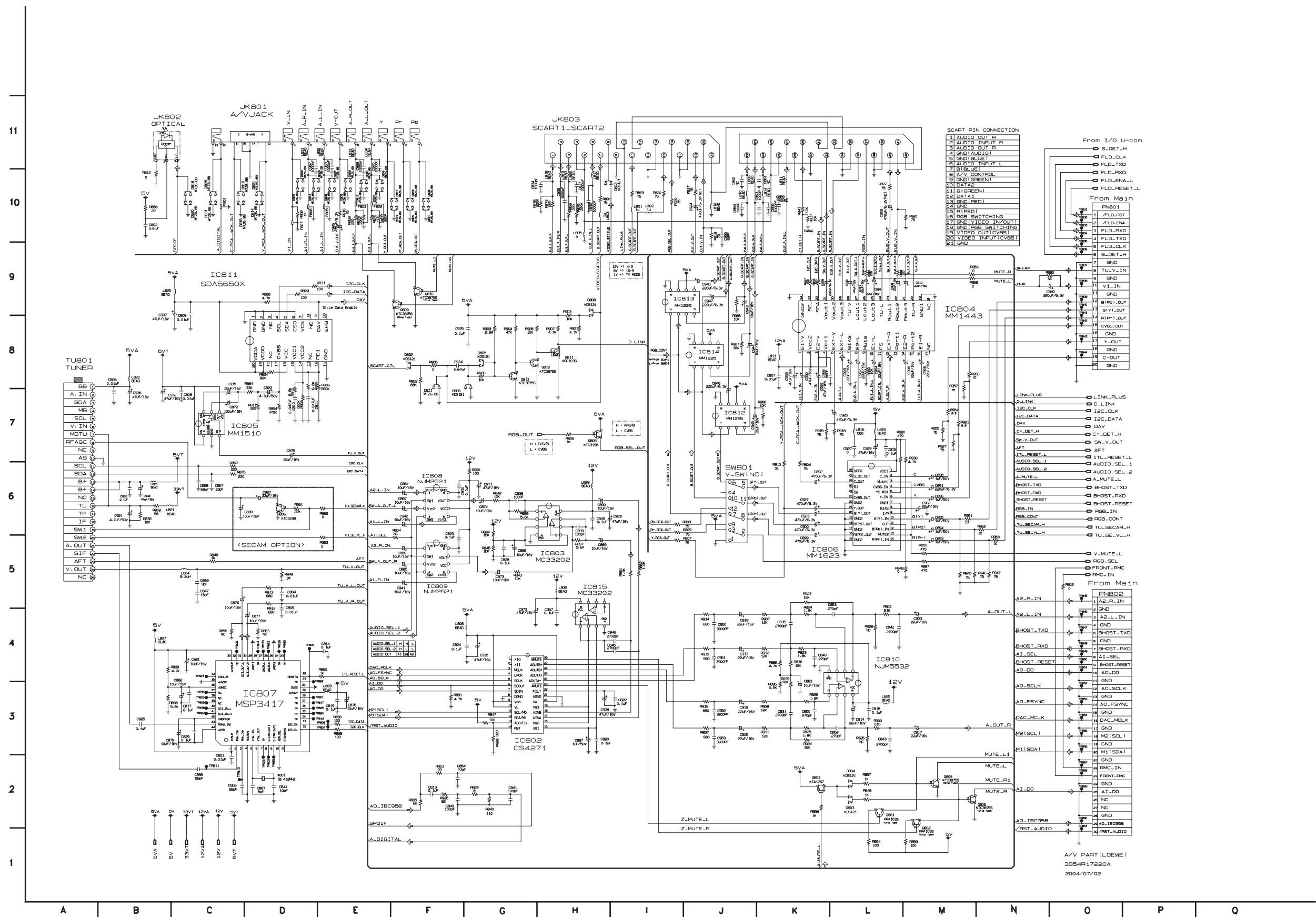
9. I/O MICOM CIRCUIT DIAGRAM

11
10
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4
3
2
1

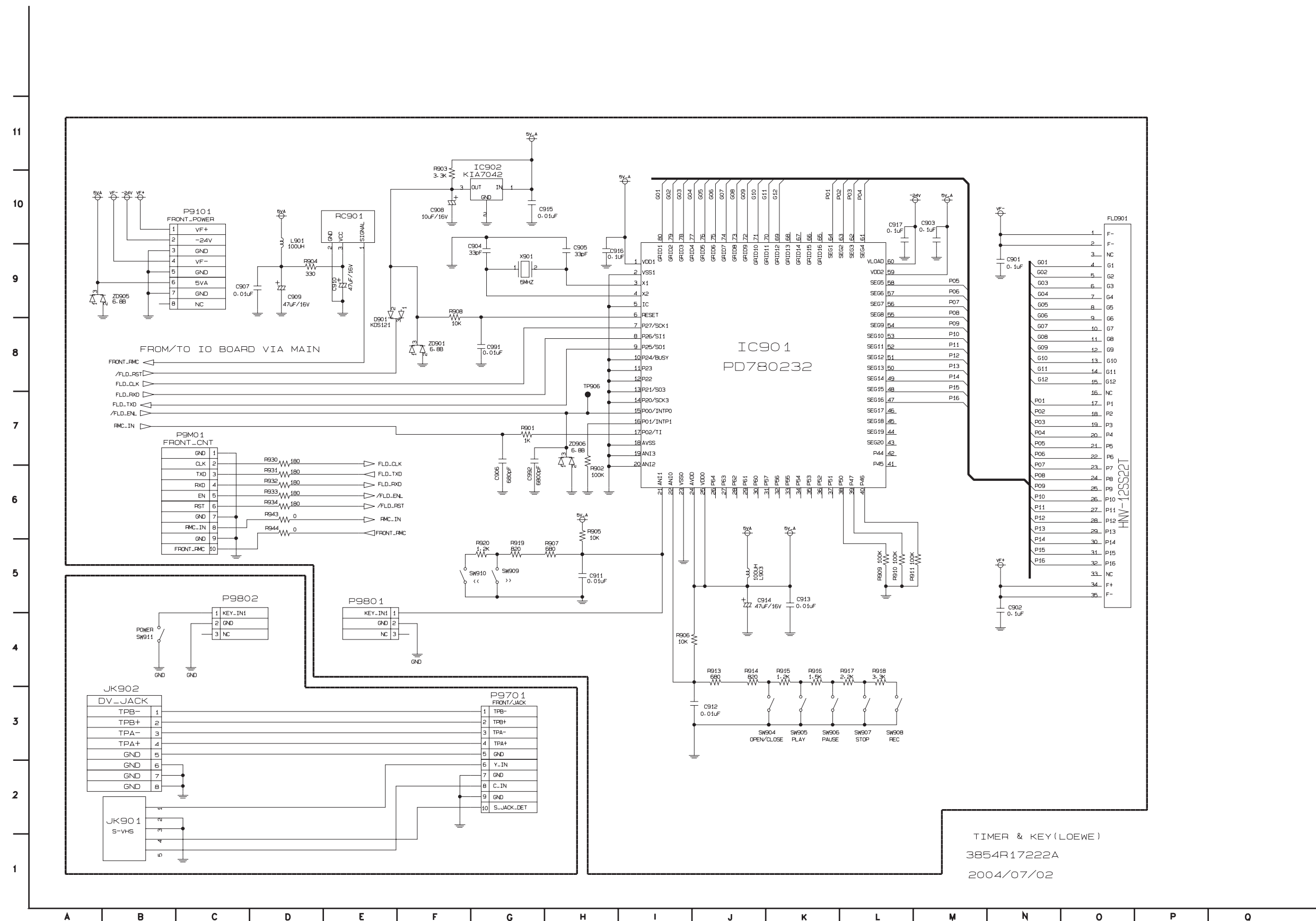


I/O MICOM (LOEWE)
3854R17218A
2004/07/02

10. I/O JACK CIRCUIT DIAGRAM

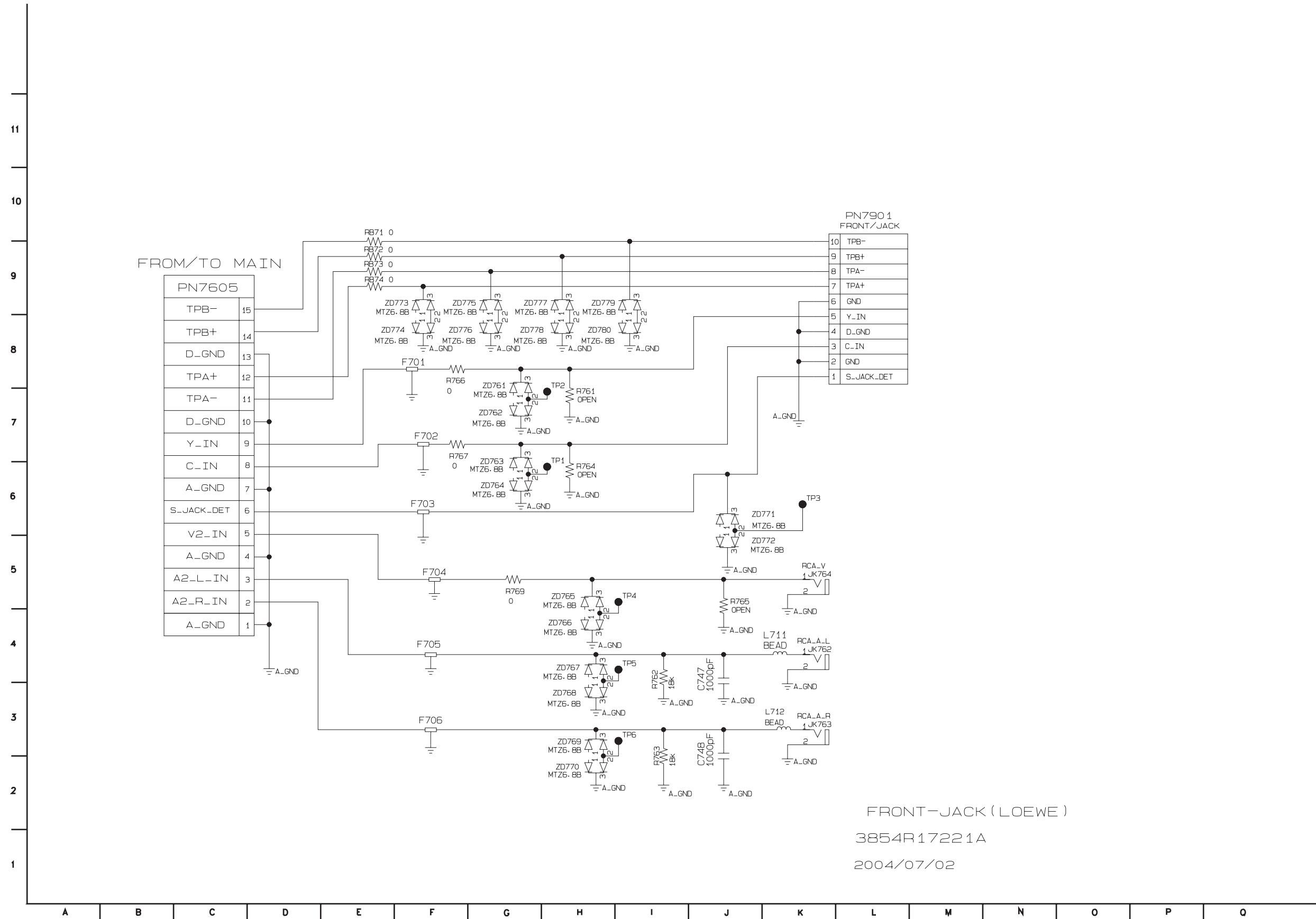


11. FRONT CIRCUIT DIAGRAM

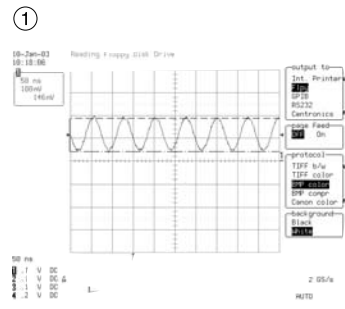


TIMER & KEY (LOEWE)
3854R17222A
2004/07/02

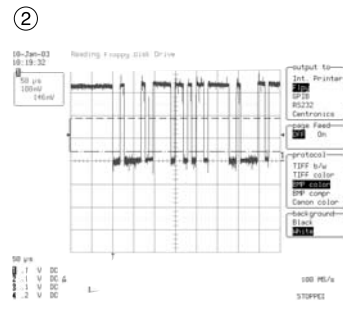
12. FRONT JACK CIRCUIT DIAGRAM



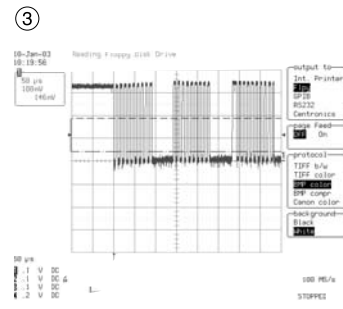
• WAVEFORMS



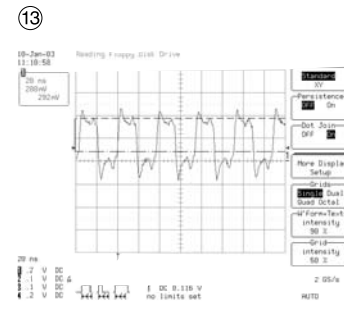
X102
13.5MHz



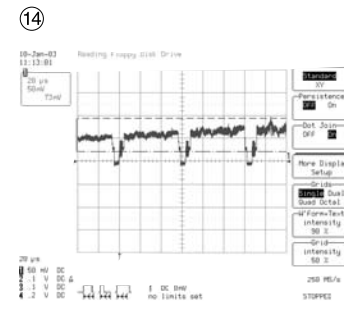
IC4009
PIN32
SDA



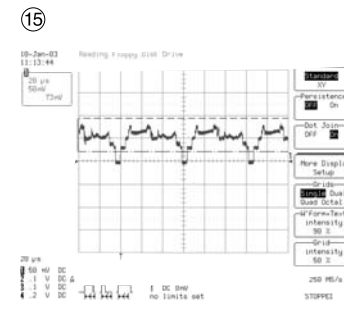
IC4009
PIN31
SCL



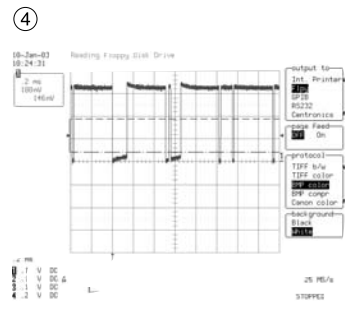
IC4002
PIN22
/PIXCLKIX



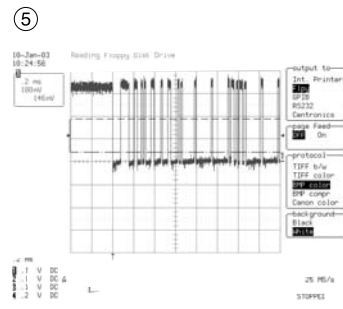
PN7401
PIN6
CVBS_OUT



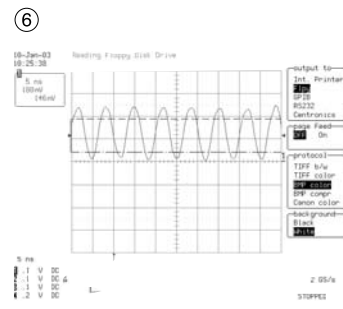
PN7401
PIN4
Y_OUT



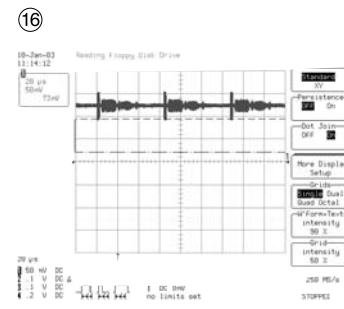
PN7601
PIN24
HOST_RXD



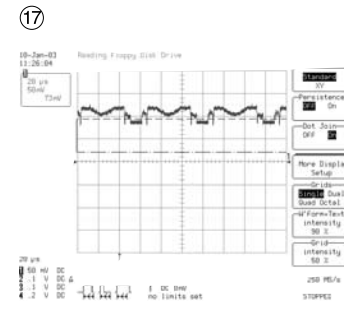
PN7601
PIN26
HOST_TXD



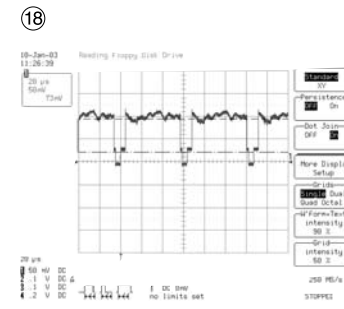
IC1094
PIN45
SDRAM_SCLK0



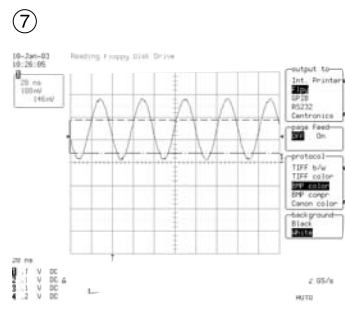
PN7401
PIN2
C_OUT



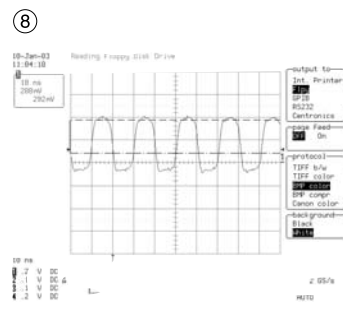
PN7401
PIN7
R_Pr_OUT



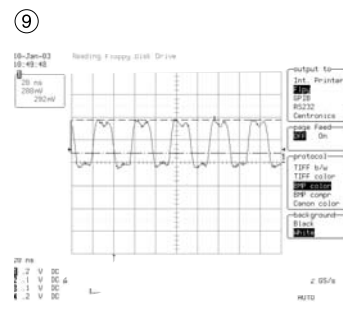
PN7401
PIN8
G_Y_OUT



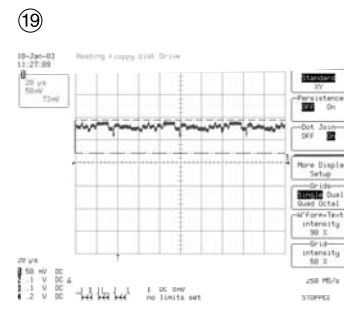
IC3048
PIN77
24.576MHz



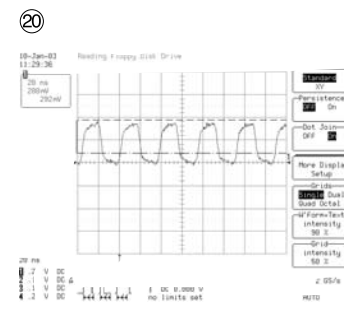
IC3048
PIN2
BIO_PHY_CLK



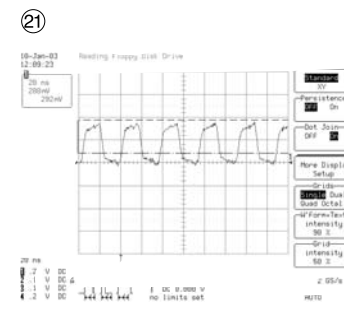
IC4009
PIN94
VI_CLK0



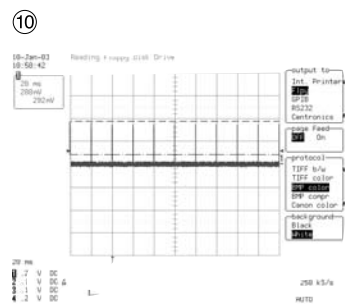
PN7401
PIN9
B_Pb_OUT



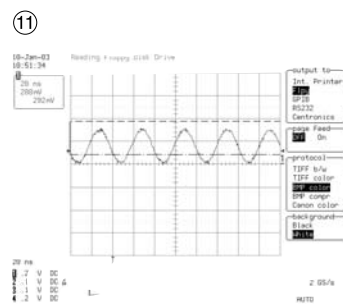
IC4003
PIN4
VO_CLK



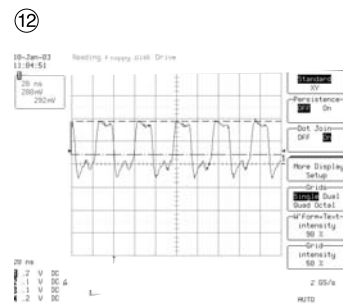
IC4002
PIN37
VO_CLK



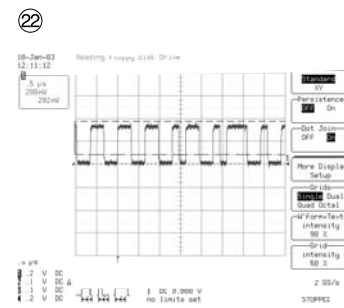
IC4009
PIN91
VI_SYNC0



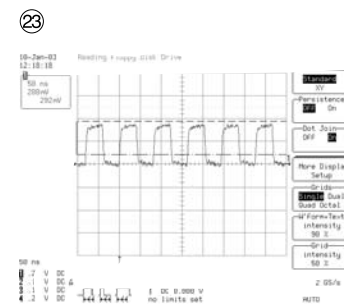
IC4009
PIN6
24.576MHz



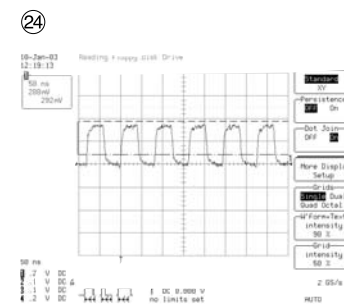
IC6902
PIN76
VI_CLK



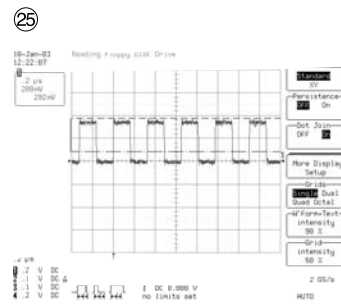
PN7601
PIN29
AO_IEC958



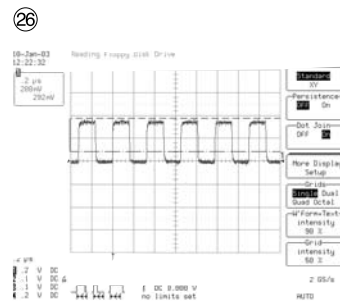
PN7601
PIN2
ADC_MCLK



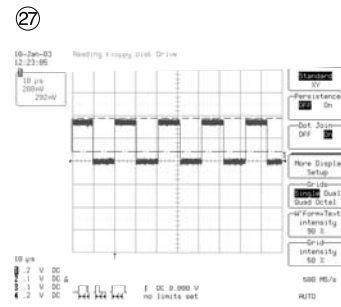
PN7601
PIN15
DAC_MCLK



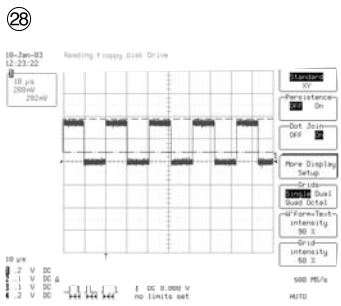
PN7601
PIN4
AI_SCLK



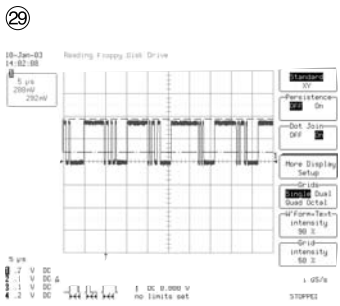
PN7601
PIN19
AO_SCLK



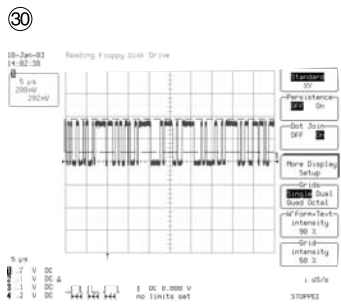
PN7601
PIN8
AI_FSYNC



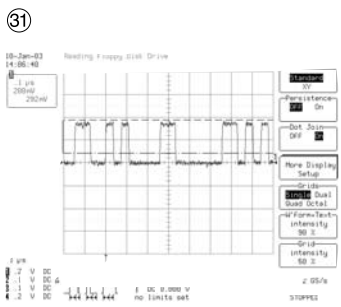
PN7601
PIN17
AO_FSYNC



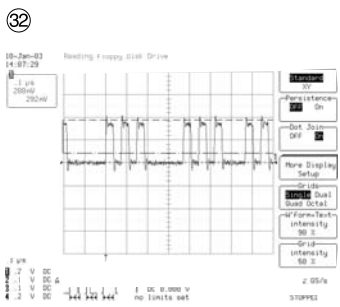
PN7601
PIN6
AI_D0



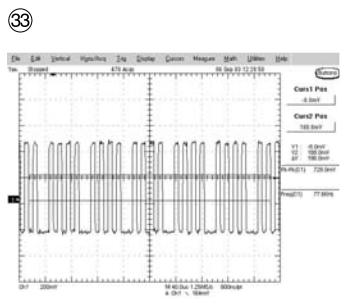
PN7601
PIN21
AO_D0



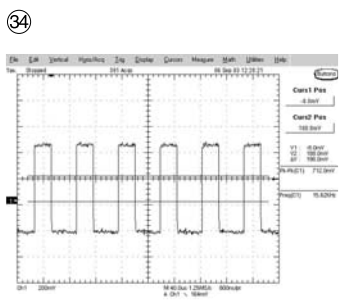
IC4002
PIN40
VO_D0



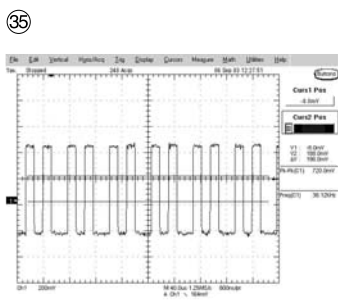
IC4009
PIN90
VI_D0



PN7401
PIN7
R_Pr_OUT



PN7401
PIN8
G_Y_OUT



PN7401
PIN9
B_Pb_OUT

• CIRCUIT VOLTAGE CHART

MODE PIN NO.	EE	PB	REC
IC2066			
1	3.28	3.28	3.28
2	3.28	3.27	3.28
3	0	0	0
4	0	0	0
5	3.28	3.27	3.28
6	0	0	0
7	3.27	3.27	3.28
8	3.28	0	0
9	3.27	3.27	3.28
10	3.27	3.27	3.28
11	0	0	0
12	3.26	3.27	3.28
13	0	0	0
14	3.28	3.27	3.28
15	3.27	3.27	3.28
16	0	0	0
17	3.27	3.27	3.28
18	0	0	0
19	3.26	3.27	3.28
20	3.27	3.27	3.28
21	3.27	3.27	3.28
22	0	0	0
23	3.27	3.27	3.28
24	0	0	0
25	3.28	0	3.28
26	0.05	0.05	0.05
27	3.27	3.27	3.28
28	3.27	3.27	3.28
29	3.27	3.27	3.28
30	0	0	0
31	3.27	3.27	3.28
32	0.19	0	0.3
33	3.27	3.27	3.28
34	3.27	3.27	3.28
35	0	0	0
36	0	0	0
37	0	0	0
38	0	0	0
39	0.02	0	0.42
40	3.28	3.27	3.28
41	3.27	3.27	3.28
42	0	0	0
43	0.04	0.04	0.04
44	0.04	0.04	0.04
IC4009			
1	3.27	3.26	3.27
2	1.85	0.3	0.3
3	3.09	2.35	2.34
4	1.3	0.13	0.02
5	0	0	0
6	1.63	1.55	1.56
7	1.53	1.53	1.52
8	3.3	3.26	3.27
9	0	0	0

MODE PIN NO.	EE	PB	REC
10	0.4	0.61	0.6
11	3.3	3.25	3.26
12	0	0	0
13	1.05	1.06	1.05
14	0	0	0
15	0	0	0
16	0	0	0
17	3.3	3.25	3.26
18	1.03	1.03	1.03
19	1.04	1.04	1.04
20	0	0	0
21	0	0	0
22	0.025	0.02	0
23	3.26	3.25	3.26
24	0	0	0
25	3.27	0.01	3.27
26	0	0	0
27	3.3	3.28	3.28
28	1.66	1.06	1.66
29	0.9	0.05	0
30	3.3	3.26	3.27
31	3.18	3.1	2.8
32	3.2	3.2	3.2
33	3.3	3.26	3.27
34	3.3	3.26	3.27
35	3.3	3.27	3.28
36	2.97	2.96	3
37	1.27	1.28	1.28
38	0	0	0
39	1.64	1.64	1.64
40	1.6	1.61	1.6
41	1.27	1.28	1.28
42	2.36	2.35	2.35
43	3.3	3.27	3.27
44	0	0.08	0
45	1.73	1.72	1.72
46	0	0	0
47	0	0	0
48	0	0	0
49	0	0	0
50	0	0	0
51	3.3	3.27	3.27
52	0	0	0
53	0	0	0
54	0	0	0
55	0	0	0
56	0	0	0
57	0	0	0
58	3.27	3.27	3.27
59	0	0.03	0.03
60	0	0	0
61	0	0	0
62	0	0	0
63	0	0	0
64	0.08	0.06	0

MODE PIN NO.	EE	PB	REC
65	0.09	0.06	0
66	0.1	0.08	0
67	0.1	0.03	0
68	3.3	3.27	3.27
69	0.08	0.08	0
70	0.05	0.05	0
71	0.05	0.04	0
72	0.05	0.04	0
73	2.35	2.35	2.35
74	2.7	0.03	0
75	3.3	0.03	3.27
76	0	0	0
77	2.35	2.35	2.34
78	0	0.02	0
79	2.35	2.34	2.34
80	0	0	0
81	1.98	0.97	2
82	0.42	0.42	0.43
83	3.3	3.27	3.27
84	3.3	3.44	0.45
85	0.71	0.02	0.7
86	0.45	0.45	0.46
87	0.46	0.46	0
88	0	0	0
89	0.47	0.47	0.47
90	0.89	0.89	0
91	0	0.02	0
92	2.7	2.71	2.7
93	3.3	3.27	3.27
94	1.7	1.7	1.7
95	2.05	0	2.05
96	0	0	0
97	2.35	2.34	2.35
98	2.35	2.34	2.34
99	2.35	2.34	2.34
100	0	0	0
IC4003			
1	0	0	0
2	0	0	0
3	0	0	0
4	0	0	0
5	0	0	0
6	3.3	3.26	3.27
7	0	0	0
8	3.3	3.27	3.27
9	2.38	2.35	2.37
10	0.2	0.5	0
11	0.18	0.5	0
12	3.3	3.27	3.27
13	0	0	0
14	3.3	3.27	3.28
15	3.2	3.2	3.2
16	0	0.62	0
17	3.27	3.23	3.27
18	3.23	3.23	3.24

MODE PIN NO.	EE	PB	REC
19	1.56	1.56	1.56
20	1.6	1.58	1.58
21	0.4	1.25	0.43
22	0.4	1.25	0.43
23	0.42	1.25	0.34
24	1.35	1.25	0.95
25	3.27	2.27	3.27
26	0	0	0
27	0	0	3.27
28	2.49	2.5	2.5
29	3.25	3.26	3.27
30	2.9	0.6	0
31	1.58	0.59	0
32	3.3	0.42	0
33	0	0	0
34	0	0	0
35	0.62	0.57	0.58
36	1.6	1.58	1.58
37	0.06	0.02	0
38	0.06	0	0
39	0.02	0	0
40	0.02	0	3.15
41	1.23	1.24	1.24
42	0.47	0.5	0.47
43	3.3	3.26	3.27
44	3.3	0	3.27
45	0.47	0.5	0.47
46	1.22	1.25	1.24
47	0.69	0.71	0.7
48	0	0	0
49	0	0	0
50	3	3.25	3.26
51	0	0	0
52	3.3	3.26	3.27
53	3.25	3.26	3.27
54	0	0	0
55	2.38	2.38	2.38
56	2.38	2.37	2.38
57	0	0	0
58	3.28	3.27	3.27
59	0	0	0
60	0.04	0	0.1
61	0.78	1.55	0.8
62	0.42	0.89	0.42
63	0.4	0.89	0.4
64	1.64	1.27	0.96
IC2075			
1	0	0	0
2	0	0	0
3	0	0	0
4	0	0	0
5	0	0	0
6	0	0	0
7	0	0	0
8	3.27	3.28	3.28

MODE PIN NO.	EE	PB	REC
9	2.94	2.95	2.95
10	3.27	3.28	3.28
11	3.28	3.28	3.28
12	2.95	2.95	2.95
13	3.27	3.28	3.28
14	3.28	3.28	3.28
IC1057			
1	0.03	0.03	0.03
2	0.03	0.03	0.03
3	0.03	0.03	0.03
4	0.03	0.03	0.03
5	0.03	0.03	0.03
6	0.03	0.03	0.03
7	0.03	0.03	0.03
8	0.03	0.03	0.03
9	0.03	0.03	0.03
10	0.03	0.03	0.03
11	3.26	3.28	3.28
12	3.28	3.28	3.28
13	0	0	0
14	3.26	3.28	3.28
15	0	0	0
16	0.03	0.03	0.03
17	0.03	0.03	0.03
18	0.03	0.03	0.03
19	0.03	0.03	0.03
20	0.03	0.03	0.03
21	0	0	0
22	0	0	0
23	0	0	0
24	0	0	0
25	0	0	0
26	3.26	3.28	3.28
27	0	0	0
28	3.26	3.28	3.28
29	0	0	0
30	0	0	0
31	0	0	0
32	0	0	0
33	0	0	0
34	0	0	0
35	2.89	2.91	2.989
36	0	0	0
37	2.26	2.26	2.28
38	0	0	0
39	2.91	2.91	2.92
40	0	0	0
41	0	0	0
42	2.9	2.91	2.92
43	0	0	0
44	0	0	0
45	0	0	0
46	0	0	0
47	3.26	3.28	3.28
48	0.03	0.03	0.03

MODE PIN NO.	EE	PB	REC
IC3048			
1	0	0	0
2	1.54	1.56	1.54
3	0	0	0
4	1.04	1.04	0
5	0	0	0
6	3.27	3.27	3.27
7	0.01	0.02	0
8	0.01	0.02	0
9	3.25	3.27	3.26
10	0	0	0
11	0	0	0
12	0	0	0
13	0	0	0
14	0	0	0
15	0	0	0
16	0	0	0
17	3.27	3.27	3.27
18	0	0	0
19	3.27	3.27	3.26
20	0	0	0
21	0	0	0
22	0	0	0
23	0	0	0
24	0	0	0
25	3.26	3.27	3.26
26	3.27	3.27	3.27
27	3.26	3.26	3.27
28	0	0	0
29	3.27	3.27	3.27
30	3.25	3.27	3.27
31	3.25	3.27	3.28
32	0	0	0
33	0	0	0
34	3.25	3.27	3.26
35	3.25	3.27	3.27
36	0	0	0
37	0	0	0
38	0	0	0
39	0	0	0
40	0	0	0
41	0	0	0
42	0	0	0
43	0	0	0
44	3.26	3.27	3.27
45	3.27	3.27	3.27
46	3.27	3.27	3.26
47	3.26	3.27	3.26
48	0	0	0
49	0	0	0
50	0	0	0
51	3.27	3.27	3.27
52	3.27	3.27	3.26
53	3.26	3.26	3.26
54	3.27	3.27	3.27

MODE PIN NO.	EE	PB	REC
55	0	0	0
56	0	0	0
57	3.27	3.26	3.26
58	3.26	3.26	3.26
59	3.26	3.26	3.27
60	0	0	0
61	0	0	0
62	3.25	3.27	3.28

MODE PIN NO.	EE	PB	REC
29	0.1	0.1	0.1
30	0	0	0
31	2.52	2.52	2.51
32	2.5	2.52	2.52
33	0.1	0.1	0.1
34	0.1	0.1	0.1
35	0	0	0
36	4.96	4.94	4.97
37	4.75	4.75	4.75
38	0	0	0
39	4.97	4.96	4.96
40	0.3	0.32	0.32
41	0.32	0.32	0.32
42	0.04	0.04	0.04
43	0	0	0
44	5.1	5.08	5.1
45	0	0	0
46	5.2	5.18	5.2
47	0.02	0.03	0.03
48	2.5	2.5	2.5
49	0.09	0.09	0.09
50	2.56	2.55	2.55
51	2.56	2.55	2.55
52	0.03	0.02	0.02
53	0.03	0.02	0.02
54	0.12	0.1	0.1
55	0	0	0
56	5.1	5.08	5.08
57	0	0	0
58	0	0	0
59	5.11	5.1	5.1
60	5.1	5.1	5.1
61	5.03	5	5
62	0	0	0
63	2.49	2.47	2.47
64	3.84	3.82	3.82
65	0	0	0
66	0.33	0.32	0.31
67	0	0	0
68	4.8	4.9	4.9
69	0	0	0
70	5.2	5.22	5.22
71	0	0	0
72	0	0	0
73	5.1	5.1	5.1
74	0	0	0
75	1.45	1.47	1.47
76	1.43	1.43	1.45
77	5.21	5.21	5.21
78	2.5	2.52	2.52
79	0	0	0
80	0	0	0
81	3.42	3.4	3.41
82	5.08	5.11	5.1
83	0	0	0

MODE PIN NO.	EE	PB	REC
84	0	0	0
85	0	0	0
86	2.53	2.54	2.52
87	2.53	2.54	2.52
88	4.94	4.96	4.96
89	4.04	4.06	4.06
90	2.18	2.2	2.2
91	4.27	4.3	4.3
92	5.06	5.1	5.1
93	0	0	0
94	0.02	0.02	0.02
95	0	0	0
96	0.31	0.31	0.31
97	0	0.02	0.02
98	0	0.02	0.02
99	2.06	2.06	2.06
100	0	0	0
101	0	0	0
102	0	0	0
103	0	0	0
104	0	0	0
105	0	0	0
106	0	0	0
107	0	0	0
108	0	0	0
109	0.03	0.06	0.06
110	0	0	0
111	0	0	0
112	5.1	5.2	5.2
IC705			
1	4.95	4.96	4.96
2	1.87	1.9	1.91
3	2.08	2.1	2.13
4	1.98	2	2
5	0	0	0
6	2.68	2.71	2.71
7	4.93	4.95	4.96
8	4.93	4.95	4.96
9	0.04	0.06	0.06
IC802			
1	2.57	2.57	2.57
2	2.56	2.55	2.56
3	0.00	1.64	1.64
4	1.63	1.62	1.62
5	1.63	1.63	1.63
6	0.00	1.30	1.30
7	1.48	1.62	1.55
8	0.00	0.00	0.00
9	0.00	0.00	0.00
10	0.00	0.00	0.00
11	5.01	5.01	5.01
12	0.00	0.00	0.00
13	0.00	0.00	0.00
14	3.26	3.25	3.26
15	2.52	2.52	2.52

MODE PIN NO.	EE	PB	REC
16	2.53	2.53	2.53
17	2.53	2.53	2.53
18	2.52	2.52	2.52
19	2.53	2.53	2.53
20	5.07	5.06	5.07
21	0.00	0.00	0.00
22	4.99	4.99	4.99
23	5.06	5.05	5.06
24	2.57	2.57	2.57
25	2.46	2.46	2.46
26	2.47	2.47	2.47
27	2.58	2.57	2.58
28	5.03	5.02	5.02
IC804			
1	2.64	2.74	2.54
2	12.24	12.26	12.26
3	2.64	2.75	2.75
4	12.24	12.26	12.25
5	2.66	3.41	3.41
6	5.78	5.83	5.77
7	5.83	5.81	5.82
8	5.75	5.74	5.74
9	4.67	0.00	0.00
10	5.75	5.74	5.75
11	0.00	11.64	11.64
12	5.81	5.82	5.82
13	0.00	0.00	0.00
14	0.00	5.74	5.74
15	0.00	0.00	0.00
16	5.75	5.74	5.74
17	0.00	0.00	0.00
18	0.00	0.00	0.00
19	0.00	0.00	0.00
20	5.81	5.81	5.81
21	0.05	0.00	0.00
22	5.88	5.84	5.84
23	5.80	5.76	5.76
24	0.00	0.00	0.00
25	0.05	0.00	0.00
26	5.87	5.84	5.85
27	5.79	5.76	5.76
28	3.58	3.73	3.57
29	0.00	0.00	0.00
30	0.00	0.00	0.00
31	2.30	2.32	2.32
32	5.04	5.05	5.05
33	5.08	5.08	5.08
34	0.00	0.00	0.00
IC806			
1	5.13	5.07	5.08
2	2.41	0.00	2.41
3	4.95	0.00	4.95
4	0.00	1.50	1.45
5	0.00	0.00	0.00
6	0.00	0.00	0.00

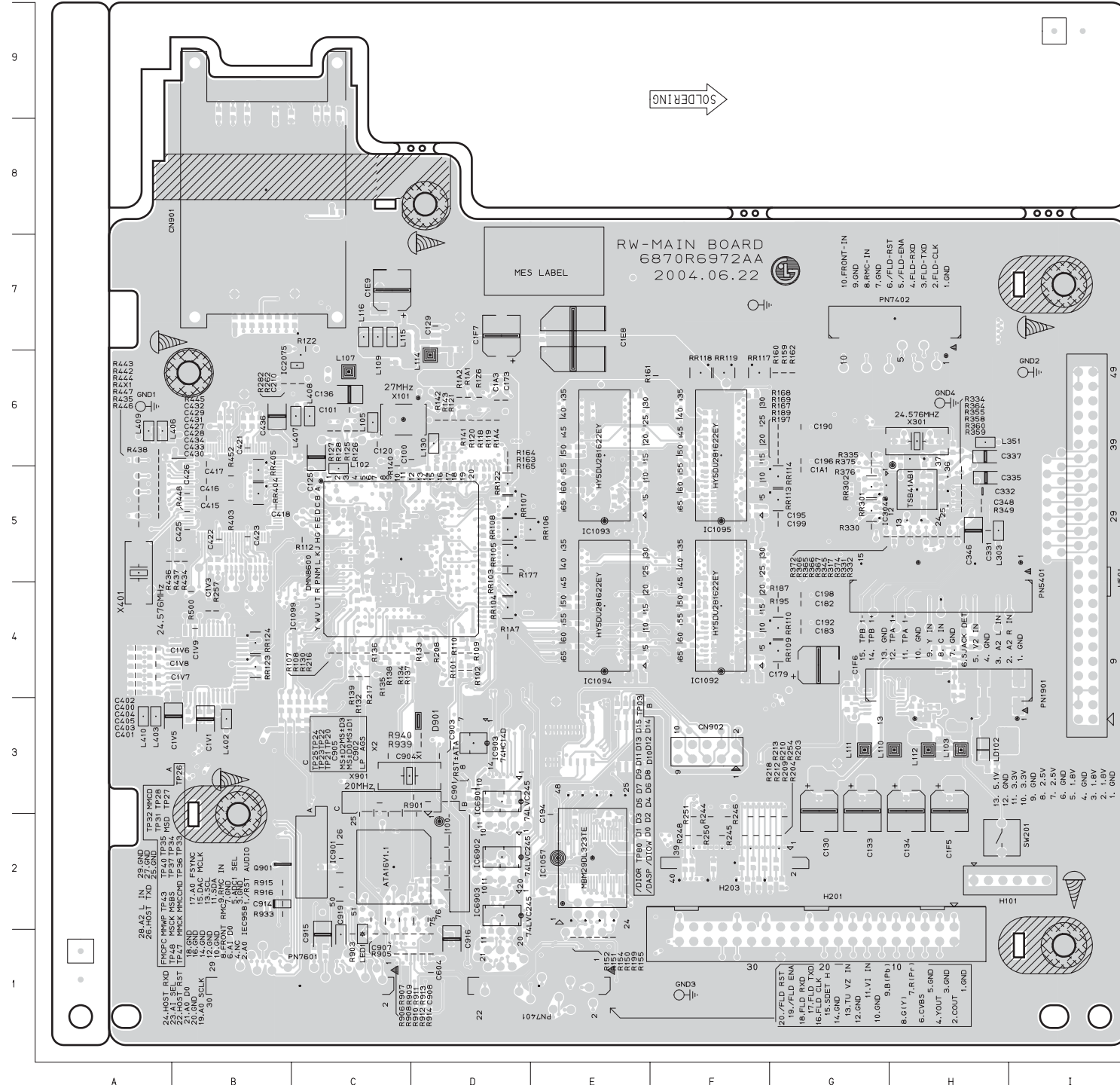
MODE PIN NO.	EE	PB	REC
7	0.00	0.00	0.00
8	2.46	0.00	2.45
9	0.00	0.00	0.00
10	1.13	1.25	1.24
11	0.00	0.00	0.00
12	0.00	0.00	0.00
13	4.95	4.95	4.95
14	2.41	2.40	2.40
15	0.00	0.00	0.00
16	2.47	2.47	2.47
17	0.00	0.00	0.00
18	2.44	2.44	2.44
19	0.00	0.00	0.00
20	0.00	1.42	1.32
21	0.00	2.08	1.89
22	0.00	0.00	0.00
23	1.16	1.99	1.85
24	0.00	0.00	0.00
25	0.00	0.00	0.00
26	2.49	2.48	2.48
27	0.00	0.00	0.00
28	5.07	5.07	5.07
IC807			
1	5.08	5.07	5.08
2	1.52	1.52	1.52
3	1.52	1.52	1.52
4	0.00	0.00	0.00
5	2.39	2.42	2.40
6	2.18	2.18	2.18
7	0.40	0.23	0.24
8	0.39	0.00	0.00
9	0.39	0.00	0.00
10	0.00	0.00	0.00
11	0.00	0.00	0.00
12	5.07	5.08	5.08
13	5.04	5.06	5.05
14	0.41	0.24	0.28
15	0.38	0.23	0.25
16	0.41	0.00	0.00
17	0.36	0.23	0.29
18	0.44	0.24	0.44
19	5.08	5.08	5.08
20	0.00	0.00	0.00
21	0.00	0.25	0.24
22	0.00	0.00	0.00
23	0.00	0.00	0.00
24	0.00	0.00	0.00
25	0.00	0.00	
26	0.00	0.00	0.00
27	0.00	0.00	0.00
28	0.00	0.00	0.00
29	0.00	0.00	0.00
30	2.55	2.55	2.55
31	2.56	2.56	2.53
32	0.00	0.00	0.00

MODE PIN NO.	EE	PB	REC
33	5.08	5.08	5.08
34	4.96	4.96	4.96
35	0.00	0.00	0.00
36	2.55	2.55	2.54
37	0.00	0.00	0.00
38	0.00	0.00	0.00
39	0.00	0.00	0.00
40	2.54	2.55	2.53
41	0.00	0.00	0.00
42	2.61	2.54	2.60
43	0.00	0.00	0.00
44	0.00	0.00	0.00
IC808			
1	8.16	8.16	8.16
2	5.12	5.12	5.12
3	7.96	7.94	7.94
4	0.00	0.00	0.00
5	8.16	8.16	8.16
6	12.09	12.10	12.10
7	7.31	7.31	7.31
8	0.00	0.00	0.00
IC807			
1	0.00	0.00	0.00
2	0.00	0.00	0.00
3	0.00	0.00	0.00
4	5.07	5.07	5.07
5	5.04	5.04	5.04
6	0.00	0.00	0.00
7	0.18	0.18	0.18
8	0.00	0.00	0.00
9	5.19		
10	0.00	5.19	5.19
11	0.00	0.00	0.00
12	4.93	4.93	4.93
13	0.00	0.00	0.00
14	4.98	4.98	4.98
15	4.93	4.93	4.93
16	1.51	1.51	1.51
17	1.44	1.44	1.44
18	0.00	0.00	0.00
19	5.19	5.19	5.19
20	0.00	5.19	5.19
IC153			
1	12.8		
2	12		
3	0		
4	4.9		
IC156			
1	12.8		
2	12		
3	0		
4	2		
IC159			
1	2.9		
2	4.1		

MODE PIN NO.	EE	PB	REC
3	0		
4	1.2		
5	2.5		
IC158			
1	14		
2	12		
3	0		
4	4.3		
IC151			
1	5.7		
2	5		
3	0		
4	4.3		
IC154			
1	3.8		
2	3.4		
3	0		
4	4.9		
IC160			
1	2.2		
2	4.3		
3	0		
4	0		
5	1.8		
Q120			
	5.2	4.4	5.1
Q121			
	0	4.9	0.7
Q122			
	0	5	0
Q123			
	32.7	32	32.6
Q124			
	5.2	4.5	5.2

PRINTED CIRCUIT DIAGRAMS

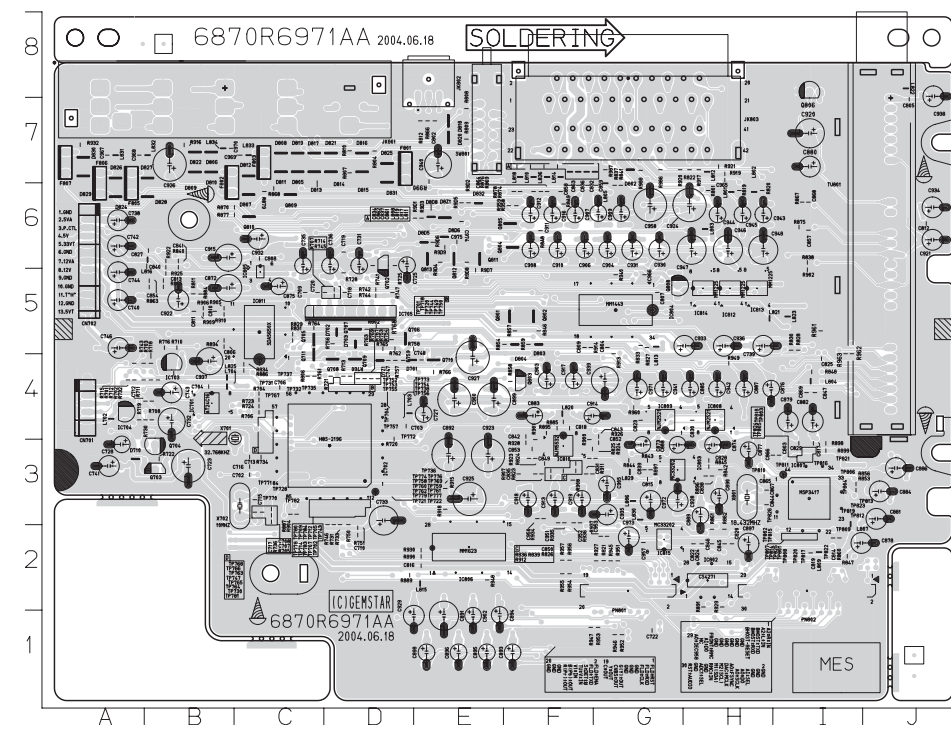
1. MAIN P.C.BOARD(TOP SIDE)



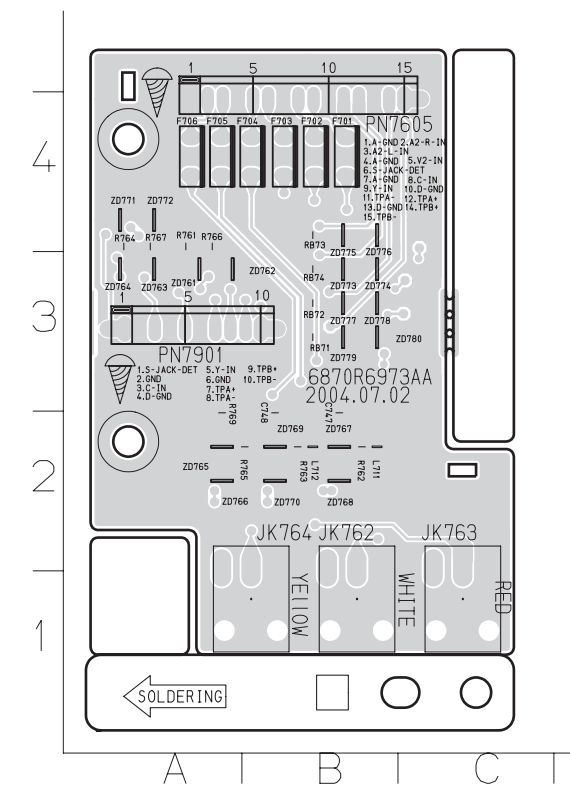
LOCATION GUIDE

/DASPD2	C4223	B5	IC1095	F6	RR119	D6	RR915	B2	
/DIORD2	C4225	B5	IC1099	C5	RR120	D6	RR916	B2	
/DIOWD2	C4226	B5	IC2075	C6	RR121	D6	RR933	B2	
/RST=ATAC	D3	A5	IC3048	H5	RR125	C6	RR939	D3	
AGS	D3	A5	IC6901	D3	RR126	C6	RR940	D3	
C100	D6	A5	IC6902	D2	RR127	C6	RR103	D4	
C101	C6	A5	IC6903	D2	RR128	C6	RR104	D4	
C120	C6	A5	IC901	C2	RR130	C4	RR105	D5	
C125	C6	A6	IC903	D3	RR132	C4	RR106	D5	
C129	D7	A5	L102	C5	RR133	D4	RR107	D5	
C130	G3	A5	L103	H3	RR134	C4	RR108	D5	
C133	G3	B6	L105	C6	RR135	C4	RR109	G4	
C134	H3	D1	L107	C6	RR136	C4	RR110	G4	
C136	C6	D3	L109	C7	RR137	C4	RR113	G5	
C173	C6	D3	L110	H3	RR138	C4	RR114	G5	
C179	G4	D3	L111	G3	RR139	C4	RR117	F6	
C182	G4	C3	L112	H3	RR140	C5	RR118	F6	
C183	G4	C3	L114	D6	RR141	D6	RR119	F6	
C190	G6	C1	L115	C7	RR142	D6	RR122	D5	
C192	G4	C3	L116	C7	RR143	D6	RR123	B4	
C194	F2	B2	L130	D6	RR150	F1	RR124	B4	
C195	G5	C1	L303	H5	RR151	F1	RR301	G5	
C196	G6	D1	L351	H6	RR152	F1	RR302	G5	
C198	G4	C1	L402	B3	RR154	F1	RR404	B5	
C199	G5	C2	L403	A3	RR155	F1	RR405	B5	
1A1	G5	B8	L406	A6	RR159	G6	SW201	H2	
1A3	D6	F3	L407	C6	RR160	G6	TP03	D3	
1E9	F7	D0	L408	C6	RR161	F6	TP20	C3	
1E9	C7	D1	L409	A6	RR162	G6	TP21	C3	
1F5	H3	D2	L410	A3	RR163	D6	TP22	C3	
1F6	G4	D1	LD102	H3	RR164	D6	TP23	C3	
1F7	D7	D2	LED1	C1	RR165	D5	TP24	C3	
1V1	B3	D2	LP	D3	RR167	G6	TP25	C3	
1V3	B4	D2	MMCM02	C2	RR168	G6	TP26	C3	
1V5	B3	D2	MMCK	C2	RR169	G6	TP27	C2	
1V6	A4	D2	MMCD	C2	RR177	D5	TP28	C2	
1V7	A4	D3	MMWP	C2	RR187	G4	TP31	C2	
1V8	A4	D4	MSB5	C2	RR189	G6	TP32	C2	
1V9	B4	D5	MSCK	C2	RR195	G4	TP33	C2	
C210	C6	D6	MSD	C2	RR197	G6	TP34	C2	
C331	H5	D7	MS±D	C3	RR199	E1	TP35	C2	
C332	H5	D8	MS±D1	C3	RR1A1	D6	TP36	C2	
C333	H5	D9	MS±D2	C3	RR1A2	D6	TP37	C2	
C337	H6	D9	MS±D3	C3	RR1A4	D6	TP40	C2	
C346	H5	FMCP	C2	PN190	H4	RR1A7	D4	TP43	C2
C348	H5	GND	F7	PN540	H4	RR1Z2	C7	TP47	C2
C400	A4	GND1	A6	PN740	F1	RR1Z6	D6	TP48	C2
C401	A4	GND2	I6	PN740	F7	RR1Z6	D6	TP80	D2
C402	A4	GND3	F1	PN760	C1	RR204	G2	X1	D3
C403	A4	GND4	H6	Q901	B2	RR208	F2	X101	C6
C404	A4	H101	H2	RR101	D4	RR209	D4	X2	C3
C405	A4	H201	H2	RR102	D4	RR210	F2	X301	H6
C415	B5	H203	F2	RR107	C4	RR212	F2	X401	A5
C416	B5	H501	I3	RR108	C4	RR213	F2	X901	C3
C417	B6	IC1057	E2	RR109	D4	RR216	C4		
C418	B5	IC1092	F4	RR110	D4	RR217	F2		
C421	B6	IC1093	F6	RR112	C5	RR218	C4		
C422	B5	IC1094	F4	RR118	D6	RR244	F2		

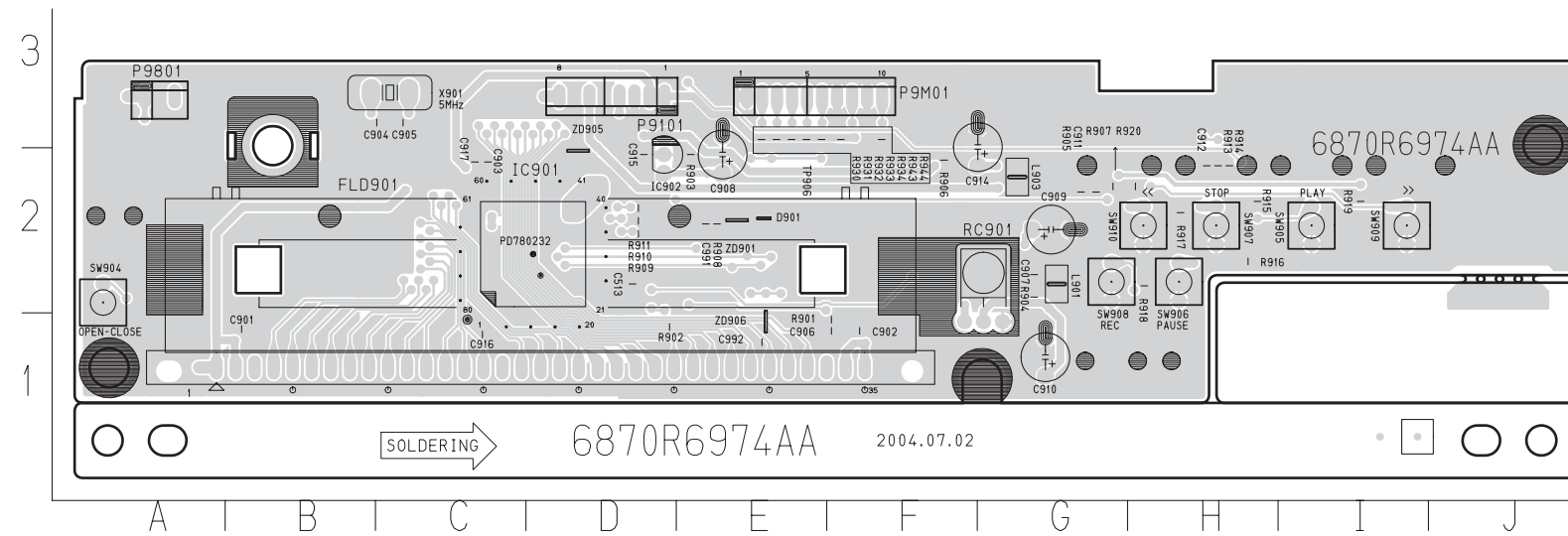
3. I/O P.C.BOARD



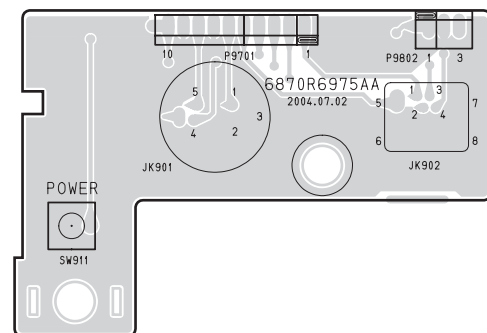
4. JACK P.C.BOARD



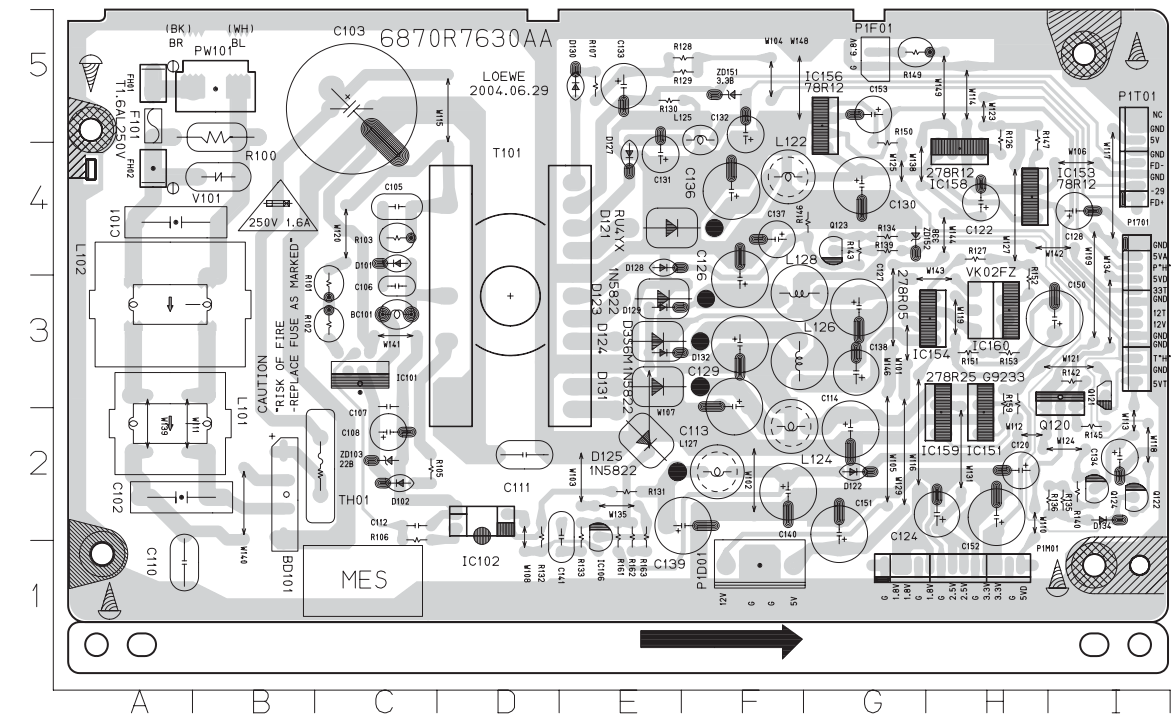
5. FRONT P.C.BOARD



6. KEY P.C.BOARD



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SUMMARY

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NEW FUNCTIONS OF DVD-RECORDER

• SUMMARY OF PRODUCT

- RECORDING FUNCTION OF DVD-RW AND DVD-R SPECIFICATIONS
 - DVD-RW: VIDEO MODE AND VR MODE RECORD AVAILABLE
 - DVD-R :VIDEO MODE RECORD AVAILABLE
- DIGITAL DUBBING FUNCTION OF DV CAMCORDER BY USING DV TERMINAL (IEEE1394)
 - RECORD, PLAY, FF/REW FUNCTION BY REMOTE CONTROL OF DV CAMCORDER
- DVD PROGRESSIVE PLAY RESPONSE
- VARIOUS FUNCTION RESPONSE OF DVD RECORDING (DISC NAVIGATION AND CONVENIENT PLAY, EDIT FUNCTION)
- OUTSIDE INPUT AND TV RECORDING AVAILABLE
 - RECORDING SCREEN QUALITY :VR(HQ, SQ, LQ), VIDEO(HQ, SQ)
- TV RESERVE RECORDING FUNCTION (AUTO MODE SETTING AVAILABLE FOR RECORDING IN ACCORDANCE WITH THE REMAINING DISC SPACE IN RESERVATION)

SUMMARIZED EXPLANATION OF MAIN FUNCTION

- DVD RECORDING FUNCTION(VR MODE RECORD / VIDEO MODE RECORD)
- 1) VR MODE RECORD : MANUAL MODE RECORDING IN ACCORDANCE WITH VARIOUS EDITING FUNCTION, REMAINING DISC SPACE AND PROGRAM TIME DVD-RW DISC RECORDED IN THE VR MODE CAN BE PLAYED WITH A DVD PLAYER CORRESPONDING TO THE DVD-RW THERE IS ALSO A PLAYER TO BE PLAYED THROUGH FINALIZING. FOR THE DVD-RW, RECORDING AND EDITING IS AVAILABLE AT THE SAME DEVICE EVEN AFTER FINALIZING.
 - 2) VIDEO MODE RECORDING : THERE IS NO EDITING FUNCTION SUCH AS VR MODE RECORDING BUT VIDEO MODE RECORDING IS PLAYED IN A GAME DEVICE (FOR EXAMPLE, "PLAY STATION 2") WITH PC, DVD PLAY FUNCTION CORRESPONDING TO DVD PLAYER, CAR DVD, DVD-ROM. TO PLAY IN ANOTHER DEVICE, FINALIZING IS REQUIRED. RECORDING, EDITING AND EDITING IS NOT POSSIBLE AFTER FINALIZING. HOWEVER, RECORDING IS ALLOWED AT THE DVD-RW DISC IF ERASING THE TITLE FINALLY RECORDED AFTER FINALIZING.
 - 3) RECORDING MODE INITIALIZATION (A KIND OF FORMATTING): BEGINS INITIALIZATION AFTER SELECTING RECORDING MODE AS VR OR VIDEO MODE BY USING INITIALIZATION FUNCTION OF THE DISC SETTING MENU. INITIALIZES DEFAULT AS VR MODE FOR DVD-RW. RECORDS IT AS VIDEO MODE WITHOUT INITIALIZATION FOR VIDEO MODE.
 - 4) FINALIZE: BEGINS FINALIZE AT THE DISC SETTING MENU DURING STOP.

PRODUCT SAFETY SERVICING GUIDELINES FOR VIDEO PRODUCTS

IMPORTANT SAFETY NOTICE

This manual was prepared for use only by properly trained audio-video service technicians.

When servicing this product, under no circumstances should the original design be modified or altered without permission from LG Electronics Corporation. All components should be replaced only with types identical to those in the original circuit and their physical location, wiring and lead dress must conform to original layout upon completion of repairs.

Special components are also used to prevent x-radiation, shock and fire hazard. These components are indicated by the letter "x" included in their component designators and are required to maintain safe performance. No deviations are allowed without prior approval by LG Electronics Corporation.

Circuit diagrams may occasionally differ from the actual circuit used. This way, implementation of the latest safety and performance improvement changes into the set is not delayed until the new service literature is printed.

CAUTION: Do not attempt to modify this product in any way. Never perform customized installations without manufacturer's approval. Unauthorized modifications will not only void the warranty, but may lead to property damage or user injury.

Service work should be performed only after you are thoroughly familiar with these safety checks and servicing guidelines.

GRAPHIC SYMBOLS



The exclamation point within an equilateral triangle is intended to alert the service personnel to important safety information in the service literature.



The lightning flash with arrowhead symbol within an equilateral triangle is intended to alert the service personnel to the presence of noninsulated "dangerous voltage" that may be of sufficient magnitude to constitute a risk of electric shock.



The pictorial representation of a fuse and its rating within an equilateral triangle is intended to convey to the service personnel the following fuse replacement caution notice:

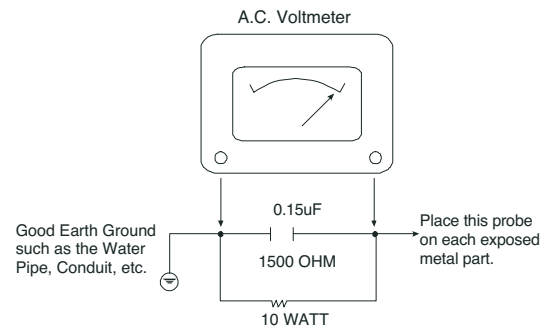
CAUTION: FOR CONTINUED PROTECTION AGAINST RISK OF FIRE, REPLACE ALL FUSES WITH THE SAME TYPE AND RATING AS MARKED NEAR EACH FUSE.

SERVICE INFORMATION

While servicing, use an isolation transformer for protection from AC line shock. After the original service problem has been corrected, make a check of the following:

FIRE AND SHOCK HAZARD

1. Be sure that all components are positioned to avoid a possibility of adjacent component shorts. This is especially important on items transported to and from the repair shop.
2. Verify that all protective devices such as insulators, barriers, covers, shields, strain reliefs, power supply cords, and other hardware have been reinstalled per the original design. Be sure that the safety purpose of the polarized line plug has not been defeated.
3. Soldering must be inspected to discover possible cold solder joints, solder splashes, or sharp solder points. Be certain to remove all loose foreign particles.
4. Check for physical evidence of damage or deterioration to parts and components, for frayed leads or damaged insulation (including the AC cord), and replace if necessary.
5. No lead or component should touch a high current device or a resistor rated at 1 watt or more. Lead tension around protruding metal surfaces must be avoided.
6. After reassembly of the set, always perform an AC leakage test on all exposed metallic parts of the cabinet (the channel selector knobs, antenna terminals, handle and screws) to be sure that set is safe to operate without danger of electrical shock. **DO NOT USE A LINE ISOLATION TRANSFORMER DURING THIS TEST.** Use an AC voltmeter having 5000 ohms per volt or more sensitivity in the following manner: Connect a 1500 ohm, 10 watt resistor, paralleled by a .15 mfd 150V AC type capacitor between a known good earth ground water pipe, conduit, etc.) and the exposed metallic parts, one at a time. Measure the AC voltage across the combination of 1500 ohm resistor and .15 mfd capacitor. Reverse the AC plug by using a non-polarized adaptor and repeat AC voltage measurements for each exposed metallic part. Voltage measured must not exceed 0.75 volts RMS. This corresponds to 0.5 milliamp AC. Any value exceeding this limit constitutes a potential shock hazard and must be corrected immediately.



TIPS ON PROPER INSTALLATION

1. Never install any receiver in a closed-in recess, cubbyhole, or closely fitting shelf space over, or close to, a heat duct, or in the path of heated air flow.
2. Avoid conditions of high humidity such as: outdoor patio installations where dew is a factor, near steam radiators where steam leakage is a factor, etc.
3. Avoid placement where draperies may obstruct venting. The customer should also avoid the use of decorative scarves or other coverings that might obstruct ventilation.
4. Wall- and shelf-mounted installations using a commercial mounting kit must follow the factory-approved mounting instructions. A product mounted to a shelf or platform must retain its original feet (or the equivalent thickness in spacers) to provide adequate air flow across the bottom. Bolts or screws used for fasteners must not touch any parts or wiring. Perform leakage tests on customized installations.
5. Caution customers against mounting a product on a sloping shelf or in a tilted position, unless the receiver is properly secured.
6. A product on a roll-about cart should be stable in its mounting to the cart. Caution the customer on the hazards of trying to roll a cart with small casters across thresholds or deep pile carpets.
7. Caution customers against using extension cords. Explain that a forest of extensions, sprouting from a single outlet, can lead to disastrous consequences to home and family.

SERVICING PRECAUTIONS

CAUTION : Before servicing the DVD Recorder covered by this service data and its supplements and addends, read and follow the SAFETY PRECAUTIONS. **NOTE** : if unforeseen circumstances create conflict between the following servicing precautions and any of the safety precautions in this publications, always follow the safety precautions.

Remembers Safety First:

General Servicing Precautions

1. Always unplug the DVD Recorder AC power cord from the AC power source before:
 - (1) Removing or reinstalling any component, circuit board, module, or any other assembly.
 - (2) Disconnection or reconnecting any internal electrical plug or other electrical connection.
 - (3) Connecting a test substitute in parallel with an electrolytic capacitor.
Caution : A wrong part substitution or incorrect polarity installation of electrolytic capacitors may result in an explosion hazard.
2. Do not spray chemicals on or near this DVD Recorder or any of its assemblies.
3. Unless specified otherwise in this service data, clean electrical contacts by applying an appropriate contact cleaning solution to the contacts with a pipe cleaner, cotton-tipped swab, or comparable soft applicator. Unless specified otherwise in this service data, lubrication of contacts is not required.
4. Do not defeat any plug/socket B+ voltage interlocks with which instruments covered by this service manual might be equipped.
5. Do not apply AC power to this DVD Recorder and/or any of its electrical assemblies unless all solid-state device heat sinks are correctly installed.
6. Always connect test instrument ground lead to the appropriate ground before connection the test instrument positive lead. Always remove the test instrument ground lead last.

Insulation Checking Procedure

Disconnect the attachment plug from the AC outlet and turn the power on. Connect an insulation resistance meter(500V) to the blades of the attachment plug. The insulation resistance between each blade of the attachment plug and accessible conductive parts (Note 1) should be more than 1M-ohm.

Note 1 : Accessible Conductive Parts including Metal panels, Input terminals, Earphone jacks, etc.

Electrostatically Sensitive (ES) Devices

Some semiconductor (solid state) devices can be damaged easily by static electricity. Such components commonly are called Electrostatically Sensitive (ES) Devices. Examples of typical ES devices are integrated circuits and some field effect transistors and semiconductor chip components.

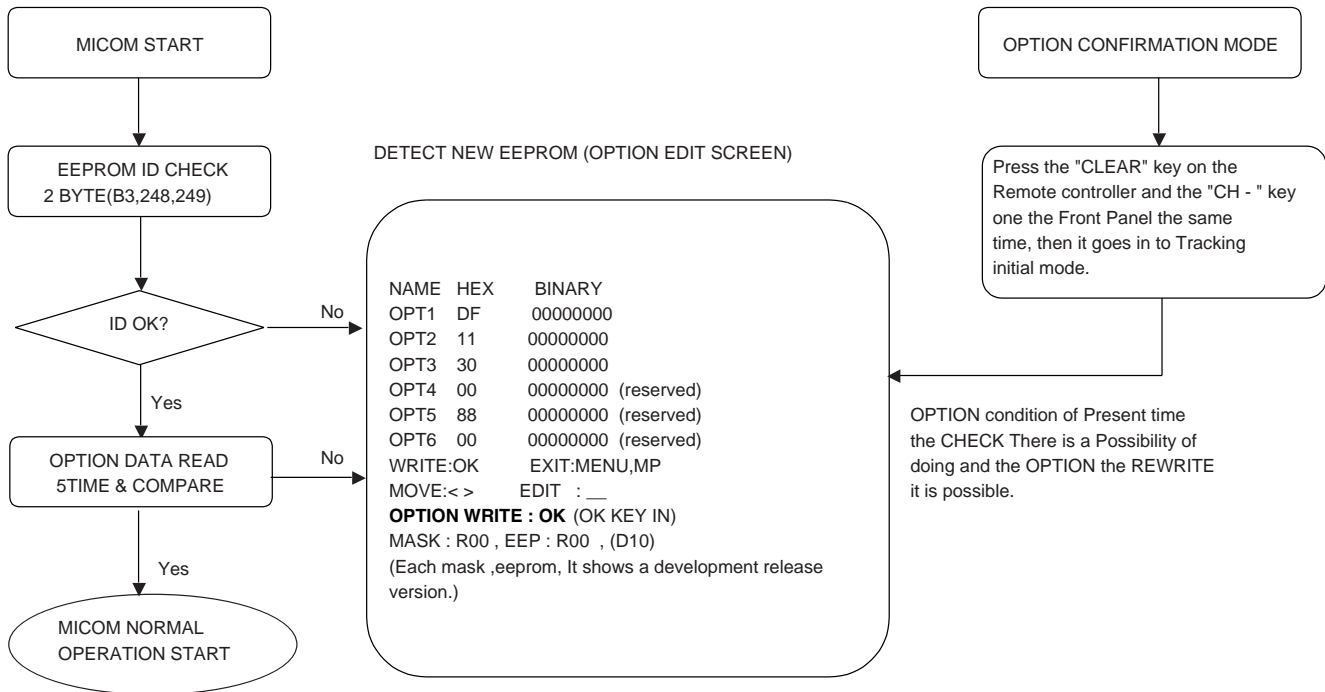
The following techniques should be used to help reduce the incidence of component damage caused by static electricity.

1. Immediately before handling any semiconductor component or semiconductor-equipped assembly, drain off any electrostatic charge on your body by touching a known earth ground. Alternatively, obtain and wear a commercially available discharging wrist strap device, which should be removed for potential shock reasons prior to applying power to the unit under test.
2. After removing an electrical assembly equipped with ES devices, place the assembly on a conductive surface such as aluminum foil, to prevent electrostatic charge buildup or exposure of the assembly.
3. Use only a grounded-tip soldering iron to solder or unsolder ES devices.
4. Use only an anti-static solder removal device. Some solder removal devices not classified a "anti-static" can generate electrical charges sufficient to damage ES devices.
5. Do not use freon-propelled chemicals. These can generate electrical charge sufficient to damage ES devices.
6. Do not remove a replacement ES device from its protective package until immediately before you are ready to install it. (Most replacement ES devices are packaged with leads electrically shorted together by conductive foam, aluminum foil, or comparable conductive material).
7. Immediately before removing the protective material from the leads of a replacement ES device, touch the protective material to the chassis or circuit assembly into which the device will be installed.

Caution : Be sure no power is applied to the chassis or circuit, and observe all other safety precautions.

8. Minimize bodily motions when handling unpackaged replacement ES devices. (Normally harmless motion such as the brushing together of your clothes fabric or the lifting of your foot from a carpeted floor can generate static electricity sufficient to damage an ES device.)

SERVICE INFORMATION FOR EEPROM IC SETTING



*** EEPROM INITIAL ***

- SETUP is displayed in the field if pressing the Front ch- & ch+ Key with the Remocon number "clear" key pressed in the status of powering on.
- AUTO SEARCH is done since the initial screen of ACMS is serviced if powering on.

SPECIFICATIONS

• GENERAL

Power requirements	AC 200-240V, 50/60 Hz
Power consumption	44W
Dimensions (approx.)	430 X 92 X 382.5 mm (16.9 x 3.6 x 15 inches) (w x h x d)
Mass (approx.)	6.4 kg (14.1 lbs)
Operating temperature	5°C to 35°C (41°F to 95°F)
Operating humidity	5 % to 90 %
Television system	PAL B/G colour system
Recording format	PAL

• RECORDING

Recording format	DVD VideoRecording, DVD-VIDEO
Recordable discs	DVD-ReRecordable, DVD-Recordable
Recordable time	Approx. 1 hour (HQ mode), 2 hours (SQ mode), 4 hours (LQ mode)

Video recording format

Sampling frequency	27MHz
Compression format	MPEG 2

Audio recording format

Sampling frequency	48kHz
Compression format	Dolby Digital

• DVD SPECIFICATIONS

Laser system	Semiconductor laser
Frequency response	DVD (PCM 48 kHz): 8 Hz to 22 kHz, CD: 8 Hz to 20 kHz
Signal-to-noise ratio	More than 100 dB
Harmonic distortion	Less than 0.008%
Dynamic range	More than 95 dB

• INPUTS

AERIAL IN	Aerial input, 75 ohms
VIDEO IN	1.0 Vp-p 75 ohms, sync negative, RCA jack x 2 / SCART
AUDIO IN	0 dBm more than 47 kohms, RCA jack (L, R) x 2 / SCART
DV IN	4 pin (i.LINK/IEEE 1394 standard)

• OUTPUTS

VIDEO OUT	1 Vp-p 75 Ω, sync negative, RCA jack x 1
S-VIDEO OUT	(Y) 1.0 V (p-p), 75 Ω, negative sync, Mini DIN 4-pin x 1 (C) 0.3 V (p-p) 75 Ω
COMPONENT VIDEO OUT	(Y) 1.0 V (p-p), 75 Ω, negative sync, RCA jack x 1 (Pb)/(Pr) 0.7 V (p-p), 75 Ω, RCA jack x 2
Audio output (digital audio)	0.5 V (p-p), 75 Ω, RCA jack x 1
Audio output (optical audio)	5 V (p-p), 75 Ω, Optical connector x 1
Audio output (analog audio)	2.0 Vrms (1 KHz, 0 dB), 600 Ω, RCA jack (L, R) x 1 / SCART

• ACCESSORY:

Video cable	1	Audio cable	1
RF Coaxial Cable.....	1	Blank DVD-R disc	2
Remote control	1	Batteries	2

SECTION 2
CABINET & MAIN CHASSIS

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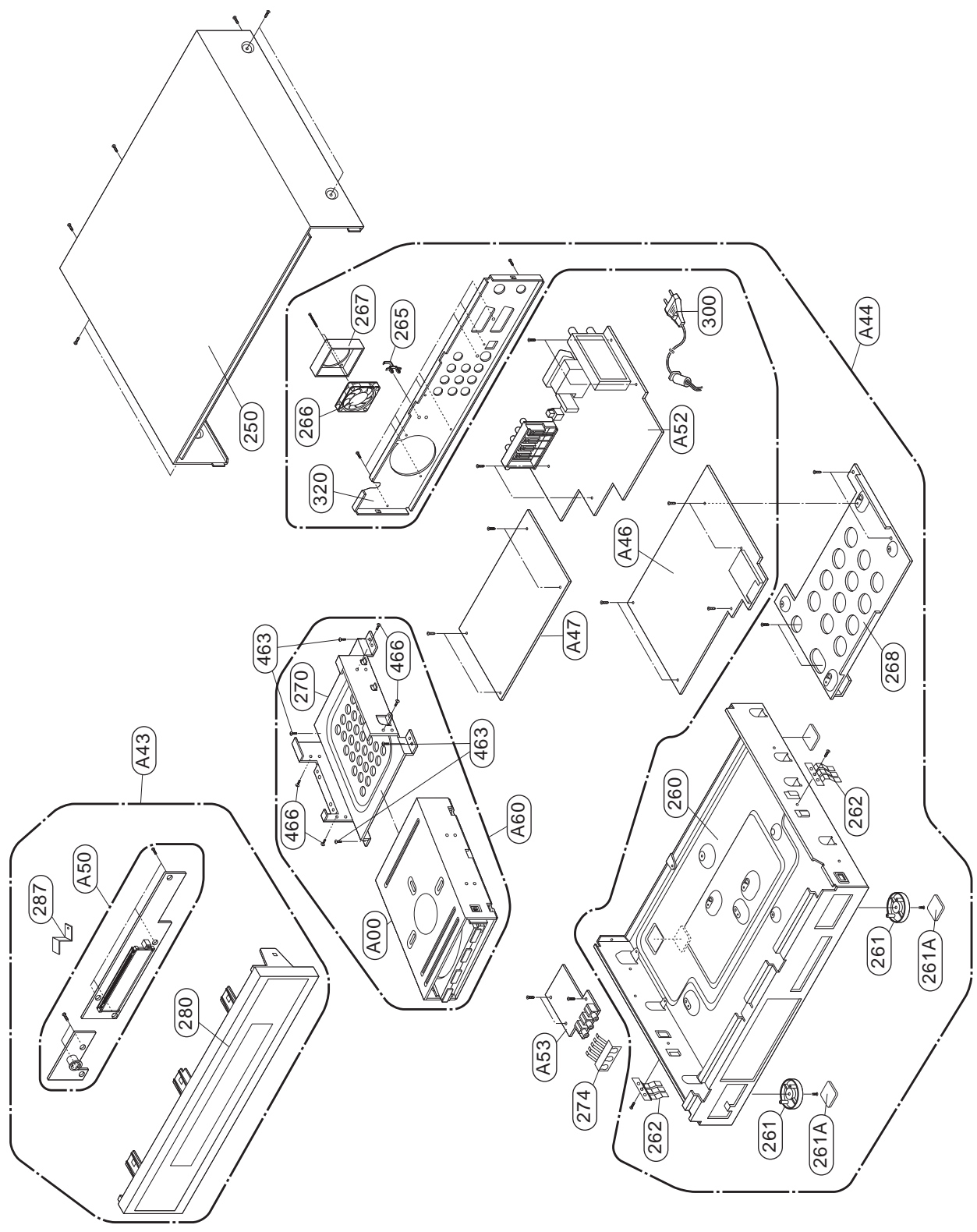
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EXPLODED VIEWS

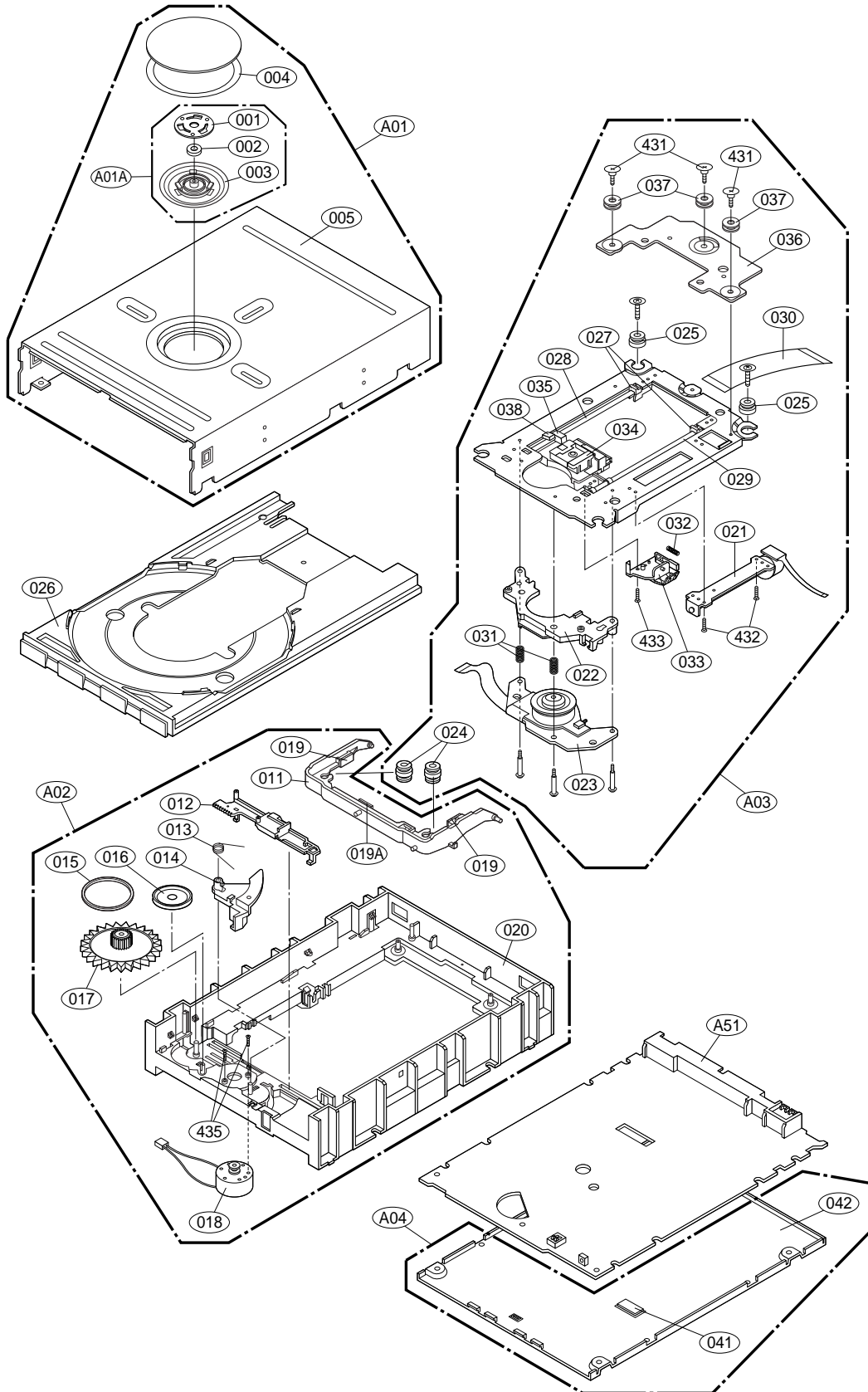
1. Cabinet and Main Frame Section

5
4
3
2
1

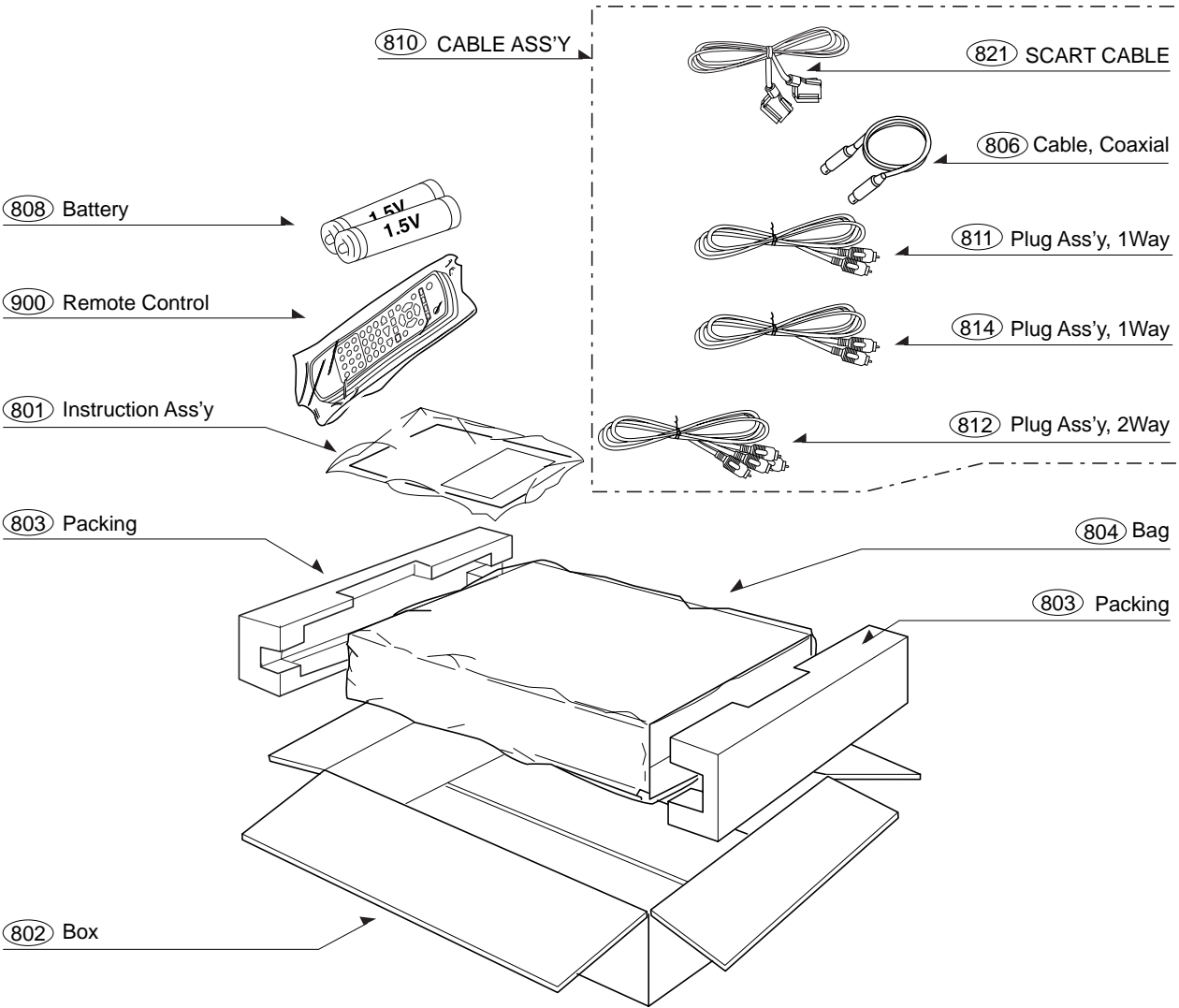


A B C D

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3. Packing Accessory Section



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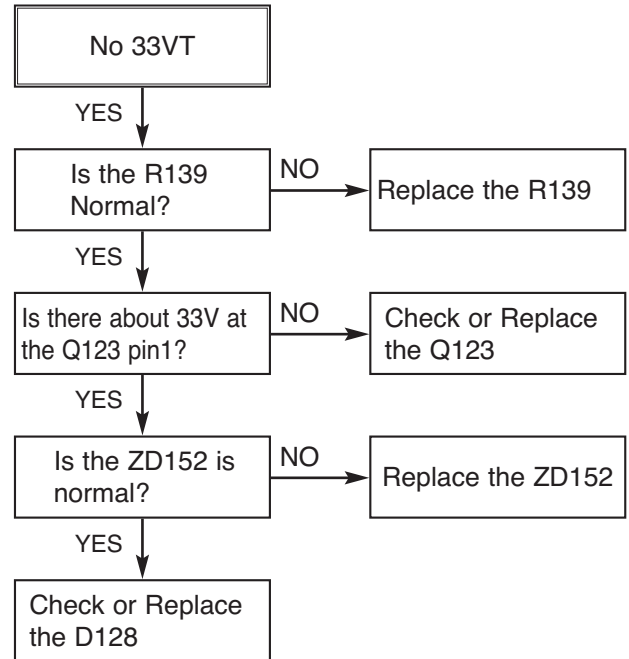
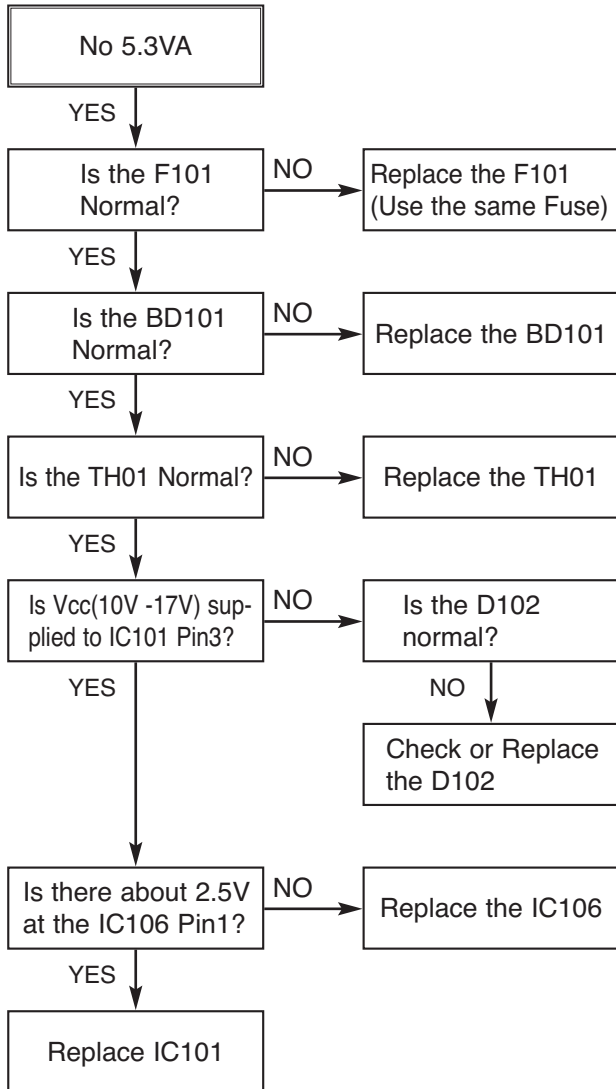
RL-01A LOADER PART

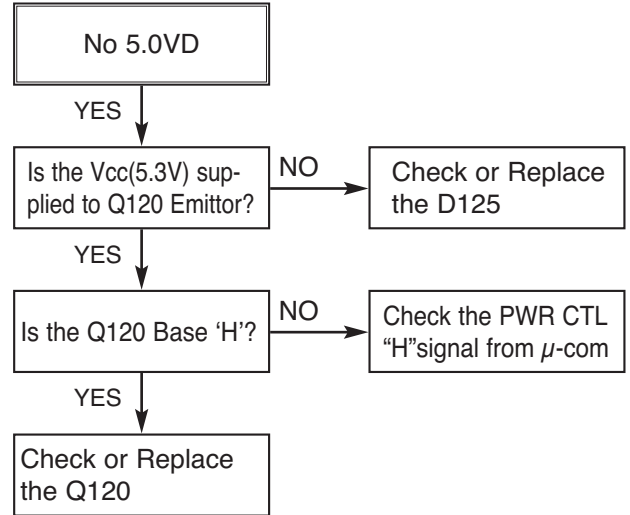
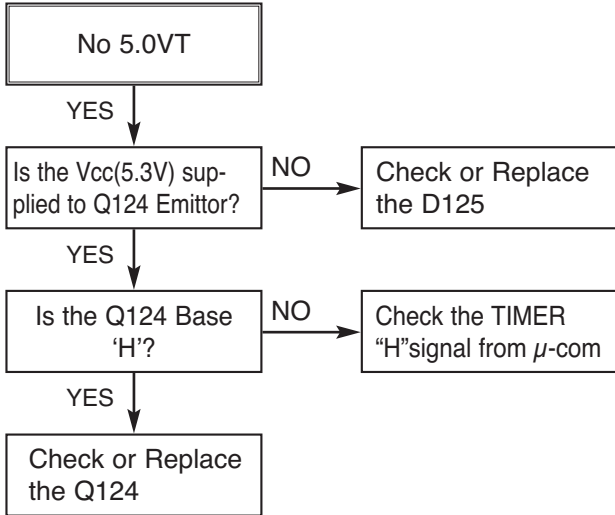
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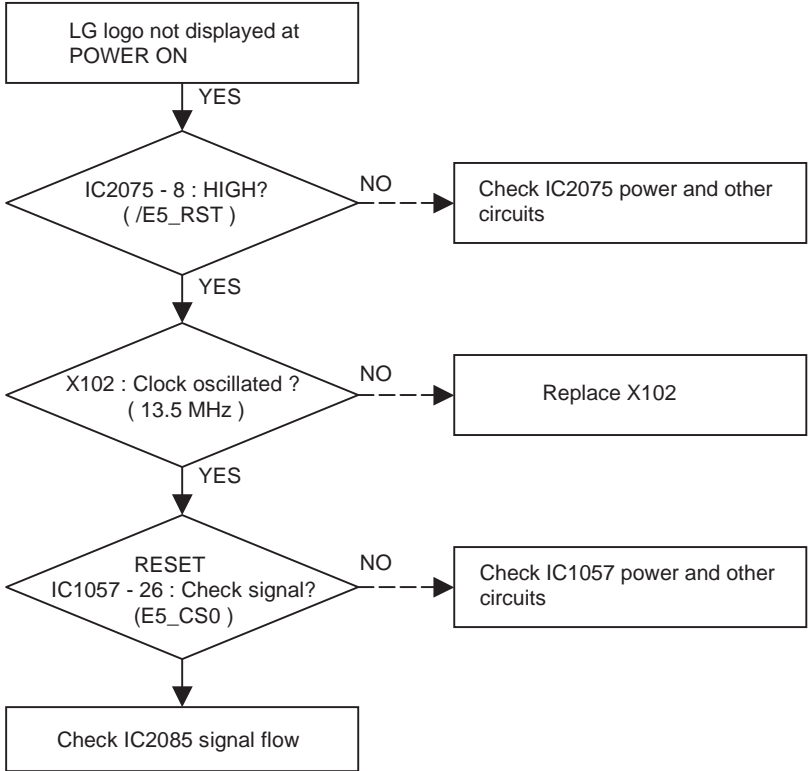
VDR PART

ELECTRICAL TROUBLESHOOTING GUIDE

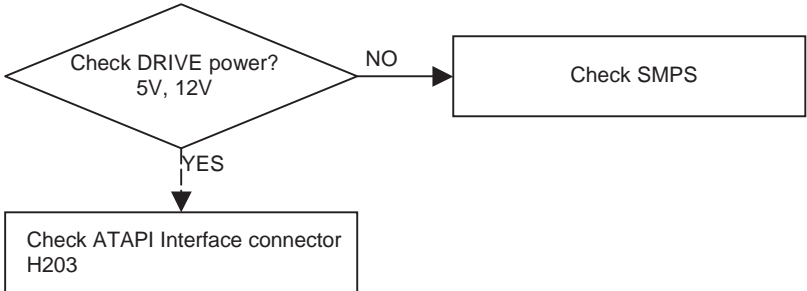




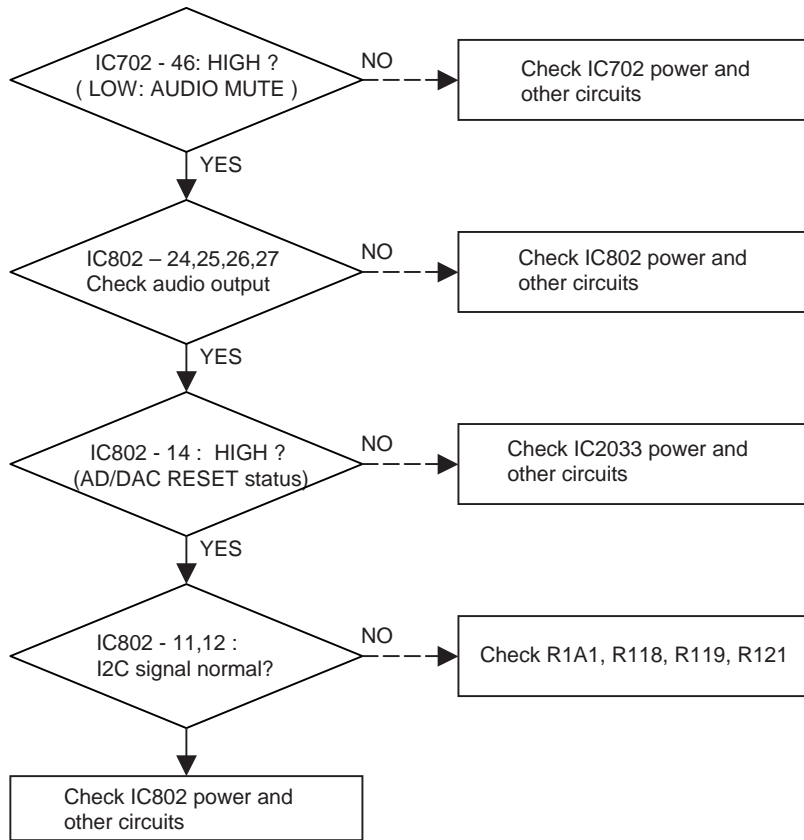
SYSTEM Section



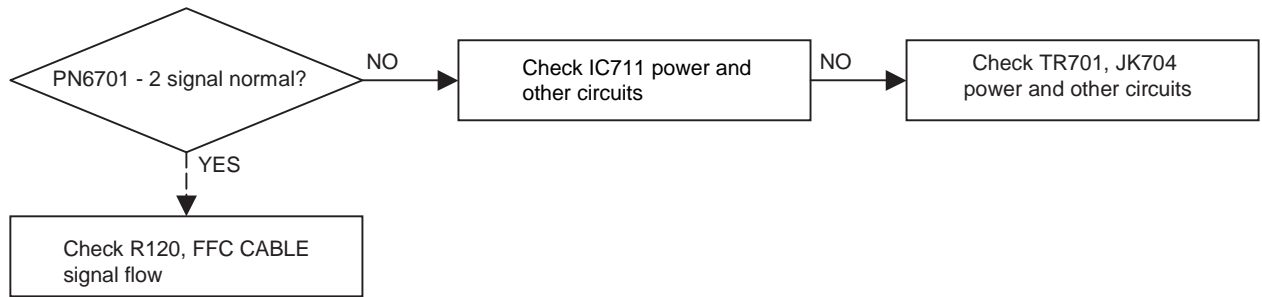
DISC not recognized



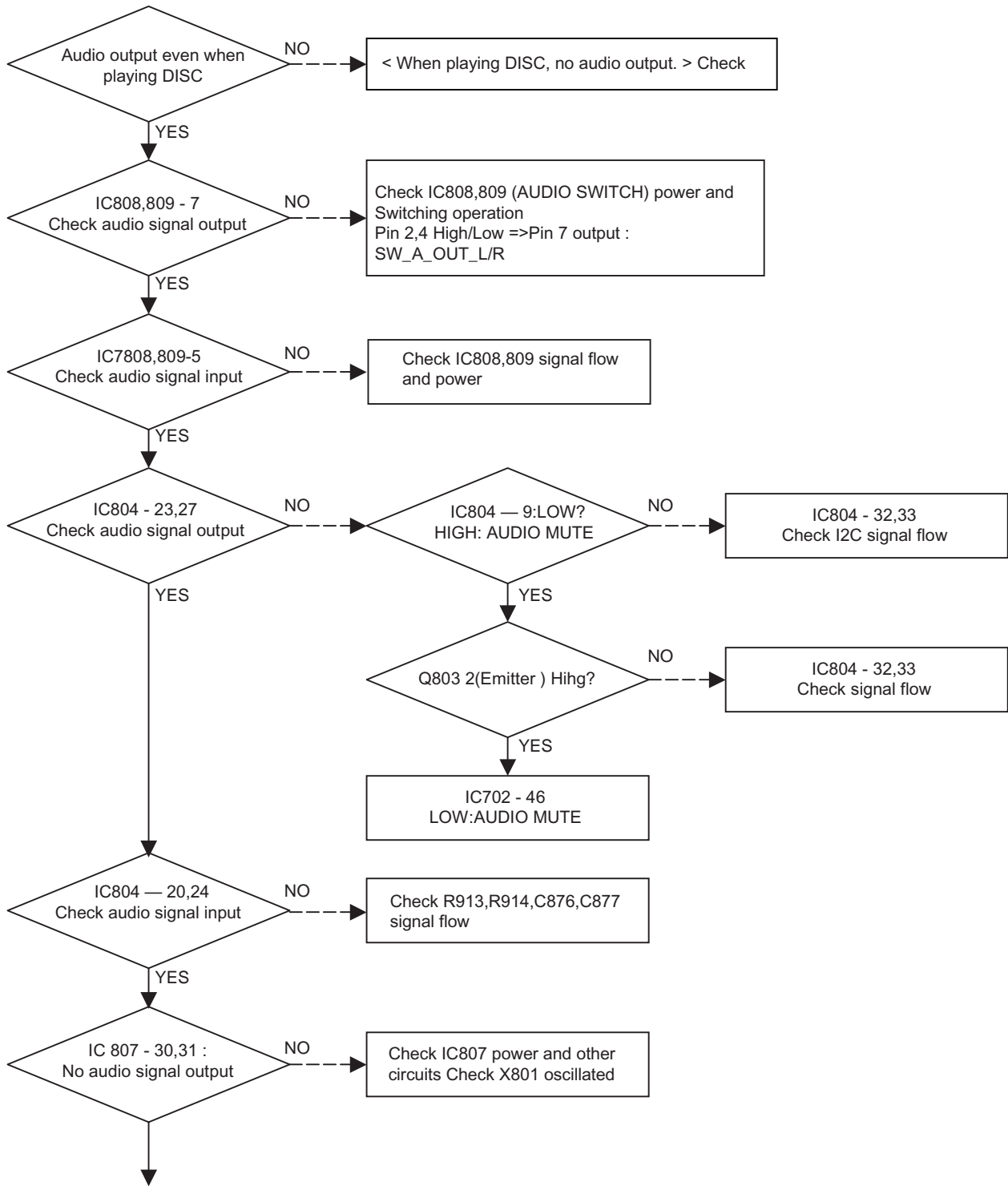
When playing DISC, no audio output

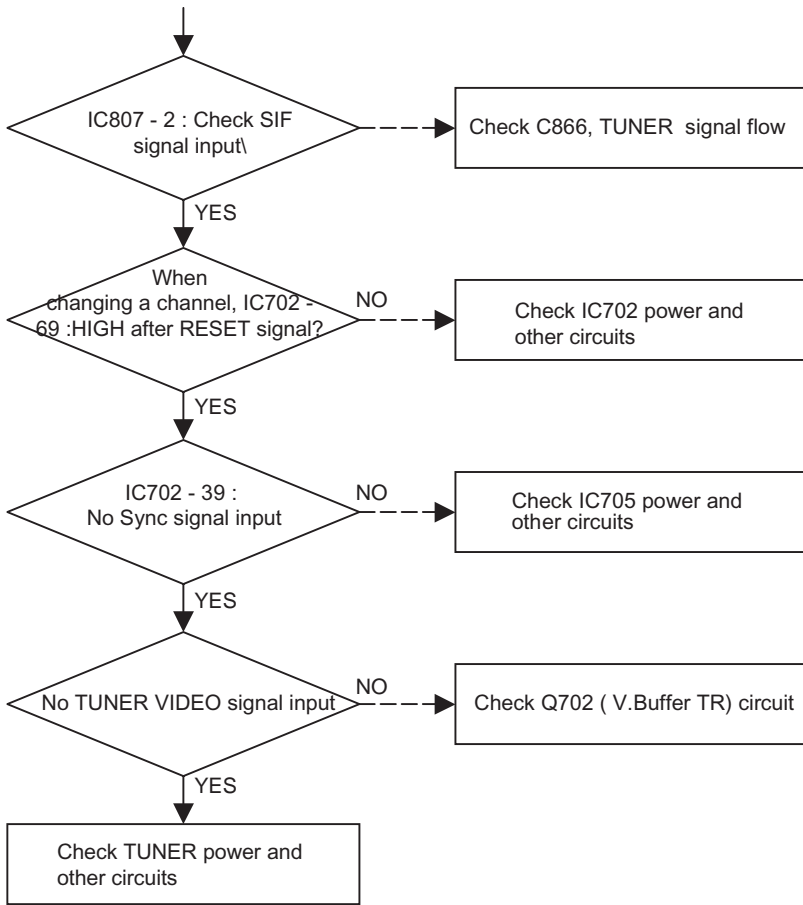


No OPTICAL / DIGITAL output

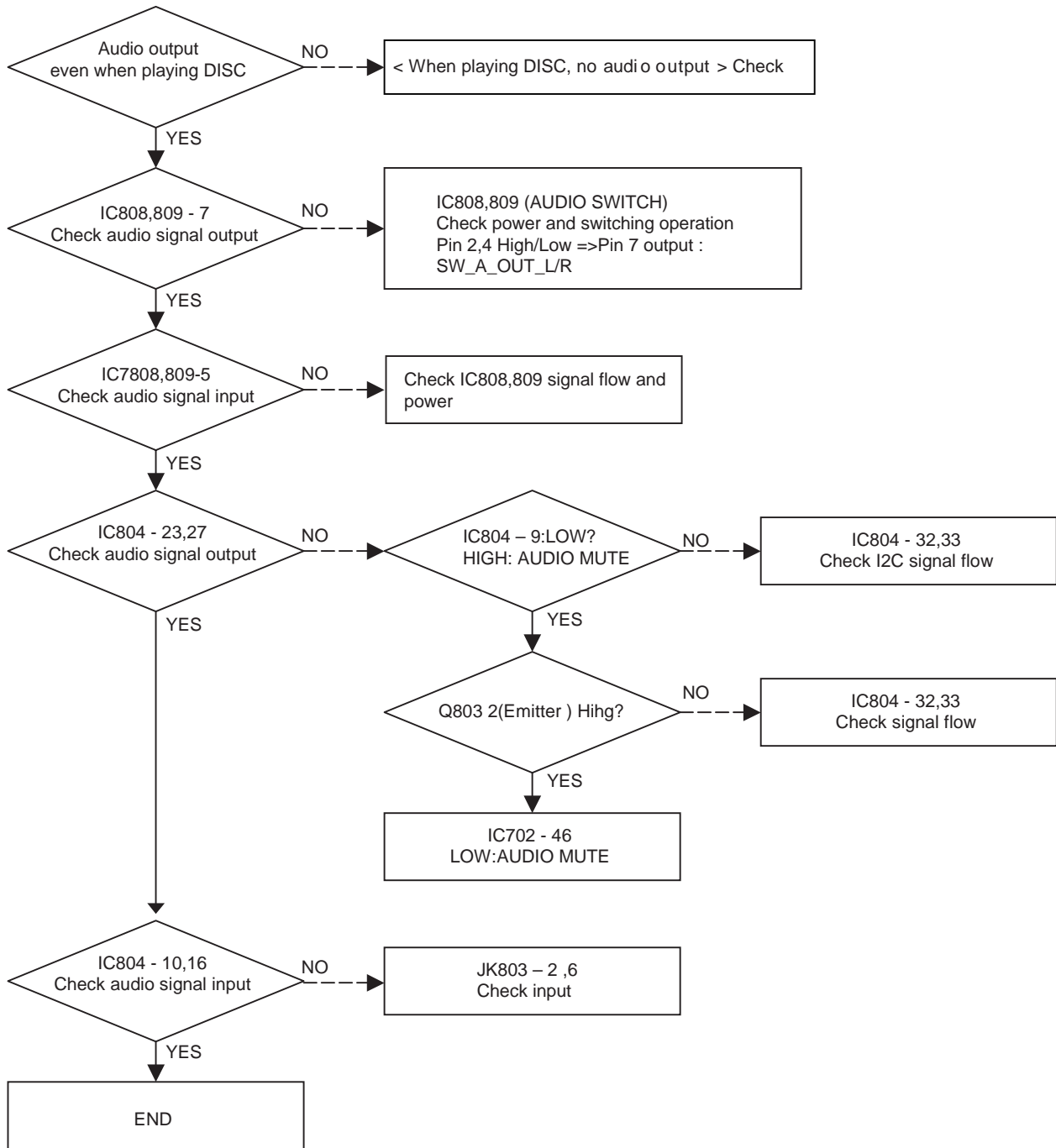


No TUNER audio output

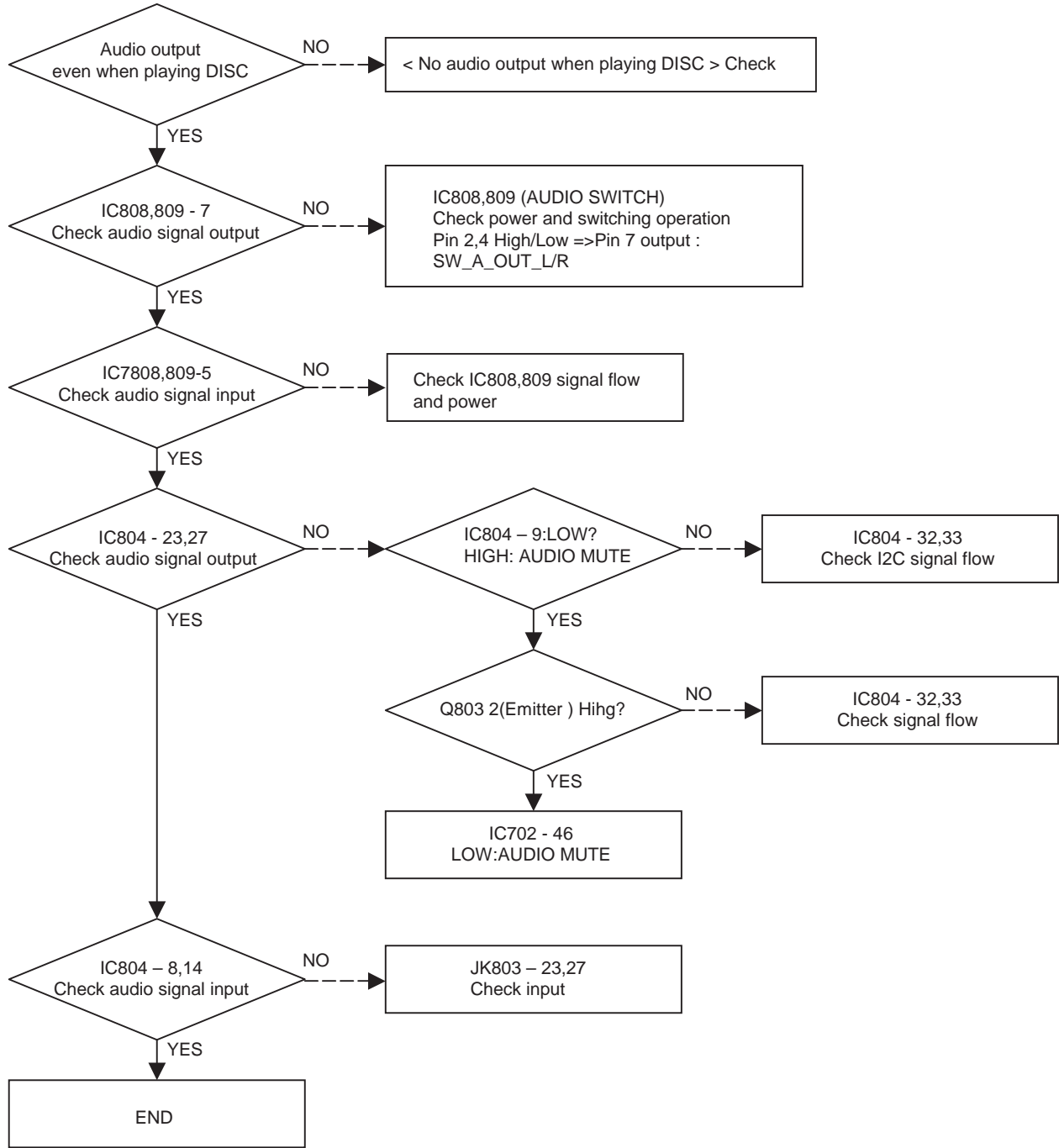




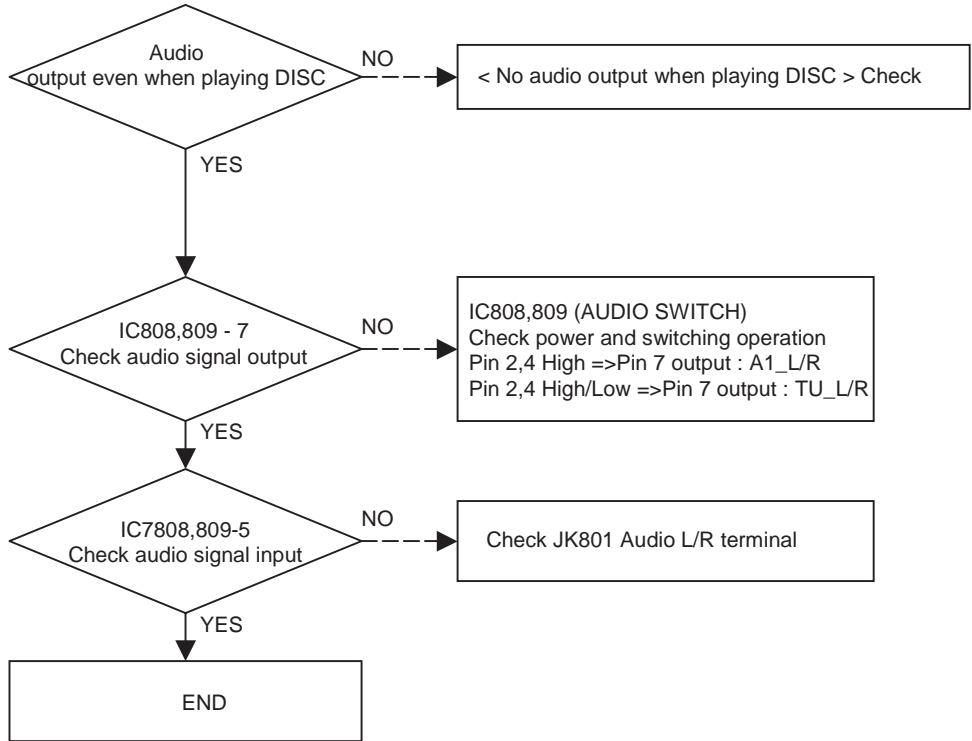
No external input 1 audio



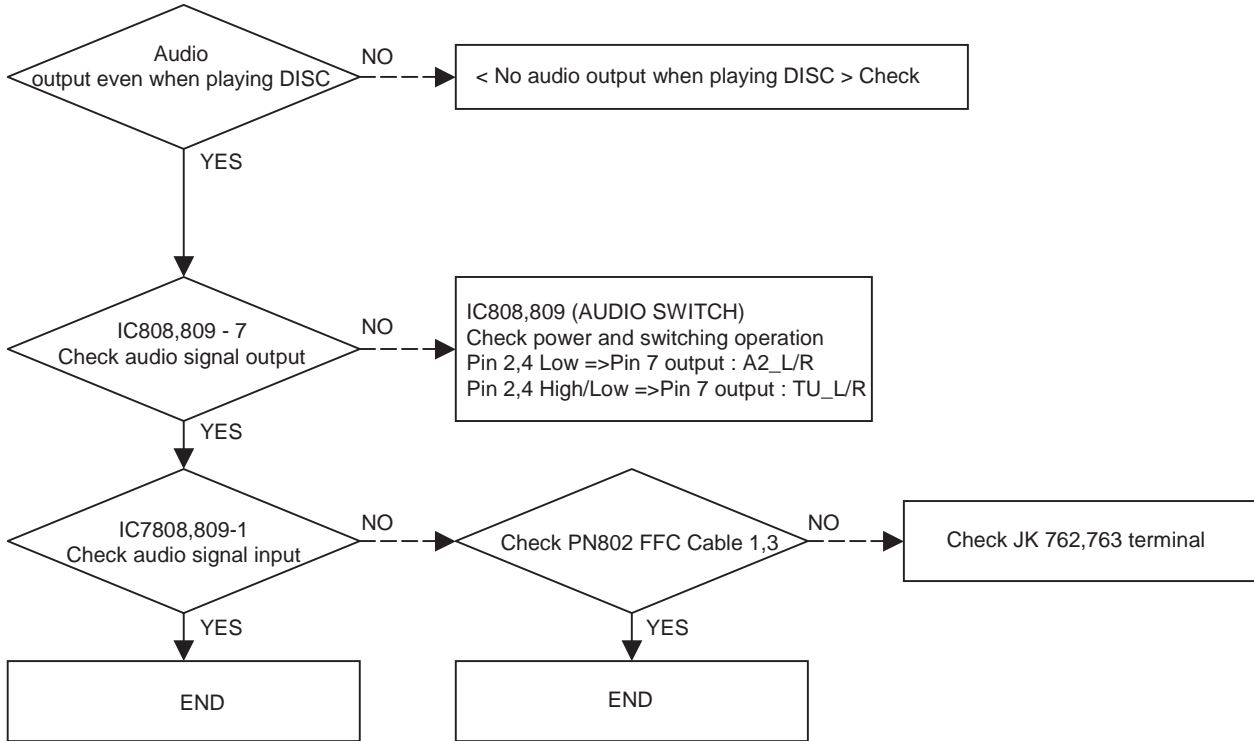
No external input 2 audio



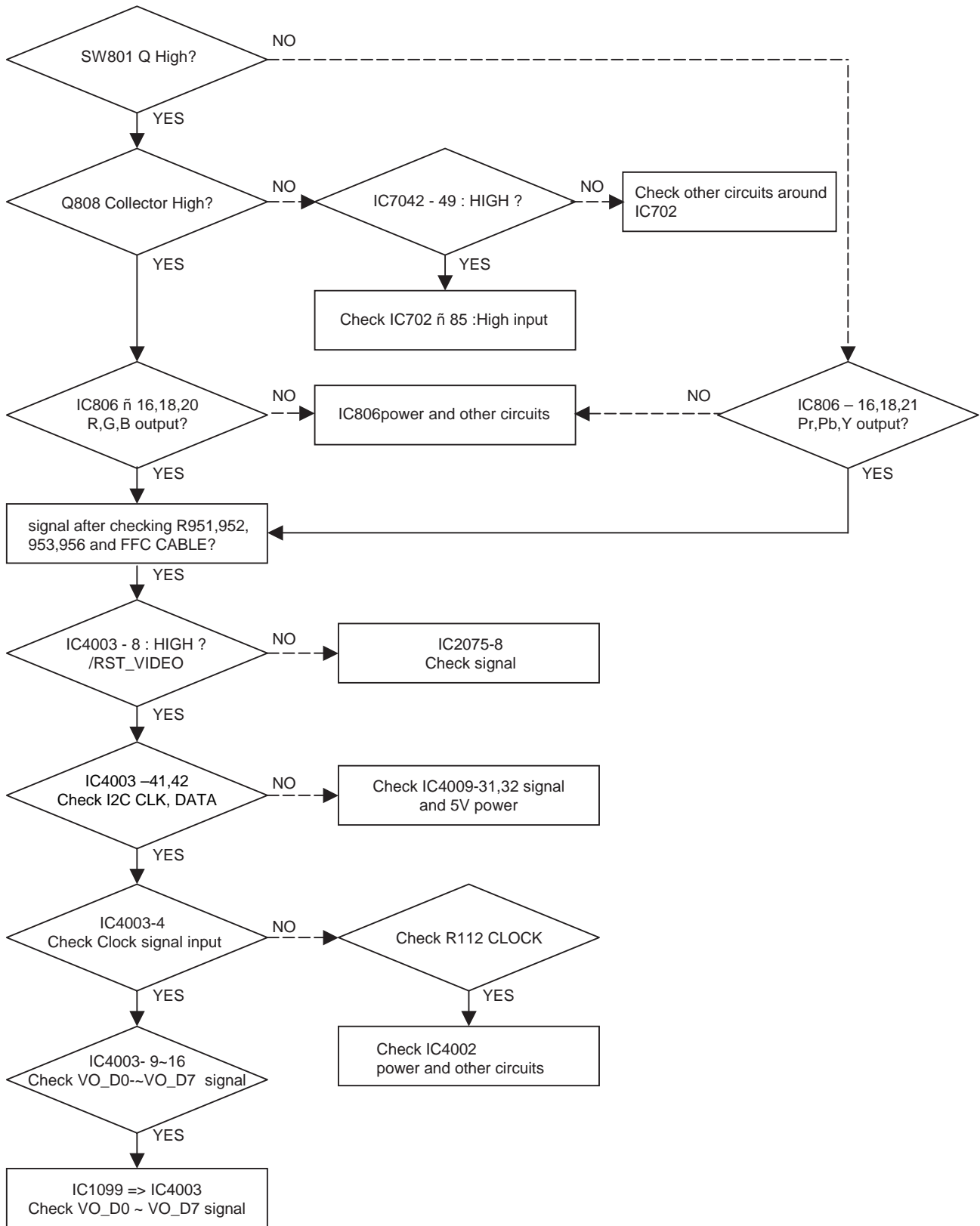
No external input 3 audio



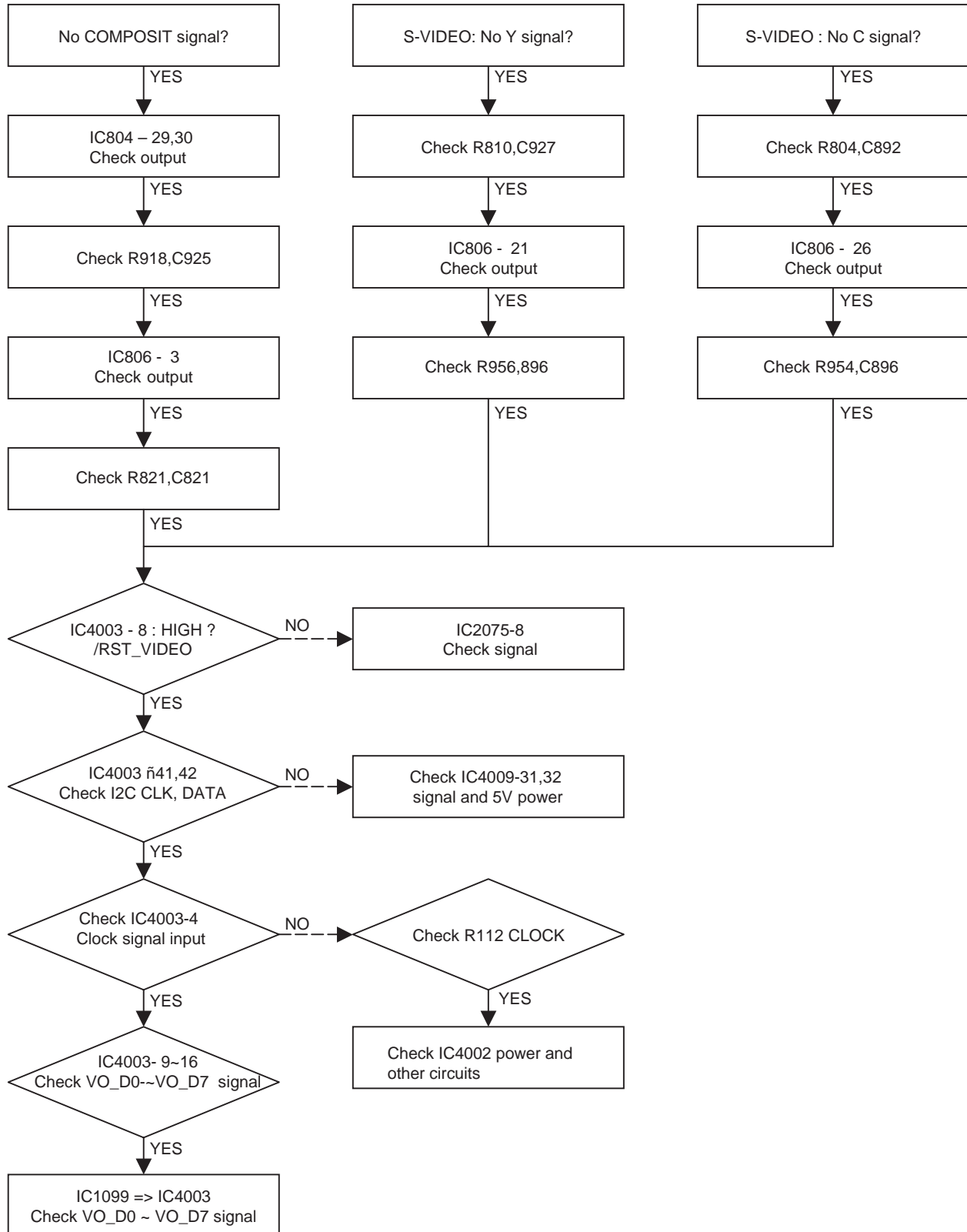
No external input 4 audio



No RGB /
Component video signal when playing DISC



No COMPOSIT / S-VIDEO signal when playing DISC



No TV , external input video signal

When connecting Tuner,
no TV video signal

YES

Check Tuner power and other
circuits

YES

Check IC804 - 28(input) and 31
(output), and then CABLE =>
IC4009 signal input

YES

No video signal of external input 1/2
(Rear Comosite input)

YES

Check IC804 - 1,3(input) and 31
(output), and then CABLE =>
IC4009 signal input

YES

No video signal of external input 3/4
(Front S-VIEDO / Comosite input)

YES

JACK CABLE => IC4009
Check signal input

YES

IC4009 - 27 : HIGH ?
/RST_VIDEO

NO

Check IC2075- 8,9 signal

YES

When RESET,
check IC4009 - 31,32
I2C CLK, DATA

NO

Check IC4003- 40,41
signal and 5V LEVEL

YES

Check IC4009 — 91,94 signal?
(VI_CLK, VI_VSYNCO)

NO

Check X401 Clock oscillated

NO

Replace X401 X-TAL

YES

IC4009 => IC1099
Check VI_D0 ~ VI_D7 signal

NO

Check IC4009 power and
other circuits

YES

< When playing DISC, no COMPONENT, COMPOSIT/S-VIDEO signal > Check

No DV(IEEE 1394) input (video/audio) signal

Check DV_JACK and CABLE connection

YES

IC3048 - 42,43,44,45
Check signal input

YES

IC3048 - 78:HIGH?
(/RST_PHY)

NO

Check IC2075 -8 signal

YES

Check IC3048 - 2 Clock?
BIO_PHY_CLK

NO

Check X301 Clock
24.576 MHz

YES

IC3048 => IC1099
Check BIO_PHY_DATA/CLK
signal

NO

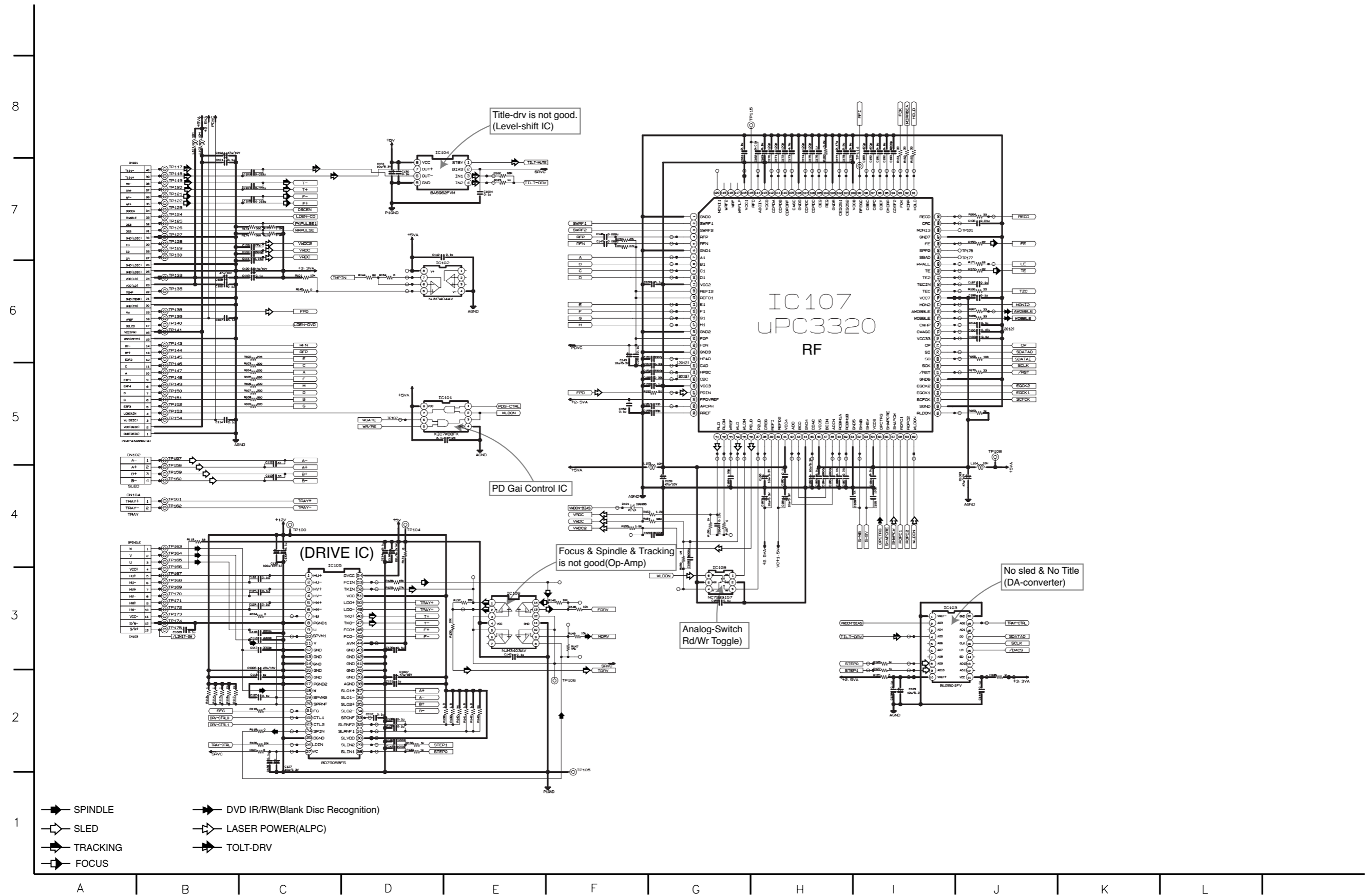
Check IC3048 power and
other circuits

YES

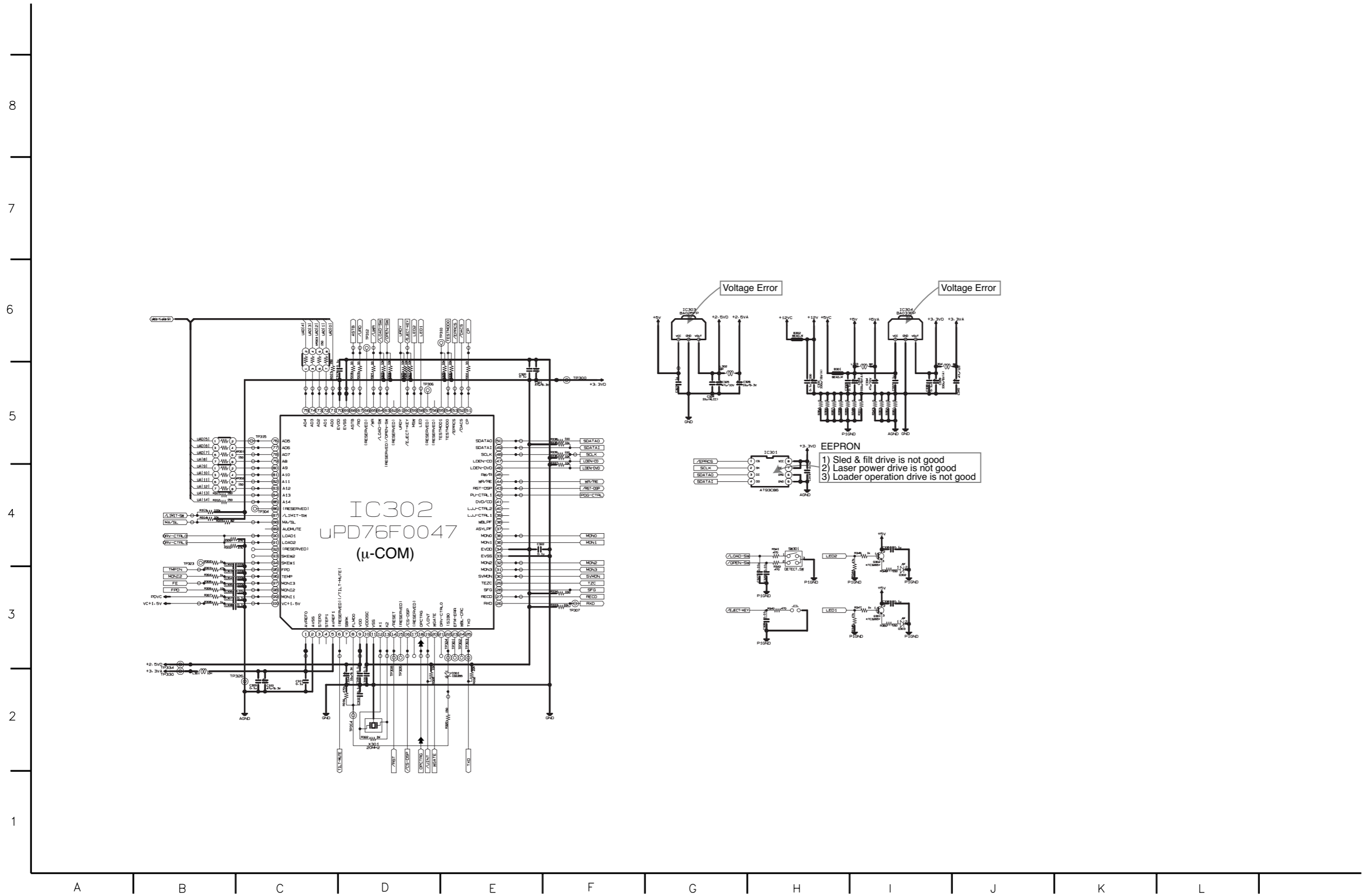
< When playing DISC, No COMPONENT, COMPOSIT/S-VIDEO signal > Check

CIRCUIT DIAGRAMS

1. RF CIRCUIT DIAGRAM



3. μ -COM CIRCUIT DIAGRAM



IC302
UPD76F004
(μ -COM)

Voltage Error

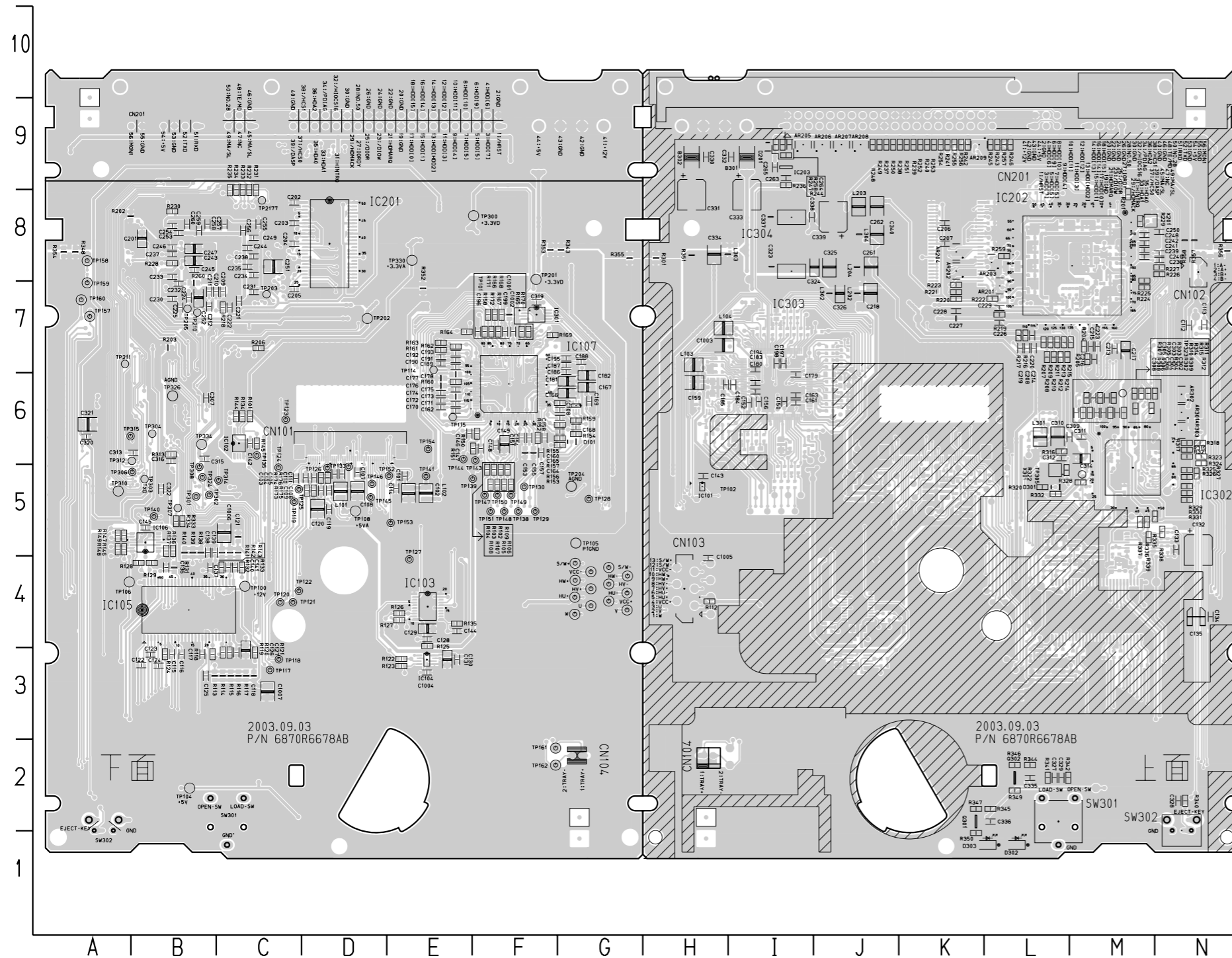
Voltage Error

EEPROM

- 1) Sled & filter drive is not good
- 2) Laser power drive is not good
- 3) Loader operation drive is not good

PRINTED CIRCUIT DIAGRAMS

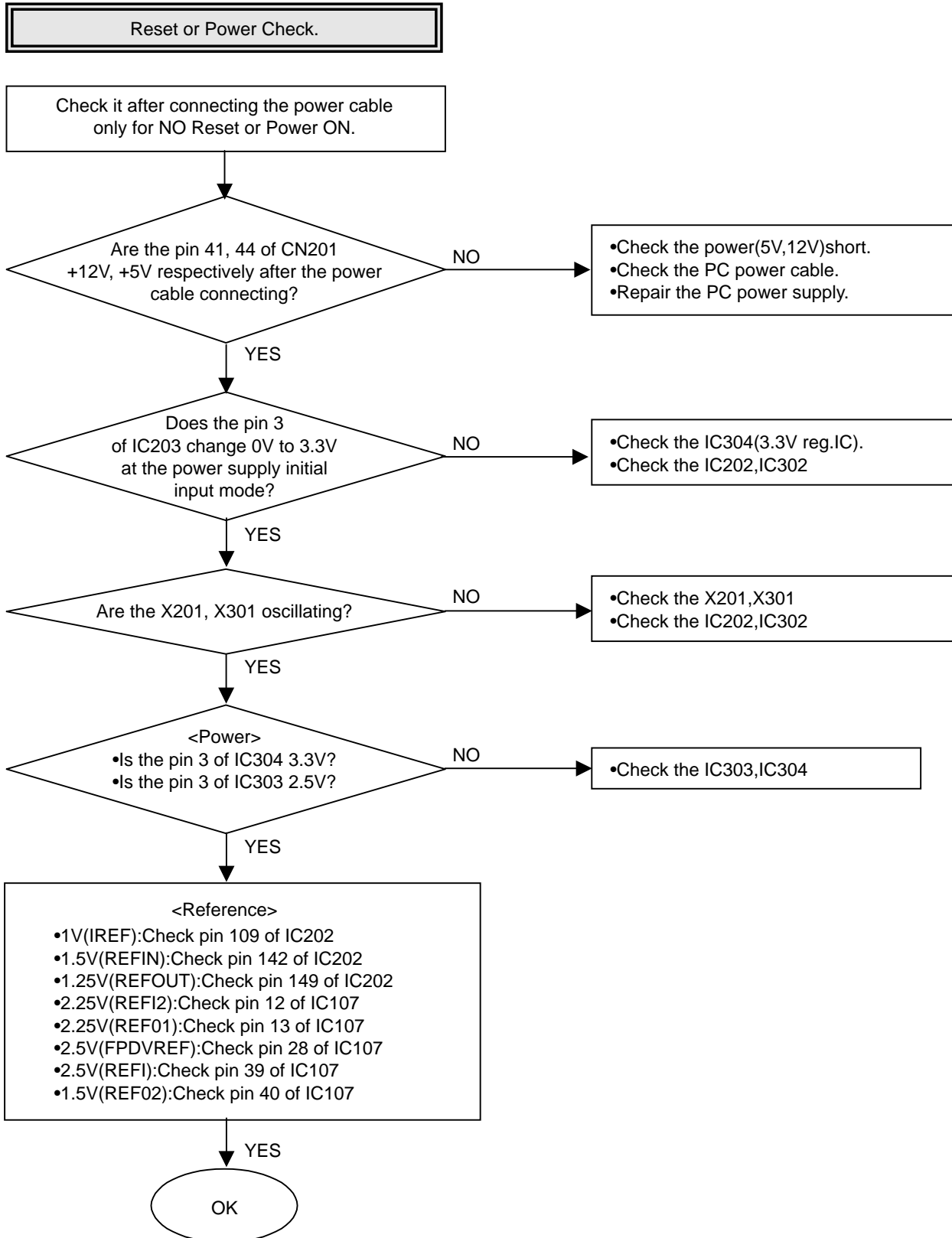
1. MAIN P.C.BOARD

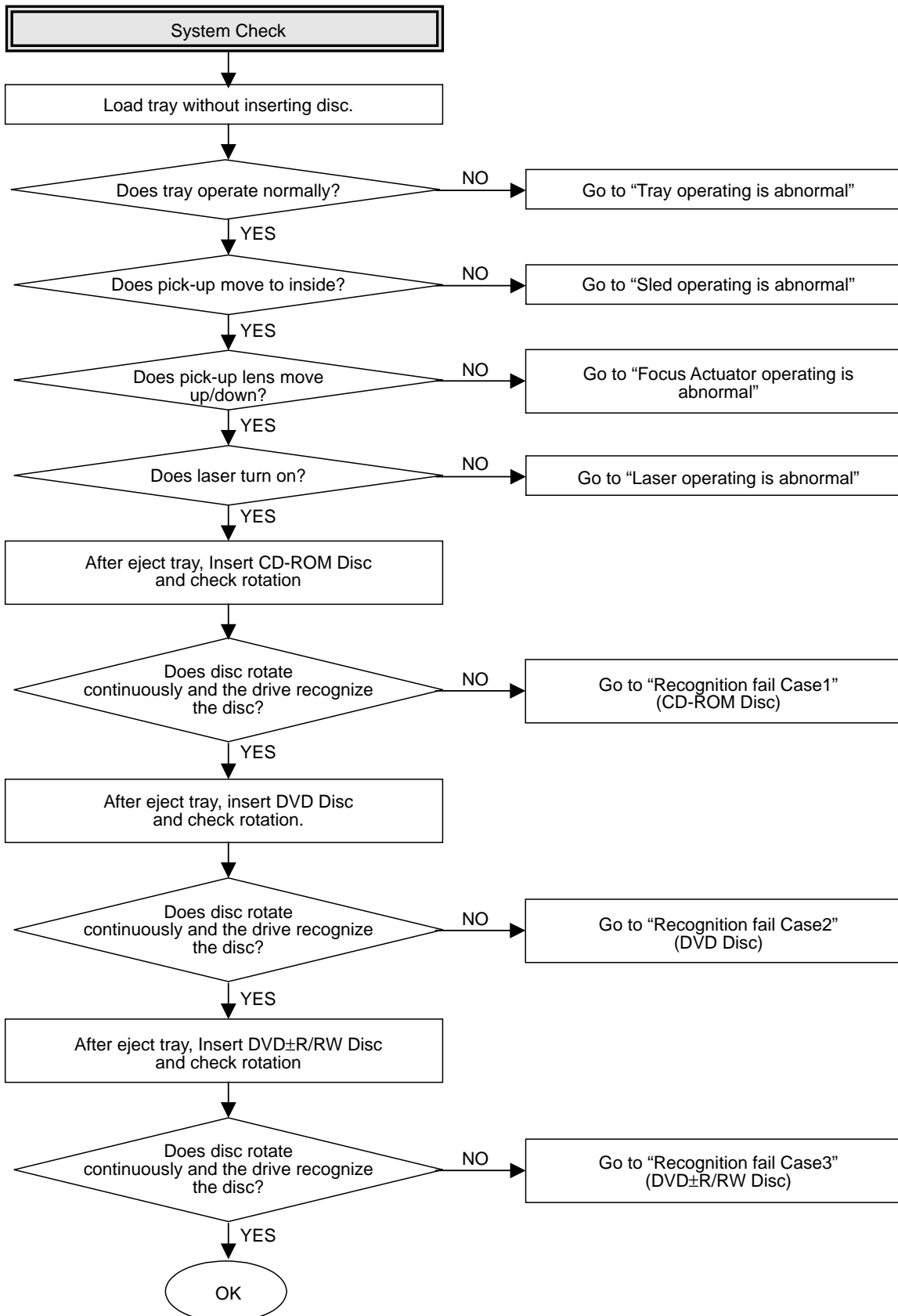


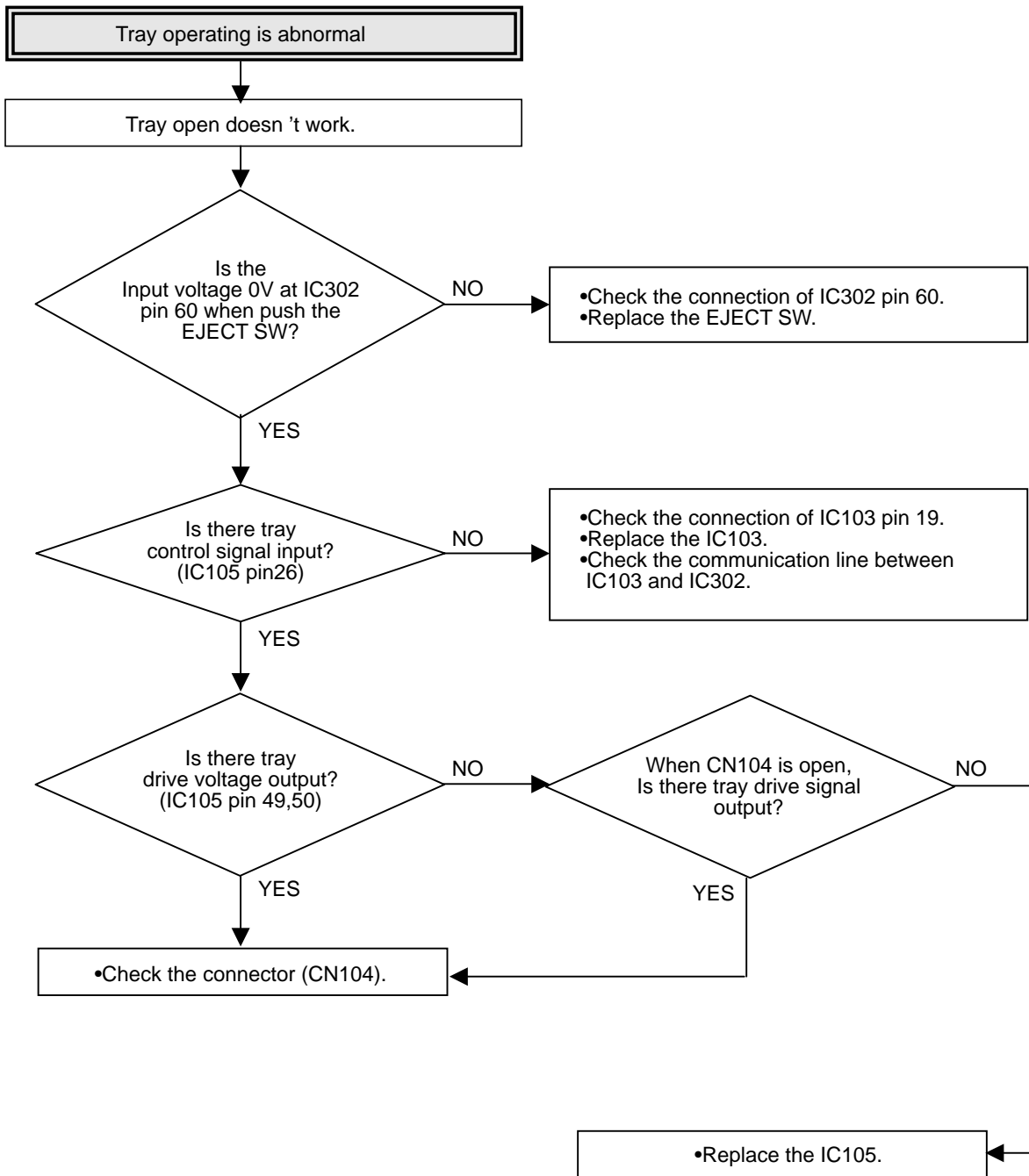
LOCATION GUIDE

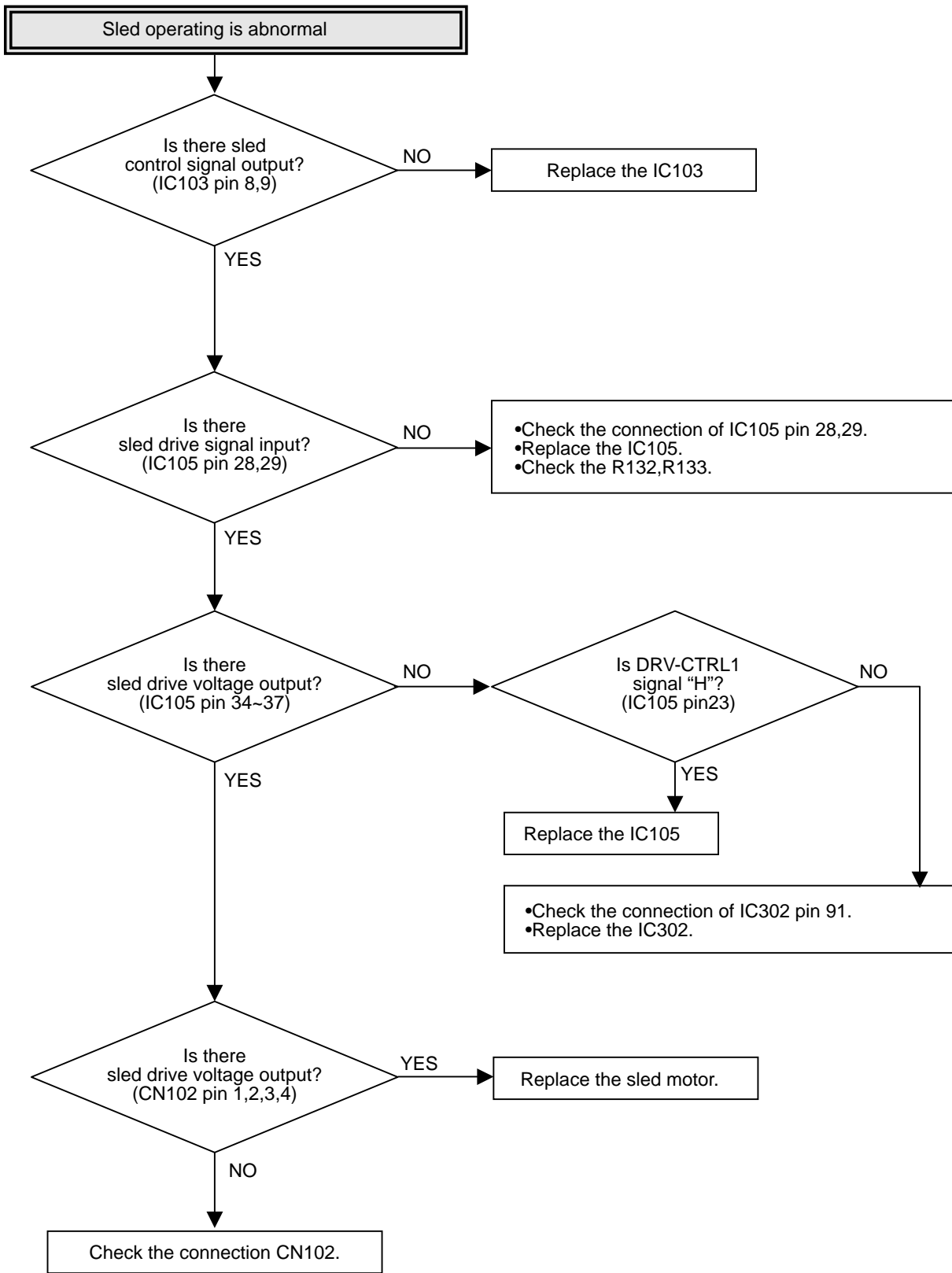
C1001	F7	C174	E6	IC102	G6	R159	G6	TP1123	E6	TP153	E5	TP2178	C8	AR201	L7	C306	M6	R223	K7	R344	L2	TP1220	N5	TP196	J9
C1002	F7	C175	E6	IC103	E4	R160	E6	TP1129	F6	TP154	E6	TP2179	C8	AR202	K8	C308	M6	R224	M7	R345	L2	TP1221	N5	TP197	J9
C1004	E3	C176	E6	IC104	E3	R161	E7	TP114	E7	TP157	A7	TP2180	C8	AR203	L8	C309	M6	R225	M7	R346	L2	TP1222	N5	TP198	J9
C1006	C5	C177	E6	IC105	B4	R162	E7	TP1141	F6	TP158	A8	TP2181	C8	AR204	K8	C310	L6	R226	N8	R347	K2	TP1223	J8		
C1007	C3	C178	E6	IC106	B5	R163	E7	TP115	E6	TP159	A7	TP2215	D9	AR205	I9	C311	M6	R227	N8	R349	L2	TP1201	K8		
C101	E5	C181	G6	IC107	F6	R164	E7	TP152	E3	TP160	A7	TP2216	D9	AR206	J9	C312	L6	R229	M8	R350	K1	TP2002	L8		
C102	E5	C182	G6	IC108	G6	R165	F7	TP156	E4	TP161	F2	TP2217	D9	AR207	J9	C314	M6	R236	I9	R351	H8	TP2003	K8		
C103	C5	C186	G7	IC201	D8	R166	F7	TP157	E4	TP162	F2	TP2218	D9	AR208	J9	C323	I8	R237	J9	R356	N8	TP2005	K8		
C104	D5	C187	G7	IC301	F7	R167	F7	TP159	E4	TP163	G4	TP2219	D9	AR209	K9	C324	J8	R238	J8	R357	N8	TP2006	J9		
C105	D5	C188	G7	IC302	G7	R168	F7	TP167	E4	TP164	G4	TP2220	D9	AR301	M6	C325	J8	R239	K9	R358	N8	TP2208	J9		
C106	D5	C189	E7	IC303	F7	R169	F7	TP171	C3	TP165	G4	TP2221	C9	AR302	M6	C326	J7	R240	K9	R361	L2	TP2013	K8		
C107	D5	C190	E7	IC304	F7	R170	F7	TP178	B3	TP166	G4	TP2222	C9	AR303	N6	C327	L2	R241	K9	R362	N1	TP2210	K9		
C108	D5	C191	E7	IC305	F7	R171	F7	TP18	C3	TP167	G4	TP2240	B7	B301	I9	C328	N2	R242	K9	R366	H7	TP2018	K8		
C109	D5	C192	E7	IC306	F5	R172	F7	TP189	B4	TP168	G4	TP2250	A8	B302	H9	C329	L2	R243	L9	R368	J6	TP2221	L7		
C110	D5	C193	E7	IC307	F5	R173	D5	TP19	C5	TP169	G4	TP2254	B8	C1003	H7	C330	H9	R244	L9	R369	H9	TP2223	L9		
C111	D5	C195	G7	IC308	F5	R174	D5	TP190	C4	TP170	G4	TP2257	B8	C1005	H4	C331	H8	R245	L9	R370	H9	TP2224	L9		
C114	E5	C196	F7	IC309	F5	R175	D5	TP192	C4	TP171	G4	TP2259	A8	C112	N7	C332	I9	R246	L9	R371	H9	TP2225	L9		
C115	B3	C199	F7	IC310	F5	R176	D5	TP194	C4	TP172	G4	TP2263	B8	C113	N7	C333	I8	R247	I9	R372	H9	TP2227	M8		
C116	B3	C201	B8	IC311	F5	R177	D5	TP195	C4	TP173	G4	TP2265	B8	C114	N7	C334	H8	R248	J9	R373	H9	TP2228	M8		
C117	B3	C202	C8	IC312	F5	R178	D5	TP196	C4	TP174	G4	TP2271	B8	C115	N5	C335	L2	R249	J9	R374	H9	TP2229	M8		
C118	C3	C203	C8	IC313	B3	R206	C7	TP198	B4	TP175	G4	TP300	F8	C134	N4	C336	L2	R250	J9	R375	H9	TP2230	M8		
C119	D5	C204	C8	IC314	C3	R218	C7	TP20	C4	TP204	C8	TP300	C6	C135	N4	C337	I8	R251	K9	R376	H9	TP2231	M8		
C120	D5	C205	C7	IC315	C3	R228	B8	TP206	B4	TP207	C8	TP300	B6	C143	H5	C338	I8	R252	K9	R377	H9	TP2232	M8		
C121	C5	C209	C7	IC316	C3	R230	B8	TP21	C4	TP209	C7	TP301	B5	C150	I6	C339	J8	R253	K9	R378	H9	TP2233	M8		
C122	B3	C210	B7	IC317	C3	R231	C8	TP217	B4	TP210	F8	TP302	B5	C152	I6	C340	J8	R254	K9	R379	H9	TP2234	M8		
C123	B3	C211	B7	IC318	B3	R232	C8	TP218	B4	TP210	C8	TP303	B5	C156	I6	C341	J8	R255	K9	R380	H9	TP2235	M8		
C124	B3	C212	B7	IC319	C3	R233	C8	TP219	B4	TP210	C8	TP304	B6	C159	H6	C342	J8	R256	K9	R381	H9	TP2236	M8		
C125	B3	C221	C7	IC320	C3	R234	C8	TP22	C4	TP215	C8	TP306	B5	C161	I6	C343	J8	R257	L9	R382	H9	TP2237	M8		
C126	C3	C222	C7	IC321	C3	R235	C8	TP221	C4	TP219	C8	TP307	B5	C163	I6	C344	J8	R258	L9	R383	H9	TP2238	M8		
C127	C3	C224	B7	IC322	E3	R260	B7	TP222	C4	TP202	D7	TP308	B5	C179	I6	D201	I9	R259	L8	R384	H9	TP2239	M8		
C128	E4	C225	B7	IC323	E3	R313	B6	TP23	C6	TP202	C8	TP310	A5	C180	I6	D301	L5	R301	H8	R385	H9	TP2240	M8		
C129	E4	C230	B7	IC324	B3	R333	B5	TP23	B4	TP203	C7	TP312	B6	C183	I6	D302	L1	R302	H8	R386	H9	TP2241	M8		
C130	E3	C231	C7	IC325	E4	R334	B5	TP233	C8	TP204	G5	TP314	C5	C184	I6	D303	L1	R303	M6	R387	H9	TP2242	M8		
C131	E3	C232	B7	IC326	E4	R343	G8	TP234	C6	TP204	D8	TP315	A6	C185	H6	D304	M6	R304	M6	R388	H9	TP2243	M8		
C132	E3	C233	B7	IC327	E4	R344	G8	TP235	C6	TP205	B7	TP316	B5	C194	I7	D305	L1	R305	M6	R389	H9	TP2244	M8		
C133	B4	C234	C8	IC328	B4	R352	E7	TP236	C6	TP206	C7	TP316	B6	C197	I7	D306	L1	R306	M6	R390	H9	TP2245	M8		
C134	B5	C235	C8	IC329	B4	R353	F8	TP237	C6	TP206	C7	TP318	B6	C198	I7	D307	L1	R307	M6	R391	H9	TP2246	M8		
C135	B5	C237	B8	IC330	B4	R354	A8	TP238	C6	TP207	C7	TP318	B6	C199	I7	D308	L1	R308	M6	R392	H9	TP2247	M8		
C136	B4	C238	C8	IC331	C4	R355	G8	TP248	E3	TP207	B7	TP316	A5	C207	K8	D309	L1	R309	M6	R393	H9	TP2248	M8		
C141	C4	C243	B8	IC332	C4	TP100	C4	TP25	C5	TP207	B7	TP319	B5	C208	L7	D310	H7	R310	M6	R394	H9	TP2249	M8		
C142	C5	C244	B8	IC333	C5	TP100	C4	TP251	A4	TP207	A4	TP310	A2	C213	M7	D311	H7	R311	M6	R395	H9	TP2250	M8		
C144	E4	C245	B8	IC334	E4	TP100	E6	TP251	A5	TP250	B7	TP316	B6	C214	L7	D312	J7	R312	M6	R396	H9	TP2251	M8		
C145	B5	C246	B8	IC335	B5	TP107	E4	TP255	A5	TP256	B6	TP317	B6	C215	M7	D313	J8	R313	M6	R397	H9	TP2252	M8		
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C147	F6	C249	C8	IC337	B5	TP101	E6	TP26	D5	TP210	B7	TP319	B6	C217	M7	D315	L6	R315	M6	R399	H9	TP2254	M8		
C148	F6	C251	C8	IC338	B5	TP102	D4	TP210	A6	TP210	A6	TP318	B6	C218	J7	D316	J7	R316	M6	R400	H9	TP2255	M8		
C149	F6	C252	B7	IC339	B5	TP107	E4	TP28	G5	TP210	A6	TP318	B6	C219	L7	D317	H8	R317	M6	R401	H9	TP2256	M8		
C151	F6	C253	B8	IC340	C5	TP109	E6	TP29	F5	TP210	E6	TP318	C4	C220	L7	D318	J8	R318	M6	R402	H9	TP2257	M8		
C153	F6	C254	B8	IC341	C5	TP102	E7	TP130	F5	TP210	E6	TP318	B6	C223	M7	D319	K2	R319	M6	R403	H9	TP2258	M8		
C154	F6	C255	C8	IC342	C5	TP102	E7	TP133	D5	TP210	F7	TP317	B6	C226	L7	D320	L2	R320	M6	R404	H9	TP2259	M8		
C155	F6	C256	C8	IC343	C5	TP102	E7	TP135	C6	TP210	F7	TP317	B6	C227	K7	D321	H4	R321	M6	R405	H9	TP2260	M8		
C157	F6	C257	C8	IC344	C5	TP102	E7	TP138	F5	TP211	A7	TP317	B6	C228	K7	D322	H4	R322	M6	R406	H9	TP2261	M8		
C158	F6	C258	B8	IC345	A5	TP103	F7	TP139	F5	TP212	C7	TP317	B6	C229	L7	D323	H4	R323	M6	R407	H9	TP2262	M8		
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C162	E6	C260	B8	IC347	A5	TP104	F7	TP141	E5	TP213	C7	TP317	B6	C231	M8	D325	L7	R325	M6	R409	H9	TP2264	M8		
C164	G6	C307	B6	IC348	A5	TP105	G5	TP143	D5	TP214	C7	TP318	B6	C240	N8	D326	L7	R326	M6	R410	H9	TP2265	M8		
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C168	G6	C319	F7	IC352	G6	TP107	F6	TP147	F5	TP218	C7	TP318	B6	C244	M8	D330	L7	R330	M6	R414	H9	TP2269	M8		
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C170	E6	C321	A6	IC354	G6	TP107	F6	TP149	F5	TP220	C7	TP318	B6	C246	M8	D332	L7	R332	M6	R416	H9	TP2271	M8		
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C172	E6	C323	A6	IC356	G6	TP108	D5	TP151	F5	TP222	C7	TP318	B6	C248	M8	D334	L7	R334	M6	R418	H9	TP2273	M8		
C173	E6	D101	G6	IC357	G6	TP111	F5	TP152	E5	TP223	C7	TP318	B6	C249	M8	D335	L7	R335	M6	R419	H9	TP2274	M8		

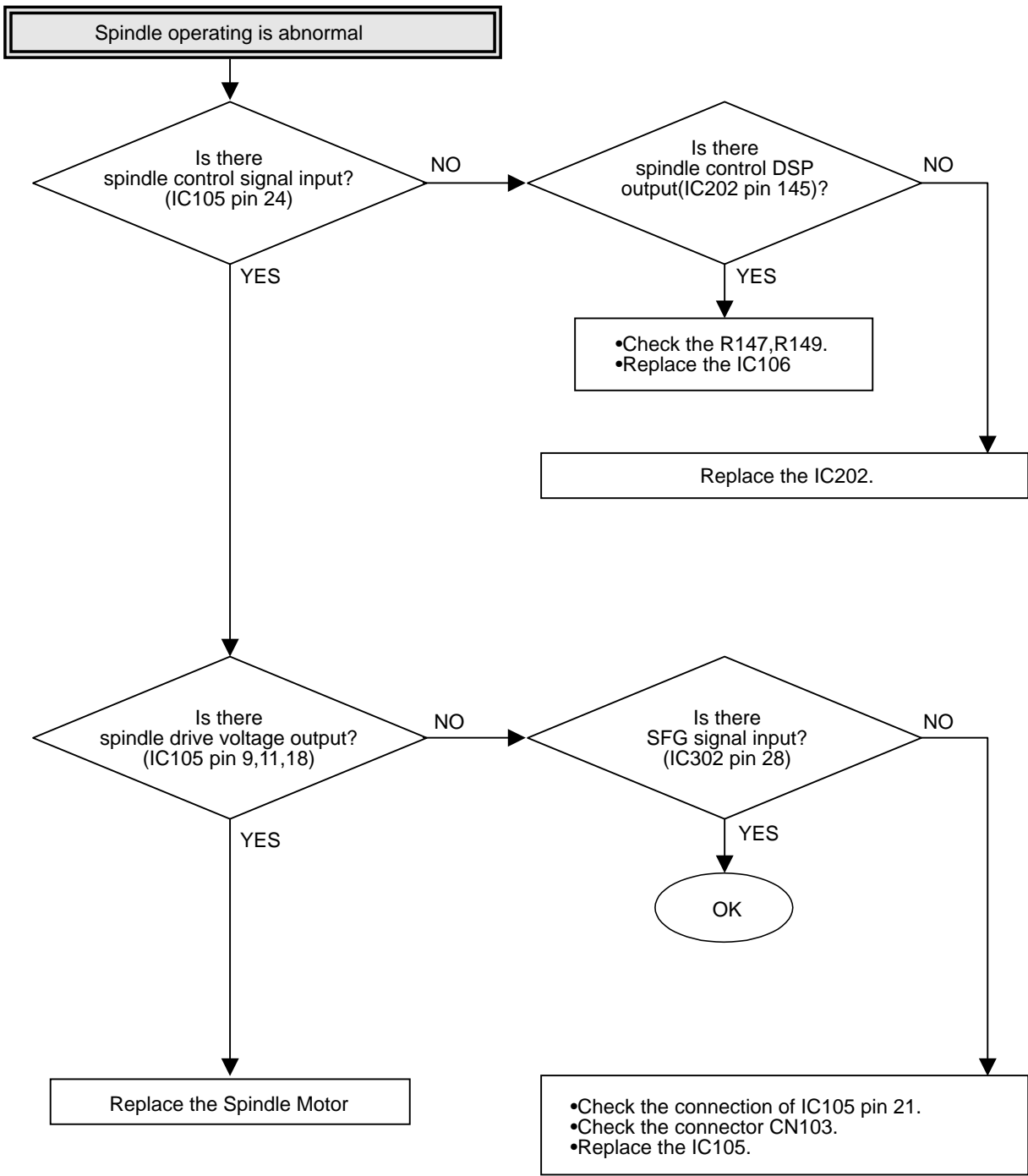
RL-01A LOADER PART ELECTRICAL TROUBLESHOOTING GUIDE

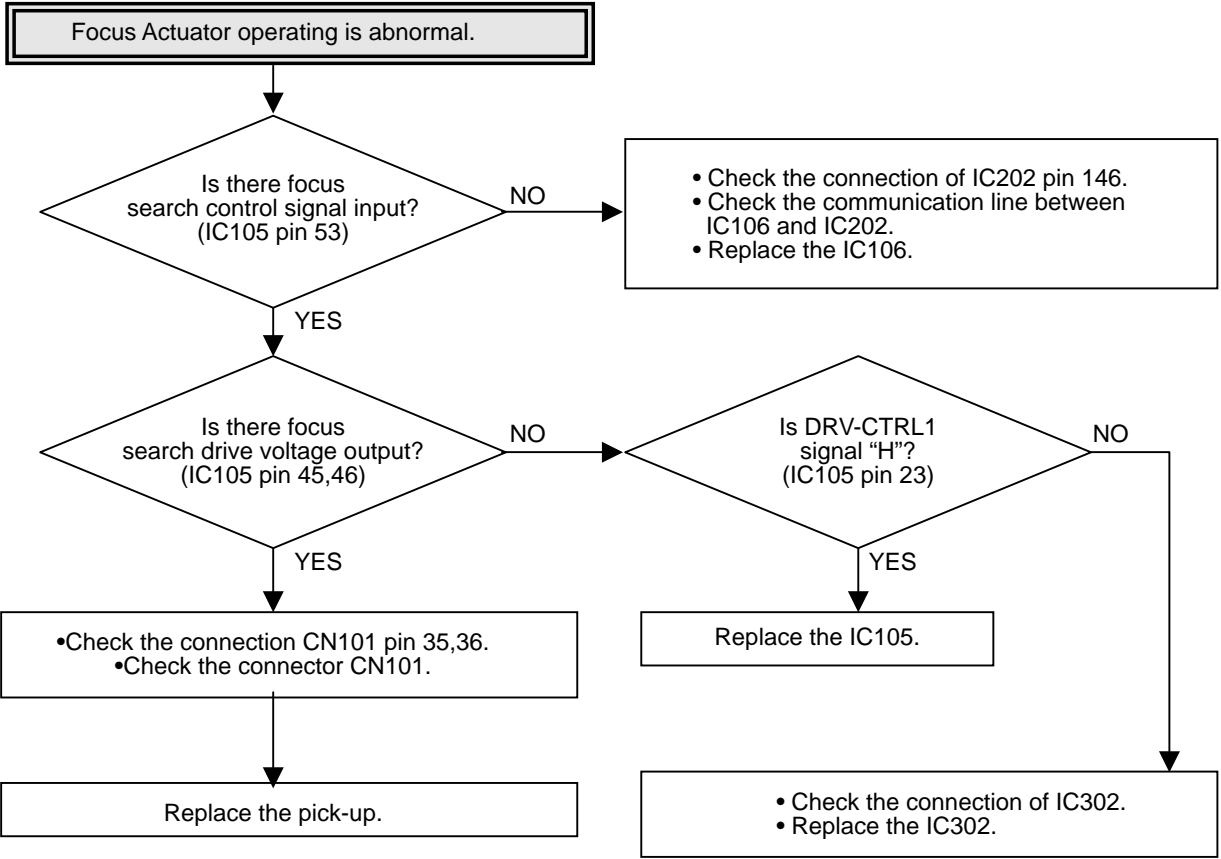
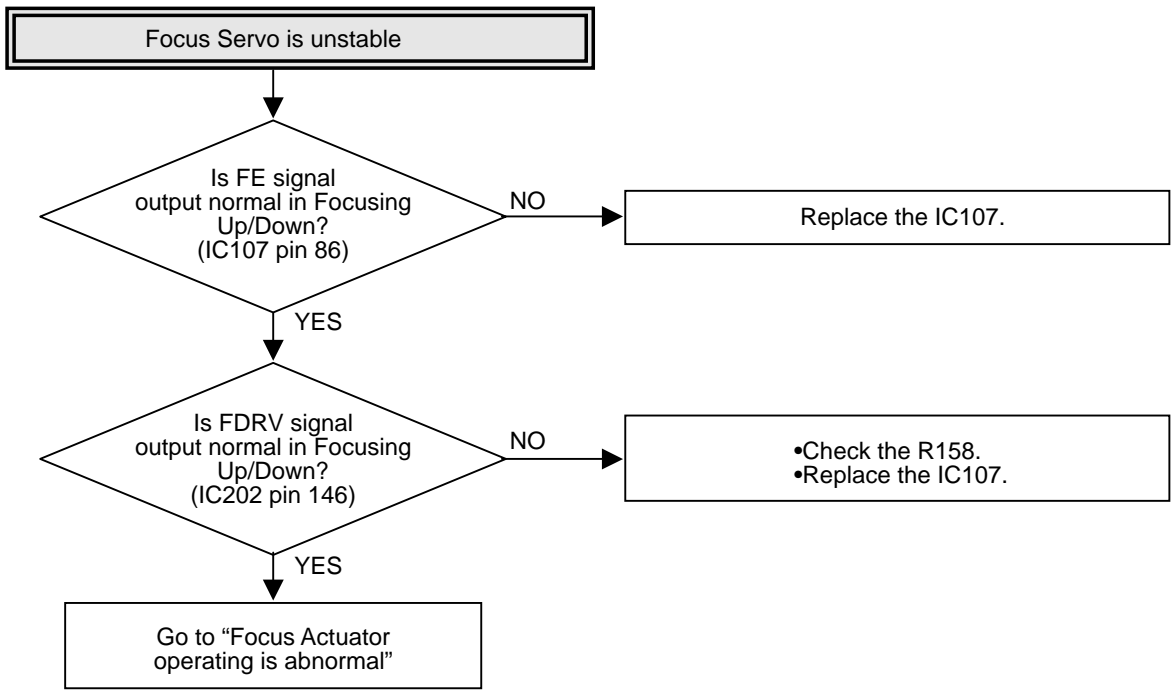


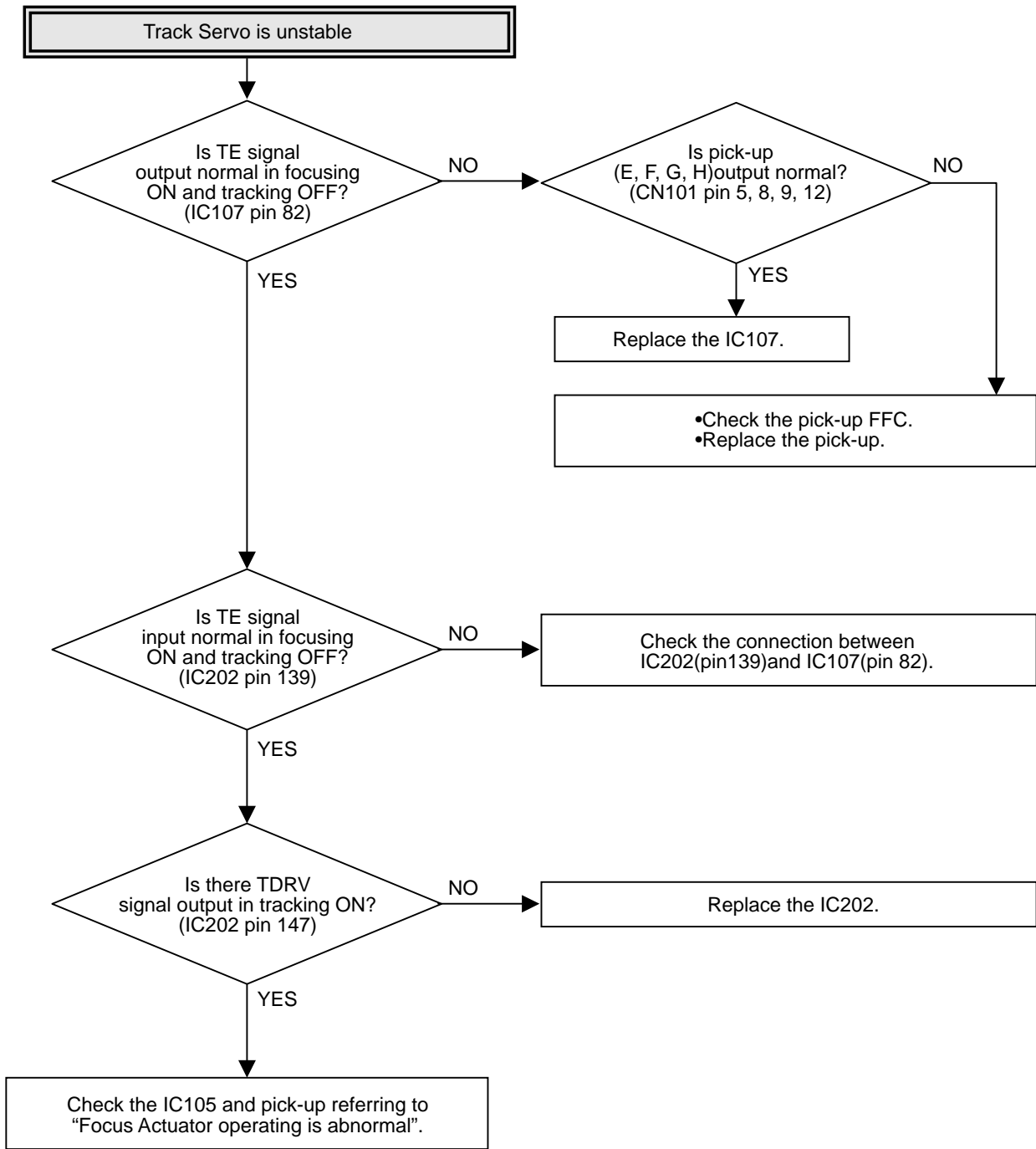


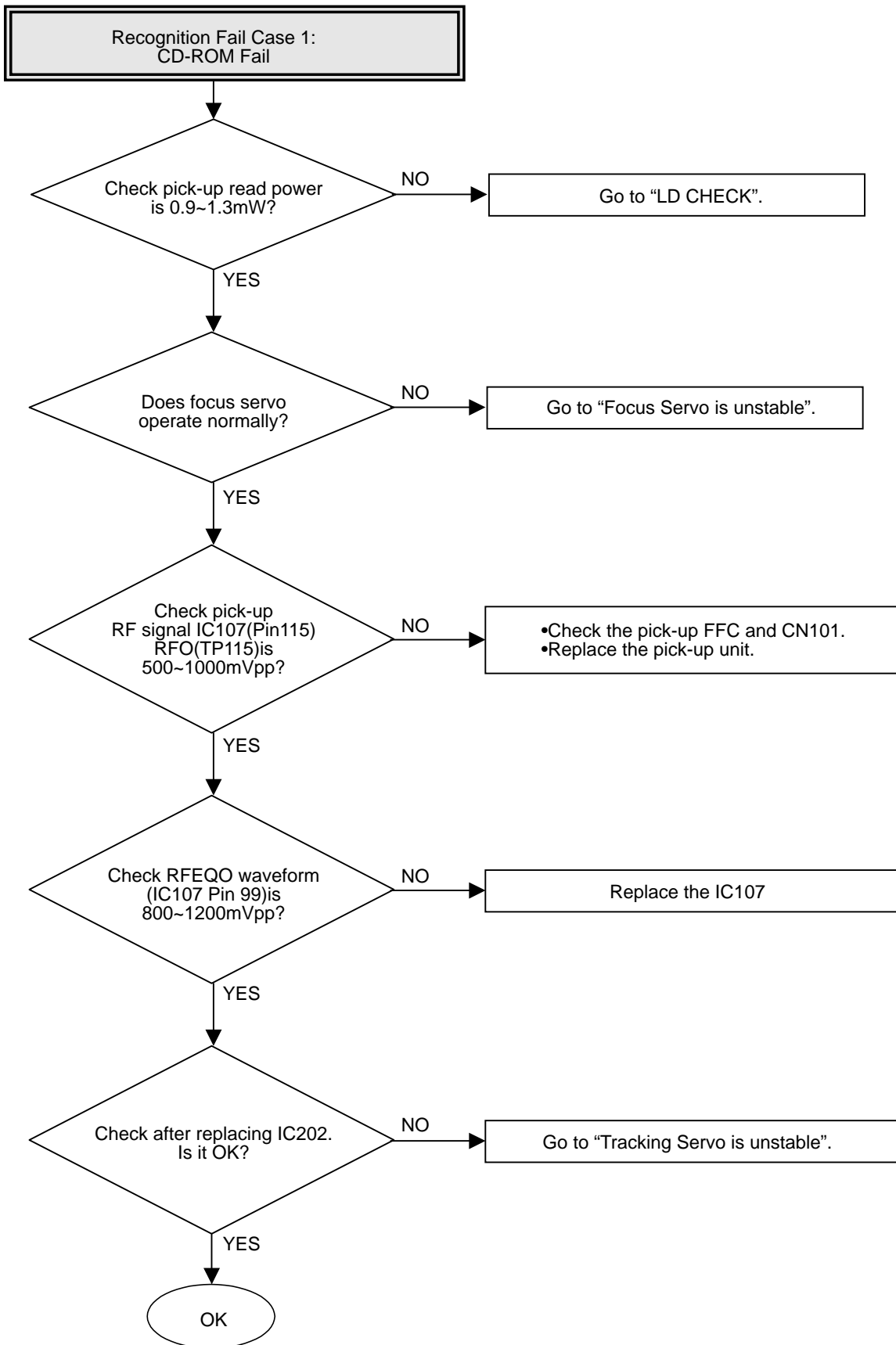


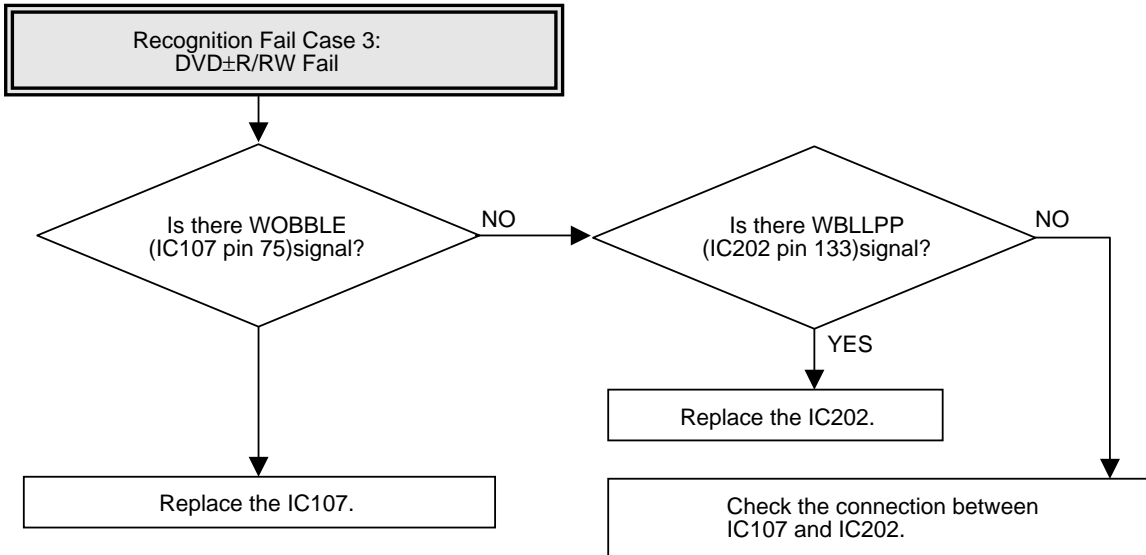
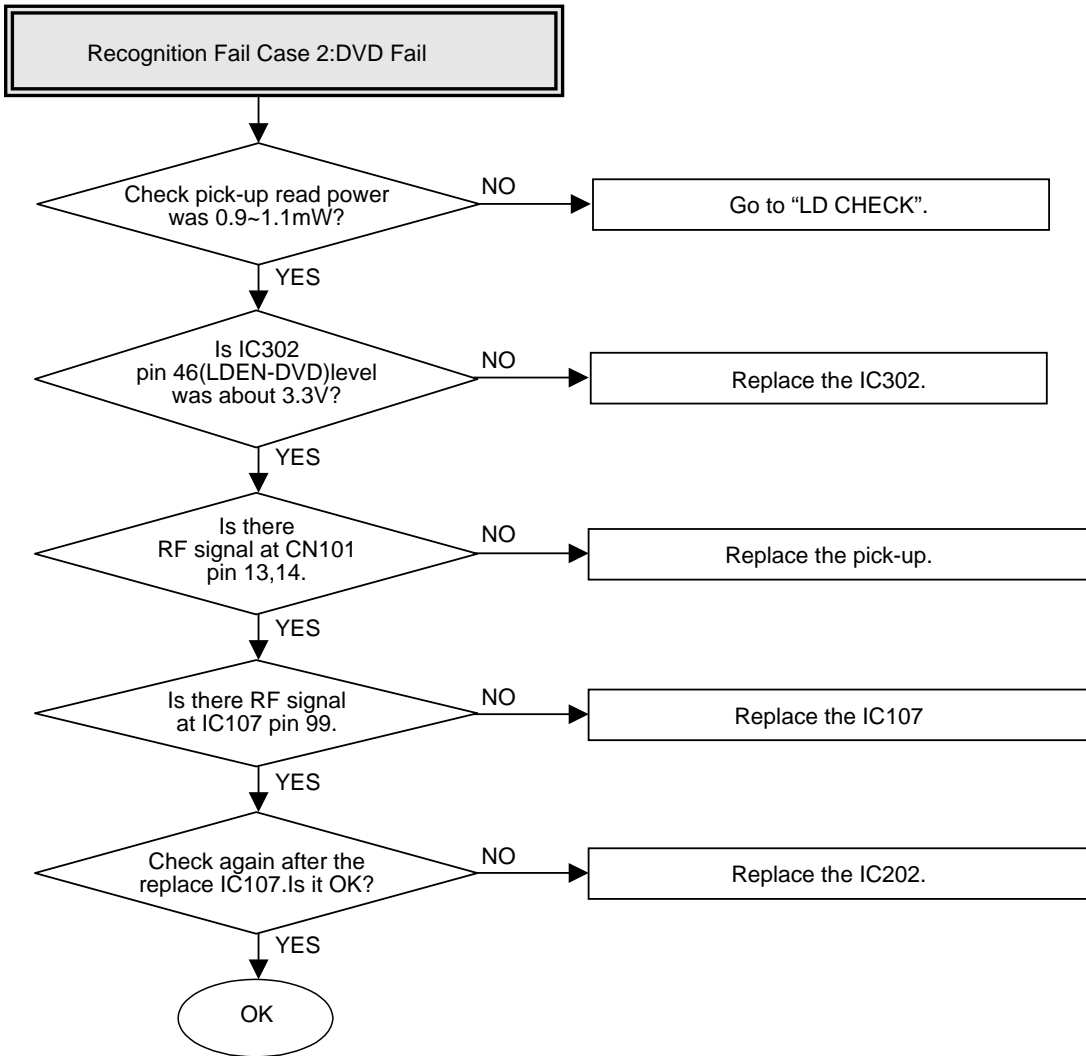


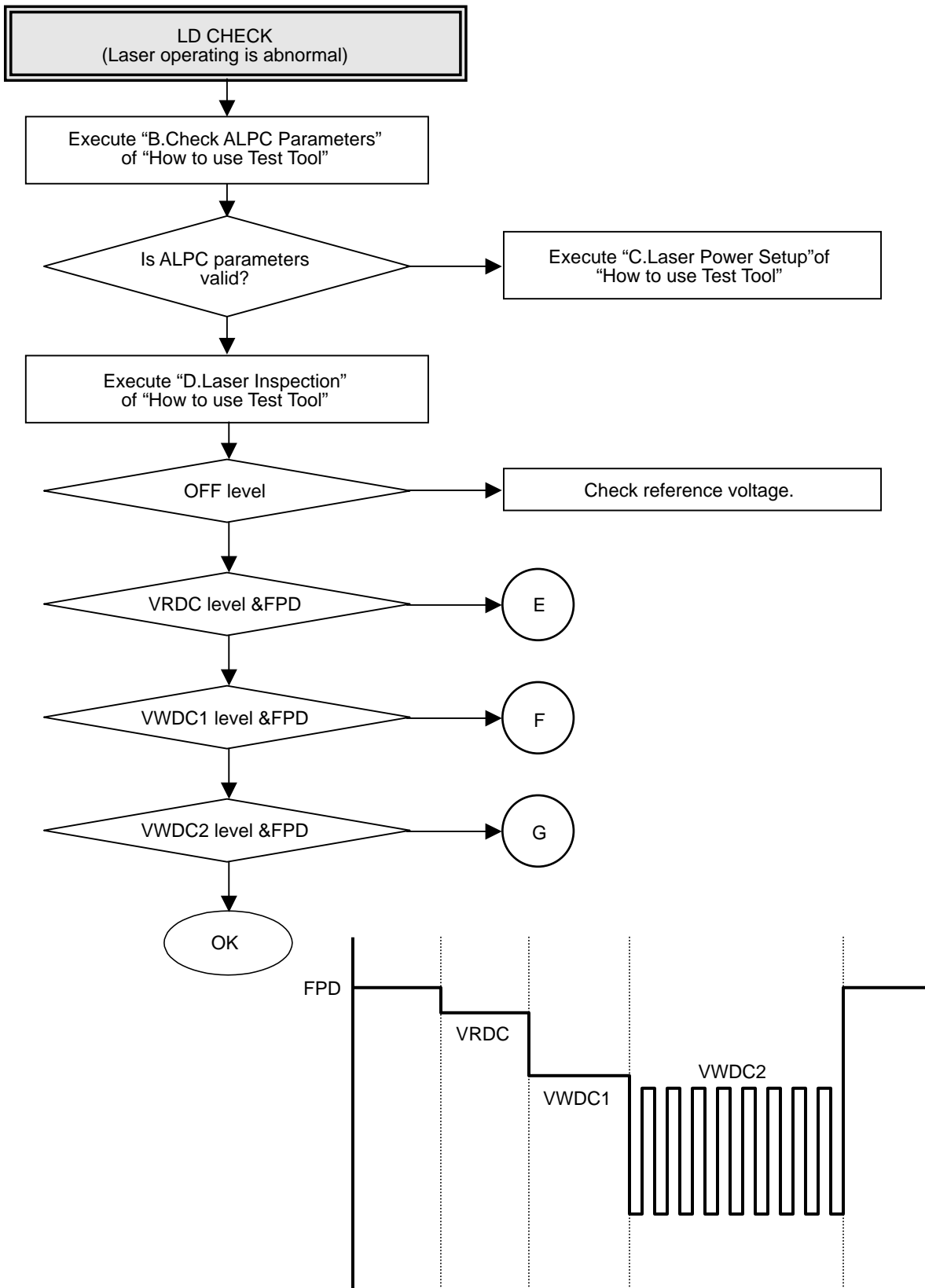


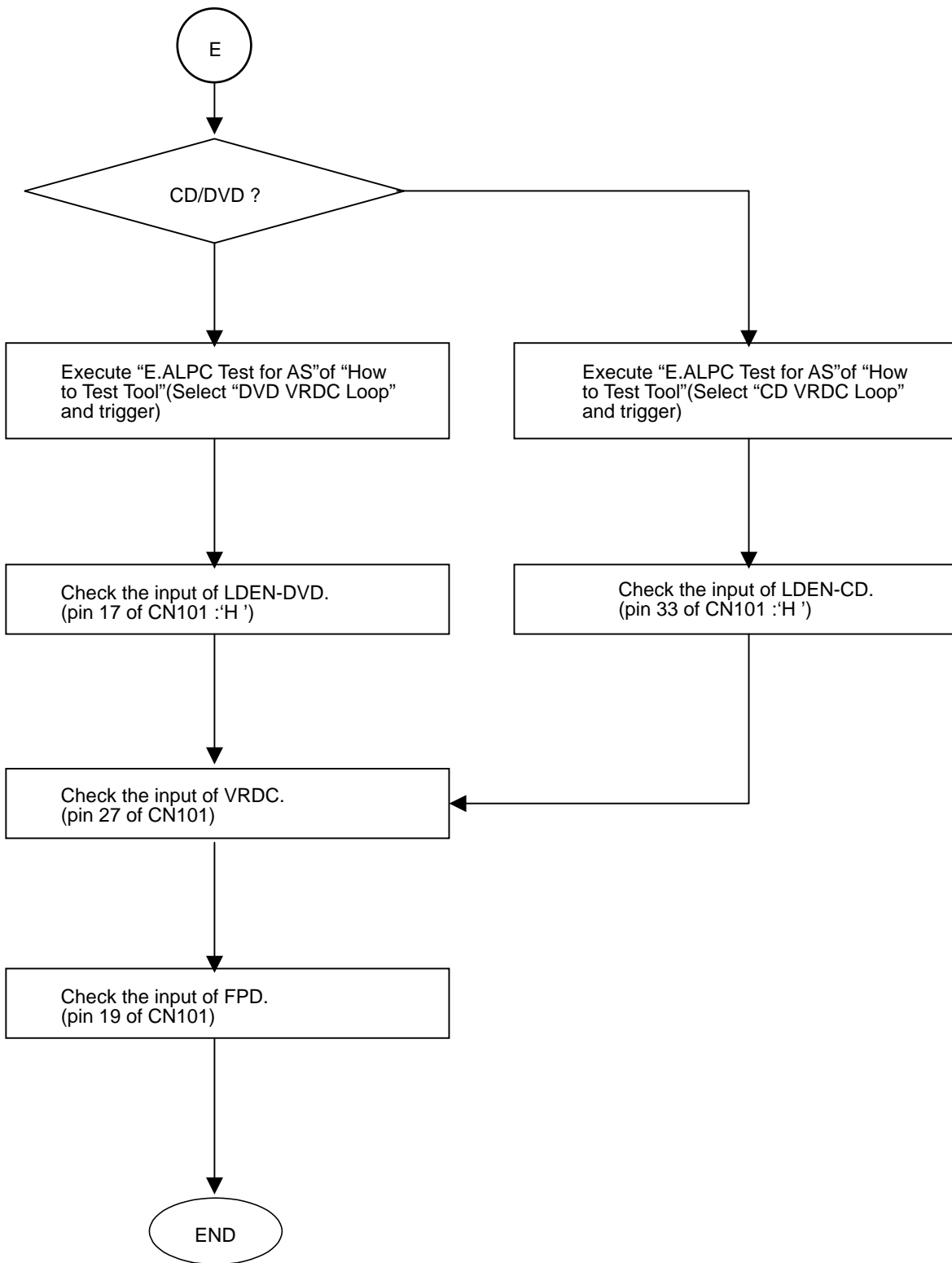


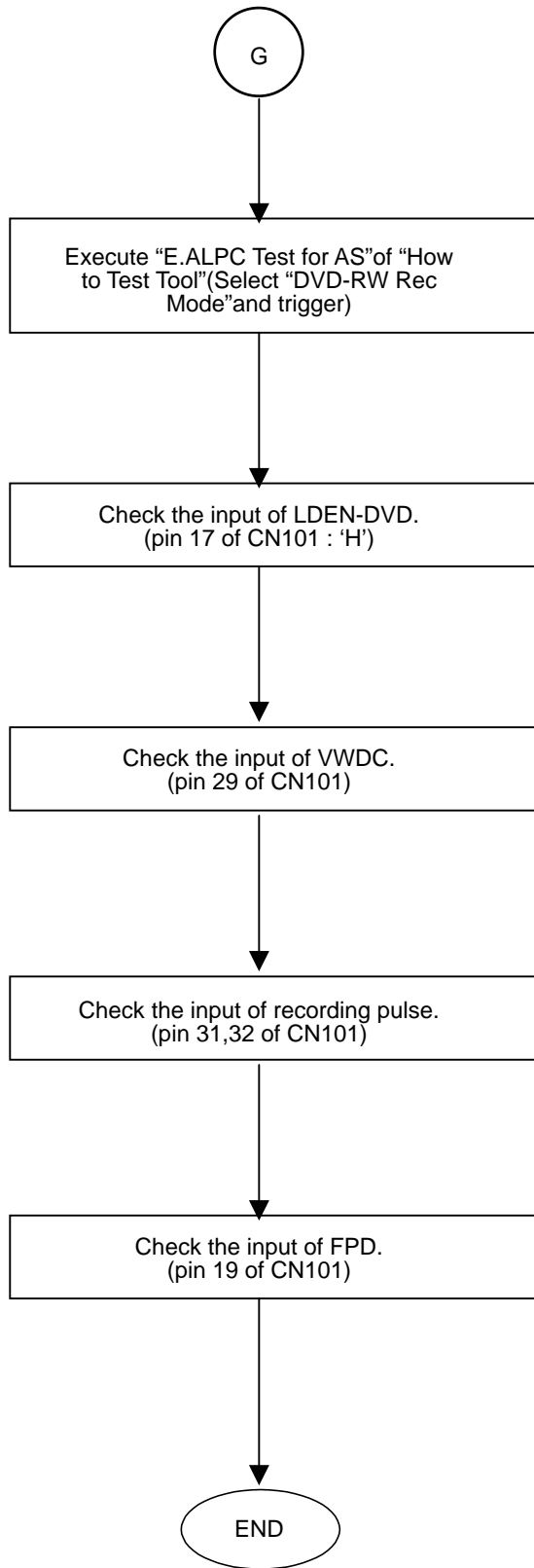
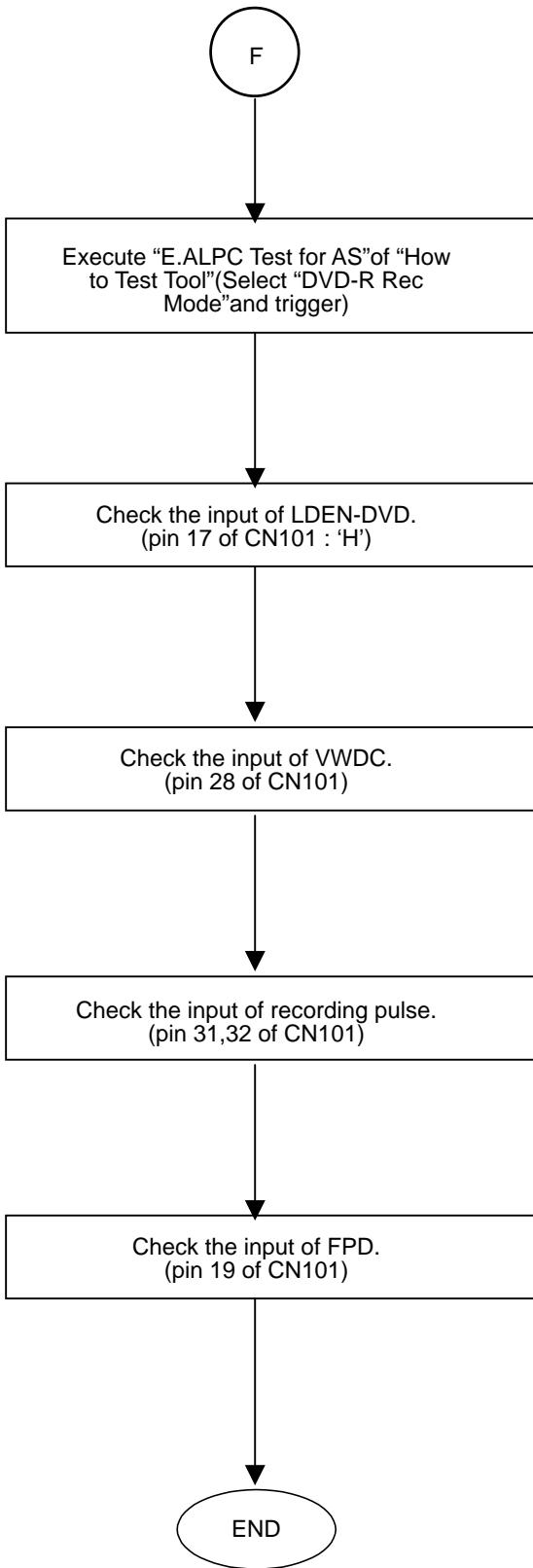












In case of writing fail.

Normal Case

Check the media
DVD ±R/RW?

NO

Check disc label.

YES

Does the disc
have any dust,scratch,
fingerprint ...?

YES

Remove the dust,fingerprint and
if the disc has long width scratch,
change it.

NO

Is the write
tool supported by
LG Drive?

NO

Use LG bundle software.

NO

Check disc information on writing tool.
[If you get some data information with
"Non Recordable Disc "message,the
disc is finalized disc:unrecordable
disc and more]

Finalized Disc?

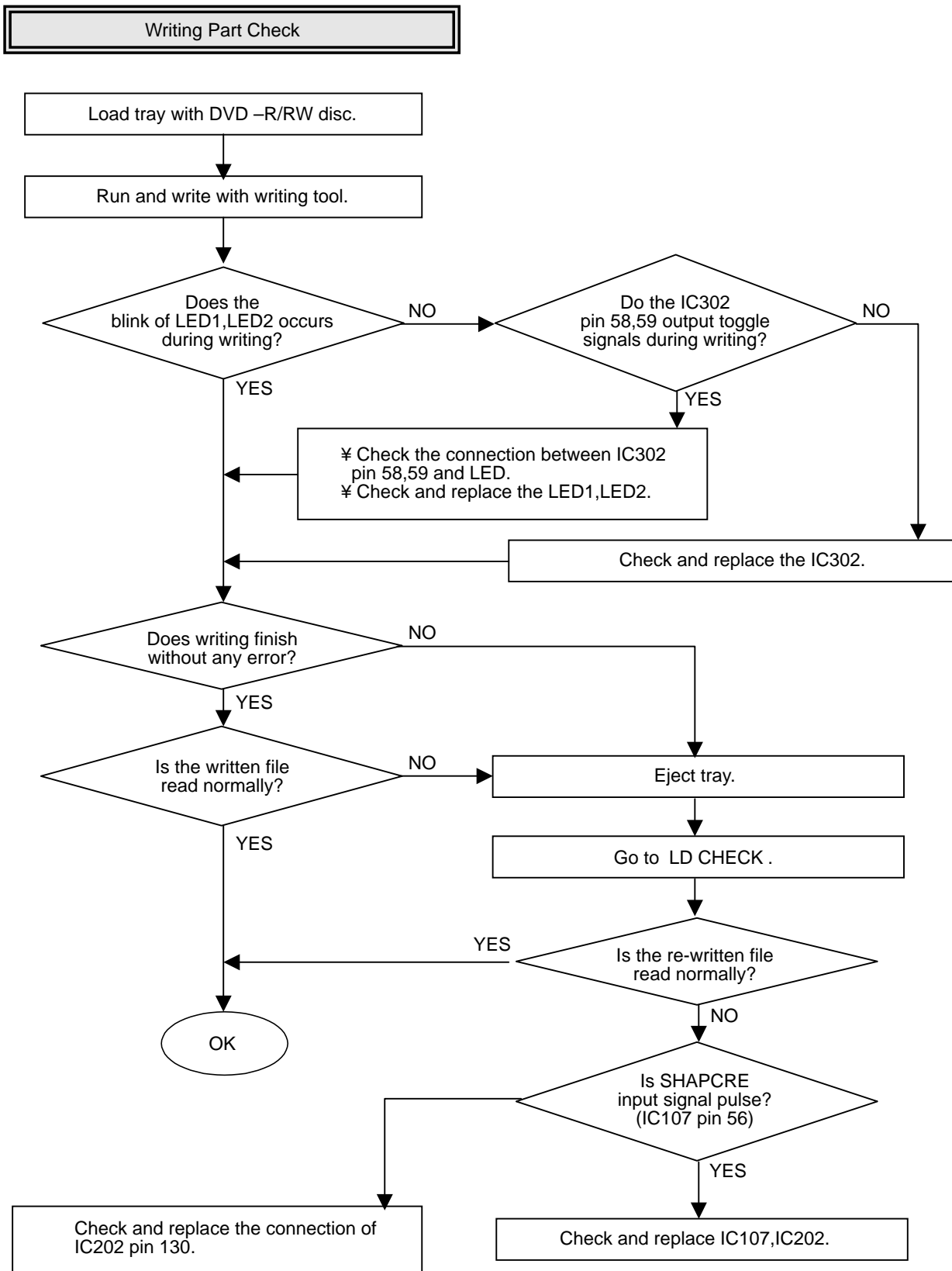
NO

Eject disc.

YES

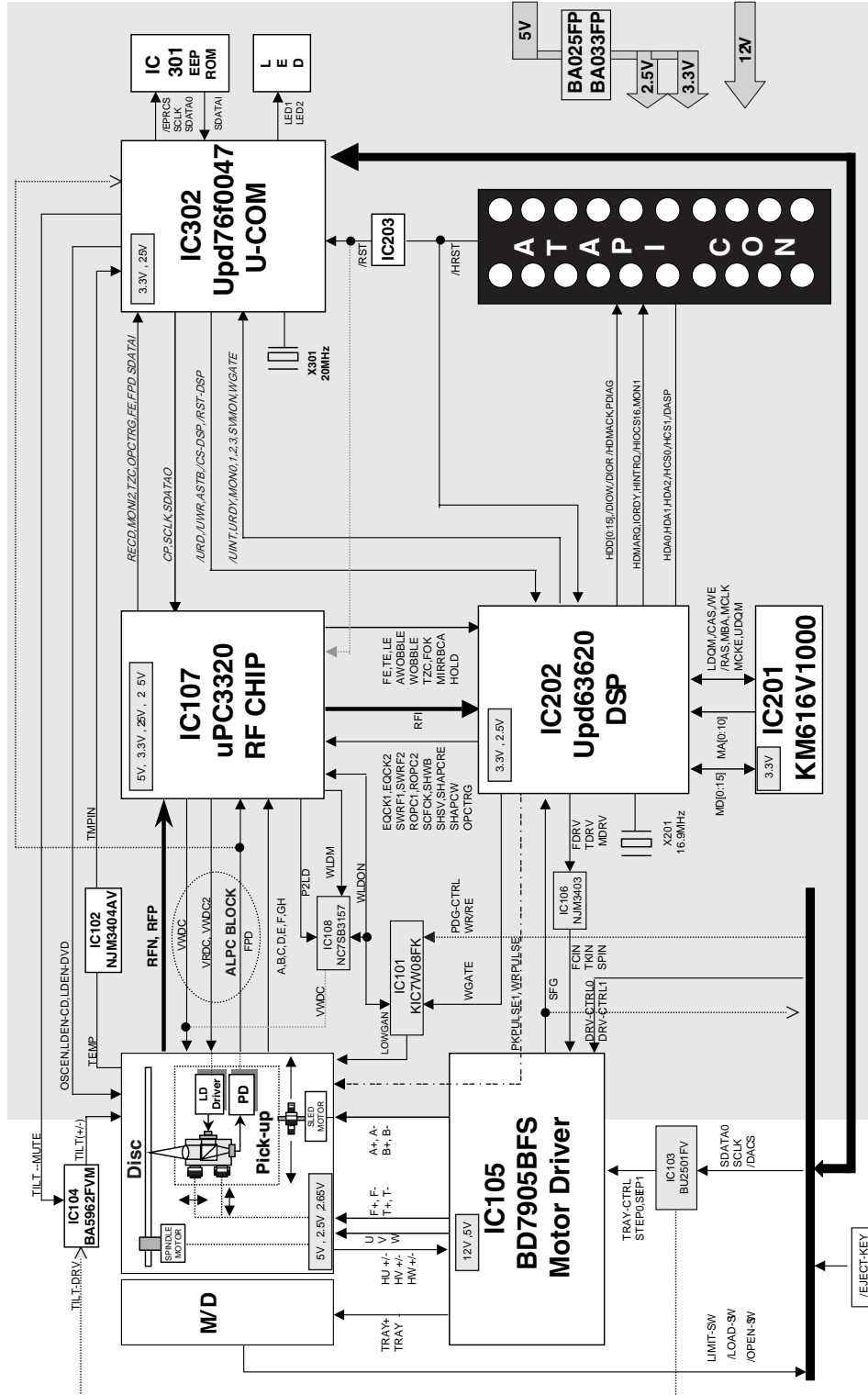
If DVD ±R disc,use new DVD ±R disc.
If DVD ±RW disc,erase the disc.

Go to "Writing Part Check".



BLOCK DIAGRAMS & DESCRIPTION

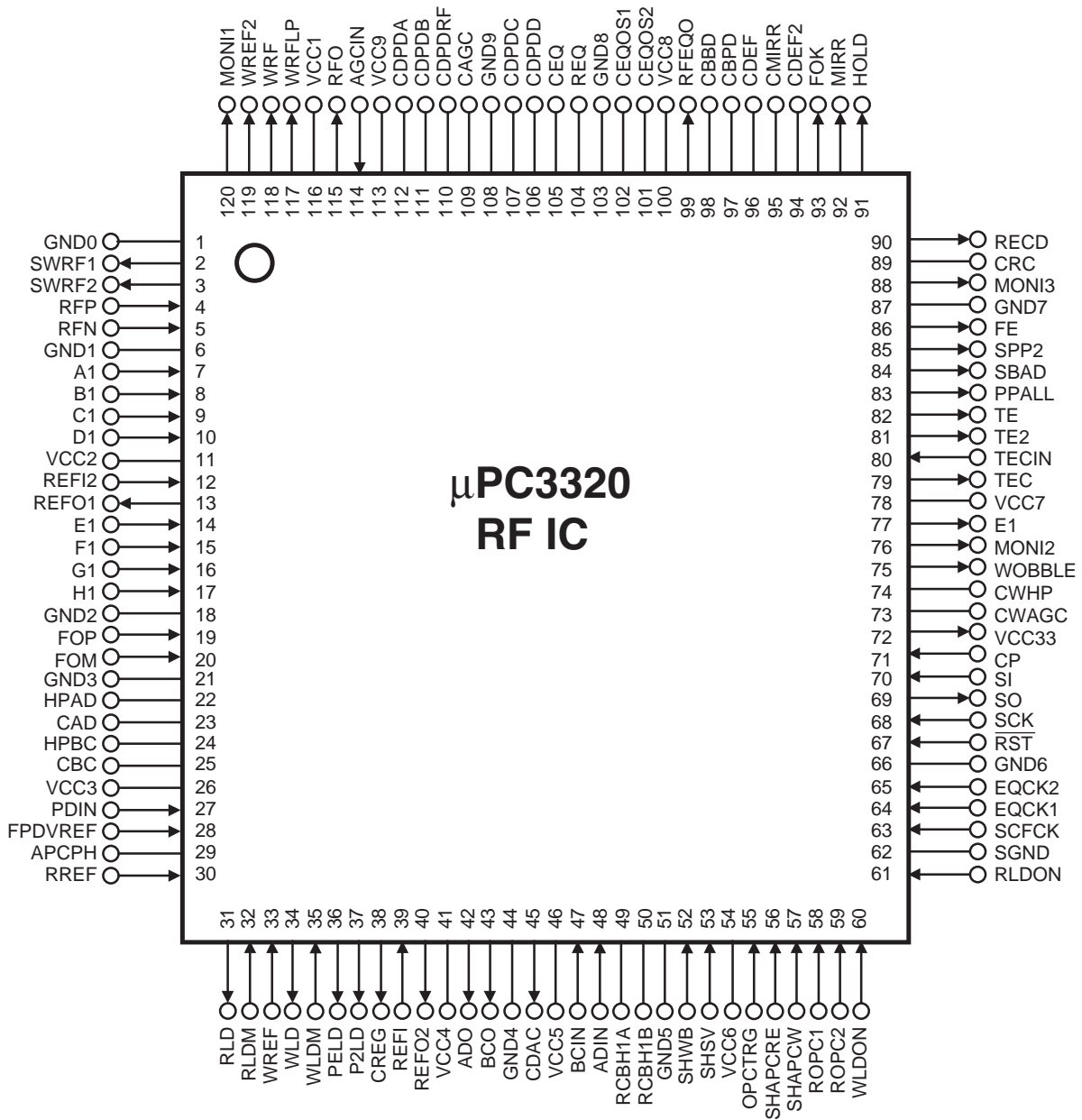
1. Overall Block Diagram



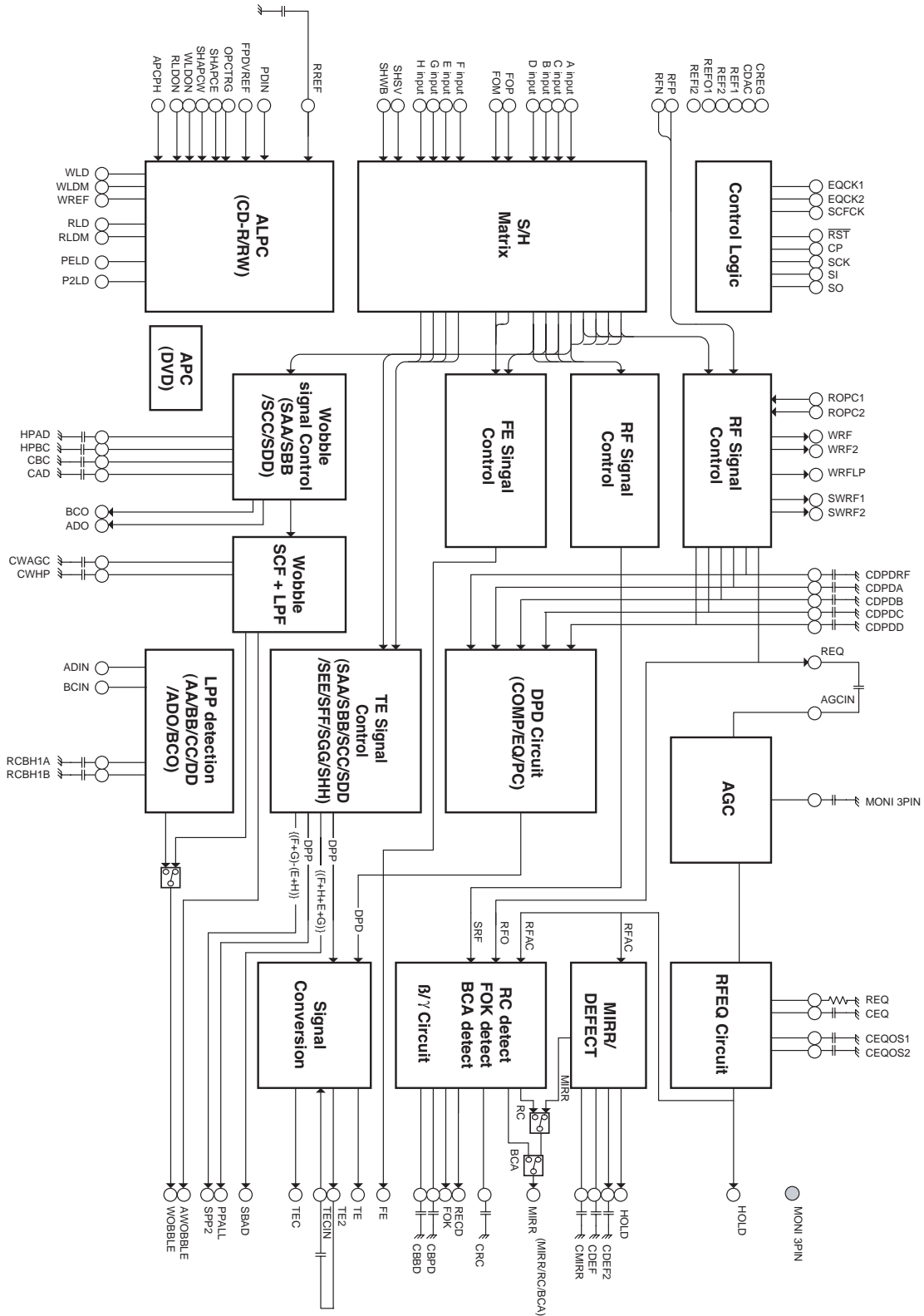
2. MAJOR IC INTERNAL BLOCK DIAGRAM AND PIN DESCRIPTION

IC101 (μ PC3320) : RF Signal Processor for CD/DVD

Pin Assignment



Block Diagram



Pin description

No.	Pin Name	Type	Description
1	GND0	-	Analog GND
2	SWRF1	OUTPUT	WRF signal sampling & hold [S/H] signal output.
3	SWRF1	OUTPUT	WRF signal sampling & hold [S/H] signal output.
4	RFP	INPUT	RF differerential signal[+] input.
5	RFN	INPUT	RF differerential signal[-] input.
6	GND1	-	Analog GND
7	A1	INPUT	Main beam signal [A1] input.
8	B1	INPUT	Main beam signal [B1] input.
9	C1	INPUT	Main beam signal [C1] input.
10	D1	INPUT	Main beam signal [D1] input.
11	VCC2	-	Analog power.
12	REFI2	INPUT	Reference voltage input pin for PDIC.
13	REFO1	OUTPUT	Pick-up internal reference voltage output[at REFI pin 2.5V: 2.25V output.]
14	E1	INPUT	Sub beam signal [E1] input.
15	F1	INPUT	Sub beam signal [F1] input.
16	G1	INPUT	Sub beam signal [G1] input.
17	H1	INPUT	Sub beam signal [H1] input.
18	GND2	-	Analog GND
19	FOP	INPUT	FO+ signal input for Focus.
20	FOM	INPUT	FO- signal input for Focus.
21	GND3	-	Analog GND
22	HPAD	-	Wobble circuit HPF band setting condenser connecting port.
23	CAD	-	Wobble circuit AGC response time setting condenser connecting port.
24	HPBC	-	Wobble circuit HPF band setting condenser connecting port.
25	CBC	-	Wobble circuit AGC response time setting condenser connecting port.
26	VCC3	-	Analog power.
27	PDIN	INPUT	Laser monitor current input.
28	FPDVREF	INPUT	Reference voltage input pin for front monitor.
29	APCPH	-	Peak-hold condenser connecting pin for ALPC .
30	RREF	-	Read ALPC Condenser connecting port.
31	RLD	OUTPUT	Read Laser drive control output.
32	RLDM	INPUT	Read Laser drive control Amp[-] input.
33	WREF	-	Write ALPC Condenser connecting port.
34	WLD	OUTPUT	Write Laser drive control output.
35	WLDM	INPUT	Write Laser drive control Amp[-] input.
36	PELD	OUTPUT	Pick power output port1.
37	P2LD	OUTPUT	Pick power output port 2.
38	CREG	OUTPUT	Regulater voltage[2.5V] output.
39	REF1	INPUT	DSP power voltage input[2.5V].
40	REFO2	OUOTPTU	DSP Reference voltage output [at REFI port 2.5V: 1.5V output].

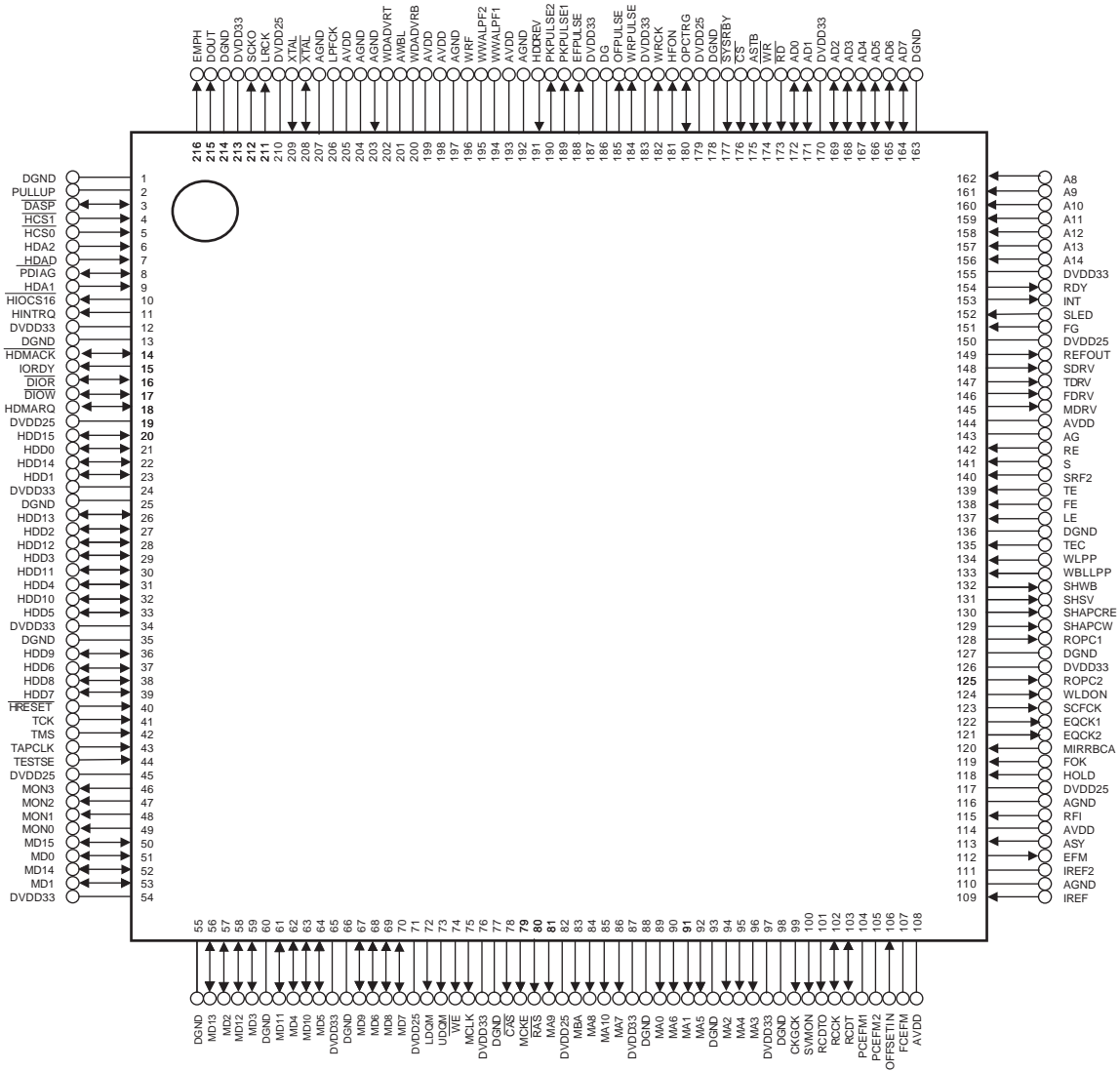
No.	Pin Name	Type	Description
41	VCC4	-	Analog power.
42	ADO	OUTPUT	Wobble circuit [A+D] signal output.
43	BCO	OUTPUT	Wobble circuit [B+C] signal output.
44	GND4	-	Analog GND
45	CDAC	OUTPUT	DAC reference voltage output.
46	VCC5	-	Digital power.
47	BCIN	INPUT	[B+C] signal input.
48	ADIN	INPUT	[A+D] signal input.
49	RCBH1A	-	RLPP circuit bottom hold condenser connecting port.
50	RCBH1B	-	RLPP circuit bottom hold condenser connecting port.
51	GND5	-	Analog GND
52	SHWB	INPUT	Sample hold pulse input for Wobble signal.
53	SHSV	INPUT	Sample hold pulse input for Servo signal.
54	VCC6	-	Digital power.
55	OPCTRG	INPUT	OPCTRG signal input.
56	SHAPCRE	INPUT	Sample hold pulse input for Read/Erase ALPC.
57	SHPCW	INPUT	Sample hold pulse input for Write ALPC.
58	ROPC1	INPUT	Sample hold pulse input 1 for WRF signal.
59	ROPC2	INPUT	Sample hold pulse input 2 for WRF signal.
60	WLDON	INPUT	Write ALPC Center signal input.
61	RLDON	INPUT	Read ALPC Center signal input.
62	SGND	-	Sub straight GND.
63	SCFCK	INPUT	SCF clock input.
64	EQCK1	INPUT	Fixed clock input.
65	EQCK2	INPUT	Equalize automatic control clock input.
66	GND6	-	Analog GND
67	RST	INPUT	Register reset input.
68	SCK	INPUT	Register setting clock input.
69	SO	OUTPUT	Serial data output.
70	SI	INPUT	Serial data input.
71	CP	INPUT	Address
72	VCC33	OUTPUT	Power voltage [3.3V monitor].
73	CWAGC	-	Wobble circuit AGC response time setting condenser connecting port.
74	CWHP	-	Wobble circuit HPF band setting condenser connecting port.
75	WOBBLE	OUTPUT	Wobble signal output [Digital signal].
76	AWOBBLE	OUTPUT	Wobble signal output [Analog signal].
77	MONI2	OUTPUT	Internal signal monitor port.
78	VCC7	-	Digital power.
79	TEC	OUTPUT	Tracking zero cross signal output.
80	TECIN	INPUT	Tracking zero cross signal input.

No.	Pin Name	Type	Description
81	TE2	OUTPUT	Tracking error signal output.
82	TE	OUTPUT	Tracking error signal output for Servo.
83	PPALL	OUTPUT	Main side push-pull signal output.
84	SBAD	OUTPUT	Sub beam signal output [(E+F+G+H) signal].
85	SPP2		Sub beam signal output [(F+G)-(E+H) signal].
86	FE	OUTPUT	Focus error signal.
87	GND7	-	Analog GND
88	MONI3	OUTPUT	Internal signal monitor port.
89	CRC	-	Radial contrast circuit condenser connecting port.
90	RECD	OUTPUT	No recording area detection.
91	HOLD	OUTPUT	Detection signal output.
92	MIRR	OUTPUT	Mirror detection/RCA signal output.
93	FOK	OUTPUT	Focus OK signal.
94	CDEF2	-	Detect circuit condenser connecting port 2.
95	CMIRR	-	Mirror circuit condenser connecting port.
96	CDEF	-	Detect circuit condenser connecting port .
97	CBPD	-	β , γ adetection[peak]condenser connecting port.
98	CBBD	-	β , γ adetection[butoff]condenser connecting port.
99	RFEQO	OUTPUT	Equalizer output.
100	VCC8	-	Analog power.
101	CEQOS2	-	RF Equalizer circuit condenser connecting port 2.
102	CEQOS1	-	RF Equalizer circuit condenser connecting port 1.
103	GND8	-	Analog GND
104	REQ	-	RF Equalizer circuit volatage setting resistance connecting port.
105	CEQ	-	Equalizer fc automatic control curcuit condenser connecting port.
106	CDPDD	-	DPD [D signal] HPF band setting condenser connecting port.
107	CDPDC	-	DPD [C signal] HPF band setting condenser connecting port.
108	GND9	-	Analog GND
109	CDPDC	-	RFAGC circuit condenser connecting port.
110	CDPDRF	-	DPD [RF signal] HPF band setting condenser connecting port.
111	CDPDRF	-	DPD [B signal] HPF band setting condenser connecting port.
112	CDPDA	-	DPD [A signal] HPF band setting condenser connecting port.
113	VCC9	-	Analog power.
114	AGCIN	INPTU	AGC input
115	RFO	OUTPUT	Read RF signal output.
116	VCC1	-	Analog power.
117	WRFLP	OUTPUT	Write RF LPF output.
118	WRF	OUTPUT	Write RF signal output.
119	WFR2	OUTPUT	Write RF2 signal output.
120	MONI1	OUTPUT	Internal signal monitor port.

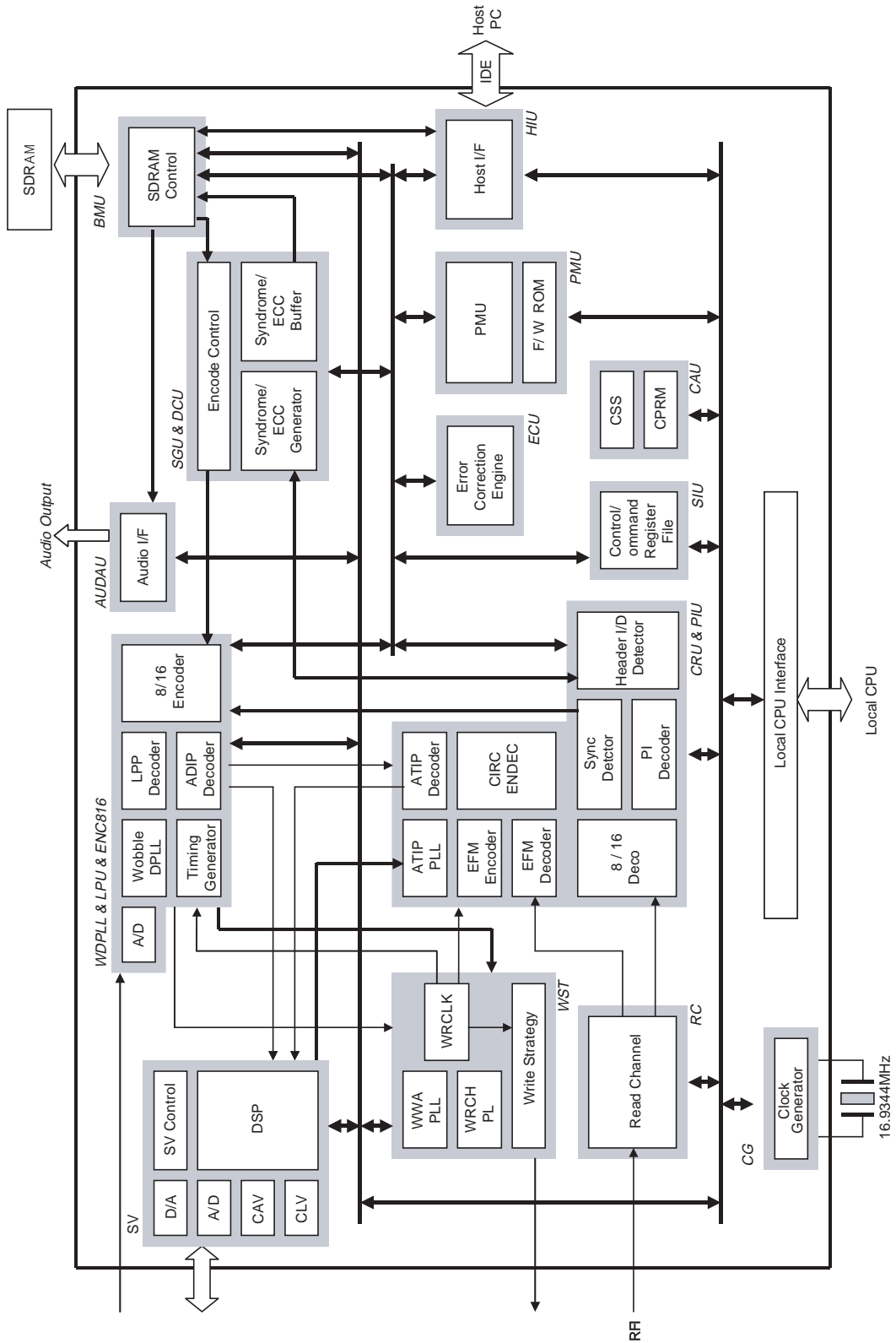
3. MAJOR IC INTERNAL BLOCK DIAGRAM AND PIN DESCRIPTION

IC201(μ PD63620) : Encoder, Decoder & DSP Signal Processor

Pin Assignment



Block Diagram



Pin description

Pin No.	Pin Name		Type		Description
1	DGND	-	-	-	Digital GND
2	PULLUP	-	-	-	Pull-up resistance connecting port.[5V or 3.3V]
3	$\overline{\text{DASO}}$	5V_tolerant	I/O	Pull-up	Drive active slave presesnt signal.[open/drain]
4	$\overline{\text{HCS1}}$	5V_tolerant	I	-	Host interface chip, pull-up selection input.
5	$\overline{\text{HCS0}}$	5V_tolerant	I	-	Host interface chip, pull-up selection input.
6	HDA2	5V_tolerant	I	-	Host interface chip, address signal input.
7	HDAO	5V_tolerant	I	-	Host interface chip, address signal input.
8	$\overline{\text{PDIAG}}$	5V_tolerant	I/O	Pull-up	Diagnostic signal [open/drain]
9	HDA1	5V_tolerant	I	-	Host interface chip, address signal input.
10	$\overline{\text{HIOCS16}}$	5V_tolerant	I	Pull-up	16 bit I/O signal [open/drain]. When Ultra DMA burst, this is 3 state port.
11	HINTRQ	5V_tolerant	O	Pull-up	Host interrupt signal output.
12	DVDD33	-	-	-	Digital power[3.3V]
13	DGND	-	-	-	Digital GND
14	$\overline{\text{HDMACK}}$	5V_tolerant	I/O	-	DMA acknowledge signal.
15	IORDY	5V_tolerant	I	Pull-up	I/O Channel ready[open/drain]. When Ultra DMA burst, this is DDMDARDY: DSTROBE signal.
16	$\overline{\text{DIOR}}$	5V_tolerant	I/O	-	Host interface read input signal. When Ultra DMA burst, this is HDMDARDY: HSTROBE signal.
17	$\overline{\text{DIOW}}$	5V_tolerant	I/O	-	Host interface write input signal. When Ultra DMA burst, this is STOP signal.
18	HDMARQ	5V_tolerant	O	Pull-up	DMA request signal output.
19	DVDD25	-	-	-	Digital power[2.5V]
20	$\overline{\text{HDD15}}$ ----- HDD8	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
21	$\overline{\text{HDD0}}$ ----- HDD6	5V_tolerant	O	Pull-up	Host interface data bus.[within slave resistance]
22	$\overline{\text{HDD14}}$ ----- HDD9	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
23	HDD1	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
24	DVDD33	-	-	-	Digital power[3.3V]
25	DGND	-	-	-	Digital GND
26	$\overline{\text{HDD13}}$ ----- HDD10	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
27	$\overline{\text{HDD2}}$ ----- HDD4	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]

Pin No.	Pin Name		Type		Description
28	DGND HDD4	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance
29	HDD3 HDD3	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
30	HDD11 HDD12	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
31	HDD4 HDD2	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance
32	HDD7 HDD13	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance]
33	HDD7 HDD1	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance].
34	DVDD33	-	-	-	Digital power[3.3V]
35	DGND	-	-	-	Digital GND
36	HDD9 HDD14	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance].
37	HDD6 HDD0	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance].
38	HDD8 HDD15	5V_tolerant	I/O	Pull-up	Host interface data bus.[within slave resistance].
39	HDD7	5V_tolerant	I/O	Pull-up	Host interface data bus.
40	HRESET	5V_tolerant	I/O	-	Host reset input.
41	TCK	3V	I	-	Test port. It must be connected to DGND.
42	TMS	3V	I	-	Test port. It must be connected to DGND.
43	TAPCLK	3V	I	-	Test port. It must be connected to DGND.
44	TESTSE	3V	I	-	Test port. It must be connected to DGND.
45	DVDD25	-	-	-	Digital power[2.5V]
46	MON3	3V	O	L	Monitor: test signal.
47	MON2	3V	O	L	Monitor: test signal.
48	MON1	3V	I/O	L	Monitor: test signal.
49	MON0	3V	I/O	L	Monitor: test signal.
50	MD15	3V	I/O	Pull-up	Buffer memory , Interface data bus.
51	MD0	3V	I/O	Pull-up	Buffer memory , Interface data bus.
52	MD14	3V	I/O	Pull-up	Buffer memory , Interface data bus.
53	MD1	3V	I/O	Pull-up	Buffer memory , Interface data bus.
54	DVDD33	-	-	-	Digital power.[3.3V](Buffer. Memory. Block)

Pin No.	Pin Name		Type		Description
55	DGND	-	-	-	Digital GND.(Buffer. Memory. Block)
56	MD13	3V	I/O	Pull-up	Buffer memory , Interface data bus.
57	MD2	3V	I/O	Pull-up	Buffer memory , Interface data bus.
58	MD12	3V	I/O	Pull-up	Buffer memory , Interface data bus.
59	MD3	3V	I/O	Pull-up	Buffer memory , Interface data bus.
60	DGND	-	-	-	Digital GND.
61	MD11	3V	I/O	Pull-up	Buffer memory , Interface data bus.
62	MD4	3V	I/O	Pull-up	Buffer memory , Interface data bus.
63	MD10	3V	I/O	Pull-up	Buffer memory , Interface data bus.
64	MD5	3V	I/O	Pull-up	Buffer memory , Interface data bus.
65	DVD33	-	-	-	Digital power.[3.3V](Buffer. Memory. Block)
66	DGND	-	-	-	Digital GND.(Buffer. Memory. Block)
67	MD9	3V	I/O	Pull-up	Buffer memory , Interface data bus.
68	MD6	3V	I/O	Pull-up	Buffer memory , Interface data bus.
69	MD8	3V	I/O	Pull-up	Buffer memory , Interface data bus.
70	MD7	3V	I/O	Pull-up	Buffer memory , Interface data bus.
71	DVDD25	-	-	-	Digital power.[2.5V]
72	LDQM	3V	O	H	Low byte, data input/output mask control signal.
73	UDQM	3V	O	H	High byte, data input/output mask control signal.
74	WE	3V	O	H	Buffer memory , Interface write enable signal.
75	MCLK	3V	O	Pull-up	SDRAM clock output.
76	DVDD33	-	-	-	Digital power.[3.3V](Buffer. Memory. Block)
77	DGND	-	-	-	Digital GND.(Buffer. Memory. Block)
78	$\overline{\text{CAS}}$	3V	O	H	Buffer memory , Interface column address strobe control signal.
79	MCKE	3V	O	H	SDRAM clock enable control signal.
80	$\overline{\text{RAS}}$	3V	O	H	Buffer memory , Interface row address strobe control signal.
81	MA9	3V	O	L	Buffer memory , Interface address bus.
82	DVDD25	-	-	-	Digital power.[2.5V]
83	MBA	3V	O	L	Buffer memory , Interface bank address signal.
84	MA8	3V	O	L	Buffer memory , Interface data bus.
85	MA10	3V	O	L	Buffer memory , Interface data bus.
86	MA7	3V	O	L	Buffer memory , Interface data bus.
87	DVDD33	-	-	-	Digital power.[3.3V](Buffer. Memory. Block)

Pin No.	Pin Name		Type		Description
88	DGND	-	-	-	Buffer memory , Interface data bus.
89	MA0	3V	O	H	Buffer memory , Interface data bus.
90	MA6	3V	O	L	Buffer memory , Interface data bus.
91	MA1	3V	O	L	Buffer memory , Interface data bus.
92	MA5	3V	O	L	Buffer memory , Interface data bus.
93	DGND	-	-	-	Digital GND
94	MA2	3V	O	L	Buffer memory , Interface data bus
95	MA4	3V	O	L	Buffer memory , Interface data bus.
96	MA3	3V	O	L	Buffer memory , Interface data bus.
97	DVDD3	-	-	-	Digital power.[3.3V](Buffer. Memory. Block)
98	DGND	-	-	-	Digital GND (Buffer. Memory. Block)
99	CKGCK	3V	O		Clock, Generator output.
100	SVMON	3V	O	L	Servo, block monitor signal output.
101	RCDTO	3V	O	L	Read channel data output.
102	RCCK	3V	I/O	Hi-Z	Read channel clock output.
103	RCDT	3V	I/O	Hi-Z	Read channel data output.
104	PCEFM1	Analog	-	-	Read channel phase discriminator condenser connecting port.
105	PCEFM2	Analog	-	-	Read channel phase discriminator condenser connecting port.
106	OFFSETIN	Analog	-	-	Read channel phase discriminator charge pump control port.
107	FCEFM	Analog	-	-	Read channel frequency discriminator condenser connecting port.
108	AVDD	-	-	-	Analog power[2.5V]
109	IREF	Analog	o	-	Read channel analog reference voltage input.
110	AGND	-	-	-	Analog GND[EFM PLL]
111	IREF2	Analog	O	-	Non connecting port.
112	EFM	Analog	O	Pull-up	EFM comparator output.
113	ASY	Analog	O	-	EFM comparator asymmetry control voltage input.
114	AVDD	-	-	-	Analog power[2.5V]
115	RFI	Analog	O		EFM comparator RF signal input.
116	AGND	-	-	-	Analog GND[EFM]
117	DVDD25	-	-	-	Digital power.[2.5V]
118	HOLD	3v	O	-	HOLD control signal input.
119	FOK	3v	O	-	FOK signal input.
120	MIRRBCA	3V	O	-	Mirror signal or BCA signal input.

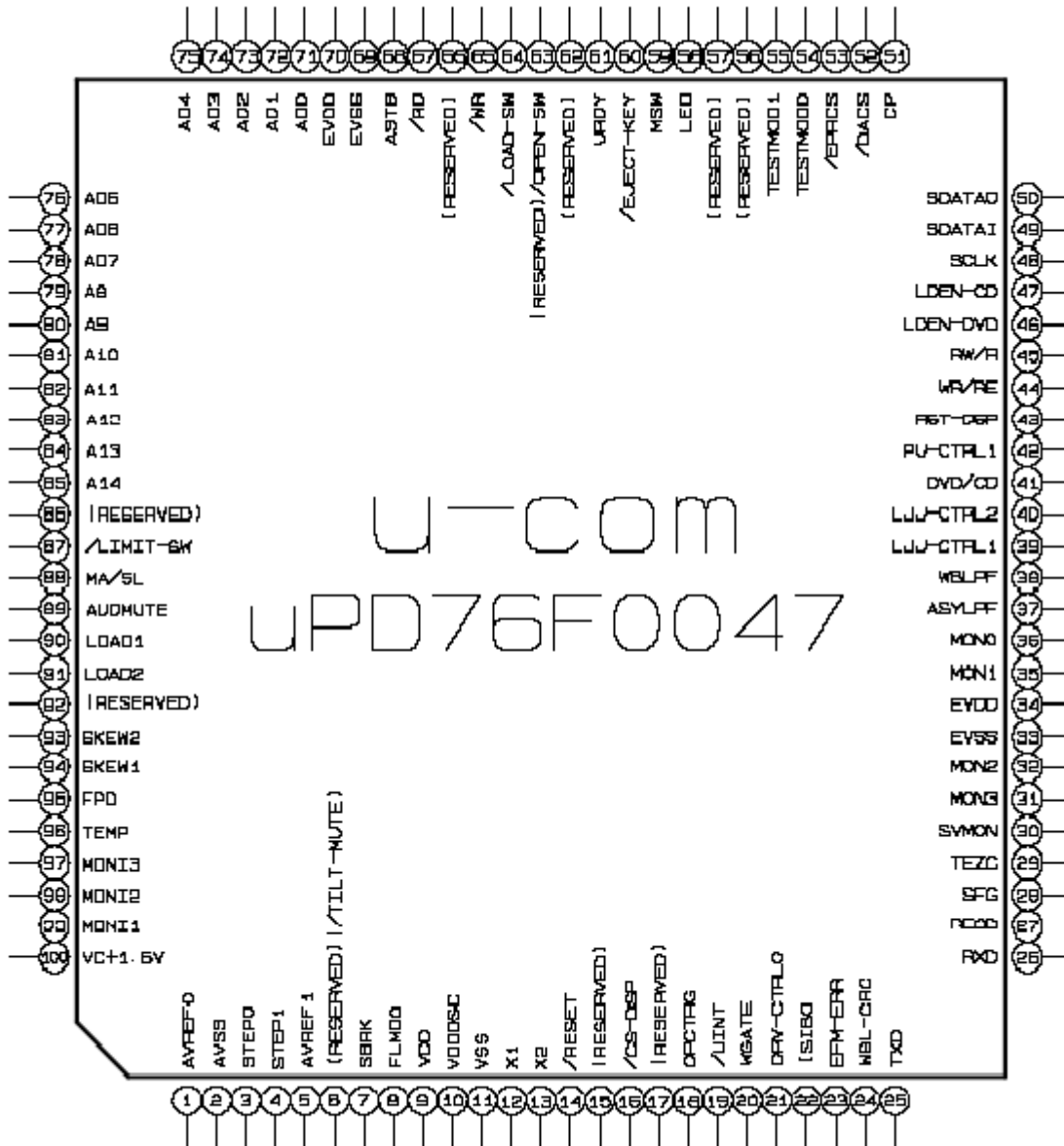
Pin No.	Pin Name		Type		Description
121	MIRRBCA	3V	O	Pull-up	RF AMP PC3320 RF equalizer automatic follow-up clock output.
122	EQCK1	3V	O	Pull-up	RF AMP PC3320 RF equalizer fixed clock output.
123	SCFCK	3V	O	P	RF AMP PC3320 RF equalizer automatic follow-up clock output.
124	WLDON	3V	O	L	Laser, Driver write laser control signal.
125	ROPC2	3V	O	L	Running OPC,sample hold signal.
126	DVDD33	-	-	-	Digital power.[3.3V]
127	DGND	-	-	-	Digital GND
128	ROPC1	3V	O	L	Running OPC,sample hold signal.
129	ROPC1	3V	O	L	APC write, sample hold signal.
130	SHAPCRE	3V	O	H	APC read/erase, sample hold signal.
131	SHSV	3V	O	H	Servo, sample hold signal.
132	SHWB	3V	O	H	Wobble, sample hold signal.
133	WBLPP	3V	I	-	CD: 2 direct Wobble signal input, DVD: RLPP signal input.
134	TEC	3V	I	-	Test port. It must be connected to DGND.
135	TEC	3V	I	-	Tracking, zero, cross signal input.
136	DGND	-	-	-	Digital GND
137	LE	Analog	I	-	Lens error signal input [A/D convertor].
138	FE	Analog	I	-	Focus error signal input [A/D convertor].
139	TE	Analog	I	-	Tracking error signal input [A/D convertor].
140	SWRF2	Analog	I	-	WRF sample hold signal input [A/D convertor].
141	SWRF1	Analog	i	-	WRF sample hold signal input [A/D convertor].
142	REFIN	Analog	I	-	Reference voltage input [A/D convertor].
143	AGND	-	-	-	Analog GND[Servo A/D, D/A block]
144	AVDD	-	-	-	Analog power 2.5V[Servo A/D, D/A block].
145	MDRV	Analog	O		Spindle drive output [D/A convertor output].
146	FDRV	Analog	O		Focus drive output [D/A convertor output].
147	TDRV	Analog	O		Trackng drive output [D/A convertor output].
148	SDRV	Analog	O		Sled drive output [D/A convertor output].
149	REFOUT	Analog	O	1/2AVDD	Reference voltage output.
150	DVDD25	-	-	-	Digital power[2.5V]
151	FG	5V_tolerant	I	-	FG signal input
152	SLED	5V_tolerant	O	-	Sled position sensor input.
153	INT	5V_tolerant	O	L	Interrupted request signal output to Local CPU

Pin No.	Pin Name		Type		Description
154	RDY	5V_tolerant	I	-	Access control signal output from Local CPU to SDRAM.
155	DVDD33	-	-	-	Digital power[3.3V]
156	A14	5V_tolerant	I	-	Local CPU Address bus.
157	A13	5V_tolerant	I	-	Local CPU Address bus.
158	A12	5V_tolerant	I	-	Local CPU Address bus.
159	A11	5V_tolerant	I	-	Local CPU Address bus.
160	A10	5V_tolerant	I	-	Local CPU Address bus.
161	A9	5V_tolerant	I	-	Local CPU Address bus.
162	A8	5V_tolerant	I	-	Local CPU Address bus.
163	DGND	-	-	-	Digital GND
164	AD7	5V_tolerant	I/O	-	Local CPU Address/data mux bus.
165	AD6	5V_tolerant	I/O	-	Local CPU Address/data mux bus.
166	AD5	5V_tolerant	I/O	-	Local CPU Address/data mux bus.
167	AD4	5V_tolerant	I/O	-	Local CPU Address/data mux bus.
168	AD3	5V_tolerant	I/O	-	Local CPU Address/data mux bus.
169	AD2	5V_tolerant	I/O	-	Local CPU Address/data mux bus.
170	DVDD33	-	-	-	Digital power[3.3V]
171	AD1	5V_tolerant	I/O	-	Local CPU Address/data mux bus.
172	AD0	5V_tolerant	I/O	-	Local CPU Address/data mux bus.
173	RD	5V_tolerant	I	-	Read strobe signal input.
174	WR	5V_tolerant	I	-	Write strobe signal input.
175	ASTB	5V_tolerant	I	-	Address strobe input.
176	$\overline{\text{CS}}$	5V_tolerant	I	-	Chip selector input from Local CPU.
177	$\overline{\text{SYSRST}}$	5V_tolerant	I	-	Reset input.
178	DGND	-	-	-	Digital GND.
179	DVDD25	-	-	-	Digital power[2.5V]
180	OPCTRG	3V	I/O	-	Wobble FM demodulation data output. DVD mode: OPCTRG signal output.
181	HFON	3V	O	H	Laser, Driver high-frequency control signal.
182	WRCK	-	-	L	Write Clock.
183	DVDD33	-	-	-	Digital power[3.3V]
184	WRPULSE	3V	O	L	Write pulse [write laser/driver control signal]
185	OPPULSE	3V	O	H	Write pulse [write laser/driver control signal]
186	DGND	-	-	-	Digital GND.

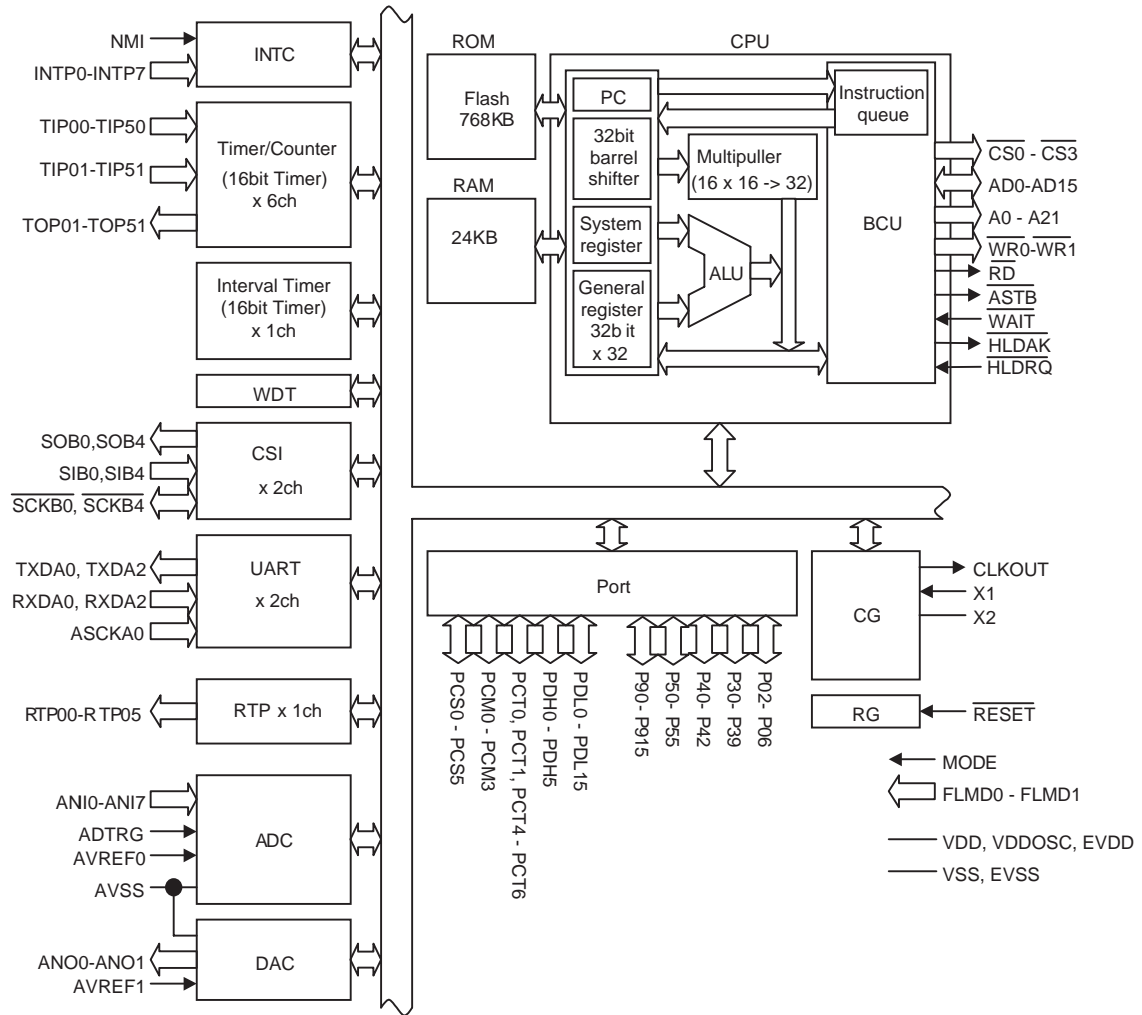
Pin No.	Pin Name		Type		Description
187	DVDD33	-	-	-	Digital power[3.3V]
188	EFPLUSE	3V	O	L	OFF pluse output[write laser/driver control signal].
189	PKPULSE1	3V	O	L	Peak pluse output[write laser/driver control signal].
190	PKPULSE2	3V	O	L	Peak pluse output[write laser/driver control signal].
191	HDDREV	3V	I	-	Host interface data bus selector.[H: general, L: reverse]
192	AGND	-	-	-	Analog GND[WWAPLL]
193	AVDD	-	-	-	Analog power 2.5V [WWAPLL]
194	WWALPF1	Analog	-	-	WWAPLL condenser connecting port.
195	WWALPF2	Analog	-	-	WWAPLL condenser connecting port.
196	WRLPF	Analog	-	-	WST DLL condenser connecting port.
197	AGND	-	-	-	Analog GND[WST DLL block]
198	AVDD	-	-	-	Analog power 2.5V [WST DLL block]
199	AVDD	-	-	-	Analog power 2.5V [WDPLL A/D block]
200	WDADVRT	Analog	-	-	WDPLL block A/D convertor condenser connecting port.
201	AWBL	Analog	I	-	Analog wobble signal input port.
202	WDADVRB	Analog	-	-	WDPLL block A/D convertor condenser connecting port.
203	AGND	-	-	-	Analog GND[WDPLL A/D block]
204	AGND	-	-	-	Analog GND[PLL block]
205	AVDD	-	-	-	Analog power 2.5V [PLL block]
206	LPFCK	Analog	-	-	Test port. It must be connected to AGND.
207	AGND	-	-	-	Analog GND[Crystal block]
208	$\overline{\text{XTAL}}$	-	I/O	-	Crystal oscillator connecting port.
209	XTAL	-	I	-	Crystal oscillator connecting port.
210	DVDD25	-	-	-	Digital power[2.5V]
211	LRCK	3V	O	Pull-up	DOUt serial audio data.
212	SCKO	3V	O	Pull-up	Serial audio data synchronizing clock output port.
213	DVDD33	-	-	-	Digital power[3.3V]
214	DGND	-	-	-	Digital GND
215	DOUt	3V	O	Pull-up	Serial audio data output port.
216	EMPH	3V	O	Pull-up	Emphasis distinguish signal.

4. IC302(uPD76f0047):MICOM

Pin Assignment



Block Diagram



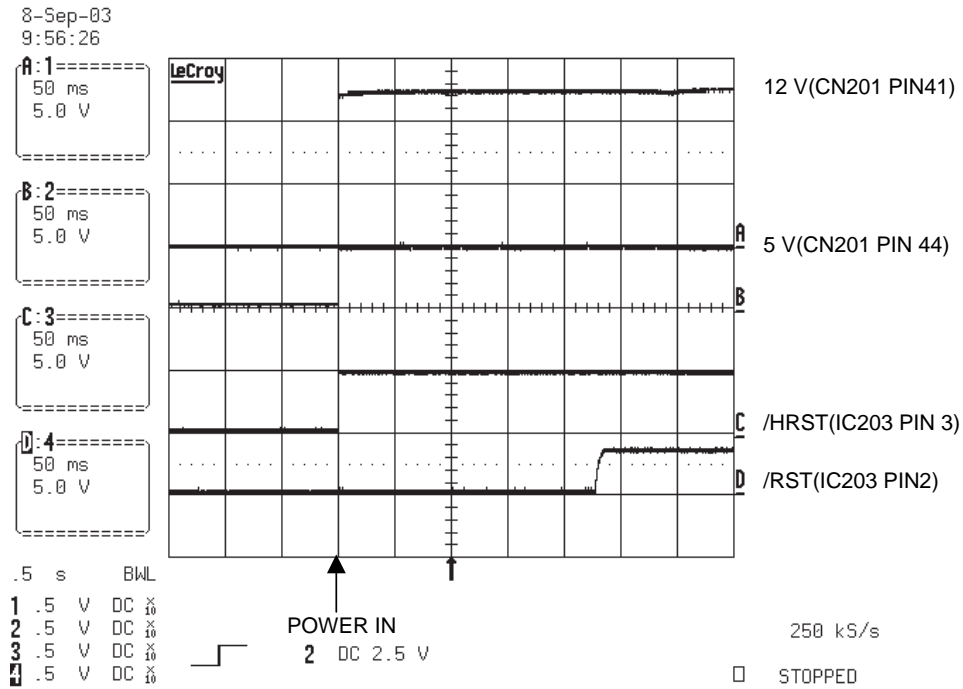
Pin description

Pin No.	Pin Name	Type	Des cription
1	AVREF0	I	A/D CONVERTER REFERENCE VOLTAGE INPUT
2	AVSS	-	A/D,D/A CONVERTER POTENTIAL
3	STEP0	O	STEPPTING MOTOR CONTROL SIGNAL
4	STEP1	O	STEPPTING MOTOR CONTROL SIGNAL
5	AVREF1	I	D/A CONVERTER REFERENCE VOLTAGE INPUT
6	TILT-MUTE	O	TILT DRIVE MUTE SIGNAL
7	SBRK	O	EXTERNAL MEMORY ADDRESS BUS
8	FLMD0	I	FLASH PROFLAMING MODE
9	VDD	-	I NTERNAL CONSTANT POWER
10	VDDOSC	-	CONSTANT POWER
11	VSS	-	INTERNAL GROUND POTENTIAL
12	X1	I	MAIN CLOCK
13	X2	-	MAIN CLOCK
14	/RESET	I	SYSTEM RESET
15	(RESERVED)	-	-
16	/CS-DSP	O	CHIP SELECTOR OUPUT
17	(RESERVED)	-	-
18	OPCTRG	I/O	WOBBLE FM DE M ODUL ATI ON DATA
19	/UINT	I	INTERRUPTED REQUEST SIGNAL INPUT
20	WGATE	I	DRIVER WRITER LASER CONTROL SIGNAL
21	DRV-CTRL0	-	-
22	SIB0	I	SERIAL CLOCK
23	EFM-ERR	-	-
24	WBL-CRC	-	-
25	TXD	O	SERIAL CLOCK
26	RXD	I	SERIAL CLOCK
27	RECD	I	NO RECODRDING AREA DETECTION
28	SFG	I	FG SIGNAL INPUT
29	TEZC	I	TRACK ZERO CROSS SIGNAL INPUT
30	SVMON	I	SERVO BLOCK MONITOR SIGNAL
31	MON3	I	M ONI TOR TEST SINGNAL
32	MON2	I	M ONI TOR TEST SINGNAL
33	EVSS	-	EXTERNAL CONSTANT POWER
34	EVDD	-	EXTERNAL CONSTANT POWER
35	MON1	I	MONITOR TEST SINGNAL
36	MON0	I	MONITOR TEST SINGNAL
37	ASYLPF	-	-
38	WBLPF	-	-
39	LJJ-CTRL1	-	-
40	LJJ-CTRL2	-	-
41	DV D/CD	-	-
42	PU-CTRL1	O	PD IC GAIN CONTROL SIGNAL
43	RST-DSP	O	RESET OUT
44	WR/RE	O	PD IC GAIN COTTROL SI NAL(WRITE/READ)
45	RW/R	-	-
46	LDEN-DVD	O	PICK-UP LD ENABLE SIGNAL (DV D)
47	LDEN-CD	O	PIC K-UP LD ENABLE SIGNAL (C D)
48	SCLK	O	REGISTER SETTING CLOCK
49	SDATAI	I	REGISTER SETTING DATA INPUT
50	SDATAO	O	REGISTER SETTING DATA OUTPUT

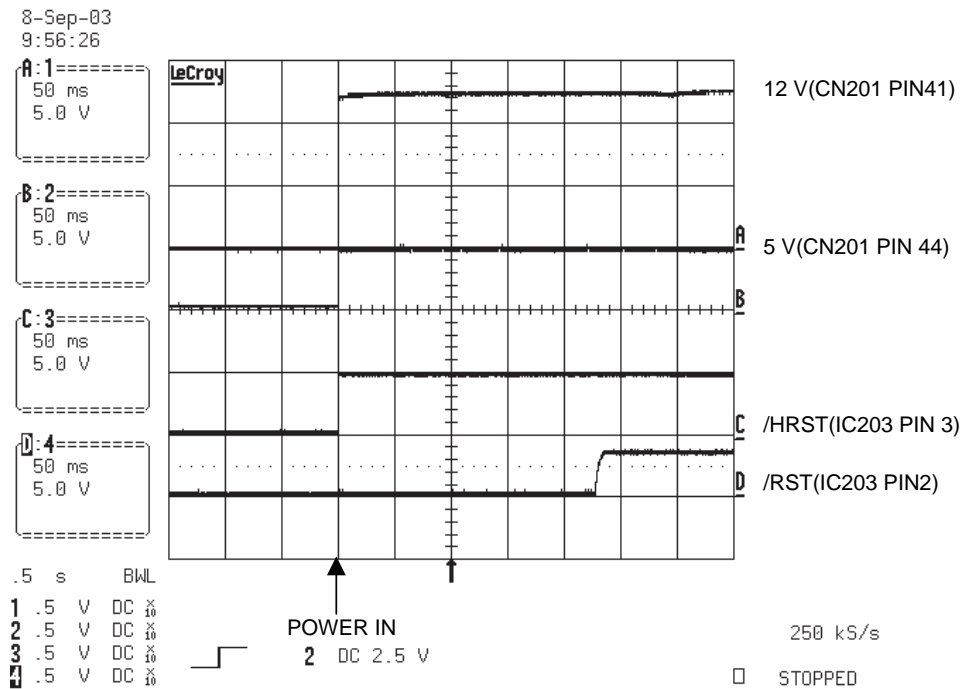
Pin No.	Pin Name	Type	Des cription
51	CP	O	REGISTER ADDRESS OUPUT
52	/DACS	O	CHIP SELECTOR
53	/BPRCS	O	EEPROM COMM UNICATON LINE
54	TEST MOD0	-	-
55	TEST MOD1	-	-
56	(RESERVED)	-	-
57	(RESERVED)	-	-
58	LED	O	LED ENABLE LINE
59	MSW	O	LED ENABLE LINE
60	/EJECT-KEY	O	TRAY OPE N LINE
61	URDY	O	ACCESS CONTROL SIGNAL INPUT FRON CPU TO SDRAM
62	(RESERVED)	-	-
63	/OPEN-SW	I	OPEN S/W INPUT
64	/LOAD-SW	I	LOAD S/W INPUT
65	/WR	O	WRITE STROBE SIGNAL OUTPUT
66	(RESERVED)	-	-
67	/RD	O	READ STROBE SIGNAL OUPUT
68	ASTB	O	ADDRESS STROBE OUPUT
69	EVSS	-	EX TERNAL CONSTANT POWER
70	EVDD	-	EX TERNAL CONSTANT POWER
71	AD0	I/O	PORT DL 16BIT INPUT/OUTPUT
72	AD1	I/O	PORT DL 16BIT INPUT/OUTPUT
73	AD2	I/O	PORT DL 16BIT INPUT/OUTPUT
74	AD3	I/O	PORT DL 16BIT INPUT/OUTPUT
75	AD4	I/O	PORT DL 16BIT INPUT/OUTPUT
76	AD5	I/O	PORT DL 16BIT INPUT/OUTPUT
77	AD6	I/O	PORT DL 16BIT INPUT/OUTPUT
78	AD7	I/O	PORT DL 16BIT INPUT/OUTPUT
79	A8	I/O	PORT DL 16BIT INPUT/OUTPUT
80	A9	I/O	PORT DL 16BIT INPUT/OUTPUT
81	A10	I/O	PORT DL 16BIT INPUT/OUTPUT
82	A11	I/O	PORT DL 16BIT INPUT/OUTPUT
83	A12	I/O	PORT DL 16BIT INPUT/OUTPUT
84	A13	I/O	PORT DL 16BIT INPUT/OUTPUT
85	A14	I/O	PORT DL 16BIT INPUT/OUTPUT
86	(RESERVED)	I/O	PORT DL 16BIT INPUT/OUTPUT
87	/LIMIT-SW	I	TRAY LIMIT S/W INPUT
88	MA/SL	I	MASTER/SLAVE MODE SELECTOR
89	AUDMUTE	-	-
90	LOAD1	O	STANDBY/BRAKE CONTROL SIGNAL
91	LOAD2	O	STANDBY/BRAKE CONTROL SIGNAL
92	(RESERVED)	-	-
93	SKEW2	-	-
94	SKEW1	-	-
95	FPD	I	TEMPERATURE MONITOR CURRENT INPUT
96	TEMP	I	MONITOR TEST SINGNAL
97	MONI3	I	FOCUS ERROR INPUT
98	MONI2	I	LASER MONITOR CURRENT INPUT
99	MONI1	I	PDIC REFERNEC VOLTAGE
100	VC+1.5V	I	VCC 1.5V INPUT

WAVEFORMS

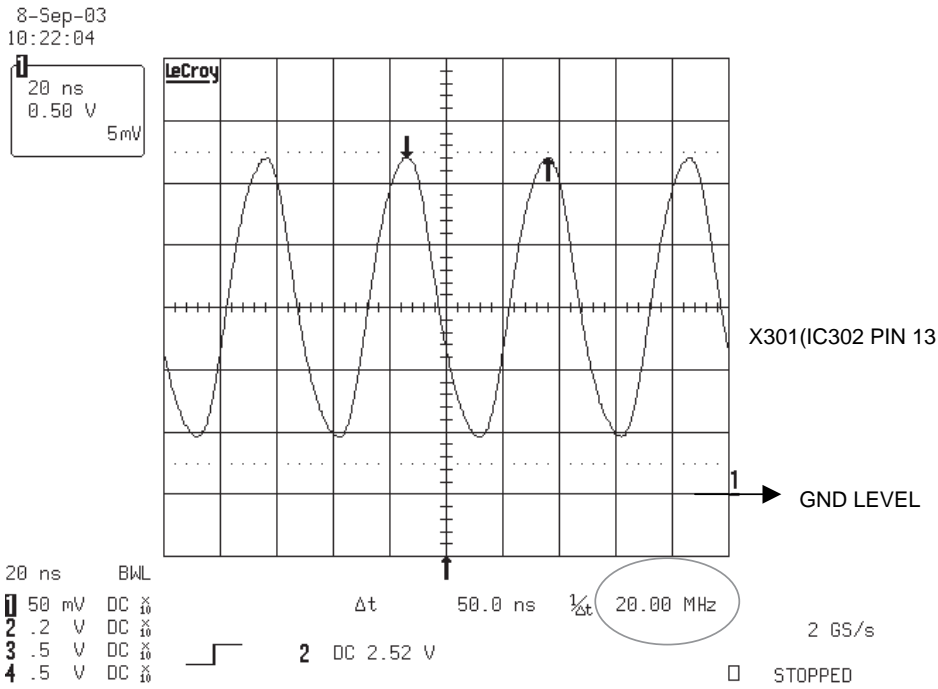
1. POWER & RESET Signal



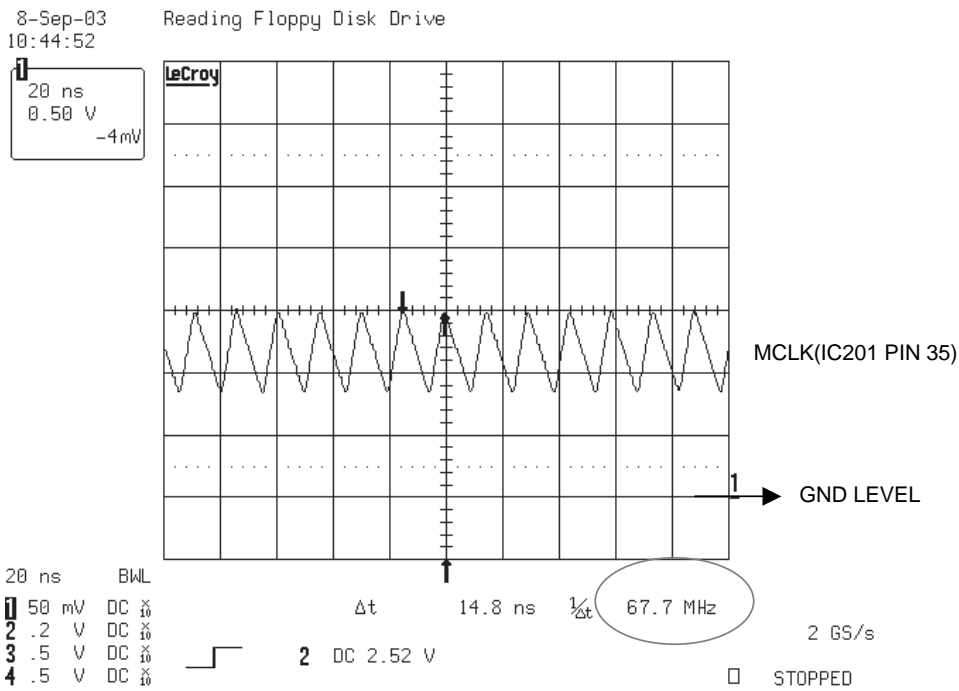
2. Main Clock1 for IC202 (16.9MHz)



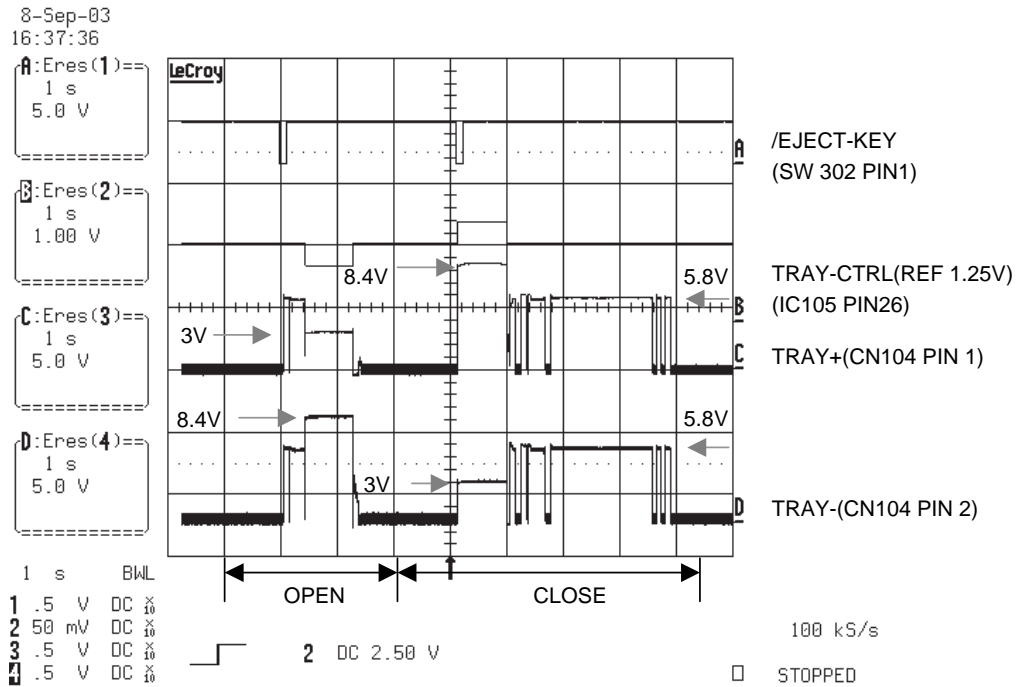
3. Main Clock2 for IC302 (20MHz)



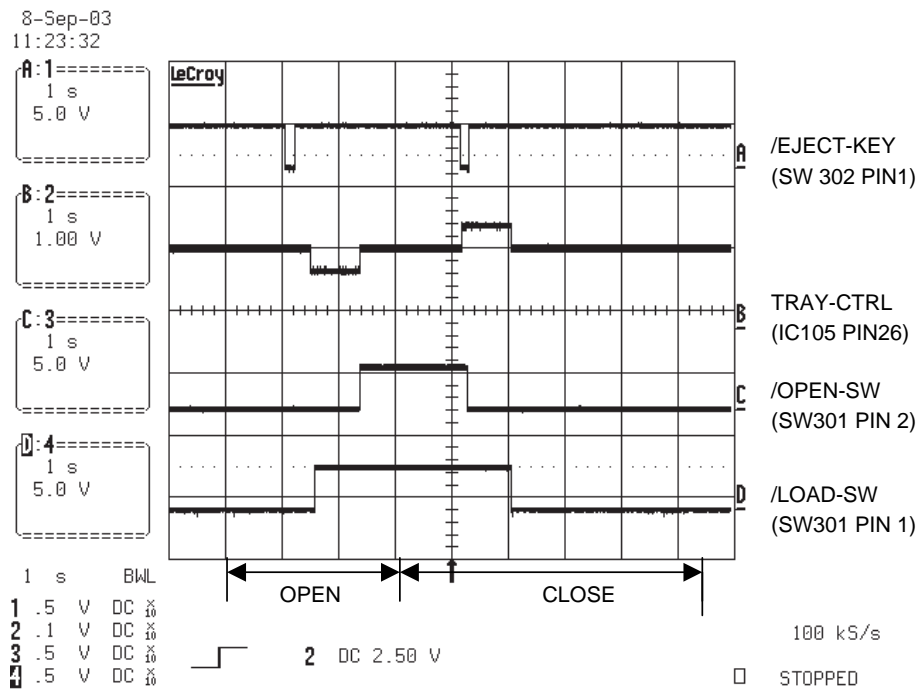
4. SDRAM Clock



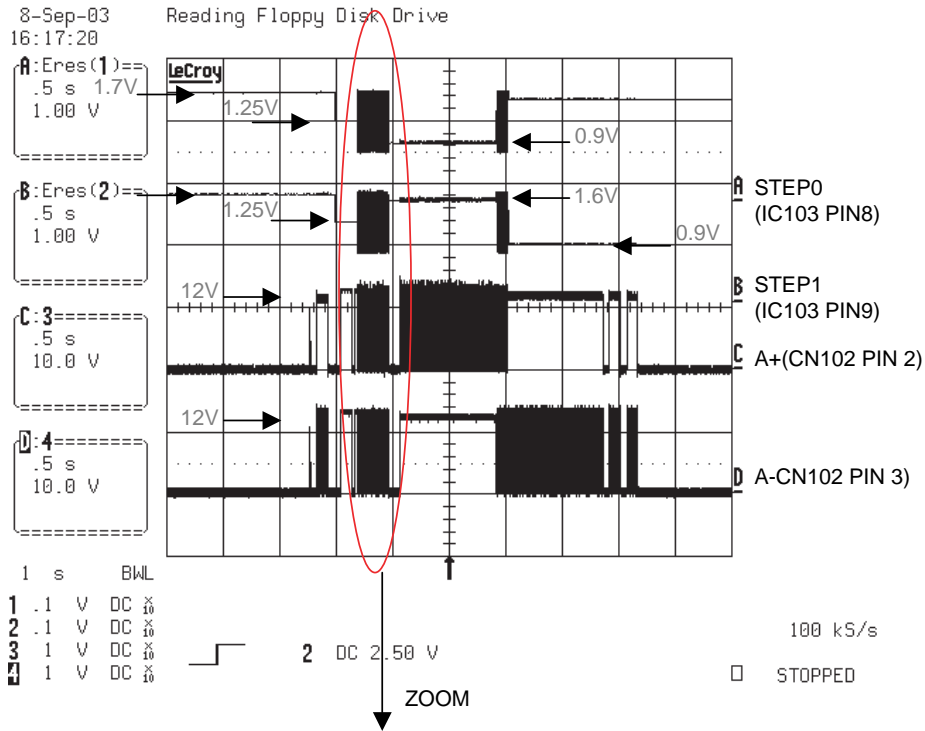
5. TRAY OPEN/CLOSE SIGNAL 1



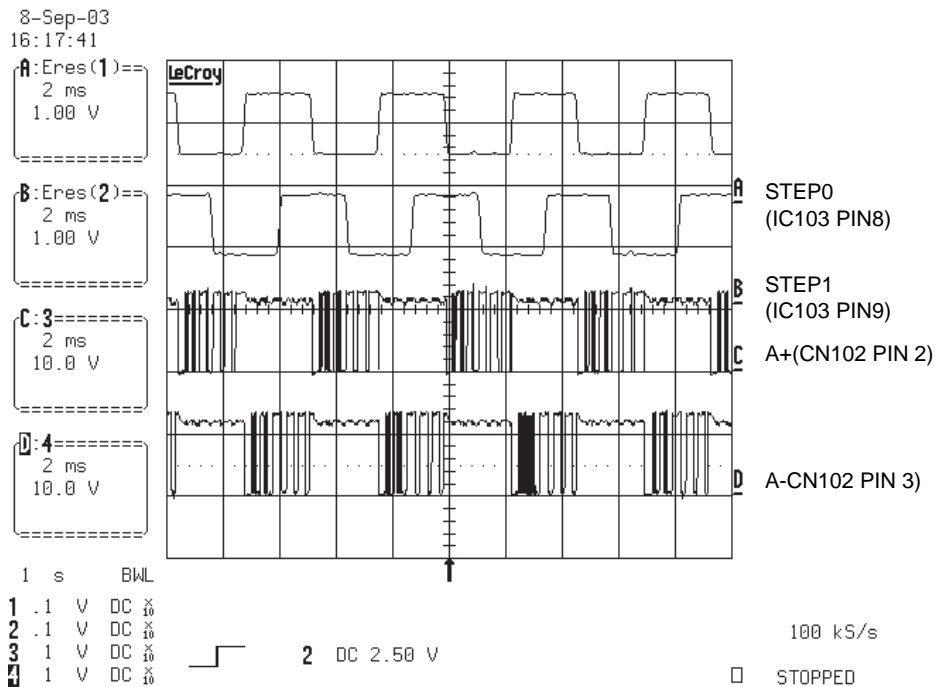
6. TRAY OPEN/CLOSE SIGNAL 2



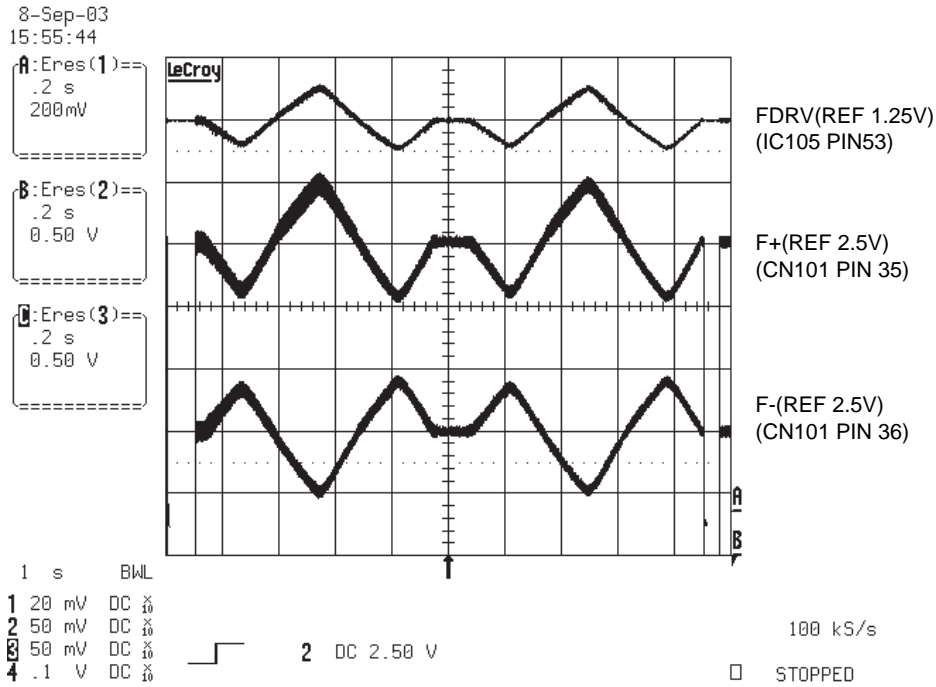
7. SLED MOVE SIGNAL 1



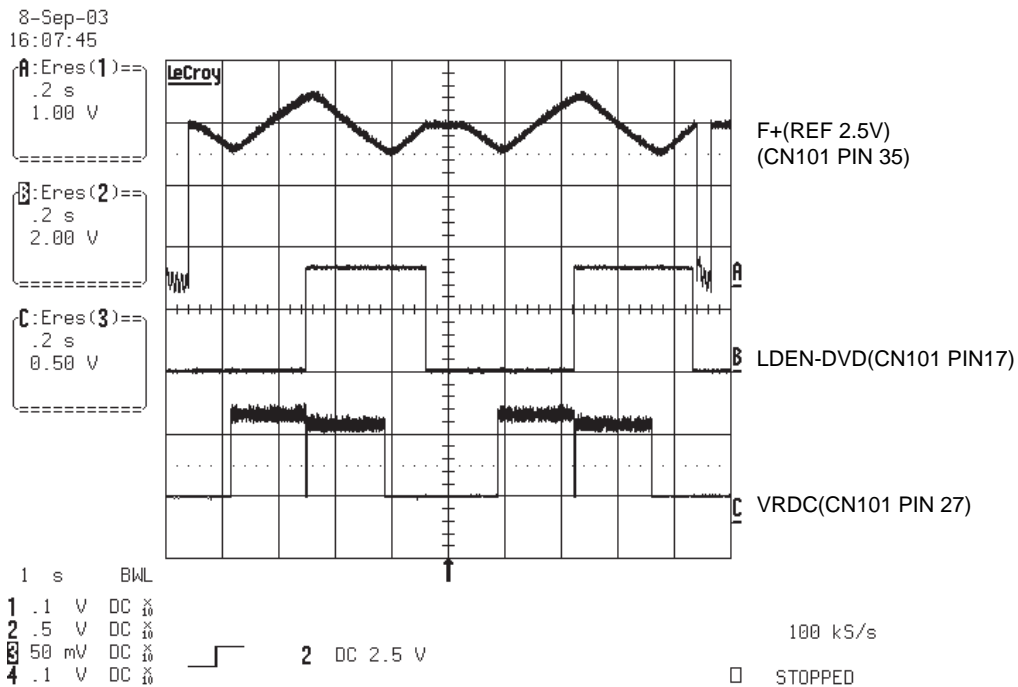
8. SLED MOVE SIGNAL 2



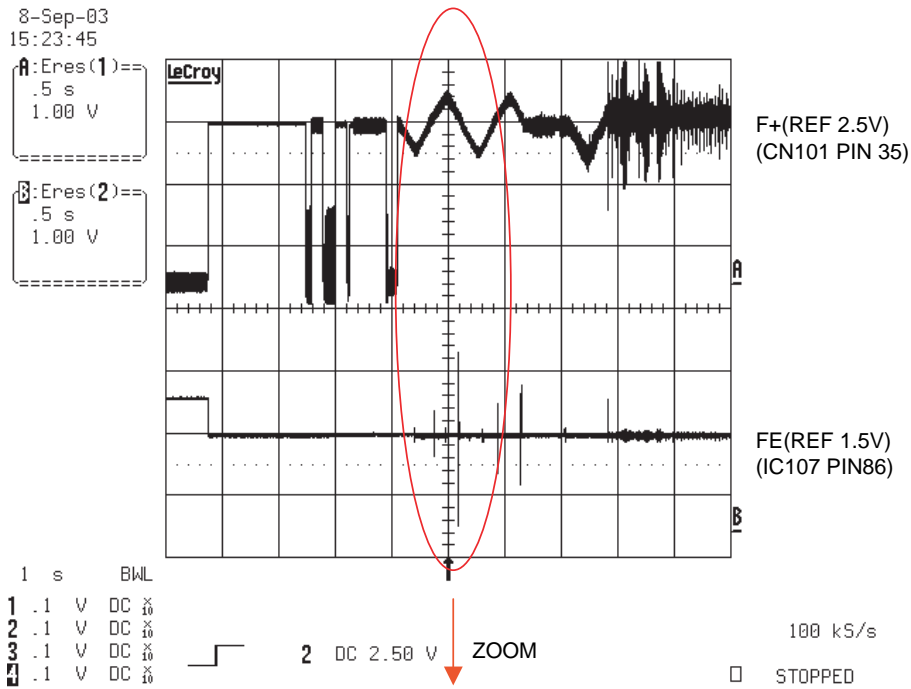
9. FOCUS SEARCH SIGNAL



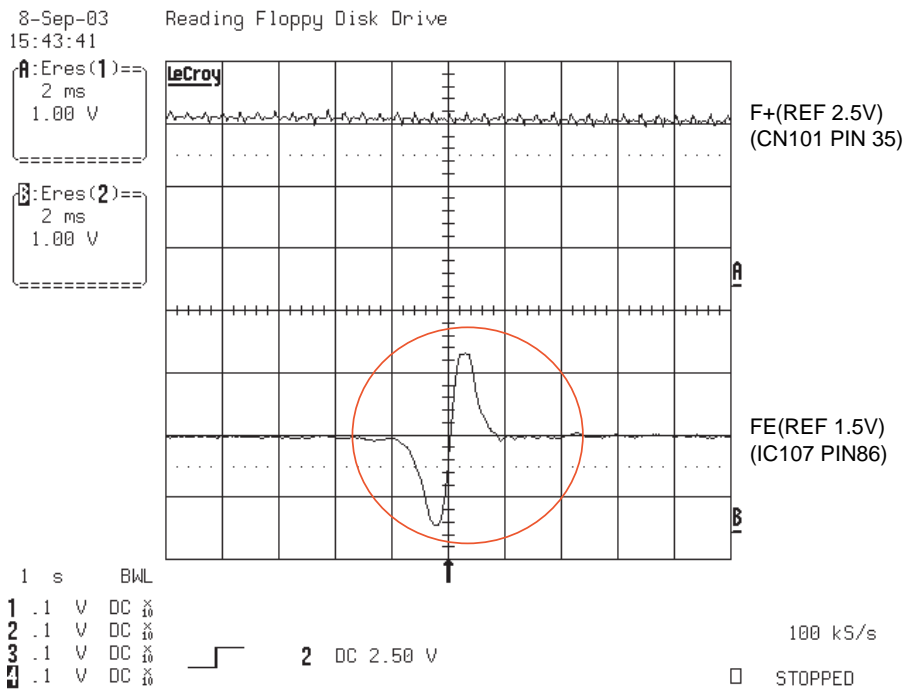
10. LASER TURN ON SIGNAL



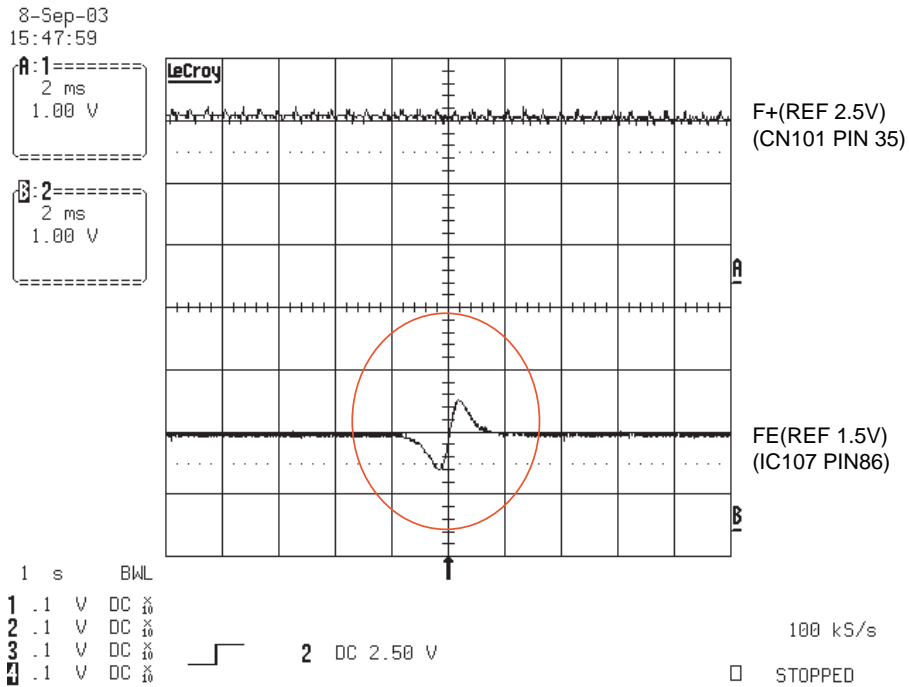
11. DISC TYPE JUDGEMENT WAVEFORM (CD SERIES)



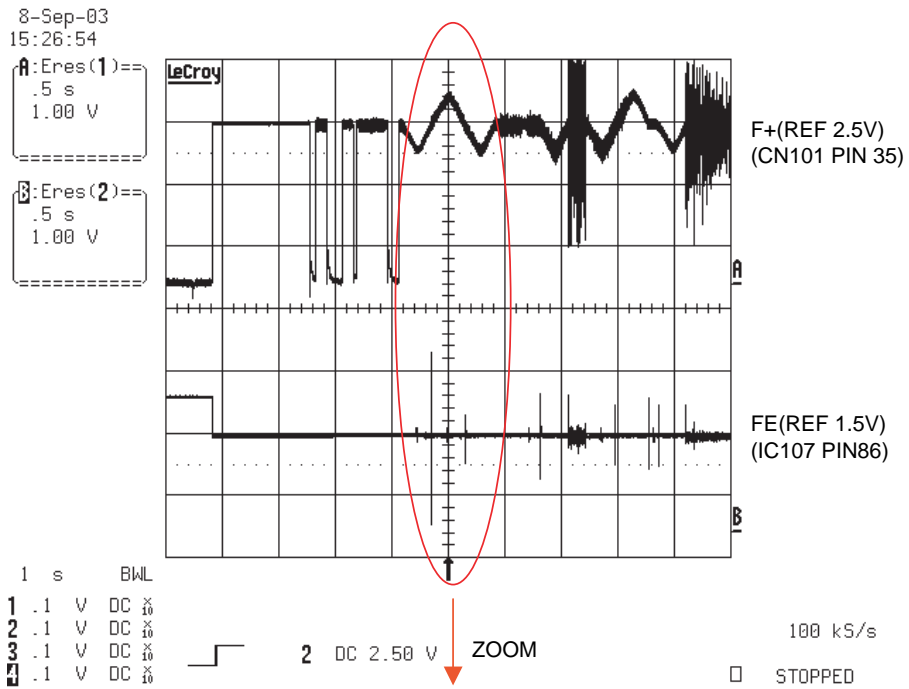
12. DISC TYPE JUDGEMENT WAVEFORM (CD&CD-R)



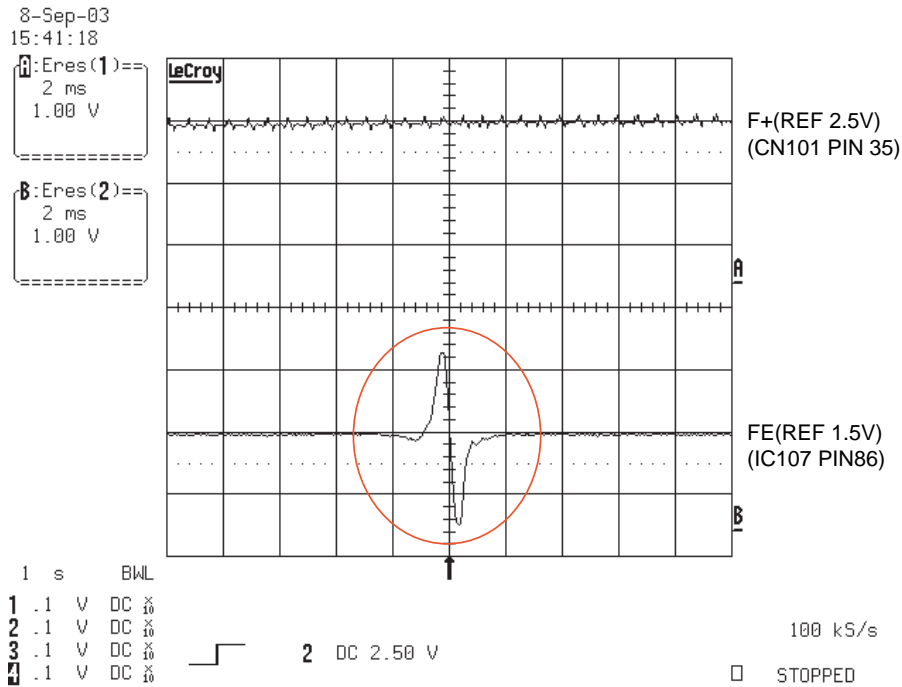
13. DISC TYPE JUDGEMENT WAVEFORM (CD-RW)



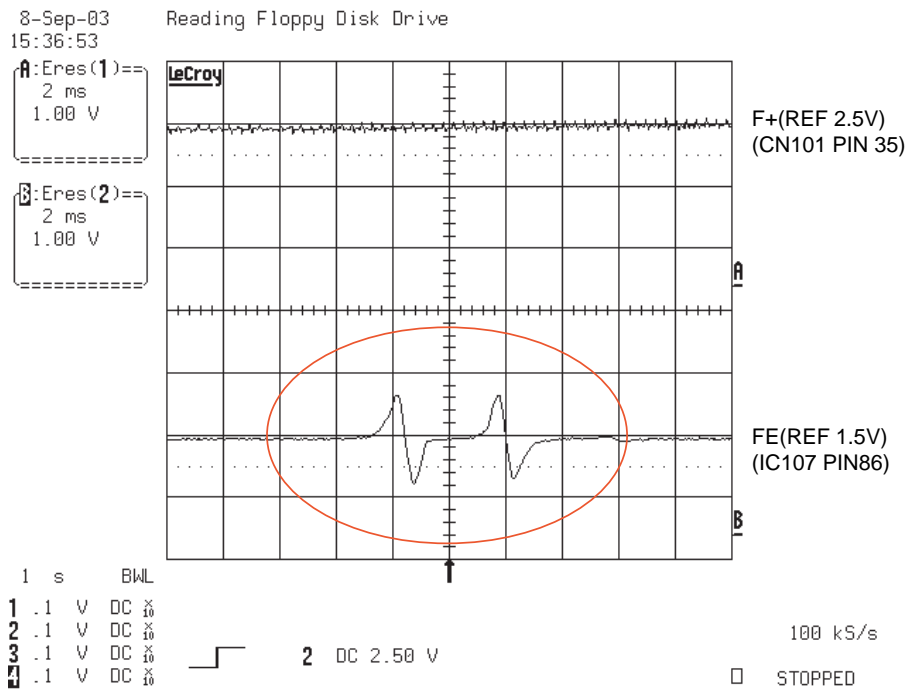
14. DISC TYPE JUDGEMENT WAVEFORM (DVD SERIES)



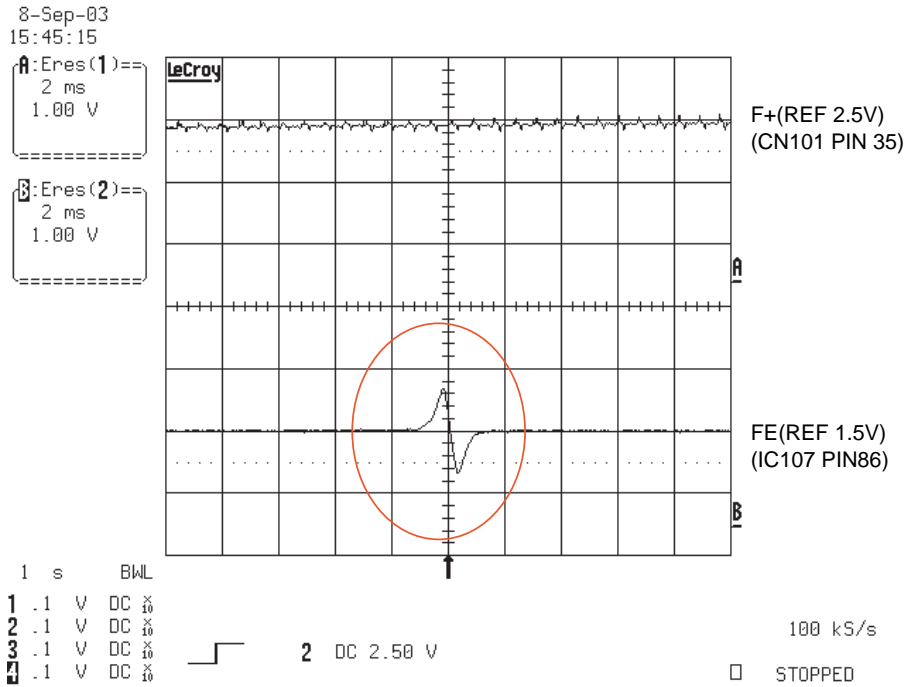
15. DISC TYPE JUDGEMENT WAVEFORM (DVD_SINGLE&R)



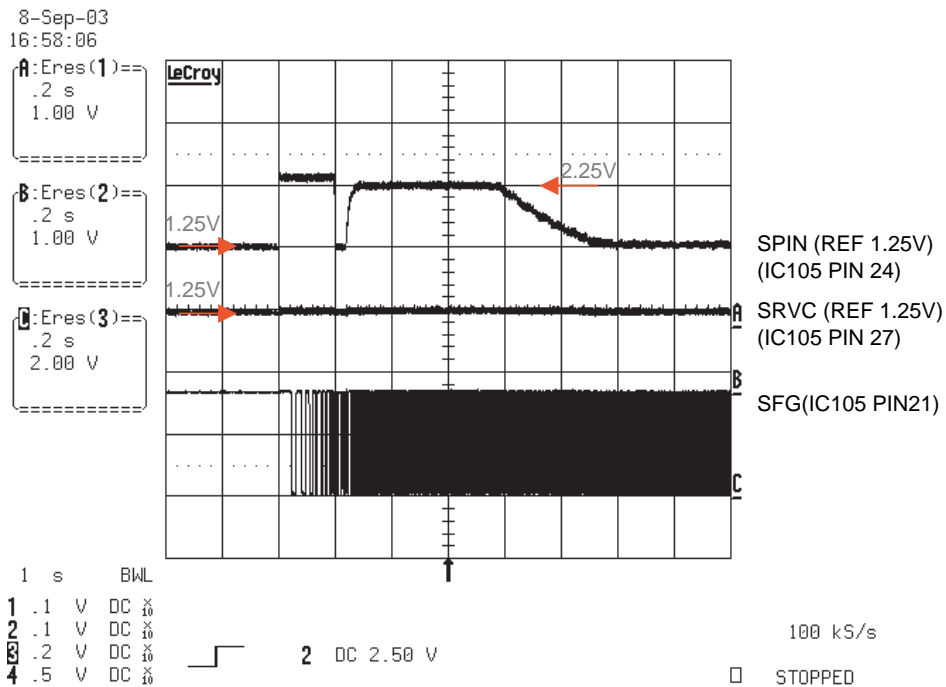
16. DISC TYPE JUDGEMENT WAVEFORM (DVD_DUAL)



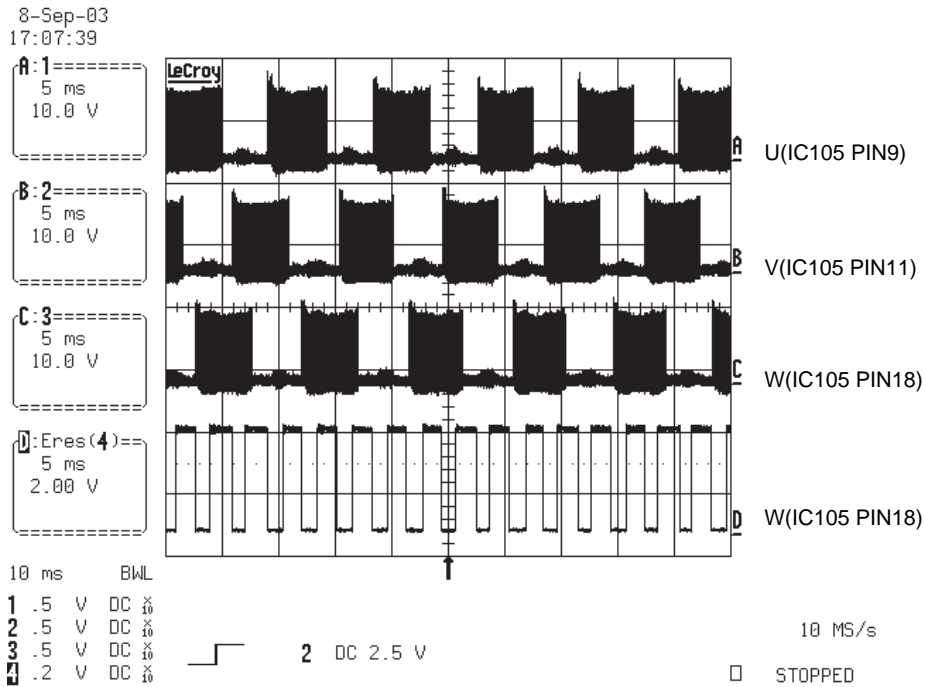
17. DISC TYPE JUDGEMENT WAVEFORM (DVDRW)



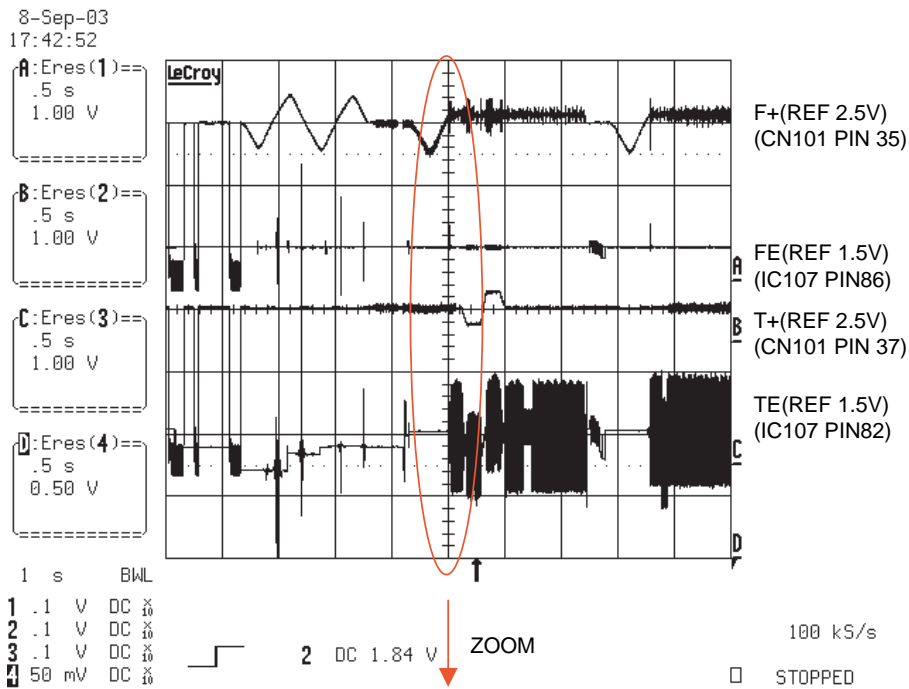
18. SPINDLE WAVEFORM1



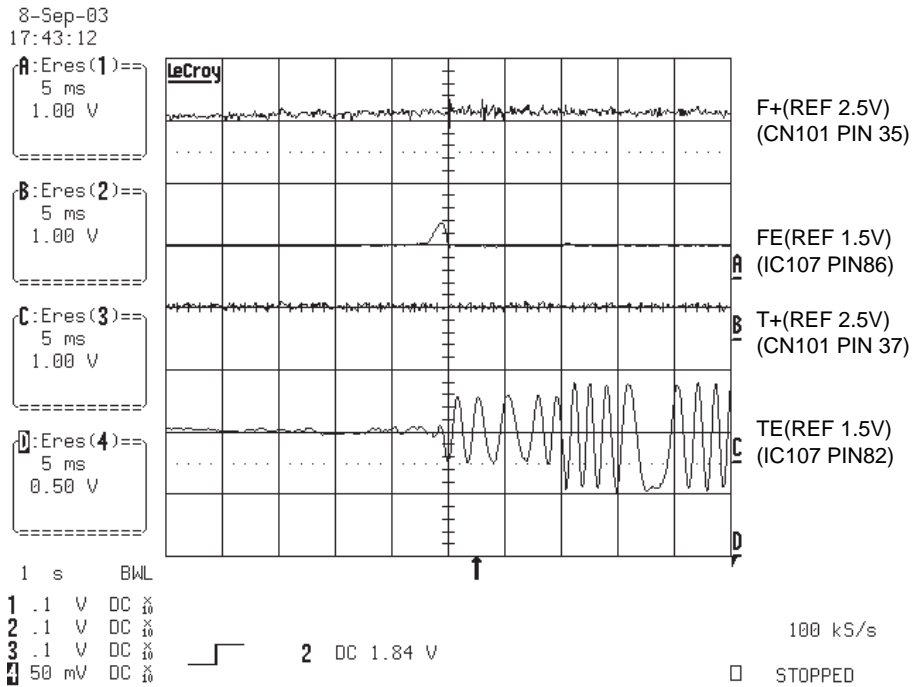
19. SPINDLE WAVEFORM2



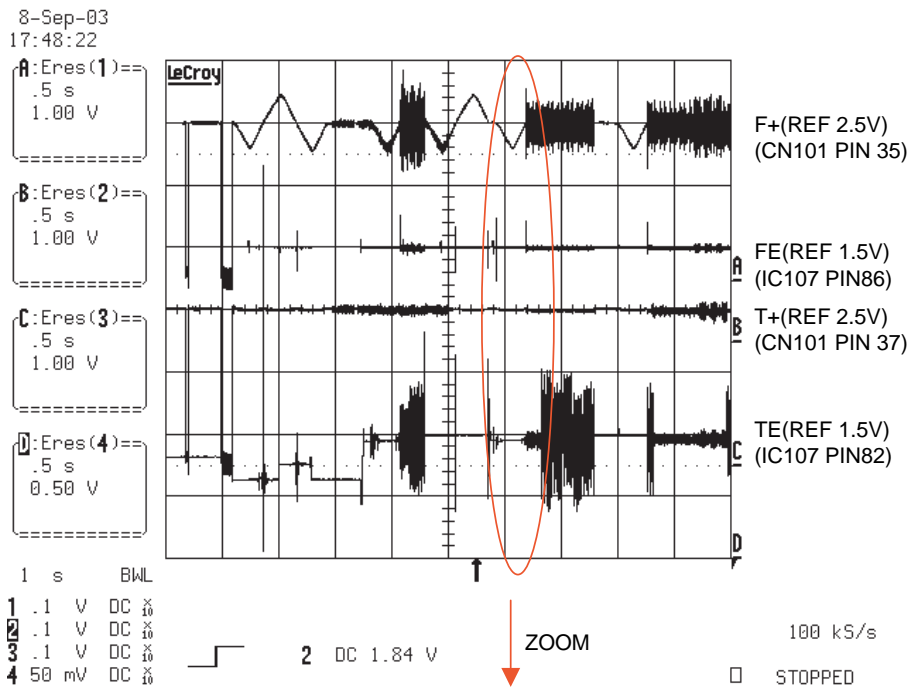
20. FOCUS ON SIGNAL(CD)



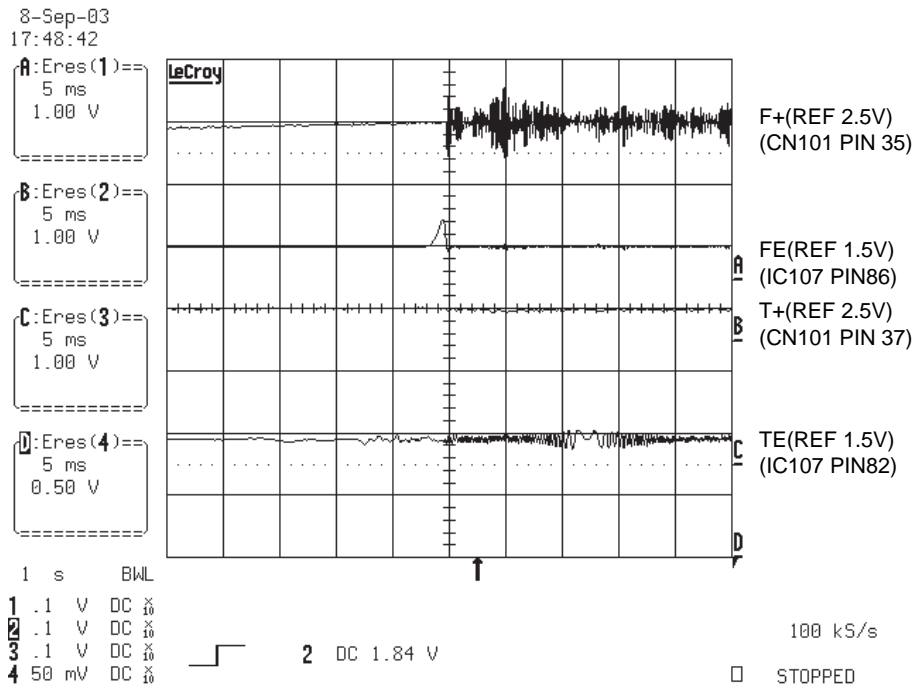
21. FOCUS ON SIGNAL(CD)



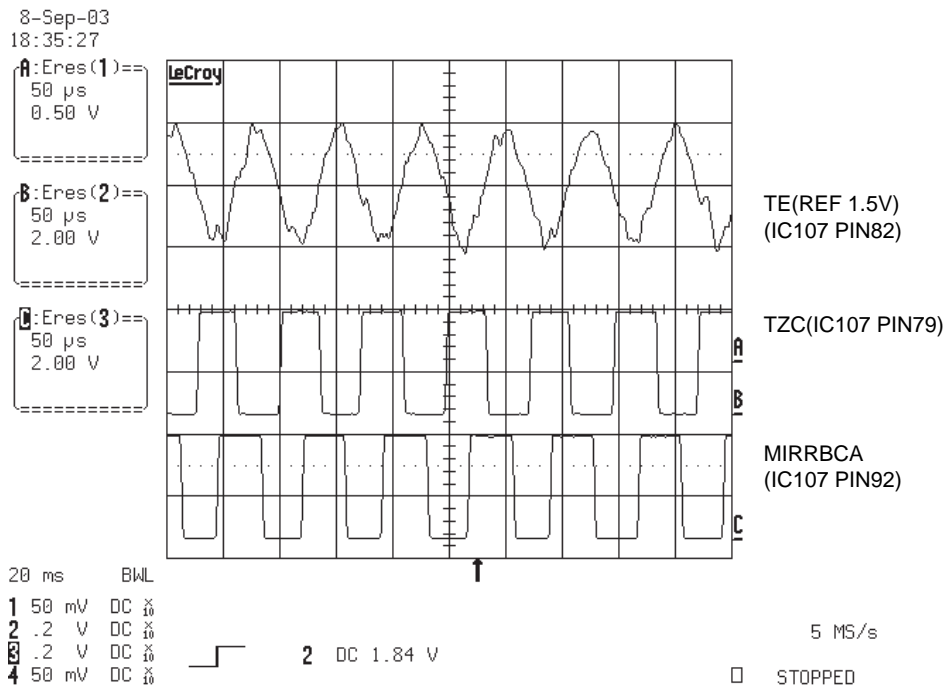
22. FOCUS ON SIGNAL(DVD)



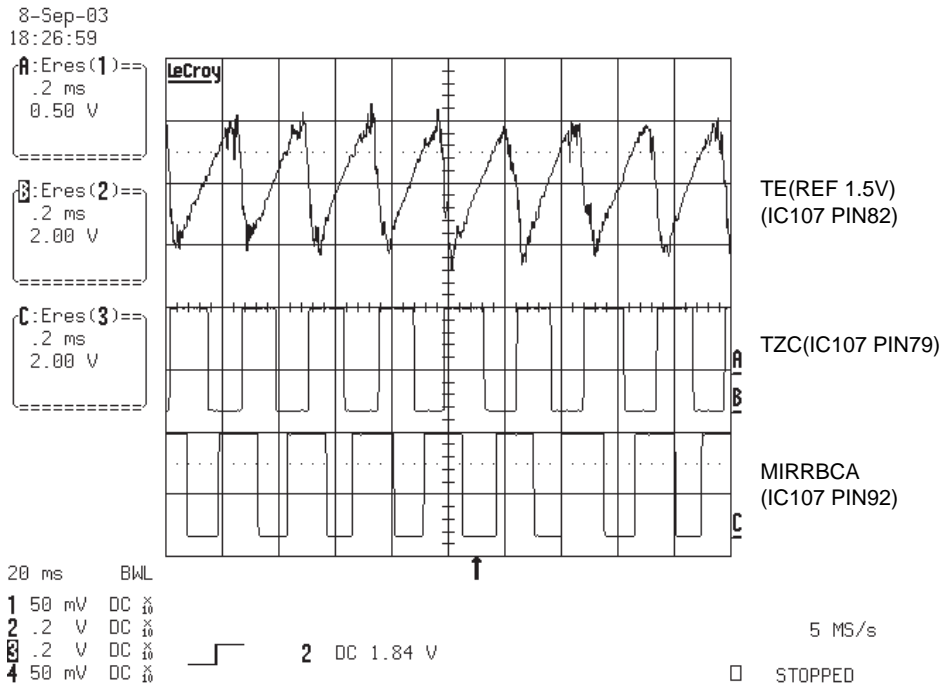
23. FOCUS ON SIGNAL (DVD)



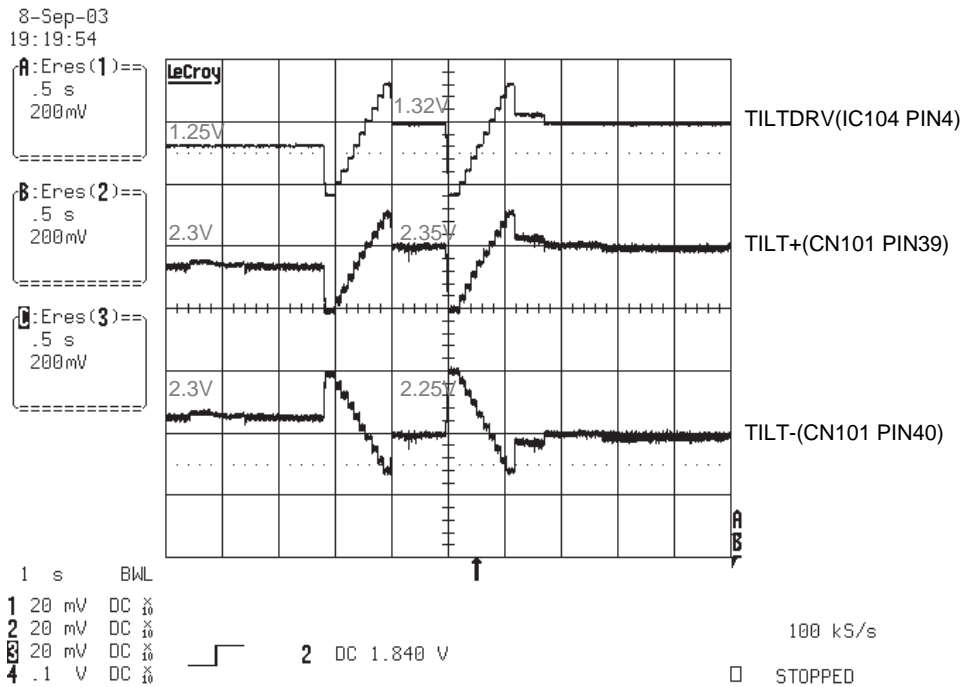
24. TRACK OFF SIGNAL(CD)



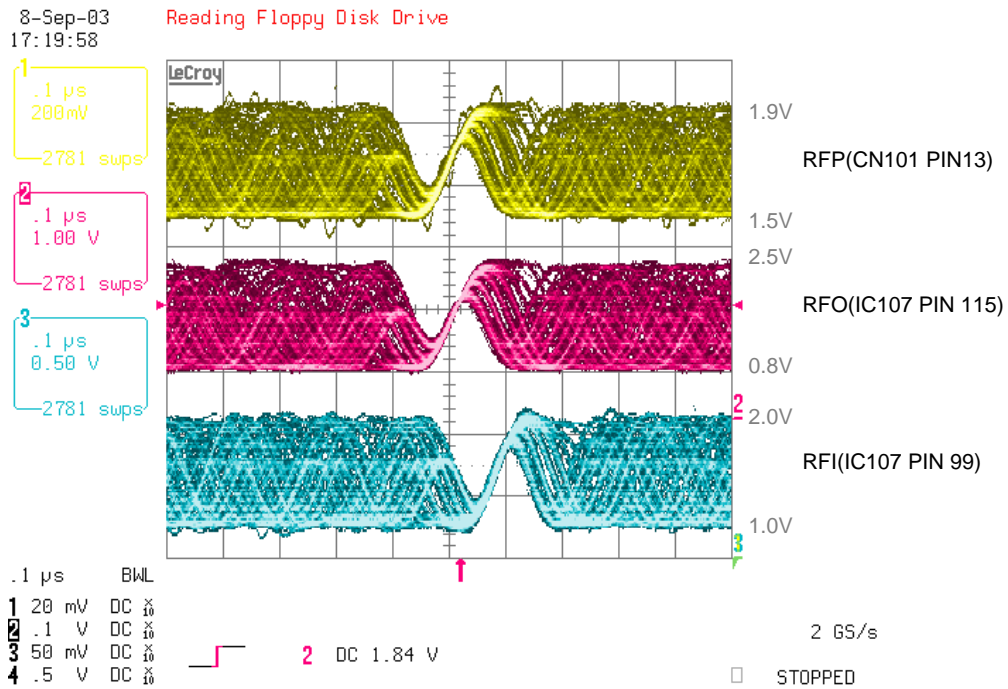
25. TRACK OFF SIGNAL(DVD)



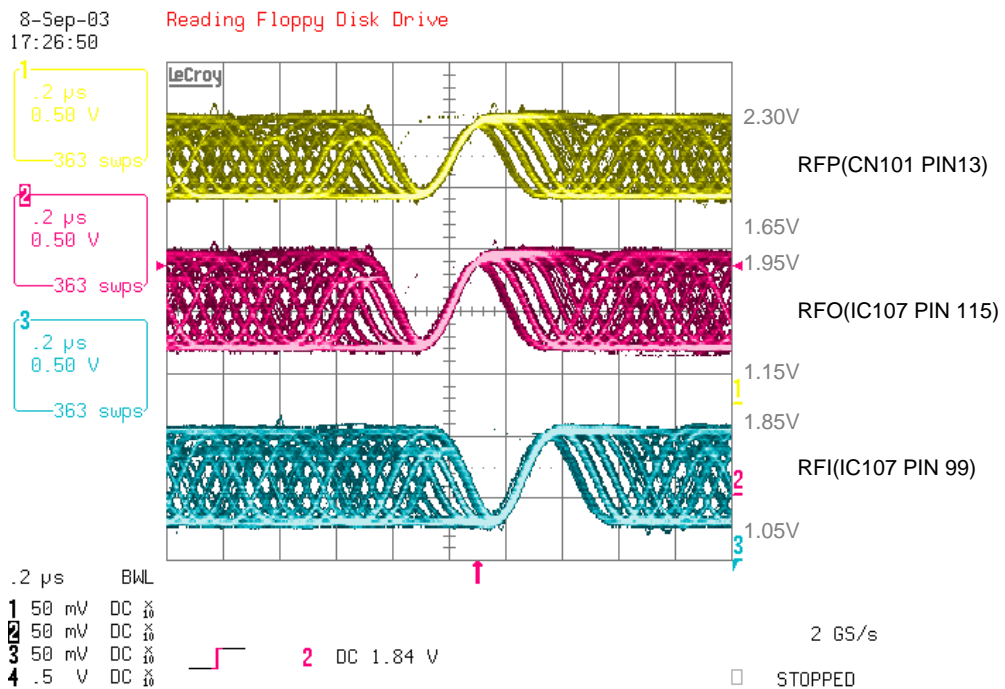
26. Tilt Driver signal(Disc reading)



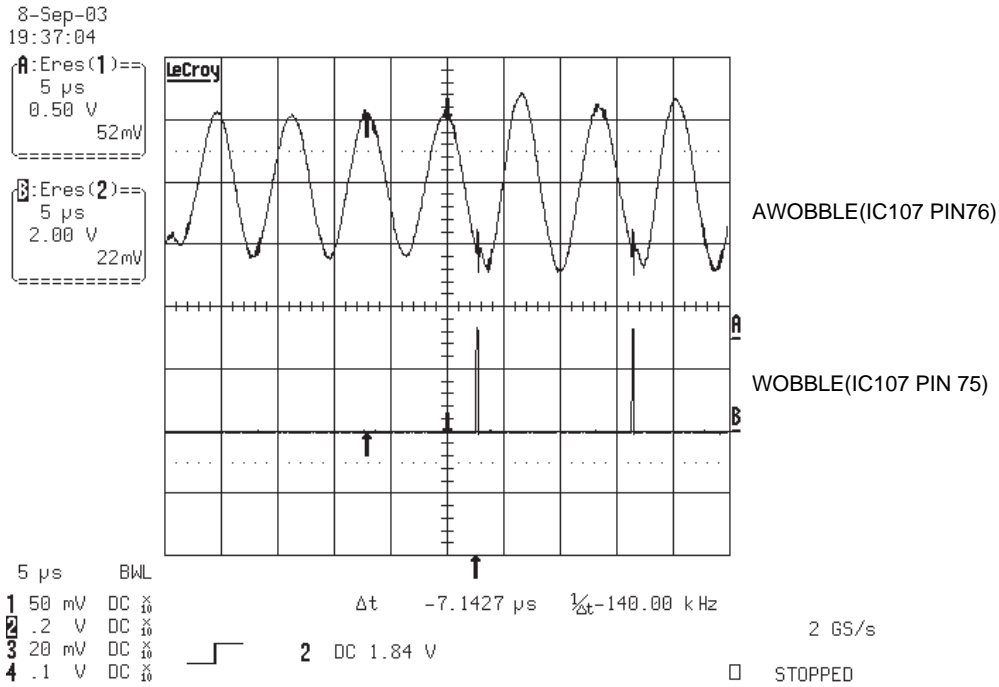
27. RF WAVEFORM(DVD)



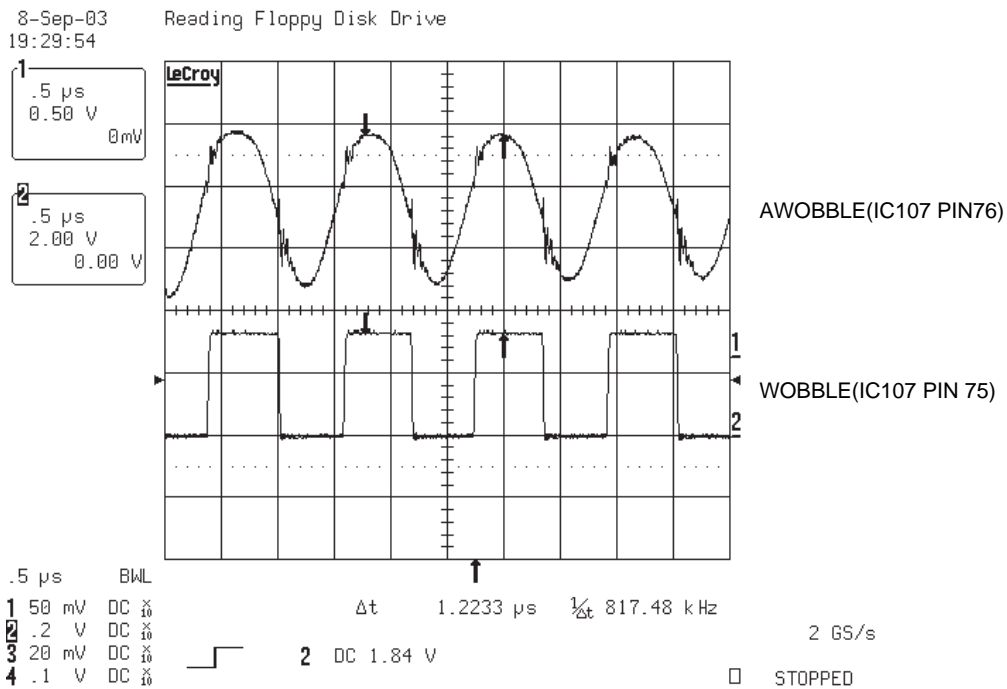
28. RF WAVEFORM(CD)



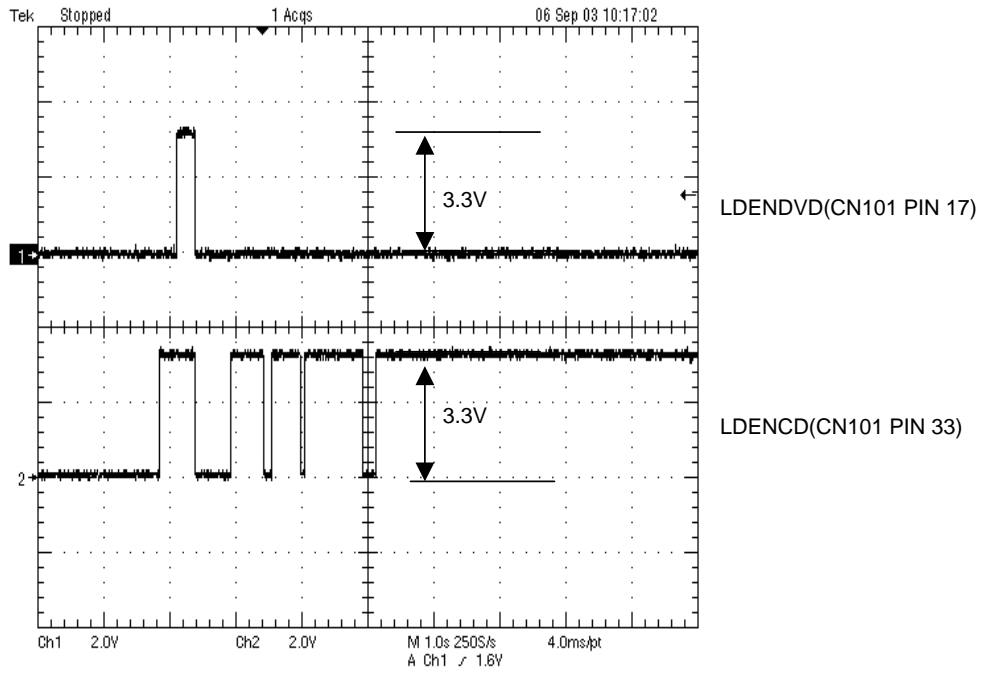
29. WOBBLE(DVD-R/RW)_READING



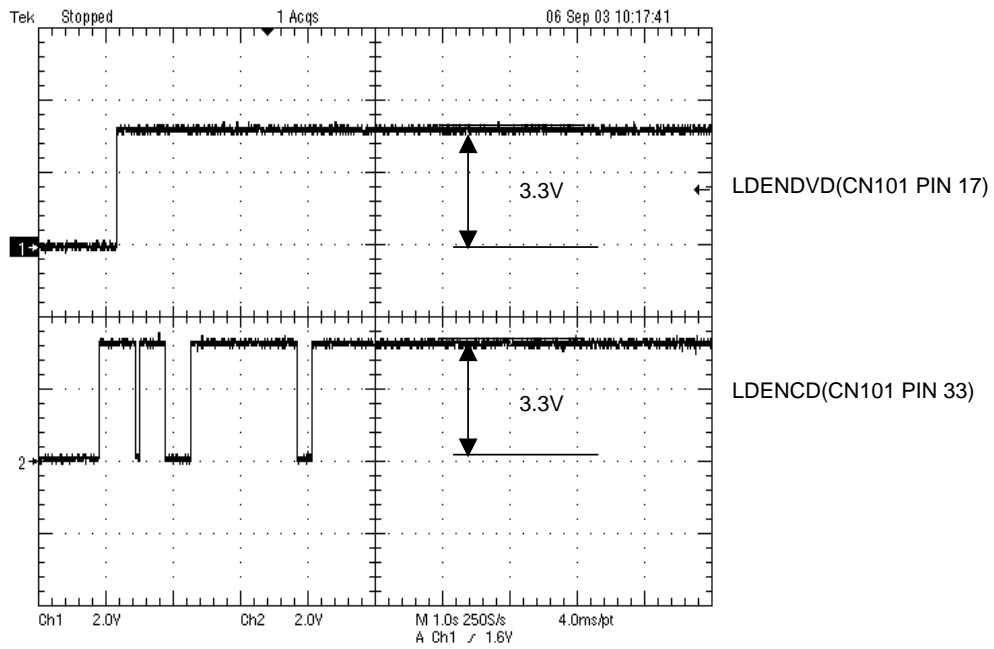
30. WOBBLE(DVD+R/RW)_READING&WRITING =>X1 SPEED



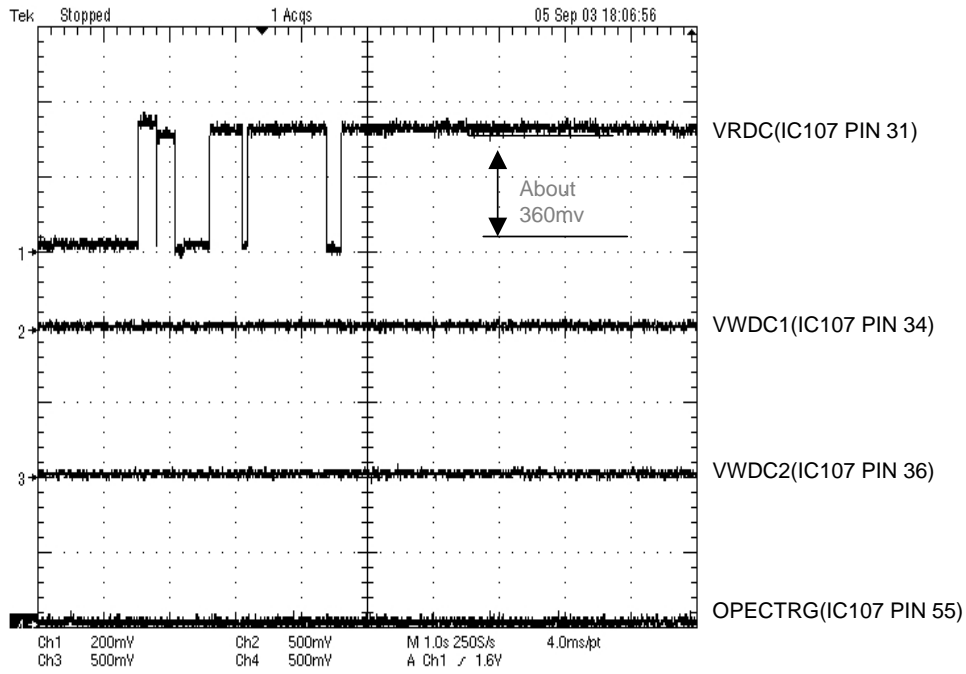
31. LD Enable(DVD)



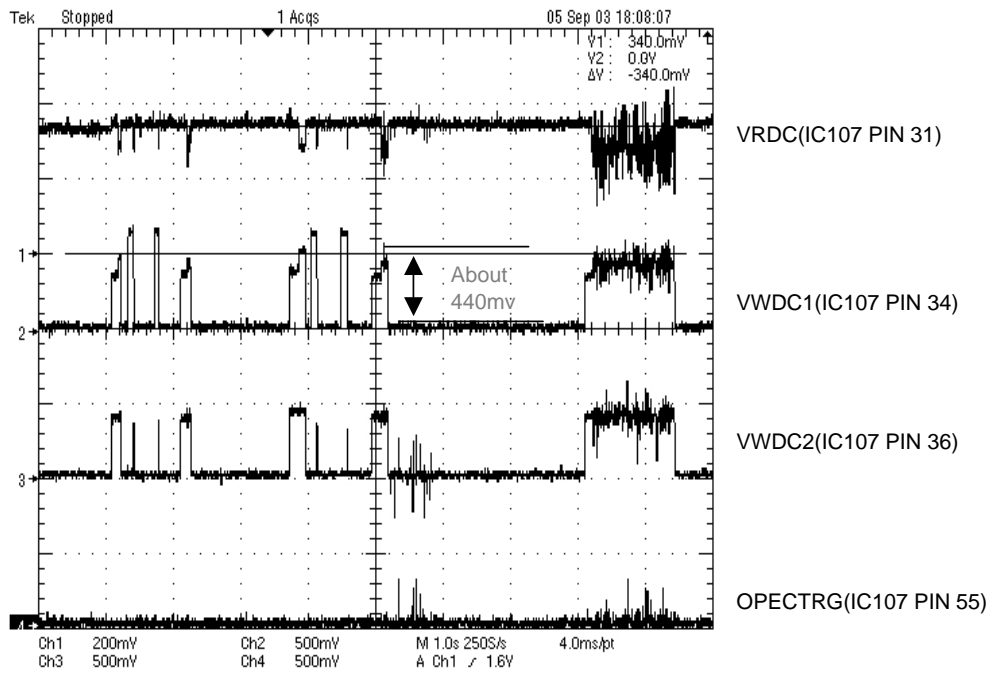
32. LD Enable(CD)



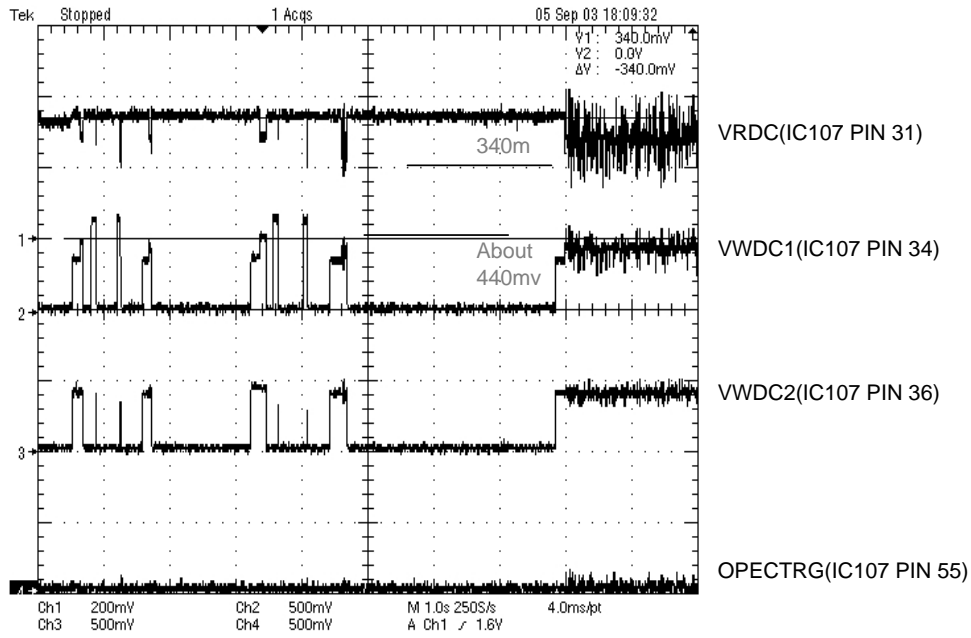
33. Laser Power(reading)_DVD+RW



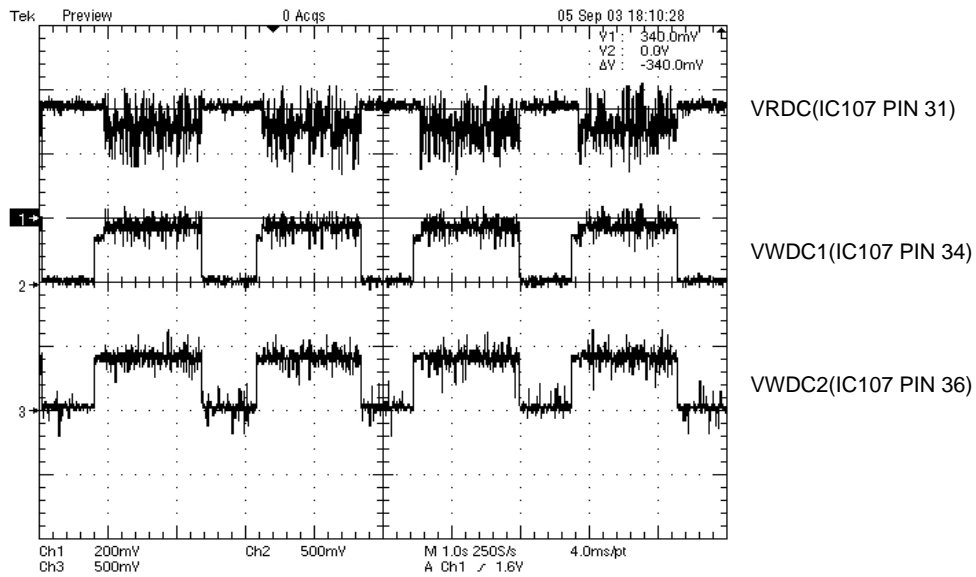
34. Laser Power(Erase)_DVD+RW



35. Laser Power(Writing)_initial state



36. Laser Power(Writing)_Processing



MEMO

A series of horizontal dotted lines for writing.