

Alignment procedure

1. Idle current (VR301, VR302).

Ensure VR301 and VR302 are set to minimum (fully counter-clockwise) before first switching on. No load should be connected and no input signal.

1.1 Connect a DVM switched to its 200mV range between TP301 and TP303.

1.2 Switch the amplifier on and, after one minute, adjust VR301 to obtain a reading of between 2 and 2.5 mV.

1.3 Transfer the DVM to TP302 and TP304 and adjust VR302 to obtain a reading of between 2 and 2.5 mV.

1.4 Leave power on for between 5 and 10 minutes to allow the idle currents to stabilize.

1.5 Re-adjust VR301 and VR302 as necessary to obtain a reading of $8 \text{ mV} \pm 2 \text{ mV}$ on each channel.

2. Common Mode rejection (VR101, VR102).

2.1 Set SW203 to BALANCED mode.

2.2 Apply a signal of 1V rms, 1kHz simultaneously to pins 2 and 3 of the Left channel balanced input jack (JK101). Pins 2 and 3 must be shorted together at the plug and the signal referenced to the ground pin (1) for this test.

2.3 Monitor the output level at the Left channel speaker terminals with an AC voltmeter (no load connected).

2.4 Adjust VR101 to produce the minimum reading on the AC voltmeter. This should be below 10mV rms.

2.5 Repeat the above procedure with the Right channel to set VR102.