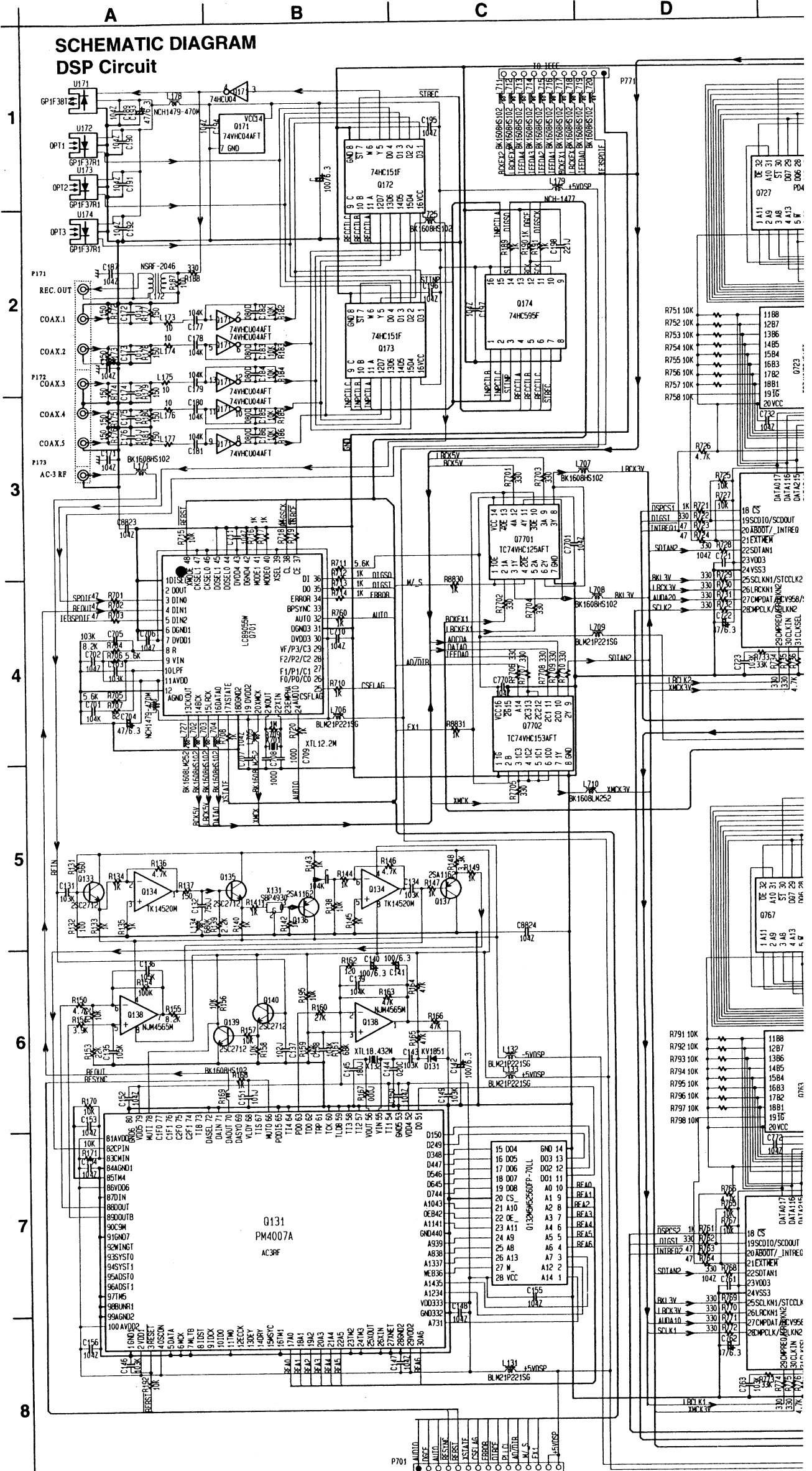
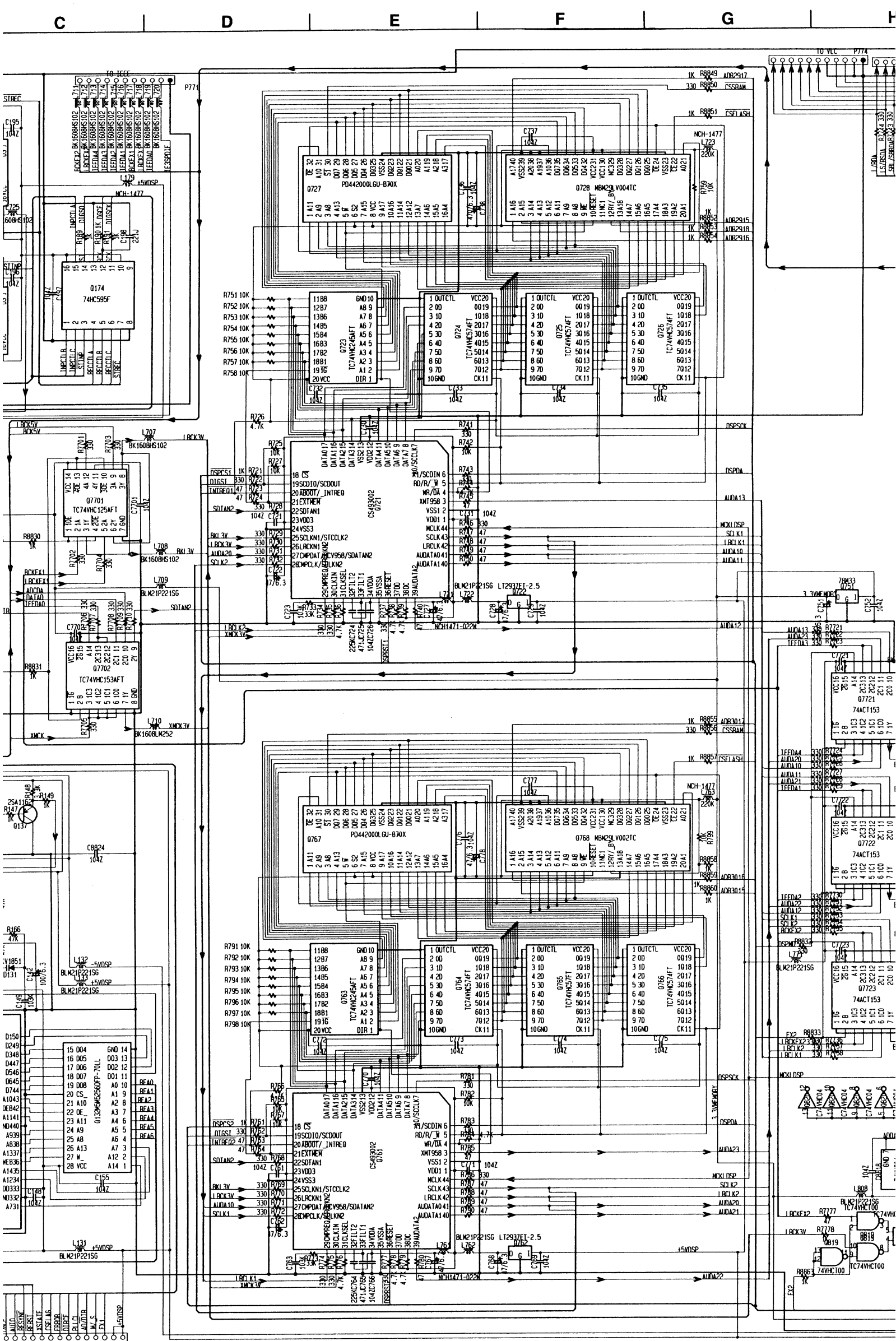


# SCHEMATIC DIAGRAM DSP Circuit





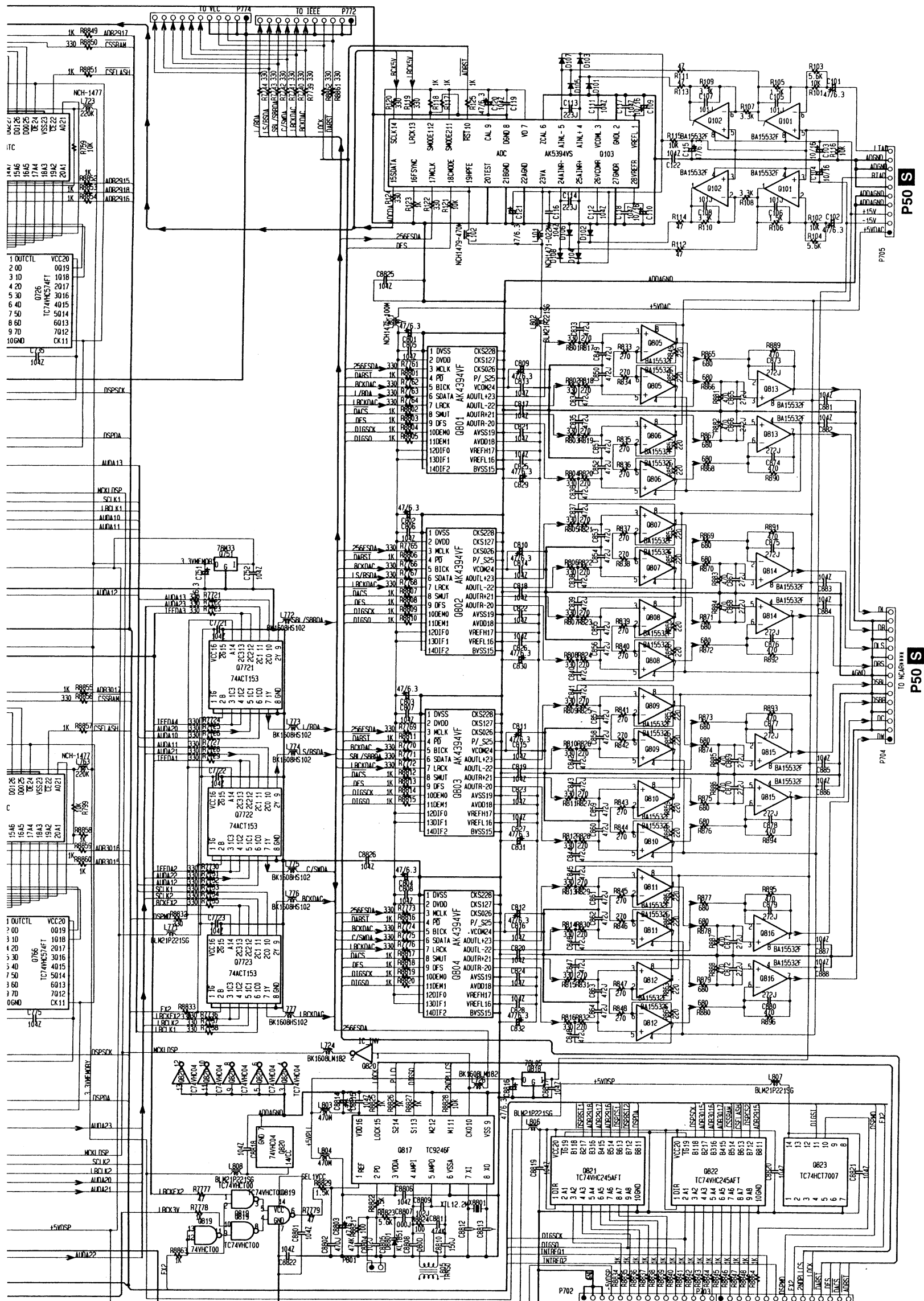
G

H

I

J

K



P50 S

P50 S

P49 S