Service Manua

Professional Digital Audio Tape Deck



SV-3900

Color

(H)...Gray Type

Area

Country Code	Area	Color
(EB)	Great Britain/ Switzerland.	4.0
(EG)	F.R. Germany/Italy/ Continental Europe.	(H)

Digital (AES/EBU type)

Input jacks: **Output jacks:** Digital (IEC TYPE II) XLR-3 type/100Ω balanced XLR-3 type/20Ω balanced

Input jack: Output jack: RCA phono type (coaxial)/75 Ω RCA phono type (coaxial)/75 Ω

Remote Control

Parallel remote:

8 pin DIN connector

Serial remote:

(43 functions available) 9 pin D-sub connector×2 (for Input and Output)

Based on ES-bus and P-2 protocols

(switchable)

*Controller SH-MK390 (option) is available for ES-bus mode

Machine number setting:

Zero thru No. 31 (available in ES-bus

mode)

Tape recording system:

SPECIFICATIONS

Sampling frequencies: For recording:

Signal Format

48 kHz/44.1 kHz

(analog/digital input) 32 kHz (digital input only) 48 kHz/44.1 kHz/32 kHz

Rotary head type DAT

No. of quantizing bits:

(selected automatically) 16-bit linear

No. of channels:

For playback:

2 (stereo)

Audio Parameters

(Recording and Playback System)

Frequency response:

For 48 kHz:

10 Hz \sim 22 kHz (\pm 0.5 dB)

For 44.1 kHz: Signal to noise ratio: 10 Hz \sim 20 kHz (\pm 0.5 dB) Greater than 92 dB, DIN audio

(dynamic range)

weighted

(22.4 Hz to 22.4 kHz bandpass)

Total harmonic

distortion:

Less than 0.05% (1 kHz, +4 dBu) Less than 0.007% (1 kHz, +22 dBu)

Wow and flutter:

Unmeasurable

General

Power supply:

AC 110 V/127 V/220 V/240 V

50/60 Hz

Power consumption:

35 W

External dimensions:

43×12.2×31.5 cm

 $(W \times H \times D)$

Weight:

6.4 ka

Input/Output Jacks

Analog

Input jacks:

XLR-3 type

Nominal input level/

+4 dBu (-18 dB rec level)/

Input impedance:

Output jacks:

 $10 \, k\Omega$ balanced

Nominal output level/

XLR-3 type

+4 dBu/-10 dBu (switch selectable)/

Output impedance:

750 balanced

Phones output: Max. 30 mW/32Ω

(matching impedance $8\sim600\Omega$)

Mechanism

Heads:

Amorphous ferrite composite type

Cylinder diameter:

30 mm 2000 r.p.m.

Cylinder rotation speed:

(recording and playback)

Tape speed:

8.15 mm/sec., 12.225 mm/sec.

(selected automatically)

Search speed:

Up to 250 times normal playback

FF/Rewind speed:

Up to 400 times nomal playback

speed

FF/Rewind time:

Approx. 27 sec. (2 hours DAT tape)

Specifications are subject to change without notice.

Weight and dimensions are approximate.

Panasonic

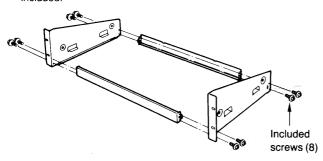
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4	ROLS 4, 5
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9~	NANCE CHART 9~14
15~	RUCTIONS 15~19
	D ADJUSTMENTS 19~2!
26~	N OF IC'S 26~30
3 7 ~	

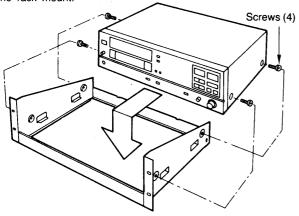
■ INSTALLATION

Installation of rack mount kit

 Assemble the rack ears and panel, and tighten with screws included.



2. Remove the 4 screws on side enclosure, and attach the unit to the rack mount.



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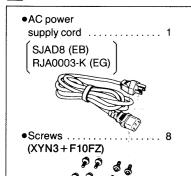
Notes on placement

- This unit is a precision instrument. Be sure to place it on a flat surface, or mounted level in a rack.
- Avoid places such as the following:
- Near any equipment or device that generates strong magnetic field.
- On any heat-generating equipment or device, or in any place where the temperature is high (35°C/95°F or higher).
- •Extremely cold locations (5°C/41°F or below).
- •Near an AM/FM tuner or TV.

(It may produce noise in the broadcast audio, or disturb the ${\sf TV}$ picture.)

- •For long periods of time in direct sunlight.
- •In dusty or smoky locations.
- •In locations prone to vibrations.
- In locations where the rear panel is less than 10 cm (about 4") away from the wall or back of an audio rack.
- Do not place heavy objects, other than system components, on top of the unit.
- When carrying or storing the unit, handle it with care and do not subject to any strong bumps.
- To avoid problems due to vibration:
- •Do not place a book or similar object under this unit.
- •Do not route the connection cables (of this or other units) across the operation panel, across the top, or under the unit.

ACCESSORIES

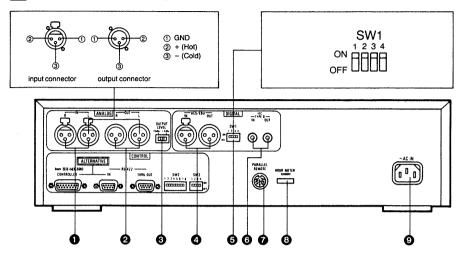








CONNECTIONS



ANALOG IN terminals

These are balanced analog audio XLR input connectors.

2 ANALOG OUT terminals

These are balanced analog audio XLR output connectors.

- OUTPUT LEVEL selector (+4 dBu/-10 dBu) Select the nominal output level corresponding to a peak level meter display of -18 dB.
- AES/EBU terminals (IN/OUT)

These terminals are for input or output of signals via the professional digital interface.

6 DIP switch settings (SW1)

These are used for the following settings:

- Digital Input Selection (IEC TYPE II or AES/EBU)
- •SCMS ID 6 Status for AES/EBU Input
- Blank Skip Mode in Program Search Mode
 (For further details, see page 6.)

(iEC TYPE II (IEC 958) jacks (IN/OUT)

These terminals are for input or output of digital signals from/to a consumer unit.

- REMOTE CONTROL jack (PARALLEL)
 See page 28.
- (3) Hour meter (5000H)

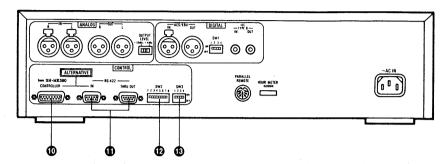
Shows the number of hours of head usage in recording and playback.

AC IN jack

Connect to the grounded AC outlet with the AC power supply cord included.

SETTING THE MACHINE NUMBER

Rear panel



Dedicated remote controller connector

This is a 15-pin serial connector used to connect an optional dedicated remote controller (SH-MK390).

Bus (RS-422) connectors

These two 9-pin connectors are used to connect the deck to a serial bus, or a computer.

Connect it using only an RS-422 type cable.

Note:

This connector cannot be used to simultaneously connect the deck to a dedicated remote controller and a computer.

DIP switch settings (SW2)

These are used for the following settings:

- Machine Number Selection
- Device Type Selection
- Test Mode Selection
- (For further details, see page 6.)

® DIP switch settings (SW3)

- These are used for the following settings:
 - Maximum Search Speed Selection
 Protocol Selection
 - Data Transmission Speed Setting

(For further details, see page 6.)

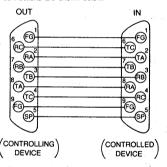
ES Bus

This format was developed jointly by the EBU (European Broad-casting Union) and the SMPTE (Society of Motion Picture & Television Engineers) for remote control of recording, production and broadcasting equipment.

The controller sends control messages along a serial bus, hence the cable connection is simple. This enables various equipment connected to the bus to be selected and controlled individually or collectively.

ES bus, Messages specific to a DAT (commands and responses) are not yet standardized. Hence the deck transmits messages in an escape mode in which messages specific to the deck can be defined. The mechanical and electrical interfaces of this deck are based on RS-422 D sub connector.

The pin connections are shown below.



CONNECTOR PIN ASSIGNMENT

FG: Frame ground RA: Receive "A" RC: Receive-signal

TB: Transmit "B"

common

TC: Transmit-signal common

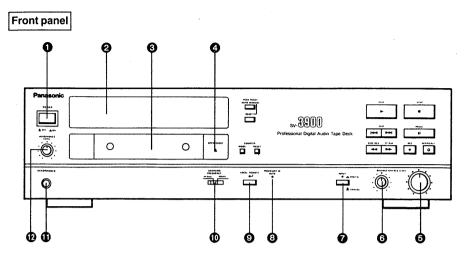
RB: Receive "B" TA: Transmit "A"

SP: Spare

P-2 Serial

P-2 is an abbreviation of Protocol-2, which is a serial transmission protocol employing a 9-pin D sub connector. It is at present widely used as a control format between a VTR and a controller. In this format, one VTR is connected to a single control input/output connector (9-pin D sub connector) of the controller. ES bus and P-2 protocol can be selected by means of a DIP switch.

LOCATION OF CONTROLS



- POWER switch (POWER)
- 2 Display panel See page 5.
- Cassette holder
- ② OPEN/CLOSE button (▲ OPEN/CLOSE)
- 6 REC LEVEL control (REC LEVEL) Use to adjust the recording level of both left and right channels
- 6 REC BALANCE control (BALANCE)
 Use to adjust recording balance between left and right
- 7 INPUT selector button/indicator (INPUT)
 Use to select digital or analog recording input.
- PNO/START ID AUTO indicator (PNO/START ID AUTO)

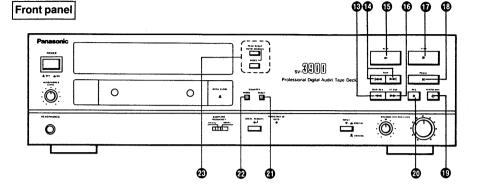
Indicates that the unit is set in the automatic mode for recording Program Number and Start ID.

The default mode on power up is set to off (manual recording mode).

Q LOCAL/REMOTE button (LOCAL/REMOTE) Use to select local or remote operating mode. The default setting on power up is the remote mode. SAMPLING FREQUENCY selector

Select the sampling frequency when making analog recordings (44.1 kHz or 48 kHz).

- Phones jack (HEADPHONES)
 A '4" connector for connecting standard stereo headphones.
- Phone level control (HEADPHONES LEVEL)
 Use this control to adjust output level to the headphones.



REW/REV button (◄◄ REW/REV)

Use to rewind the tape, or for audible high-speed search in play mode (review).

1 SKIP buttons (Idd • ▶ ▶ SKIP)

Use the skip buttons to advance to the desired program. The ▶▶ button skips the program forward by one Start ID. The ◄ button skips the program backward by one Start ID. Repeated pressing of the Skip buttons causes the unit to skip forwards or backwards several IDs.

- PLAY button/indicator (► PLAY)
 Use to initiate recording or playback mode.
 Also used to record program numbers manually.
- FF/CUE button (►► FF/CUE)
 Use to advance the tape rapidly, or for audible high-speed
- search in play mode (cue).

 (7) STOP button (STOP)
- Use to stop all functions.

 This button also clears the program memory.
- PAUSE button/indicator (II PAUSE)
 Use to temporarily interrupt playback or recording mode.
- **②** AUTO REC MUTE button (○ AUTO REC MUTE)

Use to automatically insert a silent passage approximately four seconds long during a recording.

- ② RECORD button/indicator (● REC)
 Use to place the unit in record standby mode.
- COUNTER RESET button (COUNTER RESET)
 Use to reset the tape counter to "0000" (when the display mode is set to tape counter).
- COUNTER MODE button (COUNTER MODE)
 Use to select the desired counter mode, between Absolute time, Program time, Time Remaining, TOC and Tape Counter.
- PEAK RESET buttons (PEAK RESET AUTO/MANUAL/RESET)

The AUTO/MANUAL button is used to select the peak hold reset mode, auto or manual.

AUTO: The peak level of the source is held and displayed for approximately one second before being extinguished.

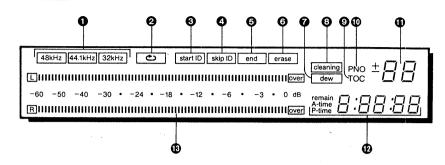
MANUAL: The peak level of the source is held and displayed continuously until the RESET button is pressed.

The RESET button is used to reset the peak hold display in manual mode.

The default setting on power up is auto mode.

Display panel

S



 Sampling frequency indicators
 Displays sampling frequency of digital signals during recording or playback Changes automatically depending on input or output signal.

2 Repeat indicator
This indicator is used for repeat loop-play function. Indicates that all programs or memorized programs can be played back repeatedly ().

Start ID indicator licates that a Start ID is being or has been recorded.

4 skip ID indicator

hat a Skip ID is being or has been recorded.

6 end indicator s that an End Mark is being or has been recorded.

@ erase indicator

licates that a Start ID or Skip ID is being erased.

dew indicator es the formation of dew within the unit.

3 cleaning indicator Indicates that the DAT head assembly needs cleaning. (See page 8, 10.) TOC (table of contents) indicator

Indicates the total program count and tape length of a commercial tape onto which the TOC information has been

PNO (program number) indicator

Program number display

Displays either the TOC information (when TOC indicator appears) or the number of the program (when PNO indicator

(2) Counter display panel

The following are displayed according to the setting of the counter mode button:

(This display is the default setting when power is first

Program time (elapsed time since the last Start ID was encountered)

3) Remaining time (approximate time, in hours and minutes

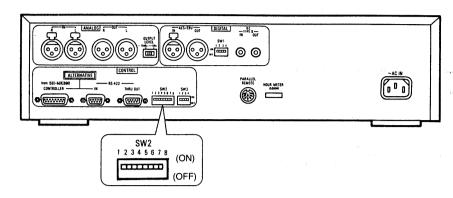
to the end of the DAT cassette)
4) TOC information

Peak level meter

Recording and playback levels are indicated by a bar graph. Standard I/O level of +4/-10 dBu is shown at -18 dB.

SETTING THE MACHINE NUMBER

Rear panel



For Network Control via ES Bus, the machine number of this deck can be set by using rear-panel DIP switch 2. The machine number is required to identify each deck when the

RS-422 connectors of several SV-3900s are connected serially. and selected deck needs to be controlled in the ES bus mode. By using the optional SH-MK390 remote controller, a number of decks can be controlled via the ES bus.

Set the machine number using DIP switches 2-1 thru 2-5 while referring to the table at right.

CAUTION!

Do not specify the same machine number as that of another deck already connected to serial controller.

Switch OFF the power before setting the machine number. If you set the machine number with the power ON, switch the power OFF then ON again to reset the machine value.

Checking the machine number

The SV-3900's machine number will appear on the counter display panel as shown below when either the cassette holder is opened,

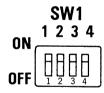


(UP=ON=1, DOWN=OFF=0)

A4	DIP SW2 settings						
Machine No.	1	2	3	4	5		
00	0	0	0	0	0		
01	1	0	0	0	0		
02	0	1	0	0	0		
03	1 .	1	0	0	0		
04	0.	0	1	0	0		
05	1	0	1	0	0		
06	. 0 ,	1	1	0	0		
07	1	1	1	0	0		
08	0	0	0	1	0		
09	. 1	0	0	1	0		
10	0	1	0	- 1	0		
11	1	1	0	1	0		
12	0	0	1	1	0		
13	1	0	1	1	0		
14	0	1	1	1	0		
15	1	1	1	1	0		
16	0	0	0	0	1		
. 17	1.	0	0	0	1		
18	0	1	0	0	1		
19	1	1	0	0	1		
20	0	0	1	0	1		
21	1	0	1	0	1		
22	0	1	1	0	1		
23	1	1	1	0	1		
24	0	0	0	1	1		
25	1	0	0	1	1		
26	0	1	0	1	1		
27	1	1	0	1	1		
28	0	0	1	1	1		
29	1	0	1	1	1		
30	0	1	1	1	1		
31	-1	1	1	1	1		

■ DIP SWITCH SETTINGS

DIP Switches on the Rear Panel



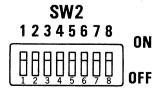
The following settings can be made by using DIP switch 1.

	Switch Setting	Function					
1	Digital Input/Output Selection	O	N=IEC	958	OFF=AES/EBU		
2	SCMS ID6 Status for AES/EBU Input	SW2 OFF ON ON	SW3 — OFF ON	ID 00 10 11	Meaning Copy Free No further copies One copy allowed		
4	Blank Skip Mode in Program Search	selec appro	900 will tion dur	adva	OFF=0 ince to the next frogram Play mode, it is seconds of silence		

Note

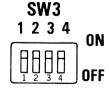
6

IEC 958 is shown as IEC TYPE II on the rear panel of SV-3900



The following settings can be made by using DIP switch 2.

	Switch Setting		Function							
1 2 3 4 5	Machine Number		For details on how to set the made number, see page 10.							
6 7	Device Type Setting	SW6 OFF OFF ON	SW7 OFF ON ON	Device SV-3900 MII VCR SONY VCR						
8	Test Mode	OFF=N ON=Te	ormal mode st mode	9						



The following Settings can be made by using DIP switch 3.

Switch Setting			Function							
Maximum Search Speed		OFF=Max. 250 times search speed possible ON=Max. 400 times search speed possible								
2	Serial Protocol	OFF=ES	BUS							
3 4	Data Transmission Speed	SW3 OFF ON OFF ON	SW4 OFF OFF ON ON	Transmission speed 38.4 kbps 9600 bps 4800 bps 2400 bps						

SCMS Copy Protection

The Serial Copy Management System (SCMS) is designed to The Serial Copy Management System (SCMS) is designed to control digital-to-digital DAT copying that a consumer can perform on various digital audio material, including Compact Discs, pre-recorded DAT tapes, and other material via IEC 958 "consumer use" digital audio interfaces (also commonly referred to as "S/P DIF" and EIAJ CP-340). SCMS does not affect the ability to make copies using a DAT recorder's analog inputs and outputs, nor does it apply to digital-to-digital copies made via the AES/EBU professional digital audio interface.

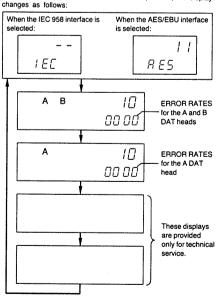
Only one serial (or generational) digital-to-digital copy can be made from a copyright protected source, such as a commercial CD or pre-recorded DAT. Serial Copy Management System labels are generated onto tape each time an SCMS-equipped consumer DAT machine records an audio signal to tape, no matter whether the source is from the analog or digital inputs.

Technically, the SCMS labels for DAT are encoded into the SubCode sections of the main digital data area, specifically "ID6". The SV-3900's rear-panel DIP switches allow the user to preset the SCMS status bits of ID6 onto the DAT recording. For legal reasons, the ID6 bits are only modified if the source input is the AES/EBU Digital I/O; during digital copying via the IEC "consumer use" input, the SV-3900 simply copies across the current status of SCMS codes indicated by the Channel Status. To prevent a master DAT tape being copied on a consumer deck, for example, the user might select switch setting for "ID6=10", which would prevent any further digital copying of this copyrighted cassette via a consumer DAT. Alternatively, you might select "ID6=00", so that any number of digital copies could be made on a consumer deck. Selection of "IDE=11" will designate a final copyright protected pre-recorded product, which would behave the same way on consumer SCMS-equipped DAT decks as a normal commercial release.

DISPLAYING SCMS STATUS AND ERROR RATES

The SCMS status of a DAT recording can be displayed on the SV-3900 by simultaneously pressing the front-panel COUNTER MODE, RESET and PAUSE buttons. Now shown within the normal Time Display window will be the type of digital interface that has been selected via the rear-panel DIP switch—AES/EBU or IEC 958 "consumer use" -- while the PNO display now shows the ID6 value of the recording: 00, 10 or 11. If the COUNTER MODE button is pressed for a second time, the SV-3900 will display ERROR RATES; the mode can be cancelled by hitting the RESET button.

Each time the COUNTER MODE button is pressed, the display changes as follows:

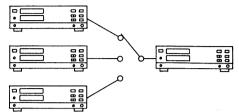


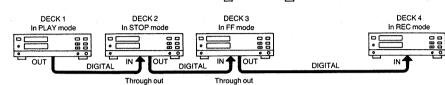
The display scheme described above will not be shown, when the

■ DIGITAL THROUGH PUT FUNCTION

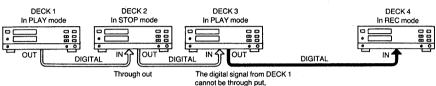
A special function provided on the SV-3900 Pro-DAT enables up to five machines to be connected together in a serial mode, whereby the digital output from any single machine on the network can be recorded onto a master SV-3900. In this way, a network array can be set up for digital dubbing and insert-editing between several replay DATs and a master recorder. For such applications as automated replay for broadcast or permanent-sound systems, a series of SV-3900s can be controlled from an external computer or SH-MK390 Remote Controller, to form a multi-machine replay system with a common digital or analog output. The digital input to any machine in the network that has been set to Digital Through Put mode will be automatically routed to its digital output. In replay mode, however, the selected SV-3900 will output directly onto the digital buss, and hence to the master or replay unit.

The attached figure shows a typical set up. Machine #1 is connected via its AES/EBU digital output to the AES/EBU input of machine #2, and so. The last machine on the network, designated here as Machine #4, is set to Record Standby mode, with its input also set to AES/EBU digital I/O. The output from any low-numbered machine will be overridden by a machine that is connected between it and the master SV-3900 deck. This function will not work unless the decks are connected digitally.





If, however, deck 3 is put in the PLAY mode, as shown in the figure, the playback signals from deck 3 will be input to deck 4



because DECK 3 is in play mode

- The digital through put function is not activated when the SV-3900 is first powered up.

 When selecting the digital through put function, use the AES/EBU
- ports to connect digital signals. A maximum of five decks can be connected serially in this way

To initiate the Digital Through Put **Function**

Open the cassette drawer (select Local mode if necessary).

controls. If the Play button is pressed, however, the output of the selected SV-3900 will pass directly via the Network Digital Buss to the master machine.

2 Press the RECORD button. The Sampling Frequency indicator will now flash, indicating the Digital Through Put mode has been established. The SV-3900 will continue to respond normally to all transport

•Use this digital through put function when using the SH-MK390 optional remote controller to perform program playback and program editing between a number of decks.

To cancel the Digital Through Put **Function**

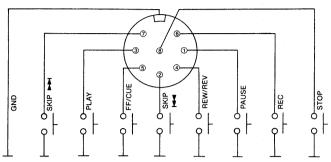
- Open the cassette drawer (select Local mode if necessary).
- 2 Press the AUTO REC MUTE button. The Sampling Frequency indicator will now cease to flash, indicating the Digital Through Put mode has been cancelled.

REMOTE CONTROL

This unit has three systems for remote control operation, using the optional SH-MK390 remote controller, the RS-422 serial remote control, and the 8-pin parallel input terminal.

Parallel Input Terminals

By connecting the following circuits, the SV-3900 can be operated by remote control using normal switches. This remote control operation functions in the Local mode only.



Expansion function

Connected via the Parallel Input Terminal to computer, the signals shown in the table below can be used to control the following 43

FUNCTION	DIN Jack Number							_	П	FUNCTION		DIN Jack Number								
FUNCTION	3	2	5	8	7	4	6	1	Ц	FONCTION	3	2	5	8	7	4	6	1		
PAUSE	1	1	1	1	1	1	1	0		1	0	0	0	1	0	0	0	0		
REC	1	1	1	1	1	1	0	1		2	0	0	0	1	0	0	0	1		
REW	1	1	1	1	1	0	1	1	Ц	3	0	0	0	1	0	0	1	0		
FWD SKIP	1	1.	1	1	0	1	1	1	Ц	4	0	0	0	1	0	0	1	1		
STOP/CLEAR	1	1	1	0	1	1	1	1	Ц	5	0	0	0	1	0	1	0	0		
FF	1	1	0	1	1	1	1	1		6	0	0	0	1	0	1	0	1		
REV SKIP	1	0	1	1	1	1	1	1		7	0	0	0	1	0	1	1	0		
PLAY	0	1	1	1	1	1	1	1	L	8	0	0	0	1	0	1	1	1		
(NO. KEY)	1	1	1	1	1	1	1	1		9	0	0	0	1	1	0	0	0		
OPEN/CLOSE	0	0	0	0	0	0	0	1	Ц	0	0	0	0				0	1		
AUTO REC MUTE	0	0	0	0	0	0	1	0	L	ID-ERASE	0	0	1	0	0	0	0	0		
FADE IN	0	0	0	0	0	0	1	1	L	RENUMBER	0	0	1	0	0	0	0	1		
FADE OUT	0	0	0	0	0	1	0	0	L	SKIP PLAY/OFF	0	0	1	0	0	0	1	0		
COUNTER RESET	0	0	0	0	0	1	0	1	L	AUTO PNO/OFF	0	0	1	0	0	0	1	1		
COUNTER MODE	0	0	0	0	0	1	1	0		MUSIC SCAN	0	0	1	0	0	1	0	0		
MEMORY	0	0	0	0	1	0	0	0	L	DIRECT REC PAUSE	0	0	1	0	0	1	0	1		
END SEARCH	0	0	0	0	1	0	1	0		DIRECT REC PLAY	0	0	1	0	0	1	1	0		
REPEAT	0	0	0	0	1	0	1	1		UNLOAD	0	0	1	0	0	1	1	1		
START-ID	0	0	0	0	1	1	0	0		ST-ID WRITE	0	0	1	0	1	0	0	0		
SKIP-ID	0	0	0	0	1	1	0	1		SK-ID WRITE	0	0	1	0	1	0	0	1		
END-ID	0	0	0	0	1	1	1	0		ST-ID ERASE	0	0	1	0	1	0	1	0		
ID-WRITE	0	0	0	0	1	1	1	1		SK-ID ERASE	0	0	1	0	1	0	1	1		

(ON=0, OFF=1)

(ON=0, OFF=1)

OPERATION NOTES

Serial remote control using RS-422

ports

 α

You can transmit messages (commands and responses) to and from the SV-3900 via the RS-422 interface (9-pin D-sub connec-

tor). Message transfer is based on switchable ES bus or P2 protocols; see page 6 for more details.

Messages specific to a DAT (commands and responses) have not yet been standardized. By developing suitable software to run on a conventional PC or terminal, it is possible to create an automatic system that can control the SV-3900 in a variety of applications

Condensation

In winter, moisture droplets may form on the window panes of a heated room. This phenomenon is called condensation. Condensation may occur with this unit or with cassette tape in the following in a room where a heater has just been turned on

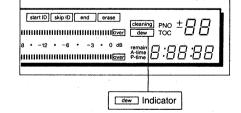
- in a room where steam or excessive moisture are present
 when the unit is brought in suddenly from a cold place to a warm
- when the unit is directly in the path of cold air from an air

In such cases, the unit will not operate correctly, and even if it does operate, the tape may be damaged. In particular, if moisture condenses on the rotating heads, the tape

will stick. This will prevent the tape from running properly, and could damage it.

If Condensation Occurs

condensation occurs, the am indicator on the display panel flashes. While the am indicator is flashing, apart from the OPEN/CLOSE function, buttons on the unit will not operate



If a cassette tane is loaded in the unit, remove it and leave the power switched on. Wait until the indicator goes out. The time required for moisture to evaporate will vary according to the environment, but it may be 60 minutes or longer if humidity is high. Condensation usually sets in gradually, so the indicator may not flash for the first 10~15 minutes. If room temperature or humidity changes abruptly, therefore, wait for about 20 minutes before switching on power to check that the indicator is not flashing.

Unloading Function

If the unit is in the stop, pause or record standby modes for more than 12 hours the cassette tape will be released automatically from the rotating head mechanism

Operating the Unit

Always keep the cassette holder closed. If it is left open, dust will accumulate in the med tape playback or recording performance to deteriorate. Do not touch the inside of the cassette holder

- If the unit is not used for extended periods, remove the cassette tape.
- The top of the deck will become hot during use. but this is perfectly normal.

■ Keep the replay volume low.

With DAT (Digital Audio Taperecorder) cassettes, there is much less noise than with compact cassettes and a wider dynamic

There will be a sudden burst of sound when the audio material begins. As this may damage the speakers, due care should be

Be especially careful when using headphones

*Dvnamic Range

Dynamic range Dynamic range is the difference between the lowest sound level and highest sound level, and is expressed in decibels (dB). The greater this difference, the more dynamic the recording

■ When carrying or storing the unit, take care not to subject it to any severe shocks. When storing or transporting the unit, also be sure to remove the cassette tape.

Recording of Program Numbers

•During automatic or manual recording of program numbers, a start ID of 9 seconds duration is recorded

Some compact discs have a very short interval between sections Even in these cases, however, if your CD player has an auto space function, the space between musical sections can be set automatically so that program numbers are entered correctly during recording.

When Making Digital IN/OUT Connections

If the connections to the following equipment are set incorrectly, a loop may be set up where the output of the unit is connected to its recording input. This will not only set up an oscillation but may also

e monitor speakers. Attention should therefore be paid When recording or playing back through the unit's DIGITAL IN/OUT terminals, be sure to connect these terminals to the DIGITAL PLAYBACK (IN)/DIGITAL REC OUT (OUT) terminals

- DIGITAL PLAYBACK (IN)DIGITAL THE GOT (GOT)

 If the monitor amplifier has no DIGITAL PLAYBACK OR
 DIGITAL REC OUT terminals, this DAT deck can be used only
 for playback through the ordinary DIGITAL INPUT terminals on
- If connections are made as in 2) and it is desired to record, do not on any account set the INPUT SELECTOR of the amplifier to "DIGITAL".

MAINTENANCE

To clean this unit, use a soft, dry cloth. If the surfaces are extremely dirty, use a soft cloth, dipped into a soap-and-water solution or a weak detergent solution. Wring the cloth well before wiping the unit. Wipe once again with a soft, dry cloth.

Never use alcohol, paint thinner, benzine, nor a chemically treated

cloth to clean this unit.

Such chemicals may damage the finish of your unit.

Importance of Cleaning DAT Heads

As with any tape-based recorder, it is essential that the SV-3900 Pro-DAT's heads are kept perfectly clean. A Panasonic DAT Head Cleaning Tape (part number RT-RCLP, included) is available for your local dealer that automatically removes the dirt, dust and shed tape particles that gradually build up on the DAT heads.

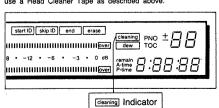
But how do you know that the heads need cleaning? In extreme conditions the audio output will mute, but long before that happens, the slow build up of dirt and oxide will "mask" the sound, and diminish the full 16-bit audio quality from this DAT machine by causing record as well as replay errors.

An ideal solution is to make yourself a "DAT Test Tape" whose error rate you can monitor on a daily or weekly basis (depending on how often you use the SV-3900). To prepare a Test Tape, first select a high-quality DAT cassette and then record a series of music selections. Rewind the DAT cassette and monitor the Error Rates during the first several minutes of tape.

Panasonic brand DAT tapes use high-quality formulations, binders, backings and calendaring process, to provide minimum abrasiveness with maximum stability of metal particles on the tape. It is specifically designed to maximize head life of your DAT

Check the Error Rates at several points in the tape and average the values. (For details of how to display error rates on the SV-3900, see page 27.) If the Error Rates increase to around 300, a Panasonic Head Cleaner Tape can be used as follows: Play the cleaning tape through the SV-3900 for approximately 15 seconds, and remove it. <u>DO NOT REWIND</u> the cleaning tape, since this action might very well spread previously removed dirt and dust onto an otherwise clean head and transport. After the Head Cleaner Tape has been used up, dispose of it properly and start to

CLEANING indicator within the display panel will flash. In this case, use a Head Cleaner Tape as described above.



- elf playback quality improves after cleaning, but then deteriorates immediately after recording or playing back several times, the cassette tape has probably reached its useful lifetime. In this case, use a new tape.
- If sound quality does not improve even after cleaning, consult your dealer.
- Cleaning tapes cannot be used for recording or playback (be sure to read also the operating instructions supplied with the tape)

SV-3900 DAT MAINTENANCE CHART

• REGULAR MAINTENANCE

The purpose of periodic maintenance as recommended is to keep the equipment in the best possible operating condition throughout its useful life. Observance of this maintenance schedule ensures that maximum performance and reliability is obtained from the machine.

Regular maintenance is necessary because the DAT Recorder is a high-technology piece of equipment, containing DC motors, head cylinder assemblies, and a complex mechanism. These components deteriorate over time. Dust and dirt can clog the head gap, which affects the sound. In light of this, it is very important that overall maintenance be performed according to the maintenance chart to avoid problems resulting from heavy image. Maintenance should also be performed after any repairs on the equipment.

Maintenance is particularly recommended for DAT Recorders used in commercial and broadcast applications for serveral reasons. Installation and application are frequently under less than ideal conditions, such as long usage times and poor environmental conditions. All of this adversely affects the life span and performance of the machine. Regular maintenance assures that the purchaser obtains maximum value for this expenditure.

Note: Refer to the hour meter to know when to perform the maintenance.

Part Name	Part Number	500	1000	1500	2000	2500	3000	3500	4000	4500	5000
Upper Cylinder	VEH0460	0	•	0	•	0	•	0	•	0	•
Cylinder Unit	VEG0752	0	0	0	0	0	0	0	0	0	•
S. Load Arm Ass'y	RXL0052										•
T. Load Arm Ass'y	RXL0054										•
Load Cam	RDK0006-1						•				
Capstan Unit	REM0001	0	0	0	0	0	0	0	0	0	•
Post Roller	RXP0008	0	0	0	0	0	0	0	0	0	•
Guide Roller	RXP0027	0	0	0	0	0	0	0	0	0	•
Pinch Roller	1NB0001ZA	0	0	0	0	0	•	0	0	0	0
S. Reel Ass'y	RXR0006										•
T. Reel Ass'y	RXR0007										•
BT Lever	RXL0048										•
Tension Band Ass'y	RXL0036						•				
S. Brake Ass'y	RXL0049						•				
T. Brake Ass'y	RXL0050						•.			13.54	
ldler Gear	RDG0071										•
Mode Motor Ass'y	REM0009								•		
Mode Cam	RDK0007-1										•
M Gear B	RDG0067										•
Drive Gear Ass'y	RXG0011						•				
Idler Gear (F)	RDG0069										•
Idler Gear (P)	RDG0068					-					•
Mode SW Ass'y	RES0002						•				
Load SW Ass'y	RES0001										•
Cassette SW	EVQWR4002										•
Earth Terminal	RUS740ZA										•
Loading Motor	MMN-6FBRC8S										•
Belt	SMQ20025		•								•
Leaf SW	SSPD18			, .			·				•

■ Replacement. ○ Cleaning

• DAT Head and Tape Transport Cleaning

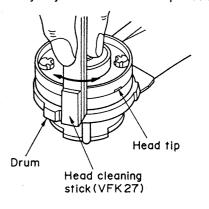
Through normal usage of any tape machine, dirt and debris from the tape accumlates on the heads, which eventually causes performance problems. By using a cleaning cassette regularly, dirt buildup can be minimized, prolonging the life of the tape heads, and also keeping tape posts, tape guides, and the pinch roller clean.

• CLEANING

- 1. Play the cleaning cassette (Panasonic Part No. RT-RCLP) for 15-20 seconds.
- 2. Do not use the same part of the cleaning tape more than once.
- 3. Clean all tape contact surface, including A/C head upper and lower drum, thoroughly with a soft cloth soaked in alcohol.
- 4. Clean both heads by gently rubbing in a horizontal direction, as depicted, using a head cleaning stick (VFK27) or a lint free cloth moistened with alcohol.
- 5. Wipe all tape contact surfaces, including upper and lower drum, with a dry soft cloth to ensure that all residual moisture is removed from the tape contact surfaces.

Note:

- 1. When cleaning the upper drum, hold it secure with your finger tips.
- 2. Use solvents sparingly, excess alcohol will dilute and remove the bearing lubricant in the capstan motor and rotary guides.
- 3. Do not, under any circumstances, insert any object into the head tip slot of the head cylinder.



IMPORTANT

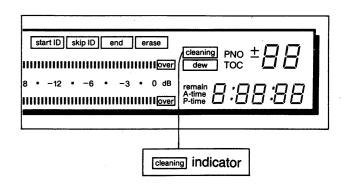
After cleaning the head and tape contact surfaces, check the error rate using the error rate reference tape (Part No.RD-ER01). Target error rate is less than 50. A unit with a new head cylinder should be capable of producing error rates below 10, as long as the reference tape is in good condition.

For information on how to display the error rate, see page 84.

REGARDING THE CLEANING INDICATOR...

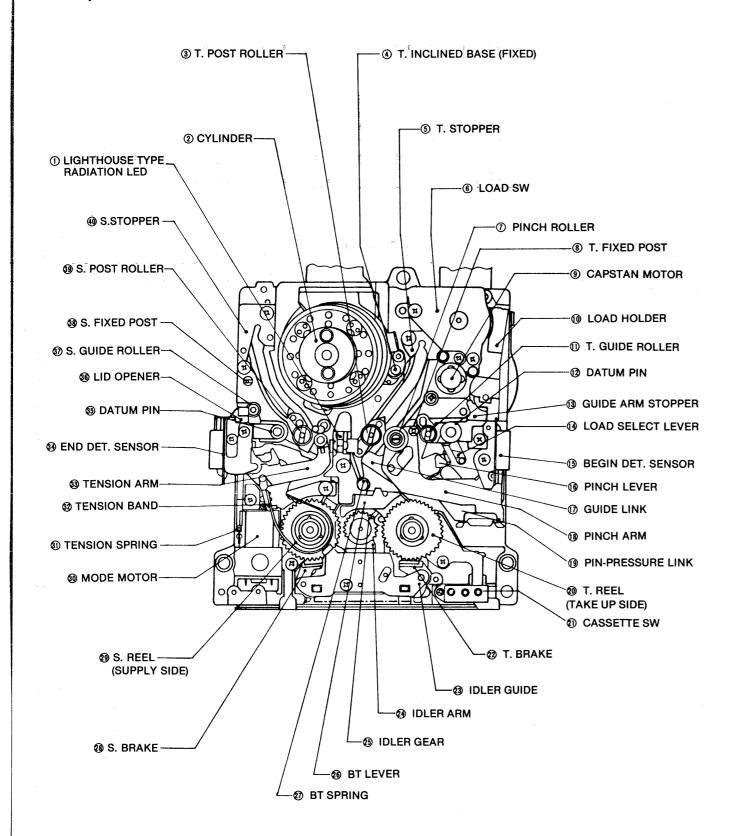
The cleaning indicator will illuminate when the error rate remains higher than 1000 for more than 10 seconds.

Although dirt and debris are the most common cause of an elevated error rate, there are other conditions that may cause it. Therefore, do not assume that a cleaning indication means that the head is dirty.

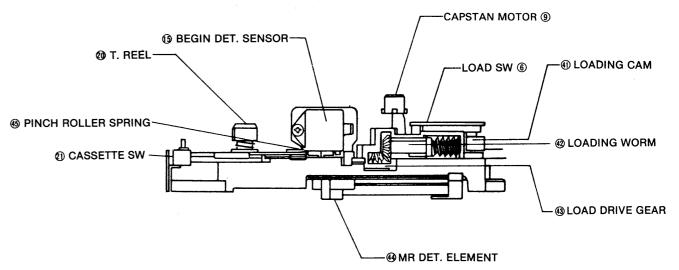


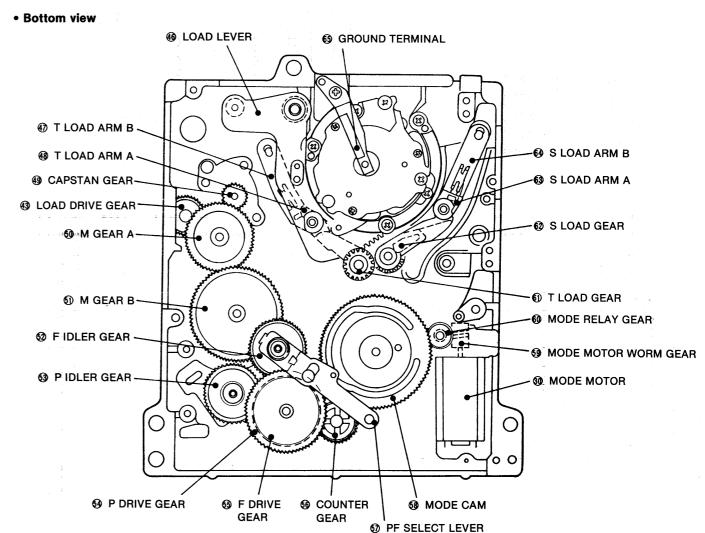
• MECHANISM COMPONENT LAYOUT

Top view



Side view



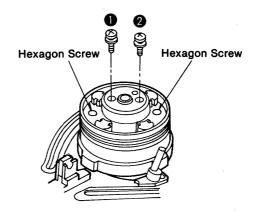


• MECHANISM CONTROLS AND FUNCTIONS

① LIGHT HOUSE TYPE RADIATION LED	Lighthouse-shaped, LEDs blink at start and end of tape.	IDLER GEAR	Transmits movement to S and T reels in accordance with mode.
② CYLINDER	30 mm in diameter, 40 FG pulses, maintains specified speed of	® BT LEVER	Applies back tension to T reel during review.
③ T. POST ROLLER	1000 to 3000 rpm. Regulates tape travel position	BT SPRING	Provides pressure for back tension lever.
T. INCLINED BASE (FIXED)	(upper edge). Regulates angle (90°) at which tape is wound around cylinder	⊗ S. BRAKE	Presses brake shoe against S reel base gear to perform braking.
(FIXED)	(stationary). Determines position of T post	S. REEL (SUPPLY SIDE)	Supply reel base, 64 FG pulses.
⑤ T. STOPPER	roller base during loading.	MODE MOTOR	6.5 V DC motor, switches mode by forward and reverse revolution.
⑥ LOAD SW	Two-bit rotary switch, detects loading position.	TENSION SPRING	Provides back tension force of
⑦ PINCH ROLLER	Presses against tape during play and review.		tension regulator. Mounted to tension regulator,
® T. FIXED POST	Regulates tape travel position.	1 	applies back tension to S reel base.
CAPSTAN MOTOR	1.5 mm in diameter, 290 FG pulses.	TENSION ARM	Detects tape condition and applies back tension during play
10 LOAD HOLDER	Contains loading drive gear and worm gear, engages and disengages M gear A.	⊕ END DET. SENSOR	and review. Light-receiving element for LED (detection at end of tape).
① T. GUIDE ROLLER	Regulates tape travel position (top edge).	DATUM PIN	Regulates width and height (left side) during loading of cassette
1 DATUM PIN	Regulates width and height (right side) during loading of cassette tape.	6 LID OPENER	Opens cassette lid during loading of tape.
③ GUIDE ARM STOPPER	Determines position of T guide roller base K during loading.	⊕ S. GUIDE ROLLER	Regulates tape travel position (bottom edge).
(1) LOAD SELECT LEVER	Switches engagement and disengagement of loading gear in accordance with loading	S. FIXED POST	Regulates tape travel (bottom edge).
	conditions.	S. POST ROLLER	Regulates tape travel position (top edge).
(1) BEGIN DET. SENSOR	Light-receiving element for LED (detection at start of tape).	⊕ S. STOPPER	Determines position of S post roller base during loading.
® PINCH LEVER	Presses pinch roller against tape during play and review.	⊕ LOADING CAM	Uses movement transmitted from
① GUIDE LINK	Links T post roller base and guide roller base.	ey LOADING CAM	loading worm to move loading lever.
® PINCH ARM	Comprised of pinch roller and T holding post, presses against the	@ LOADING WORM	Transmits movement of loading drive gear and loading cam.
(1) PIN-PRESSURE LINK	capstan. Connected by the pin pressure spring and the pinch arm.	LOAD DRIVE GEAR	Transmits movement of M gear A and loading worm, engages and disengages in accordance with mode.
@ T. REEL (TAKE UP SIDE)	Take-up reel base, 64 FG pulses.	MR DET. ELEMENT	Detects magnetic changes (290
② CASSETTE SW	Detects cassette information (mistaken erasure, cassette	® PINCH ROLLER SPRING	pulses) of flywheel. Mounted to the pinch arm,
② T. BRAKE	detection). Presses brake gear against reel base gear to perform braking.	(6) LOAD LEVER	returns the pinch roller. Transmits movement of load cam
② IDLER GUIDE	Holding cover for idler arm and S	(f) T LOAD ARM B	and load gear.
	and T brakes. Moves left or right in accordance		Uses movement transmitted from T load gear to play loading.
@ IDLER ARM	with mode condition, transmits movement of counter gear to S and T reels.	T LOAD ARM A CAPSTAN GEAR	Uses movement transmitted from capstan motor to move M gear A.
	1 10010.		Superior Hotor to motor in godi A.

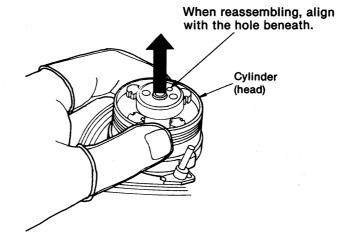
⊚ M GEAR A	Transmits movement of capstan gear and load drive gear.	68 MODE CAM	Uses movement transmitted from ode relay gear to detect mode		
	Transmits movement of M gear A and F (R) idler gear, engages and		positions.		
M GEAR B	disengages in accordance with mode.	MODE MOTOR WORM GEAR	Transmits movement of mode motor and mode relay gear.		
⊚ F IDLER GEAR	Transmits movement of M gear B and F drive gear.	60 MODE RELAY GEAR	Transmits movement of mode		
P IDLER GEAR	Transmits movement of M gear B and P drive gear.	g most need defin	motor worm gear and mode cam.		
P DRIVE GEAR	Transmits movement of P idler gear and counter gear.	T LOAD GEAR	Transmits movement of load lever and S load gear.		
ூ F DRIVE GEAR	Transmits movement of F idler gear and counter gear.	 S LOAD GEAR	Transmits movement of T load gear and S load arm A (B).		
	Transmits movement of F (P)				
69 COUNTER GEAR	drive gear and idler gear, engages and disengages in accordance	® S LOAD ARM A	Uses movement transmitted from S load gear to play loading.		
	with mode.	® S LOAD ARM B			
	Switches engagement and				
PF SELECT LEVER	disengagement of F (P) idler gear in accordance with mode conditions.	GROUND TERMINAL	Ground terminal for cylinder motor.		

• REMOVAL OF THE UPPER CYLINDER



1. Remove the 2 screws (1), 2).

Caution: Please do not touch Hexagon screws.



2. Remove the cylinder (head) in the direction of the

Note: Do not touch the cylinder (head) with your bare hand; always be sure to wear a glove or other protection.

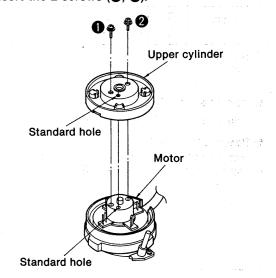


Be sure not to touch the head part.

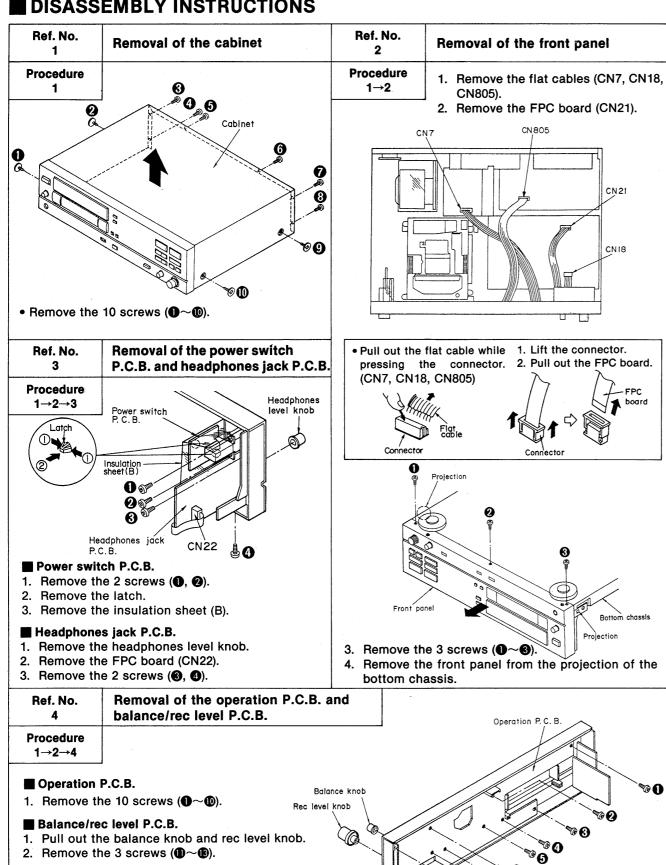
* When reassembling the cylinder (head), be sure that the direction is correct. (If it is assembled in the wrong direction, data read errors will occur.)

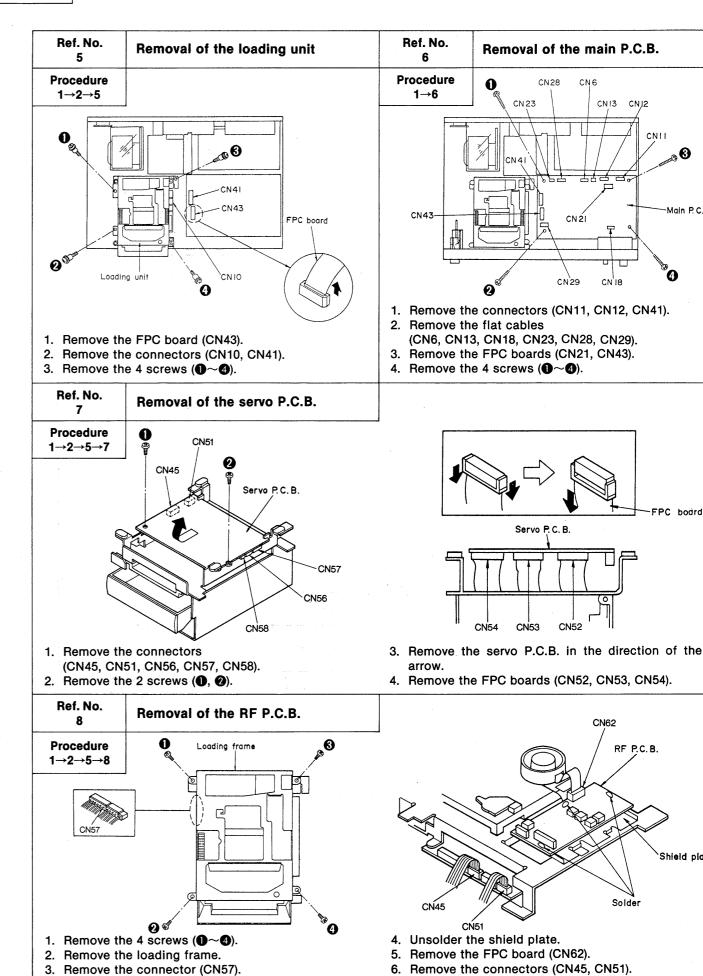
• UPPER CYLINDER MOUNTING

- 1. Mount the upper cylinder matching the motor's standard hole with the cylinder.
- 2. Insert the 2 screws (1, 2).



■ DISASSEMBLY INSTRUCTIONS





Balance / rec

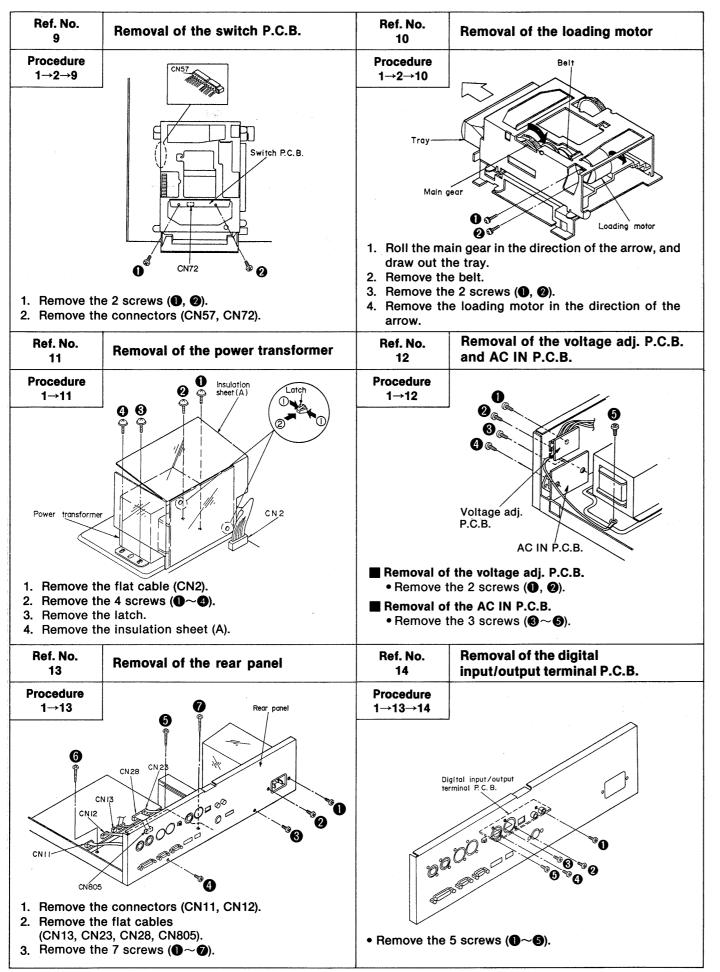
level P.C.B.

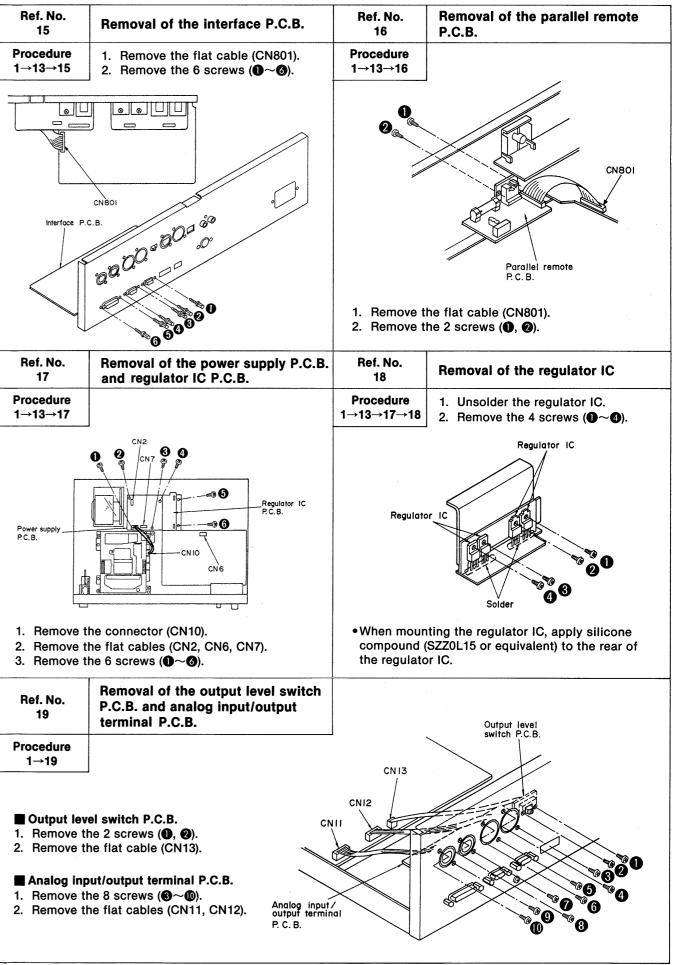
CNI3 CNI2

Main P.C.B.

Shield plate

Solder

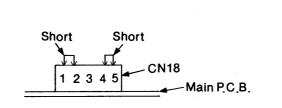




Ref. No. 20 How to check the main P.C.B. Procedure 20

To gain access to the bottom of the main P.C.B., disconnect CN18 and CN21 in order to turn the board upward.

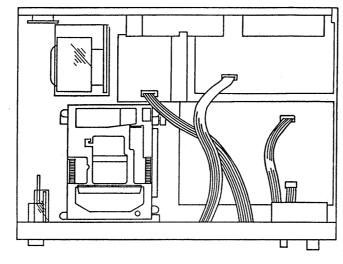
Connect a jumper wire between pins 1 & 2 and between pins 4 & 5 of CN18 when operating the unit in this condition.



■ MEASUREMENTS AND ADJUSTMENTS

PREPARATION

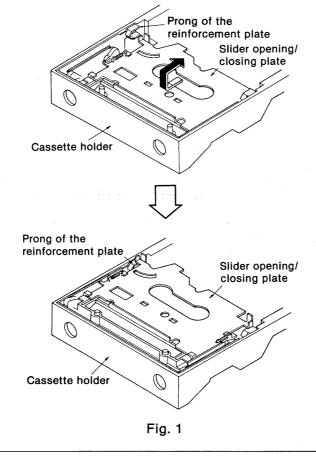
(1) Remove the cabinet (Refer to Procedure 1 under "Disassembly Instructions").



■ If the loading unit is to be removed for adjustment, note the following.

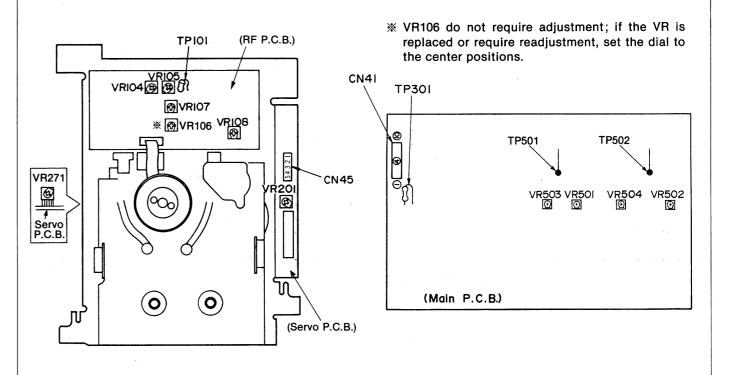
(PREPARATIONS)

- (1) Remove the loading unit. (With the underside facing up.)
 (Refer to Procedure 5 under "Disassembly
 - Instructions." However, leave the flat cable and the connector as they are.)
- (2) Open the cassette holder.
- (3) Move the slider opening/closing plate of the removed loading unit in the direction indicated by the arrow in Fig. 1, and hook it above the prong of the reinforcement plate.
 - Caution: The slider opening/closing plate will be deformed if it is left as is.
- (4) Place a tape with the slider lock released in the mechanism.
- (5) Switch on the power and check the loading operation.



ELECTRICAL ADJUSTMENT

Adjustment points

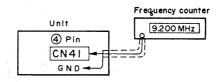


Equipment and Tools

- 2-channel 30 MHz oscilloscope with external trigger and dual time base
- 2 oscilloscope probes (10 : 1)*
- Frequency counter
- AF oscillator (OSC)
- Distortion analyser
- DC electronic voltmeter (EVM)
- Post roller adjustment screwdriver
 - : SZZV1102C
- Standard test tapes
 - : RD-PG01 (PG reference tape)
 - : RD-ER01 (Error rate tape)
 - : RD-LR02 (Linearity adjustment tape)
- Blank DAT cassette for recording and playback
 - : RT-R60P, RT-R90P, RT-R120P
- · Standard electrical tools and equipment
- * NOTE: The oscilloscope voltage settings in the charts and the waveform examples assume use of the specified 10: 1 probes.

1. PLL Free Run Adjustment

- Connect the frequency counter as shown in figure.
- 2. Set the power switch to "ON".
- 3. Open the cassette holder drawer.
- Adjust VR108 as required until the frequency counter reads 9.2 ± 0.2 MHz.

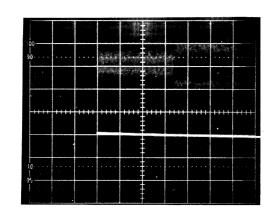


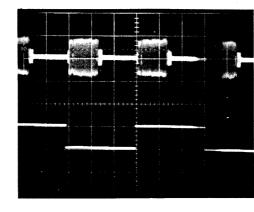
2. PG Phase Adjustment

 Set up the oscilloscope and connect as shown below.

	CH-1	CH-2			
Test point	TP101 (RPRF)	TP301 (R3CP)			
Volts/Div.	50 mV 0.5 V				
Time/Div.	5msec.				
Delay Time/Div.	50 μ	sec.			
Trig.	CH	1-2			
AC-GND-DC	AC DC				
Adjustment point	VR201				

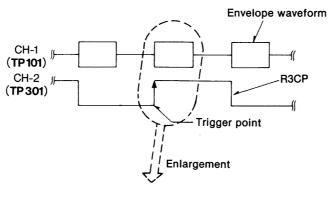
- 2. Note the "t" time indicated on the PG reference tape (RD-PG01), then load and play the tape.
- 3. While the tape is being played, the waveform shown on the right should appear.
- Adjust the delay time for the dual time base to display the leading edge of the CH-2 (R3CP) waveform.
- 5. The time from the leading edge of R3CP to the leading edge of the 522kHz portion of the RF wafeform must be within ±40μsec of the time indicated on the PG reference tape. Adjust VR201 as required, so that the time "t" (in the figure below), falls within specified limits.

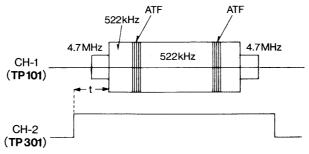




Note:

If the output levels of heads A and B are not equal, the "ATF RF Recording Level Adjustment" described in procedure 4 might be made improperly. Adjust the ATF RF Recording Level by following procedure 4 and then check the output level of the heads again. (At this time, it is not necessary to make adjustment dascribed in procedure 2.)





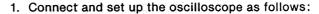
t: Value (µsec) indicated on the standard tape ±40µsec.

3. Linearity Adjustment

CAUTION:

ONLY THE POST ROLLERS ARE USED FOR THE LINEARITY ADJUSTMENT. DO NOT ADJUST THE GUIDE ROLLERS.

THIS ADJUSTMENT IS VERY CRITICAL AND AFFECTS COMPATIBILITY WITH OTHER DAT RECORDERS.



	CH-1	CH-2	
Test point	CN45 ③ (PILOT)	TP301 (R3CP)	
Volts/Div.	0.2 V	2.0 V	
Time/Div.	A 5msec. B 1msec. C 0.2msec.		
Delay Time/Div.			
Trig.	CH	l-2	
AC-GND-DC	AC	DC	
Adjustment point	Post rollers (only)		

- Load and play the linearity adjustment tape (RD-LR02). Please use the recorded signal portion on tape (after 600 count from begining of tape)
- 3. Use the leading edge of the CH-2 (R3CP) waveform to trigger the oscilloscope to monitor the head "A" side of the RF signal envelope.
- 4. While the tape is playing, gradually adjust the height of the POST ROLLERS until the RF envelope (B) is rectangular.

CAUTION 1:

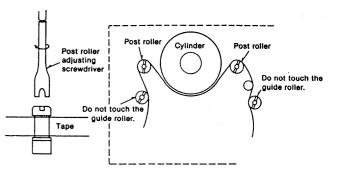
ADJUSTMENTS MUST BE MADE VERY GRADUALLY.

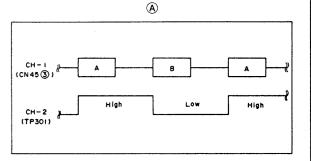
CAUTION 2:

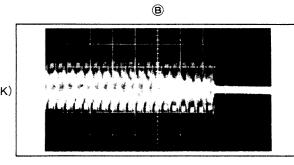
DO NOT "OVER ADJUST" the POST ROLLERS. It is unlikely that the POST ROLLERS will require more than a QUARTER of a turn in either direction to make the waveform rectangular.

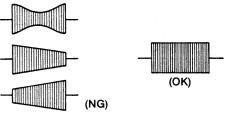
- Adjust VR107 so that the amplitude (V₀) of the PILOT waveform © is 1.8 Vp-p.
- On the PILOT signal waveform ©, verify that the amplitude difference between adjacent tracks falls within 200 mV (See figure below). If not, adjust the post rollers.
- Play the standard test tape (RD-ER01), and perform the PG Phase adjustment and ATF gain adjustment again.

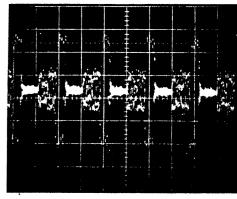
However, perform the ATF gain adjustment only when adjusting the linearity.

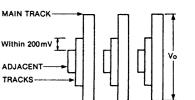










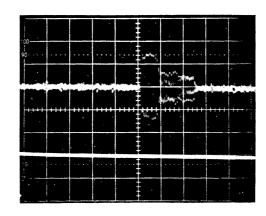


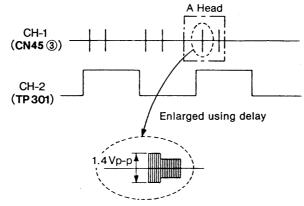
4. ATF Gain Adjustment

 Set up the oscilloscope and connect as shown below.

	CH-1	CH-2	
Test point	CN45 ③ (PILOT)	TP301 (R3CP)	
Volts/Div.	50 mV	0.5 V	
Time/Div.	5 msec.		
Delay	0.1 msec.		
Trig.	CH-2		
AC-GND-DC	AC DC		
Adjustment point	VR107		

- 2. Load and play the error rate tape (RD-ER01).
- 3. Adjust the delay time for the dual time base to select and display the PILOT signal with the largest amplitude.
- 4. Adjust VR107 so that the amplitude of the PILOT signal is 1.4±0.2 Vp-p. Check the amplitudes of the other PILOT signals to insure that they are a minimum of 1.2 Vp-p. If not optimize the adjustment of VR107 for the minimum signal level.





5. ATF RF Recording Level Adjustment

NOTE: This adjustment should only be made after confirming the playback "ATF Gain Adjustment" in step 3 above. Failing to do so will invalidate this adjustment.

- 1. Make a "0" level recording:
 - (a) Load a blank tape (RT-R60P etc.) into the
 - (b) Put the unit into RECORD mode.
 - (c) Run the tape, recording for a minimum of 20 to 30 seconds.
 - (d) Rewind the tape to the beginning of the "0" level signal recording.
- 2. Connect and set up the oscilloscope as follows:

	CH-1	CH-2	
Test point	CN45 ③ (PILOT) TP301 (R3CF		
Volts/Div.	50 mV 0.5 V		
Time/Div.	2msec.		
Delay Time/Div.	0.2 msec.		
Trig.	CH-2		
AC-GND-DC	AC DC		
Adjustment point	VR104: Head A, VR105: Head E		

 While playing back the blank signal portion of the tape, verify that the signal amplitude falls in the range of 1.4±0.2Vp-p.

Standard value: 1.4±0.2Vp-p

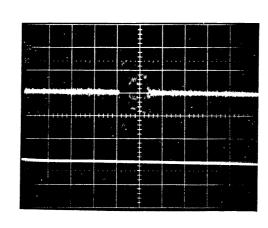
 If the ATF signal is not within specification press the stop button and adjust VR104 and VR105 accordingly. Making finer adjustments as the trials approach the specified level.

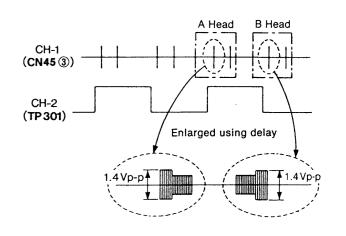
LEVEL/HEAD	A: VR104	B: VR105
More than 1.6V	Turn ()	Turn 🔾
Less than 1.4V	Turn ()	Turn ()

After making the adjustment, make another blank signal recording, on a different portion of the tape (use the END SEARCH feature), for 20 to 30 seconds. Rewind the tape to the beginning of the new recording and repeat from step 3 until the specification is met.

Note:

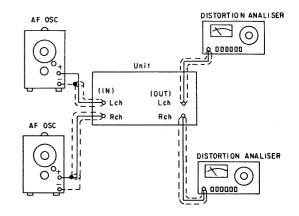
It is necessary to use a different portion of the tape for each trial because DAT does not erase the original signal, it "over writes" the new signal. Using a new portion of the tape will prevent maladjustment due to incomplete "over write" of the previously recorded signal.





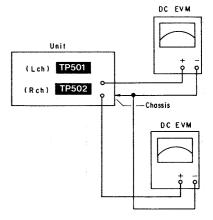
6. DAC Output Balance Adjustment

- 1. Load a blank tape for recording.
- Use a signal generator to feed a 1kHz signal to ANALOG IN.
- Set the recorder to the record mode and adjust INPUT VR so that the level meter moves to -20dB
- Adjust VR501 (Lch) and VR502 (Rch) to minimize the distortion rate. (Reference distortion rate of about 0.05%)
- 5. After adjustment, check that the playback output is -20dB±0.2dB with respect to 0dB at 1kHz.



7. DAC Offset Adjustment

- 1. Set the recorder to DIGITAL IN in the stop mode.
- 2. Connect a digital voltmeter to TP501 (Lch) and TP502 (Rch).
- Adjust VR503 (Lch) and VR504 (Rch) so that the voltage at the test point above is 0±1 mV.

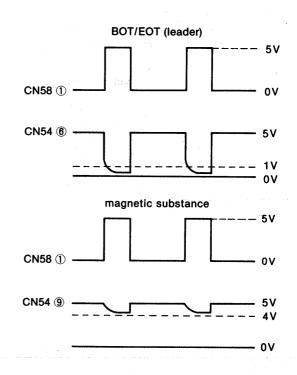


8. BOT/EOT Detection Sensitivity Verification and Adjustment

- 1. Make sure that the tape stops with the leader portion (the beginning and ending portion).
- If the tape does not stop at the leader, make adjustment by following procedure outlined below.
- Insert a blank tape into the tape compartment of the set and press the playback button at the end of the tape.
- ②Set up the oscilloscope and connect as shown below.

	CH-1	CH-2
Test point	CN54 (BOT/EOT) (magnetic substance)	CN58 ①
Volts/Div.	0.2V	0.2 V
Time/Div.	2msec.	
Delay	### MANAGEMENT OF THE PROPERTY	
Trig.	CH-2	
AC-GND-DC	AC DC	
Adjustment point	VR	271

③ Adjust the amplitude of waveform to less than 1V at the magnetic substance and more than 4V at the leader on VR271.



■ TERMINAL FUNCTION OF IC'S

• IC101 (AN7030SE2): RF AMP.

Pin		1/0		
No.	Mark	Division	Function	
1	V _{cc} 1	ı	Power supply terminal	
2	ACH FB	0	Playback feed back signal (Ach)	
3	ACH IN	I	Playback amp. signal (Ach)	
4	GND 1		GND terminal	
5	BCH IN	ı	Playback amp. signal (Bch)	
6	BCH FB	0	Playback feed back signal (Bch)	
7	AREC PCM			
8	AREC PLT			
9	AREC ATF	1	DE recording level edi terminal	
10	BREC ATF		I RF recording level adj. terr	Ar recording level adj. terminar
11	BREC PLT			
12	BREC PCM			
13	REC CNT 1	ı	Track pitch signal	
14	REC CNT 2	Į.	ATF area det. signal	
15	SRRF IN	ı	Recording signal	
16	GND 2	_	GND terminal	
17	VREF	0	Reference voltage terminal (Not used, open)	
18	AREC OUT	0	Recording signal (Ach)	
19	BREC OUT	0	Recording signal (Bch)	
20	BTL REC	0	Recording control signal	
21	V _{cc} 2	1	Power supply terminal	
22	REC ON	0	Recording drive terminal (REC: "H")	
	PLAY ON	0	Playback drive terminal (PLAY: "H")	

Pin No.	Mark	I/O Division	Function
24	HSW	ı	Head switching signal
25	AR/RSEL	I	Not used, connected to power supply
26	R/PSEL	1	Recording/playback select signal (REC: "H", PLAY: "L")
27	EQ OUT	0	Equalization signal
28	EQ IN 3		
29	EQ IN 2	I	Equalization amp. signal
30	EQ IN 1		
31	BF REQ	ı	Equalization amplitude drive terminal (Bch)
32	B PHASE	ı	Equalization phase drive terminal (Bch)
33	B GAIN	ı	Equalization gain drive terminal (Bch)
34	AF REQ	1	Equalization amplitude drive terminal (Ach)
35	A PHASE	1	Equalization phase drive terminal (Ach)
36	A GAIN	l	Equalization gain drive terminal (A ch)
37	SV RF	0	Playback signal
38	GND 3	<u>—</u>	GND terminal
39	A INT	I	Playback amp. signal (Ach)
40	B INT	ı	Playback amp. signal (Bch)
41	B INT IN	0	Playback amp. signal (Bch)
42	A INT IN	0	Playback amp. signal (Ach)
			·

• IC801 (M37450M4-356): Microcomputer for interface

Pin No.	Mark	I/O Division	Function
1	NAESDO	1	D I/O select terminal ("L": AES/EBU)
2	NHMTR	0	Hour meter control
3	TXD/ 422 OUT	0	Transceiver drive terminal
4	RXD/ 422 IN	I	Transceiver unive terminal
5 \ 8	DSW11 DSW8	ı	DIP switch setting terminal

Pin No.	Mark	I/O Division	Function
9 \ 16	DTIN7 S DTIN0	1	S/P data input terminal
17	SDEN	O Parallel data enable terminal ("H": enable)	
18	PDEN		("H": enable)
19	OUTCONT	0	Transceiver drive enable terminal ("H": enable)
20	NC	_	Not used, open

Pin No.	Mark	I/O Division	Function
21	NCCLR	0	Full det. counter clear terminal ("L": clear)
22	RGFUL	ı	S/P register full signal ("H" full)
23	BRK	1	Break det. terminal ("L": break)
24	NPRDY1	0	Ready signal ("L": ready)
25	R/W		Not wood onen
26	SYNC	0	Not used, open
27	GND	_	GND terminal
28	NRST2	ı	Reset terminal ("L": reset)
29	XIN	ı	Crustal OCC terminal (0.92MUn)
30	XOUT	0	Crystal OSC terminal (9.83 MHz)
31	BRKCLK	0	Break det. clock signal
32	GND	_	GND terminal

		1	T
Pin No.	Mark	I/O Division	Function
33	DINC1		
40	DINC8		DIN input signal
41	DSW7		5.5
48	DSW0		DIP switch setting terminal
49	DTQUT7		Dio data aimad
56	DTOUT0	0	P/S data signal
57	AŅ2		Not are described to OND
59	ANO	_	Not used, connected to GND
60	DA2		Not word and
61	DA1	0	Not used, open
62	VREF		CND terminal
63	AVSS	_	GND terminal
64	VCC	ı	Power supply terminal

• IC202 (MN53020SDQ): ATF

Pin No.	Mark	I/O Division	Function	
1	NSNCOK	0	SYNC det. monitor terminal	
2	SVAL	ı	ATF select terminal	
3	РСМОК	1	PCM playback monitor terminal	
4	SPE	0	Starting pulse of counter track lock	
5	SP 2		Sampling pulse signal for pilot	
6	SP 1	0	signal of adjacent track	
7	DCYLPG	ı	Cylinder PG signal	
8	DCAPFG 1			
9	DCAPFG 2		Capstan FG signal	
10	DRLFGT	ı	Take-up reel FG signal	
11	DCYLFG	ı	Cylinder FG signal	
12	SYNC	ı	ATF sync. det. terminal	
13	NRST	ı	Reset signal	
14	R3CP	•	Timing signal for RF envelope	
15	ENVT	•	signal control	
16	FCH	-1	System clock signal (9.408 MHz)	
17	V _{DD}	ł	Power supply terminal	
18	V _{ss}		GND terminal	
19	MODE 1	1	SYNC det. select terminal (Not used, connected to GND)	
20	HFCH	ł	Clock signal for PLL off-set data	
21	PLLOFS	1	PLL off-set data signal	

_				,
	Pin No.	Mark	I/O Division	Function
	22	TEST 6	_	Not used, connected to GND
	23	P MODE	ı	Pulse width select terminal
	24 \$ 28	TEST 1	1	Test terminal (Not used, connected to GND)
-	29	SPHT	_	Not used, open
	30	HSWS	0	Head switching signal (33.33 Hz)
-	31	HSWR		Head Switching Signal (33.33112)
	32	SEL A	1	CAPFGTU signal select terminal
	33	SEL B	I	R3TU signal select terminal
	34	PLL 0		
	35	PLL 1	0	Output signal after decoded 4
	36	PLL 2	U	bit parallel data of PLLOFS
	37	PLL 3		
	38	MODE 2	_	Not used, open
	39	V _{SS} 2	-	GND terminal
	40	V _{DD} 2	-	Power supply terminal
	41	R3TU	0	Building-up edge signal of R3CP/DRLFGT
	42	CAPFGTU	0	Capstan FG signal/Take-up reel FG signal
	43	CAPER	0	Capstan rotative direction control signal
	44	NLNROK	0	Track linearity monitor terminal

• IC201 (MN6742SDR): Servo processor

Pin No.	Mark	I/O Division	Function	Pin No.	Mark	I/O Division	Function
1	OP10A	0	Cylinder rotative stop signal	33	CAE	0	Capstan velocity control signal
2	SCK	ı	Serial clock signal	34	CYE	0	Cylinder velocity control signal
3	SDA	1/0	Serial data signal	35	END	1	VREF or ATFTER voltage signal
4	OSC 1	I	04	36	VSY	1	CYLPG signal
5	OSC 2	0	System clock (8MHz) signal	37	ASH 1	1	Capstan FG or RLFGT signal after EXOR
6	NRST	ı	Reset signal	20	NC		
7	NC		Not connection	38	NC	-	Not connection Inverter amp. signal of ATFTER
8	OP20A	0	SSP ready signal	39	AFB 1	0	input (Not used, open)
9	NC	_	Not connection	40	NC	<u> </u>	Not connection
10	V _{SS}	_	GND terminal	41	AFG 1	1	ATF tracking error voltage terminal
11	VHS	_	Not used, open	42	ASH 2	0	Not used, connected to GND
12	OP 101	0	CAPFG/RLFGT select signal	43	AFB 2	0	Not used, open
13	TP 2	0	R3CP/RLFGT select signal	44	NC	_	Not connection
14	TP 3			45	AFG 2	ı	Reference voltage terminal
15	TP 4	1,	PLL off-set/parallel data signal	46	VDA	ı	Power supply terminal
16	TP 5			47	VSA	_	GND terminal
17	TP 6			48	ORE	0	
18 .	TP 7	ı	PLL off-set/data effective flag terminal	49	IRE	ı	Reference voltage terminal
19	TP 8	ı	Not used, connected to power supply	50	GND		GND terminal
20	MOS	ı	Serial port/strobe signal	51	IPL	0	Not used, open
20	IVIOS	1	Test mode terminal	52	NC		Not connection
21	TST	I	(Normal, connected to GND)	53	CLP	l	Not used, connected to GND
22	ENC		Connected to GND terminal	54	CP 1	0	Not used, open
23	NC		Not connection	55	CP 2	I	Supply reel FG signal
24	NC		Not connection	56	NC		Not connection
25	V _{DD}	1	Power supply terminal	57	NC		Not connection
26	NC	_	Not connection	58	CN 1	0	Not used, open
27	RSW	_	Not used, open	59	CN 2	ı	Not used, connected to GND
28	HAS	0	A/D input select signal	60	CTL	0	Not used, open
29	AVM	_	Not used, connected to GND	61	PFG	ı	Cylinder FG signal
30	VLP	_	Not used, open	62	PGM	ı	Not used, connected to GND
31	STM	1	R3TU or RLFGT (64 P/R) signal	63	CUL	o	Capstan rotative direction signal
32	STR	1	Comparator reference signal of STM input	64	NC	_	Not connection
			•				

• IC102 (AN7035SCE2): Playback PLL

Pin No.	Mark	I/O Division	Function	Pin No.	Mark	I/O Division	Function
1	LPF	0	Buffer amp. 1 reference terminal	16	V _{cc} 1	. 1	Power supply terminal
				17	VCOC 1	0	VCO terminal
2	GND 1		GND terminal	18	VCOC 2	0	VCO terminal
3	ENVC	О	ENV time constant setting terminal	19	V _{CC} 2	1	Power supply terminal
4	ENVR	I ⁻	ENV threshold voltage adj. terminal	20	PLL CP 1	0	Clock (2CK) signal (Not used, open)
5	RSENV	0	RF envelope signal	21	PLL CP 2	0	Clock (CK) signal
6	RSENVC	0	RSENV time constant setting terminal	22	DEMCOD	0	NRZI demodulated signal for playback signal with PLL
7	RSRF	ı	RF signal	23	SVSYNC		ATE area signal
8	DELOUT	0	RF signal			0	ATF sync. signal
9	DELIN 1	<u> </u>	Delay (45°) signal	24	GND 2	_	GND terminal
				25	OP OUT 3	0	ATF 3 signal
10	DELIN 2	1	Delay (90°) signal	26	OP IN 3	1	ATF 3 signal
11	PDOUT	0	Phase comparator signal	27	OP OUT 2	0	ATF 2 signal
12	VREF 1	ı	V/I converter reference voltage terminal	28	OP IN 2	l	ATF 2 signal
13	vcov	ı	OSC frequency control terminal	29	OP OUT 1	0	ATF 1 signal
4.4	D/D	1	Recording/playback select	30	OP IN 1	ı	ATF 1 signal
14	14 R/P	I	terminal (Not used, connected to GND)	31	VREF 2	1	Reference voltage terminal
15	VCOR		OSC frequency adj. terminal	32	COMP 1	ı	Output amp. 1 (+) signal

• IC405 (AK5326-VP): A/D converter

Pin No.	Mark	I/O Division	Function
1	AGND	_	Analog GND terminal
2	AINL	I	L ch analog signal input terminal
3	ZEROL	ı	L ch zero level input terminal
4	VA+	I	Analog power supply terminal
5	VA-	ı	Analog power supply terminal
6	APD	I	Analog power down terminal
7	ACAL	ı	Analog calibration terminal
8	NC	_	Not connected
9	DCAL	0	Digital calibration terminal
10	DPD	I	Digital power down terminal
11	TST 1		
12	TST 2	ı	Test terminal (Connected to GND)
13	TST 3		
14	L/R	1	Input channel select terminal

Pin No.	Mark	I/O Division	Function
15	SCLK	1	Serial data output clock terminal
16	SDATA	0	Serial data output terminal
17	VD1+		Digital power supply terminal
18	VD2+	•	Digital power supply terminal
19	DGND	_	Digital GND terminal
20	DCLKA	ı	Digital system clock terminal
21	NC	_	Not connected
22	ACLKA	0	Analog system clock terminal
23	CLKIN	ı	Master clock terminal
24	LGND	_	Digital GND terminal
25	VL+	ı	Digital power supply terminal
26	ZEROR	1	R ch zero level input terminal
27	AINR	I	R ch analog signal input terminal
28	VREF	0	Reference voltage terminal

• IC203 (AN8320NFA): Linear servo

Pin No.	Mark	I/O Division	Function	Pir No	Mark	I/O Division	Function
1	FG1 AO	0	Capstan FG signal	26	LEDH 2	_	Constant current terminal (Not used, open)
2	FG1 AI	ı	Capstan FG (-) signal	27	CYL FG	0	Cylinder FG signal
3	FG1 FI	_	Frequency characteristic setting terminal	28	CYF GSI	ı	Cylinder schmidt comparator terminal
4	CYL PG	0	Cylinder PG signal	29	CYF GAO	0	Cylinder op. amp. terminal
5	PGVR	-	PG delay time adj. terminal	-		 	
.6	CYPGI	I ·	PG schmidt comparator terminal	30	NST BY		STAND BY signal (Not used, connected to power supply)
7	GND	_	GND terminal	-			
8	SVRF	ı	ATF terminal	32	TF GAI	-	Take-up reel op. amp. (-) terminal
9	CPD	_	Det. capacity connection terminal	33	TF GAO	1	Take-up reel op. amp. terminal
10	CCI	0	Full-wave rectification buffer terminal	34	TF GSI		Take-up reel schmidt comparator terminal
11	CCO		Clamp circuit terminal	35	RLFGT	0	Take-up reel FG signal
				36	RLFGS	0	Supply reel FG signal
12	SP 1	1	SP 1 terminal SP 2 terminal	37	SF GSI	ļ	Supply reel schmidt comparator terminal
14	VSPE	. 1	SPE setting terminal	38	SF GAO	0	Supply reel op. amp. terminal
				-			
15	SPE	l	SPE terminal	39	SF GAI	<u> </u>	Supply reel op. amp. terminal
16	CSH	ı	Hold capacity connection terminal	40	V _{cc}		Power supply terminal
17	ATFTER	0	ATF control command signal	41	FG 2FI	_	Frequency characteristic setting terminal
18	CFB	_	Phase compensation terminal	42	FG 2AI	ı	Capstan FG (-) signal
19	V _{cc}	ı	Power supply terminal	43	FG 2AO	0	Capstan FG signal
20	ATFON	I	ATF ON terminal (Not used, connected to power supply)	44	FG 2SI	ı	Captan FG schmidt comparator terminal
21	PTBIA	_	Photo-transistor bias terminal (Not used, open)	45	CPFG 2	0	Capstan FG signal
22	VREF	0	Reference voltage terminal	46	FILSLD		Frequency characteristic DOWN terminal
23	LEDR 1	ı	Bias voltage terminal	47	CPFG 1	0	Capstan FG signal
24	LEDH 1	_	Constant current terminal (Not used, open)	48	FG 1SI	1	Capstan FG schmidt comparator terminal
25	LEDR 2	ı	Bias voltage terminal				

• IC271 (MN17541SDN2): Mechanism control

Pin		1/0		Pin	Τ .	1/0	
No.	Mark	Division	Function	No.	Mark	Division	Function
1	NSBOA	0	Serial data signal	34	P 51 (CLOSE)	1	Cassette close det. signal
2	NRST	1	Reset signal	35	P 52	ı	Loading start det. signal
3	NSYNC	1		35	(LOAD S)	'	Loading Start det. Signal
4	X 2	-	Not used, open	36	P 53 (LOAD E)	1	Loading stop det. signal
5	X 1			37	P 60 (SW 2)	0	Test terminal
6	V _{ss}		GND terminal		P61		
7	OSC 2	<u> </u>	Not used, open	38	(MMOD 0)	1	Tana mada dat signal
8	OSC 1	1	Clock signal	40	P 63	•	Tape mode det. signal
9	V _{DD}		Power supply terminal		(MMOD 2)		
10	NTC1B	<u> </u>	Supply reel FG signal		P 70		
11 12	NIRQ 0	l	Take-up reel FG signal	41 5	(MBUS 0)	1/0	Transfer bus terminal of system control
12	P00	1	Transfer strobe signal of	44	P 73 (MBUS 3)		Control
13	(MSTB)		system control	45	P 80 (RCC)		Not used, open
14	P 01 (MRDY)	0	Transfer ready signal of system control	46	P 81 (FIL)	0	FILTER select signal
	P 02			-	P 82		
15	(NSSTB)	0	Transfer strobe signal	47	(ATFON)		Not used, open
16	P 03 (NSRDY)	ı	Transfer ready signal	48	P 83 (NSTBY)		Not used, open
17	P 10 (ATFGT)	0	ATF gain (×1/2) select terminal	49	P 90 (NSRST)	0	Reset signal
18	P 11 (REWGT)	0	REW FG • PG gain select terminal	50	P 91 (LEDDRV)	0	Tape begin/end LED control signal
19	P 12 (LPMOD)	_	Not used, open	51	P 92 (PCMOK)	ı	PCM playback det. signal
20	P 13 (MODMT0)			52	P 93 (SVAL 0)	l	ATF effective position setting terminal
21	P 20 (MODMT1)	0	Mode motor control signal	53	NEXPS	ı	Not used, connected to power supply
22	P 21 (MODMT2)			54	PA 0 (NSNCOK)	1	ATF sync. det. terminal
23	P 22		Not used, open	55	PA 1	1	Track linearity det. terminal
24	P23 (PLG)	0	Plunger control signal	 	(NLNOK) PA 2		Capstan rotative direction
25	P 30		Not used, open	56	(CAPER)	I	command signal
26	P 31		not used, open	57	PA 3		Not used, open
27	P 32 (LOAD 1)	0	Tray motor control (+) terminal	58	PB 0 (TH 1)	ı	Tape hall det. signal
28	P 33 (LOAD 2)	0	Tray motor control (-) terminal	59 60	PB 1 (TH 2) NSBTB	ı	Muting det. signal
29	P 40	_	Not used, open	61	NSBIB		
30	P 41 (DEW)	ı	Dew sensor det. signal	62	NSBOB	1	Test terminal
31	P 42 (EOT)	ı	Tape end det. signal	63	NSBTA	ı	Serial transfer clock signal
32	P 43 (BOT)	ı	Tape begin det. signal	<u> </u>	(SCLK)		
33	P 50 (OPEN)	ı	Cassette open det. signal	64	NSBIA (SDAT)	1/0	Serial transfer data signal

• IC301 (MN188161SDL): System control

Pin No.	Mark	I/O Division	Function	Pin No.	Mark	I/O Division	Function
1	V _{DD}	- 1	Power supply terminal	37	P 37 (FLGCLK)	0	Clock signal of flag counter
2 { 9	P 67 (SPDT 7) S	1/0	Signal processor transfer address and data bus terminal	38	P 36 (FLGDT)	0	Data signal of flag counter
9	(SPDT 0)			39	P 35 (TP)	0	Track pitch signal ("L": normal
10	P 57 (SPRDY)	ı	Signal processor data transfer command signal	40	P 34 (UNLOCK)	0	DIGITAL-IN PLL unlock signal ("L": det.)
11	P 56 (PBLANK)	ı	Blank skip select ("H": no skip, "L": skip)	41	P 33	_	Not used, open
12	P 55 (R3CP)	1.	Frame sync. signal	42	P 32 (DISCHG)	0	DIGITAL-IN PLL discharge signal
13	P 54 (PMID6B)			43	P 31 (DINPLINH)	0	DIGITAL-IN PLL prohibition signal ("H": prohibition)
14	P 53 (PMID6A)	_	Main ID6 select terminal	44	P 30 (ANRST)	0	Reset signal ("H": RESET) to DAC
15	P 52	ı	Not used, connected to resistor	45	P 21 (HSW)	ı	Head switching pulse signal
16	P 51	1	Not used, connected to resistor D I/O select terminal	46	P 20 (NMRDY)	ı	Transfer command signal from mechanism control
	(PDIOSEL)		("H": AES/EBU, "L": IEC)	47	P 01		
18	EXO	_	Not used, connected to GND Not used, open	48	P 00		Not used, open
20	NRST 1		Reset signal ("L": RESET)	49	P 17 (PTXD)	0	Serial data transmission terminal
21	P 47 (NSERVRST)	0	Reset signal to servo block	50	P 16 (PRXD)	1	Serial data reception terminal
22	P 46				P 15		Serial data transmission/
23	P 45	_	Not used, open	51	(PCLK)	0	reception clock signal
24	P 44	_		52	P 14		Not used, open
25	P 43 (SLAD)	0	DIGITAL IN PLL/crystal select terminal ("L": PLL, "H": crystal)	53	P 13 (MDT 3)	1/0	Transfer data bus of
26	P 42 (XCK32)	0	32kHz OSC control ("H": OSC, "L": STOP)	56	P 10 (MDT 0)	1/0	mechanism control
27	P 41 (XCK44)	0	44.1 KHz OSC control ("H": OSC, "L": STOP)	57	P 77	_	Not used, open
28	P 40 (XCK48)	0	48kHz OSC control ("H": OSC, "L": STOP)	58	P 76 (NDEMP)	0	de-emphasis signal
29	P 27 (NPRDY)	I	Transfer ready signal from panel control	59	P 75 (SGMTG)	0	Muting signal
30	OSC 1	i	Crystal OSC terminal	60	P74	0	Digital out through select
31	OSC 2	0	Crystal OSC terminal		(DOUTTH)		("H": through)
32	V _{ss}	_	GND terminal	.61	P 73 (NRST 2)	0	Reset signal
33	ΧI		Not used, open	62	P 72	0	Transfer command terminal of
34	хо				(MSTB)		mechanism control
35	P 26		Not used, connected to power supply	63	P 71 (SPSTB)	0	Signal processor strobe signal
36	P 25 (RF ENV)	ı	RF envelope signal	64	P 70 (SPAW)	0	Signal processor address setting signal

• IC351 (MN6624): Digital signal processor

Pin No.	Mark	I/O Division	Function	Pin No.	Mark	I/O Division	Function
1	PCMCIF	•		40	V _{DD}	ı	Power supply terminal
2	IDPP	0	Flag counter terminal	41	XO 1	0	Country OCC terminal
3	IDP	0	Test terminal	42	XI 1	1	Crystal OSC terminal
4	V _{DD}	1	Power supply terminal	43	V _{ss}	-	GND terminal
5	TESTS	_	Not used, connected to GND	44	PC OUT	_	Not used, open
6	V _{ss}	_	GND terminal	45	RAD 0		
7	CKIO FS	_	Not used, open	46	RAD 1		
8	CKIO 128	0	Test terminal	47	RAD 2		
9	CKIO 512		Not used, open	48	RAD 3	0	RAM address bus termainal
10	NDALOAD	_	Not used, open	49	RAD 4		nAM address bus termamar
11	DADAT	0	DA data signal	50	RAD 5		
12	DALRCK	0	LR discrimination signal	51	RAD 6		
13	DABCK	0	Serial bit clock signal	52	RAD 7		
14	DAMCK	1204 1206 2 1 1 10 100	Not used, open	53	V _{DD}	1	Power supply terminal
15	V _{DD}	I	Power supply terminal	54	TEST 2	_	Not used, connected to GND
16	TEST 6		Not used, open	55	V _{ss}	_	GND terminal
17	V _{ss}	_	GND terminal	56	RAD C	0	RAM address bus terminal
18	ADDAT	ı	AD data signal	57	RAD E		Town addition but tomman
19	ADLRCK	0	LR discrimination signal	58	NWE	0	Write enable for memory
20	ADBCK	0	Serial bit clock signal	59	RAD D		RAM address bus terminal
21	ADMCK	0	External clock signal	60	RAD 8	0	
22	TX	0	Digital signal	61	RAD 9		
23	RX	I	Digital Signal	62	RAD B		
24	VCOS L32			63	NOE	0	Output enable for memory
25	VCOS L44	_	Not used, open	64	RAD A	0	RAM address bus terminal
26	VCOS L48			65	V _{DD}	1 '	Power supply terminal
27	DIO REF	0	Digital signal (PLL control)	66	NCS	0	Chip select terminal for memory
28	DIO VAR	0	Digital signal (FEE control)	67	V _{ss}	_	GND terminal
29	V _{DD}	1	Power supply terminal	68	RDT 7		
30	DI 512	ļ	Digital signal (512FS)	69	RDT 6		
31	V _{ss}		GND terminal	70	RDT 5		
32	XO 4	· -	Not used, open	71	RDT 4	1/0	RAM data bus terminal
33	XI 4	-	Crystal terminal (32kHz×512)	72	RDT 3		TIAM data bus terminar
34	TEST 0	_	Not used, connected to GND	73	RDT 2		
35	XO 3	_	Not used, open	74	RDT 1		
36	XI 3	1	Crystal terminal (44.1 kHz × 512)	75	RDT 0		
37	TEST 1	_	Not used, connected to GND	76	V _{ss}	_	GND terminal
38	XO 2		Not used, open	77	TEST 3	_	Not used, connected to GND
39	XI 2	1	Crystal terminal (48kHz×512)	78	V _{DD}	I	Power supply terminal

Pin No.	Mark	I/O Division	Function	Pin No.	Mark	I/O Division	Function
79	SPDT 7			102	V _{DD}	ı	Power supply terminal
80 81	SPDT 6			103	SRPR	0	Recording/playback select signal
L				-			(REC: "H", PLAY: "L")
82	SPDT 4	1/0	Address and data bus terminal	104	SRWND 2	0	ATF area det. signal
83	SPDT 3			105	SRWND 1	0	Track pitch signal
84	SPDT 2			106	PBDT	I	Playback signal
85	SPDT 1			107	PBCK	ı	Playback envelope signal
86	SPDT 0			108	RFMSK	0	Not used, open
87	V _{ss}	_	GND terminal	109	PLLOFS	0	PLL off-set information signal
88	TEST 4	_	Not used, connected to GND	110	HFCH	0	System clock signal
89	V _{DD}	ı	Power supply terminal	111	VFPLFS	0	PLL OFS effective information signal
90	SPAW	١	Signal processor address setting terminal	112	EXFCH		Not used, connected to GND
91	SPSTB	ı	Signal processor strobe signal	113	EEMD	_	Not used, connected to GND
92	SPRDY	0	Data transfer command signal	114	V _{ss}		GND terminal
93	UNLOK	0	PLL unlock signal	115	SL NRZI		Not used connected to GND
94	DISYND	_	Not used, open	116	SELF CH0	_	Not used, connected to GND
95	NSTBY	1	Not used, connected to power supply	117	SELF CH1		
96	NRST	ı	Reset signal	118	V _{DD}	1 .	Power supply terminal
30	141101	<u>'</u>	Master clock signal	119	М9СР	0	Master clock signal
97	M7CK	_	(Not used, open)	120	HSW	, I ** *	Head switching signal
98	R6CP/ (ENVT)	0	Timing signal for RF envelope	121	NR TRST		Not used, connected to power supply
99	R3CP	0	signal control	122	SUBWND		Not used, open
100	V _{ss}		GND terminal	123	IPF	_	Output terminal for flag acceptan
101	SRRF	0	Recording signal	124	SUBC 1	0	Output terminal for flag counter

• IC501 (YM3404B): Digital filter

Pin No.	Mark	I/O Division	Function	Pin No.	Mark	I/O Division	Function
1	SHL	0	1 DAC (ST="L"): Lch Deglitcher signal 2 DAC (ST="H"): L/Rch Deglitcher signal	9	DLO	0	1 DAC (ST="L"): L/Rch data output terminal 2 DAC (ST="H"): Lch data output terminal
2	X 0	0	Clock output	10	RDO	0	Rch data output (Not used, open)
3	X 1	ı	Clock input	11	wco	0	Output data word clock
4	VDD 2	1	Power supply terminal	12	всо	0	Bit clock signal
5	BCI	ı	Bit clock signal	13	vss	1	GND terminal
6	SDSY	ı	R/L signal	14	ST	ı	1 DAC/2 DAC selector terminal
7	SDI	ı	Data input	15	FEN	ı	System clock selector terminal
8	VCC 1	ı	Power supply terminal	16	SHR	0	1 DAC (ST="L"): R ch deglitch signal

• IC502 (MN53010PEH): Serial/Parallel converter

Pin	Mark	1/0	Function
No.		Division	- unotion
1	wco	O	Output data word clock (DALO, DBLO, DARO, DBRO)
2	DARO	0	Rch data output, (+) terminal
3	DBRO	0	Rch data output, (-) terminal
4	RST	ı	Reset terminal, input data to "0"
5	SVDD	I	Power supply terminal
6	svss	ı	GND terminal
7	F2DAC	I	"H": 2DAC 18-bit "L": 2DAC 17-bit
8	FLOAT	ļ	"H": 4DAC 18-bit "L": 4DAC 17-bit
9	PHASE	ı	"H": Phase inversion "L": Normal mode
10	LRCK	1	Inverter input
11	NLRCK	0	LRCK signal inverter output
12	SIN	1	Data input
13	wcı	I	Input data word clock
14	всі	ı	Input data bit clock
15	vss	ı	GND terminal
16	NC		Not connected
17	VDD2	l	Power supply terminal
18	SHR	1	Rch Deglitcher signal

1	Pin No.	Mark	I/O Division	Function			
1	19	SHL	ı	Lch Deglitcher signal			
	20 5 31	NC	_	Not connected			
				NORMAL MODE delay: 180 ms	TEST MODE delay: 1.45 ms	TEST MODE delay: 0.73 ms	TEST MODE delay: 0 ms
$\left \cdot \right $	32	NTEST 1	ı	Н	L	Н	٦
	33	NTEST 2	l	Н	н	L	L
	34	NTEST 3	1	"H": No "L": Re	ormal mo	ode	
	35	DALO	0	Lch data output, (+) terminal			
	36	DBLO	0	Rch data output, (-) terminal			
	37	VDD	. 1	Power supply terminal			
	38	VSS2	1	GND te	rminal		
	39	NC		Not connected			
	40	GAIN	0	Gain selector signal [H: 0∼−12dB L: below −12dB]			
	41	SH	0	Deglitch signal (H: sample) L: Hold			le
	42	всо	0	Output	data bit	clock	

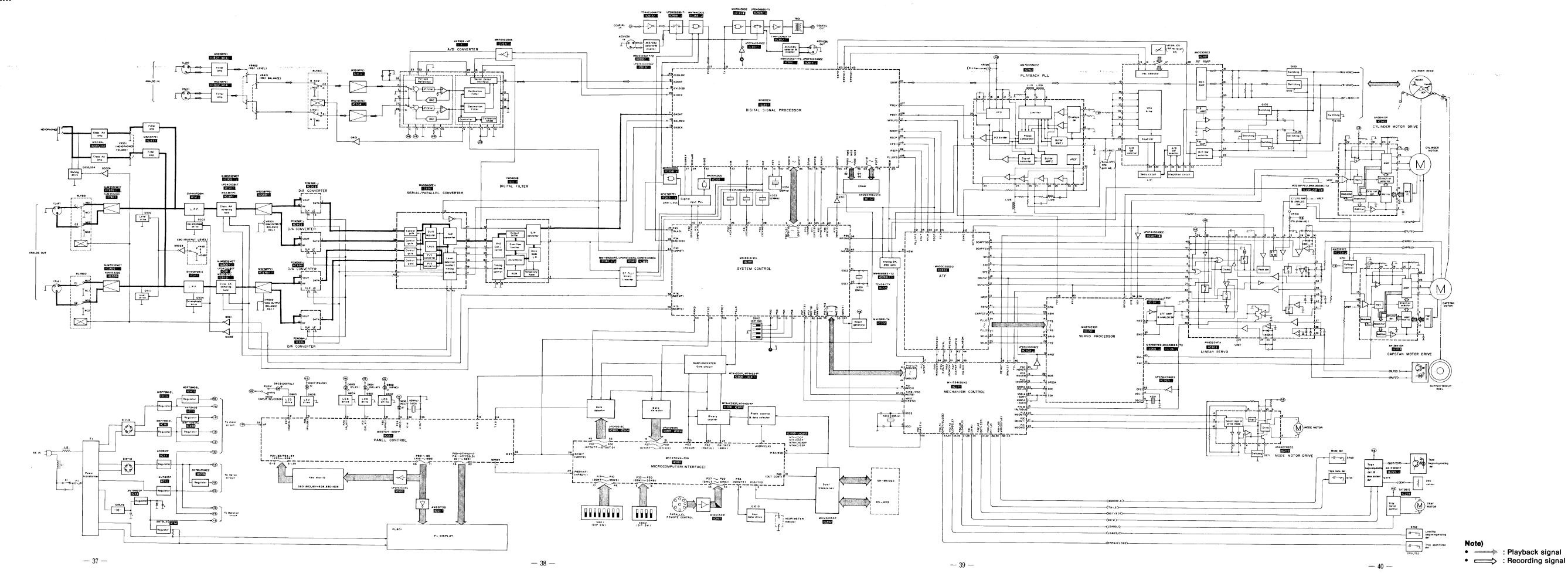
\bullet IC503 \sim IC506 (PCM56P-J): D/A converter

Pin No.	Mark	I/O Division	Function
1	-vs	1	Power supply terminal
2	DG		GND terminal
3	+VL	I	Power supply terminal
4	NC		Not connection
5	CLK	ı	Clock signal
6	LE	ı	Latch enable control signal
7	DATA	l.	Data signal
8	-VL	l	Power supply terminal
9	V OUT	0	Voltage output termial

Pin No.	Mark	I/O Division	Function
10	RF	0	Data signal
11	SJ	ı	Operation amp. supply terminal
12	A • G		GND terminal
13	I OUT	0	Current output termial
14	MSB	l .	MSB adj. terminal (Not used, open)
15	RIN		Not used, open
16	+VS	ı	Power supply terminal

• IC601 (M50754-165FP): Panel control & FL drive

Pin No.	Mark	I/O Division	Function	Pin No.	Mark	I/O Division	Function	
1	V _{ss}	_	GND terminal	33	P 55		Kou return eignel	
2	P 27	0	Input select signal (DIGITAL↔ANALOG)	34	P 54		Key return signal	
3	P 26	0	LED display drive terminal (PAUSE)	35	VP	1	Power supply terminal for FL drive	
4	P 25	0	LED display drive terminal (REC)	36	P 51 P 50	-		
5	P 24	0	LED display drive terminal (PLAY)	38	P 17	0	Segment signal for FL drive	
6 { 8	P 23	1 .	Key return signal	\ 45	} P 10			
				46	NC	-	Not connection	
9	P 20	0	Buffer control signal	47	P 07			
10	NC		Not connection	48	P 06	1		
11	NPRDY	0	Ready signal	49	P 05	-		
12	NTRCLK	1/0	Serial data transmission/ reception clock signal	50	P 04			
13	RXD	0	Serial data transmission signal	51	P 03			
14	TXD	I	Serial data reception signal	52	P 02	_	,	
15	P 33			53	P 01			
16	P 32		Not connection 54	P 00	0	Segment signal for FL drive		
17	P 31	0	LED display drive terminal (S. PLAY)	55 56	P 47			
18	P 30	0	LED display drive terminal (A. PNO)	57	P 45			
19	INT 1	1	Remote control signal	58	P 44			
20	INT 2	ī	Not used, connected to power supply	59 60	P 43			
21	CNV _{ss}		GND terminal	61	P 41	1		
22	RST	<u> </u>	Reset signal ("L": RESET)	62	P 40			
23	NC	<u> </u>	Not connection	63	V _{cc}			
24	X IN	-	That definition	64	V _{cc}	1	Power supply terminal	
25	X OUT	0	Master clock terminal (6MHz)	65	V _{SS}		GND terminal	
26	NC		Not connection	66	P 65		- Communication of the Communi	
27	X CIN		Not used, connected to GND	67	P 64			
28	X COUT		Not used, open	68	P 63			
29	V _{ss}		GND terminal	69	P 62	0	Digit signal for FL drive and key scan signal	
30	NC NC	_	Not connection	70	P 61			
31	P 57			71	P 60			
32	P 56	1	Key return signal	72	NC	_	Not connection	
UZ	1 30			<u></u>	140	L	Tet commodicin	



■INTERNAL CONNECTION OF FL

 Grid assignment diagram 48kHz 44.1kHz 32kHz ACDB start ID skip ID end erase

Anode connection

	2G				40	5 5	G
	1G	2G	3G	4G	5G	6G]
S1	111	[[]]	48KHz	-	-	-	1
S2	111	111	44.1KHz	-	-	-	1
S3	111	111	32KHz	-	-	-	1
S4	111	111	Α	1a	-	-	1
S 5	111	111	Z	1b	-	-	1
S6	111	111	В	1f	-	-	1
S7		111	start ID	1g	-	PNO	1
S8	511	111	skip ID	1c	-	тос	1
S9	111	III	end	1e	-	+	1
S10	111	111	erase	1d	-	-	1
S11		111	-	col,1	col,2	-	1
S12	111		-	2a	2a	2a	1
S13	[1]		cleaning	2b	2b	2b	
S14		111	dew	2f	2f	2f	
S15	111	111	-	2g	2g	2g	
S16	111		remain	2c	2c	2c	
S17	111		A-time	2e	2e	2e	
S18			P-time	2d	2d	2d	
S19	111		-	-	-	-	
S20	111	111	-	3a	3a	3a	
S21	111	111	-	3Ъ	3b	3b	
S22	111	111	-	3f	3f	3f	
S23	111	111	-	3g	3g	3g	
S24		111	-	3c	3c	3c	
S25			-	3e	3e	3e	
S26	over	over	-	3d	3d	3d	
S27	L	R -60~0 dB	-	-	-	-	

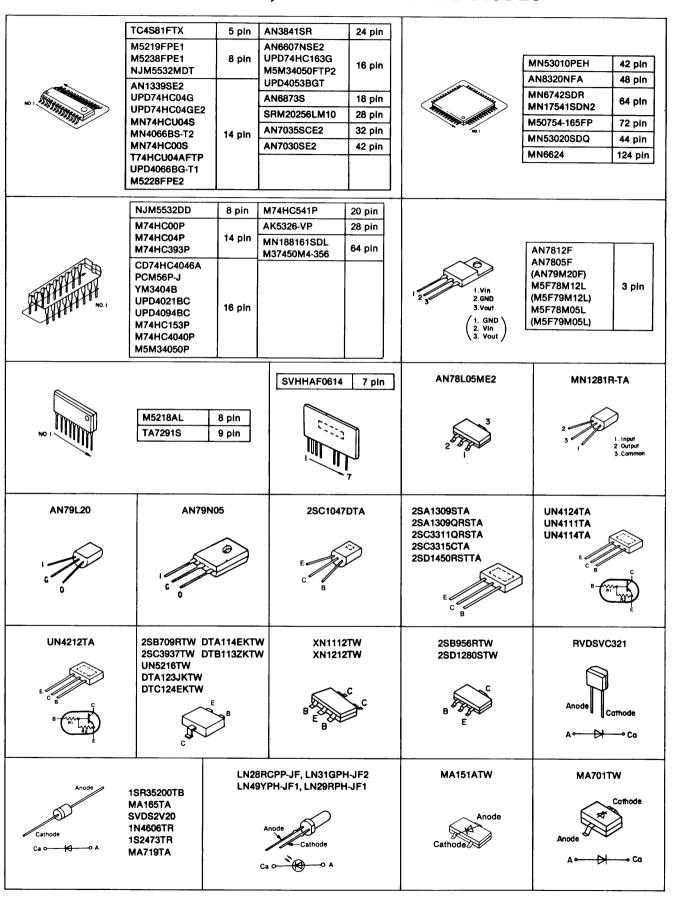
Pin connection

PIN NO. 55 54 53 52 51 50 49 48 47 46 45 44 43 42 41

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Note 1.)NP........... No pin. 2.)F1,F2....... Filament 3.)1G~6G..... Grid.

■ TERMINAL GUIDE OF IC'S, TRANSISTORS AND DIODES



■ SCHEMATIC DIAGRAM

(Parts list on pages $86\sim88$, $98\sim100$.)

(This schematic diagram may be modified at any time with development of new technology.)

otherwise.

- S901 : Output level selector switch (+4dBu/-10dBu).
- : DIP switch. • Resistance are in ohms (Ω), 1/4 watt unless specified
- $1 K = 1,000 (\Omega), 1 M = 1,000 k (\Omega)$
- Capacity are in micro-farads (uF) unless specified otherwise.
- All voltage values shown in circuitry are under no signal condition and playback mode with volume control at minimum position otherwise specified.
- ()........Voltage values at recording mode. For measurement us EVM.
- Important safety notice

Components identified by △ mark have special characteristics important for safety. When replacing any of these components, use only manufacturer's specified parts.

Part No.	Original Part No.	Supply Part No.
IC551, 552	M5218AL	M5218L

• () indicates +B (bias).

• (---) indicates - B (bias).

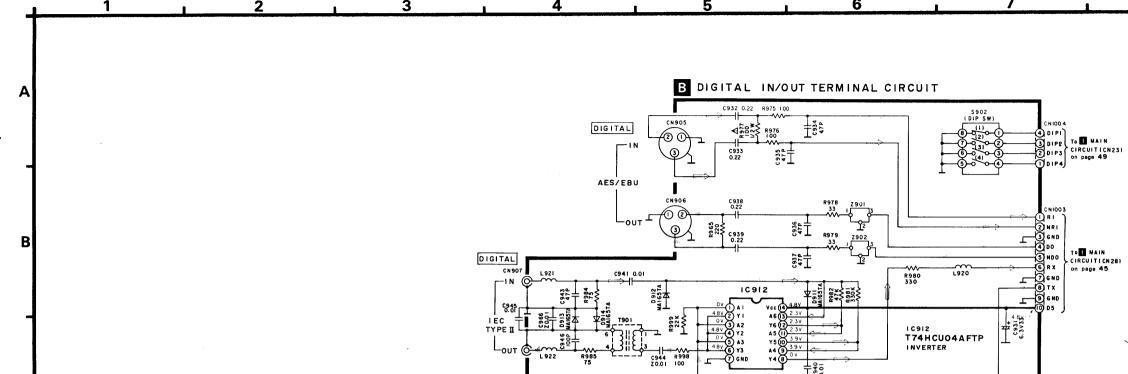
• () indicates the flow of the playback signal.

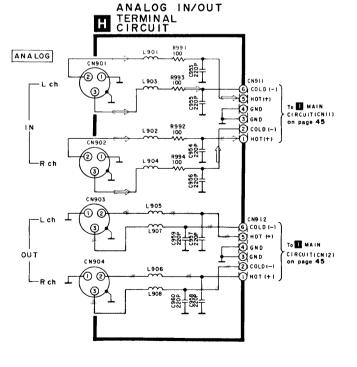
• () indicates the flow of the recording signal.

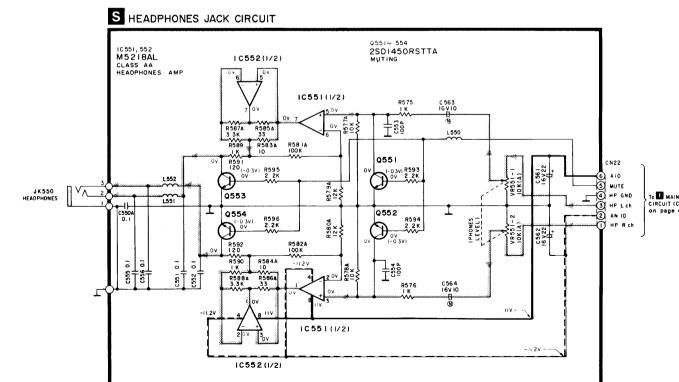
* Caution!

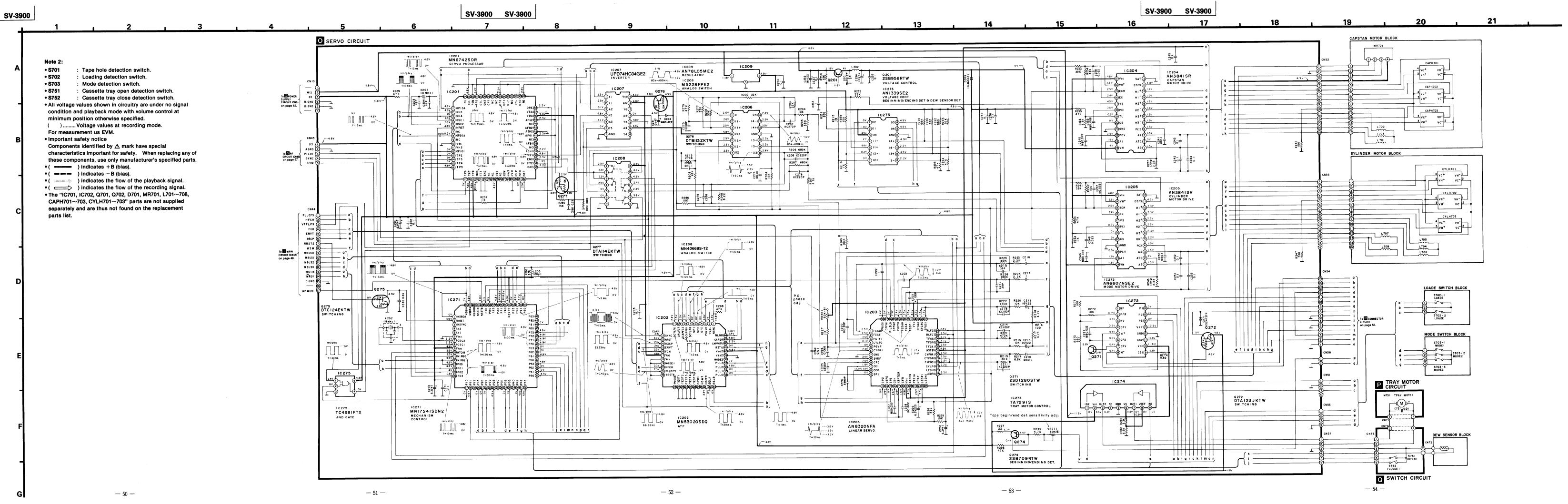
IC and LSI are sensitive to static electricity. Secondary trouble can be prevented by taking care during

- * Cover the parts boxes made of plastics with aluminum foil.
- * Ground the soldering iron.
- * Put a conductive mat on the work table.
- * Do not touch the legs of IC or LSI with the fingers directly.









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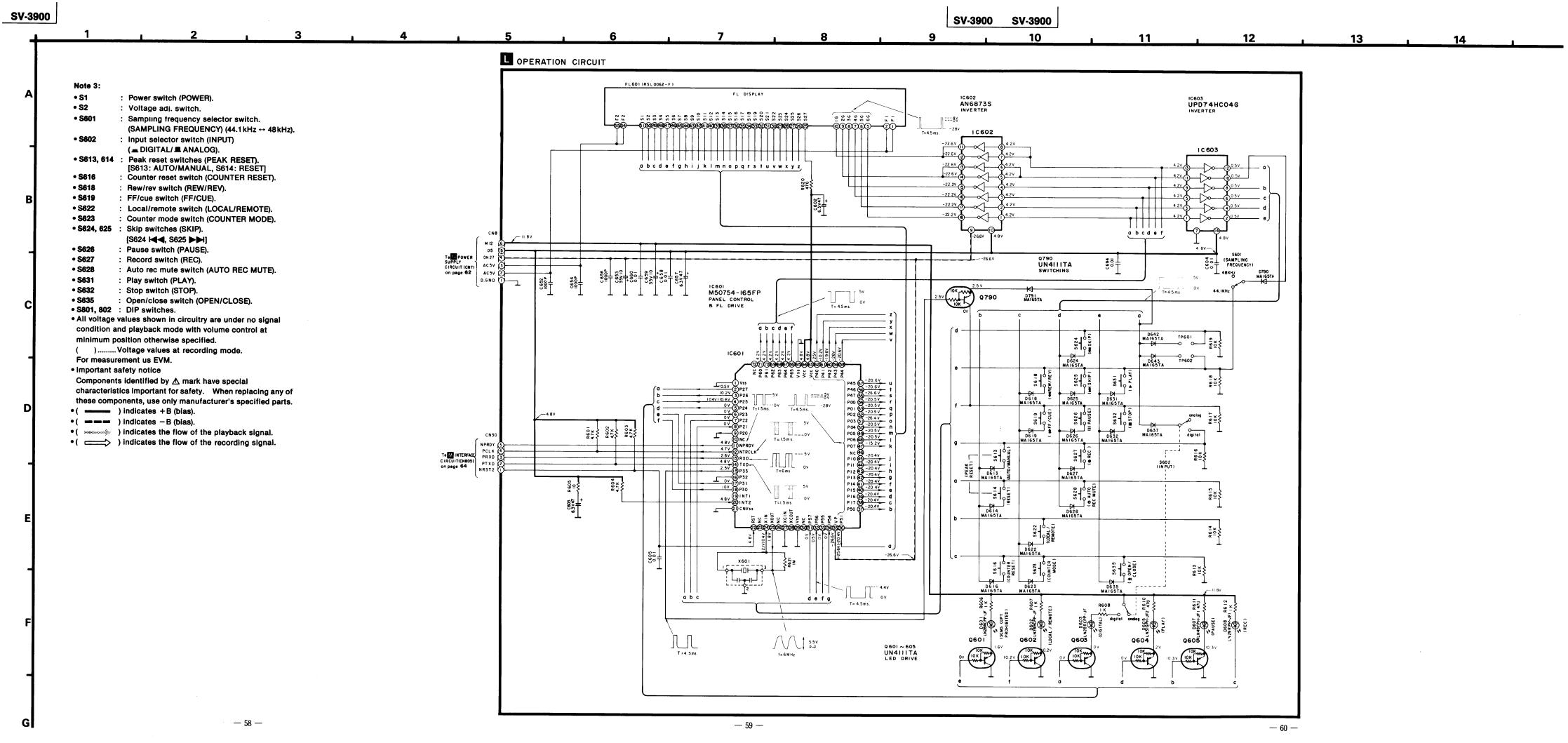
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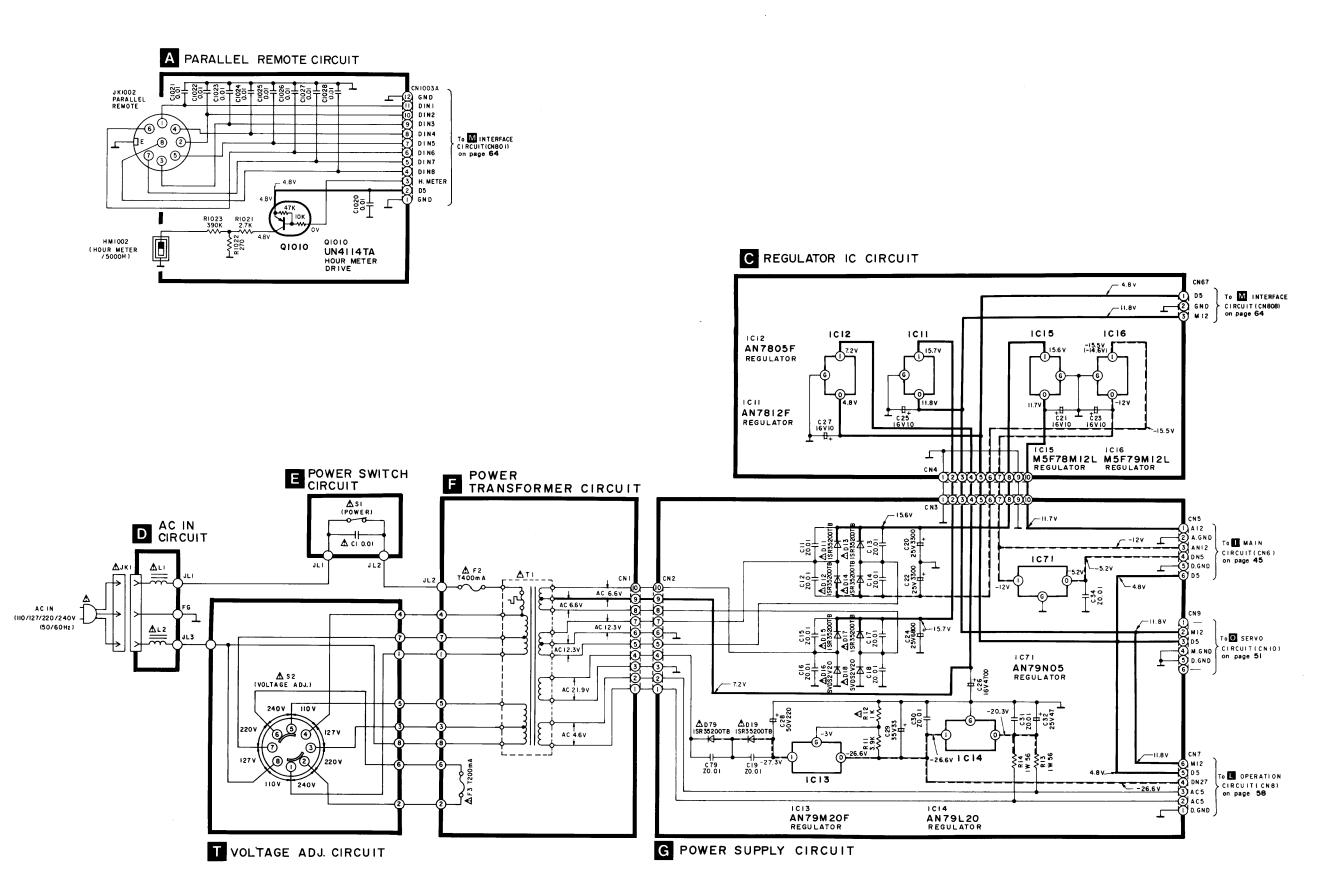
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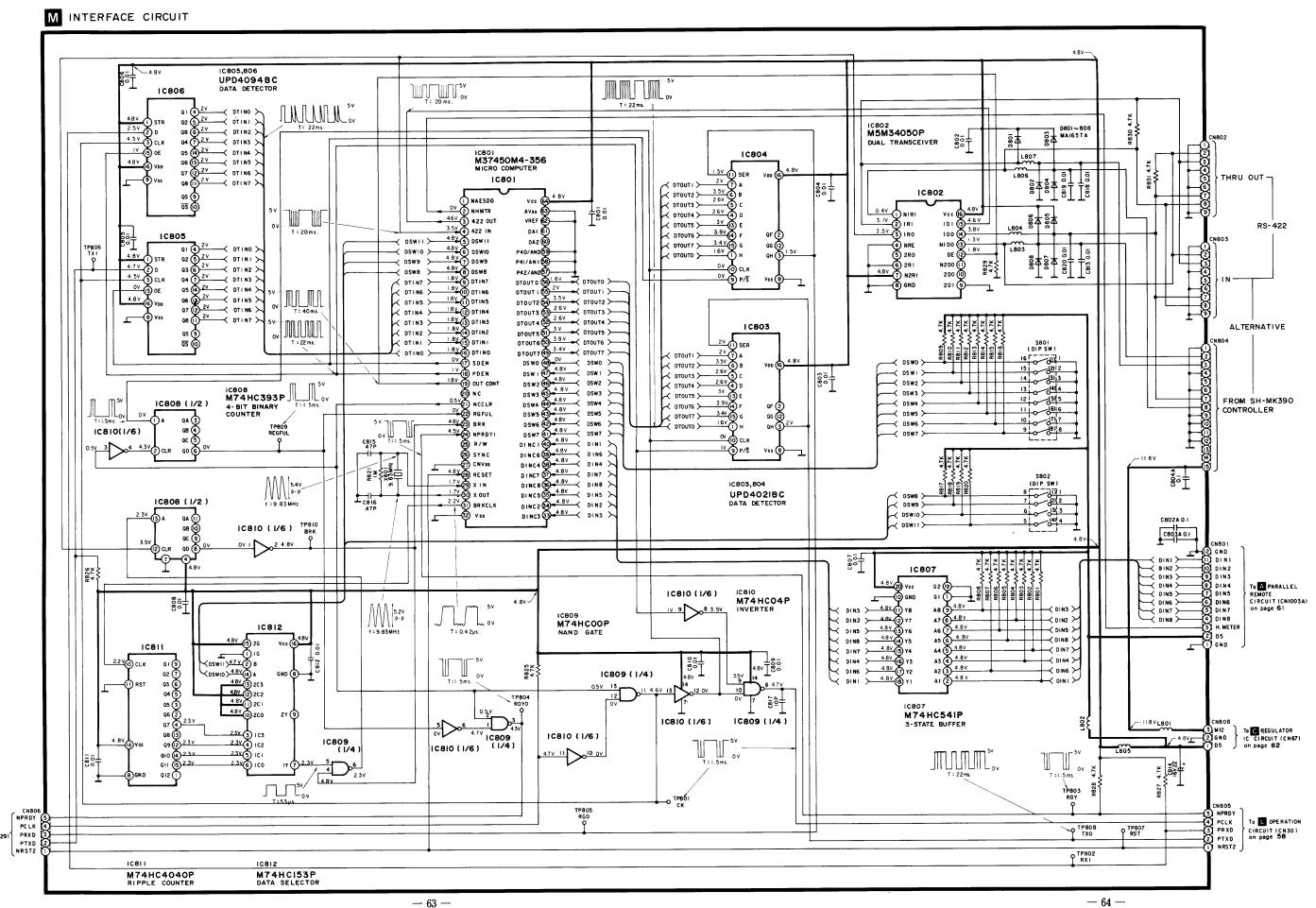
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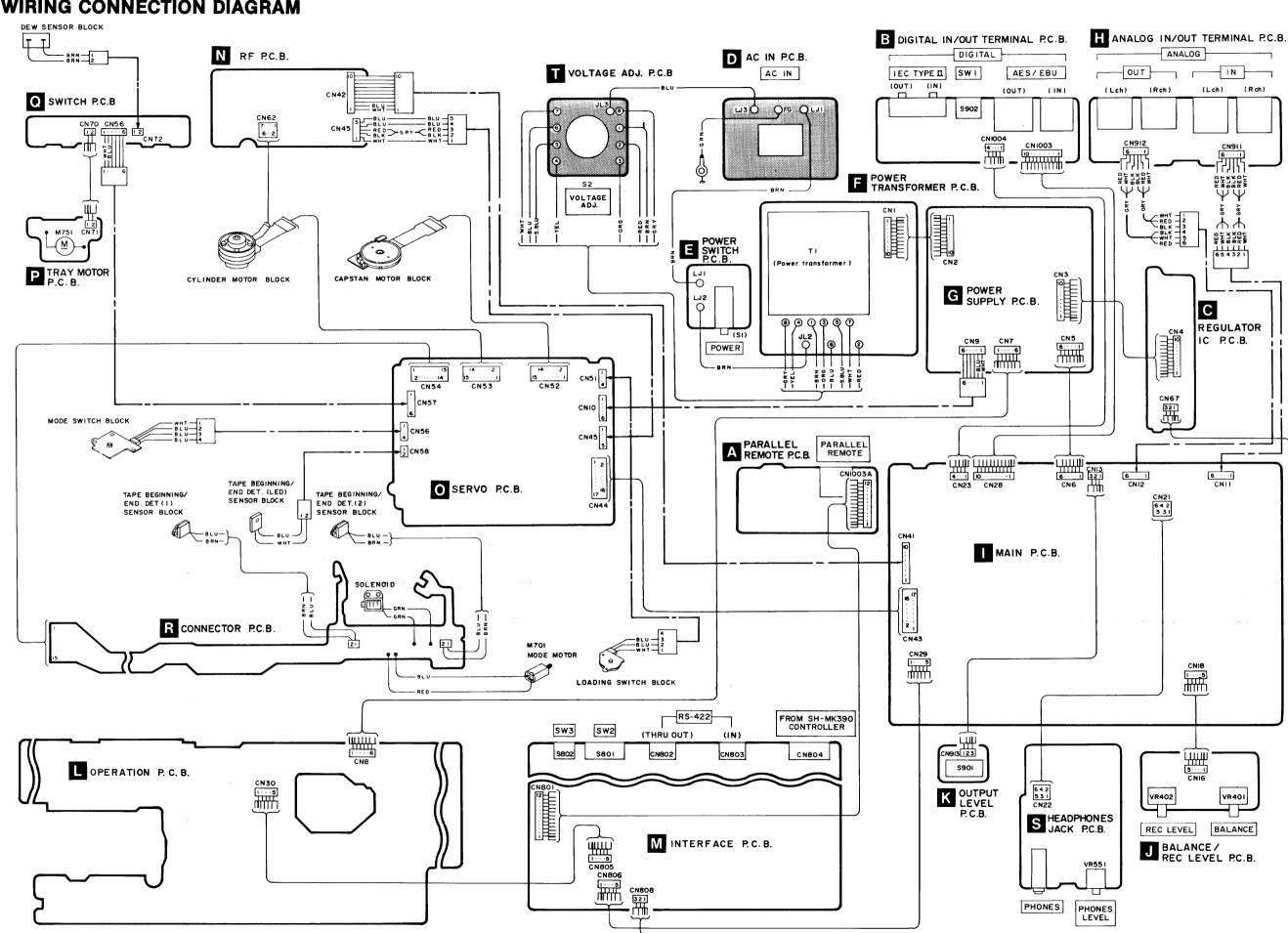


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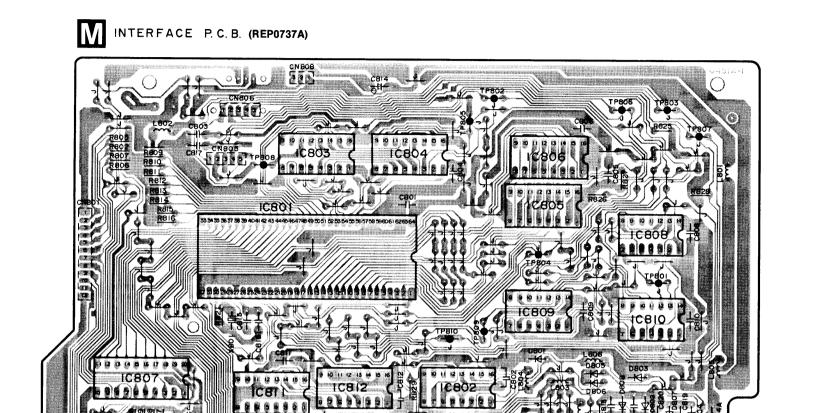


SV-3900 SV-3900

■ WIRING CONNECTION DIAGRAM



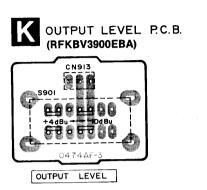
■ PRINTED CIRCUIT BOARDS

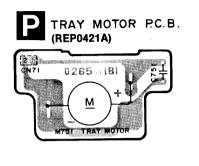


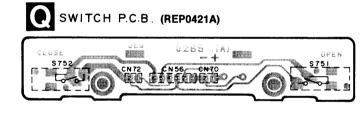
SV-3900 SV-3900

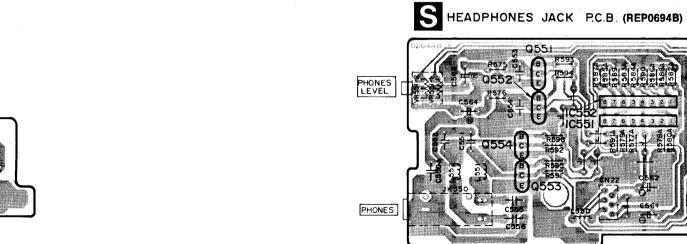
CONTROLLER

ALTERNATIVE

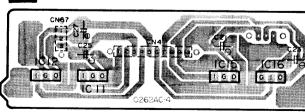








REGULATOR IC P.C.B. (REP0693B)



RS-422

CONTROL

SW2

10 11 12 13 14 15 16 17 18 19

MAIN P.C.B. (RFKBV3900EBI)

Notes:

- The circuit shown in () on the conductor indicates printed circuit on the back side of the printed circuit board.
- The circuit shown in () on the conductor indicates printed circuit on the front side of the printed circuit board.
- The symbols (•) shown in the circuit board indicate connection points between conductors on the front side and back side of the circuit board.
- This printed circuit board may be modified at any time with the developement of new technology.

TAPE BEGINNING / ENDING DET. (LED) SENSOR BLOCK

TAPE BEGINNING/ ENDING DET.(2) SENSOR BLOCK

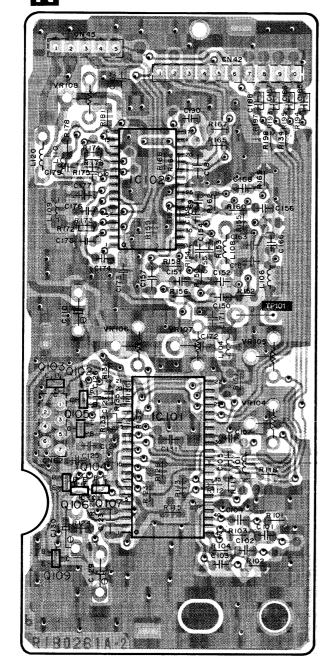
- 71 -

developement of new technology.

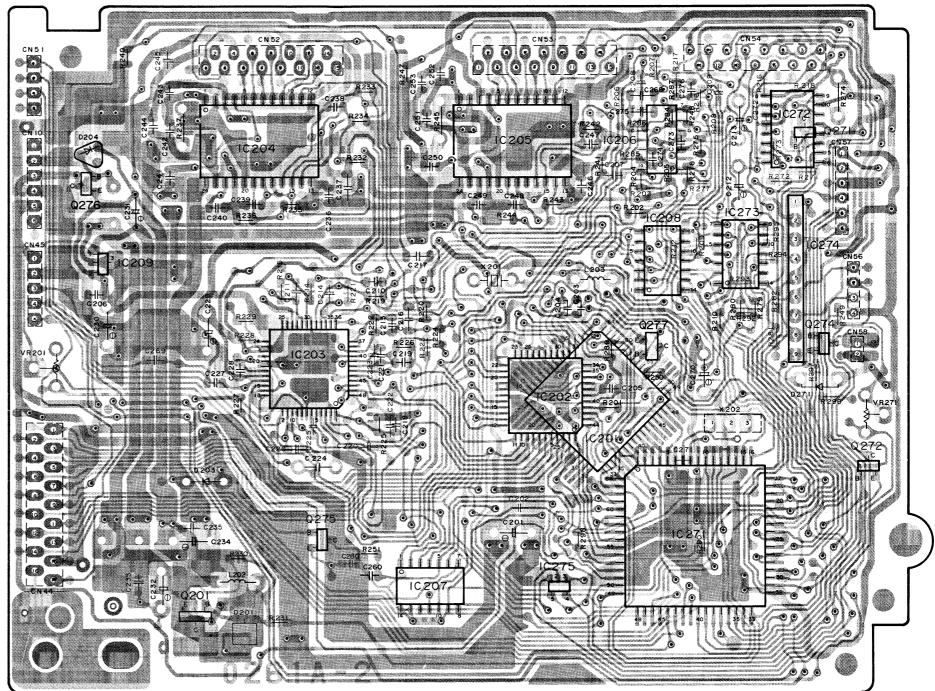
SV-3900

10 12 11 13 14 16 19

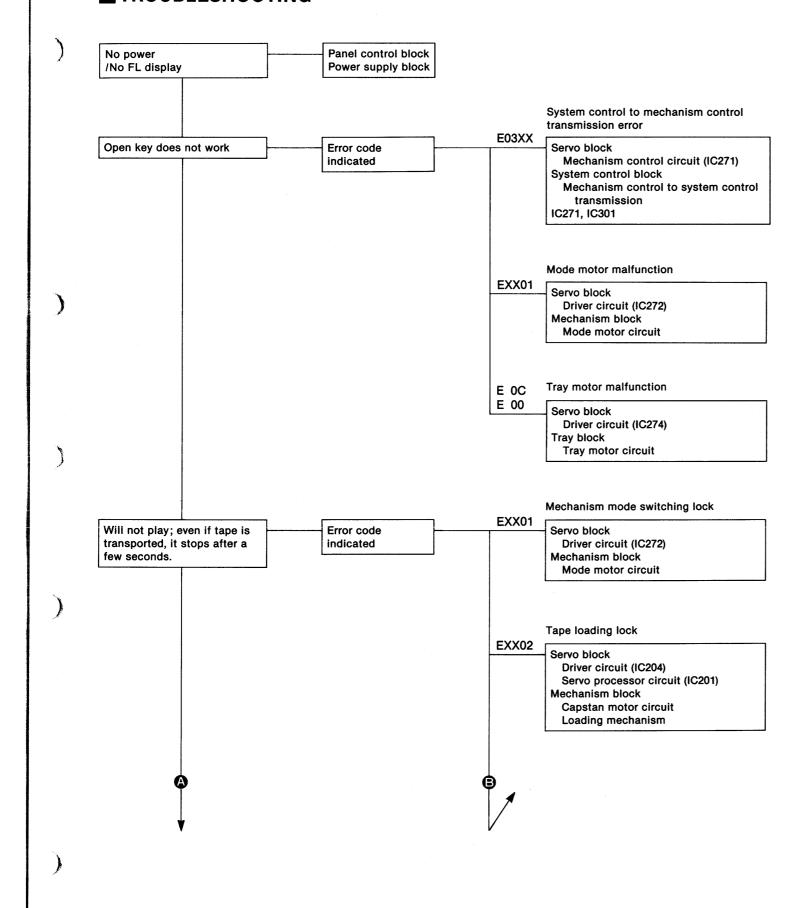
N RF P.C.B. (RFKBV3700-N)

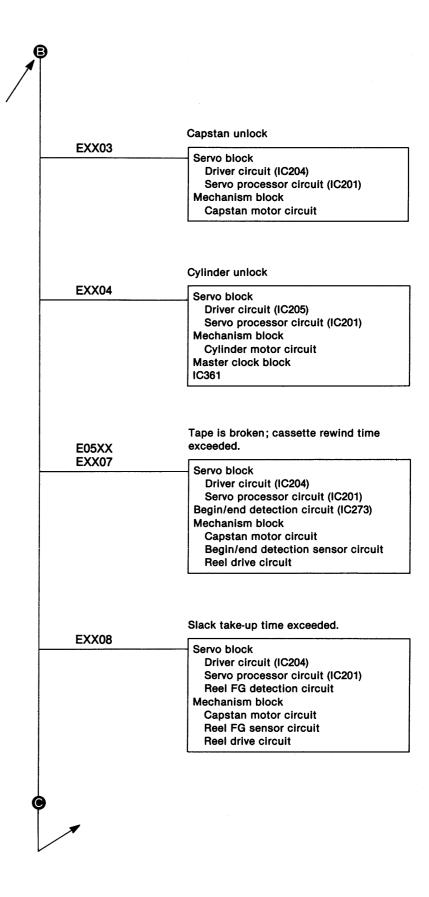


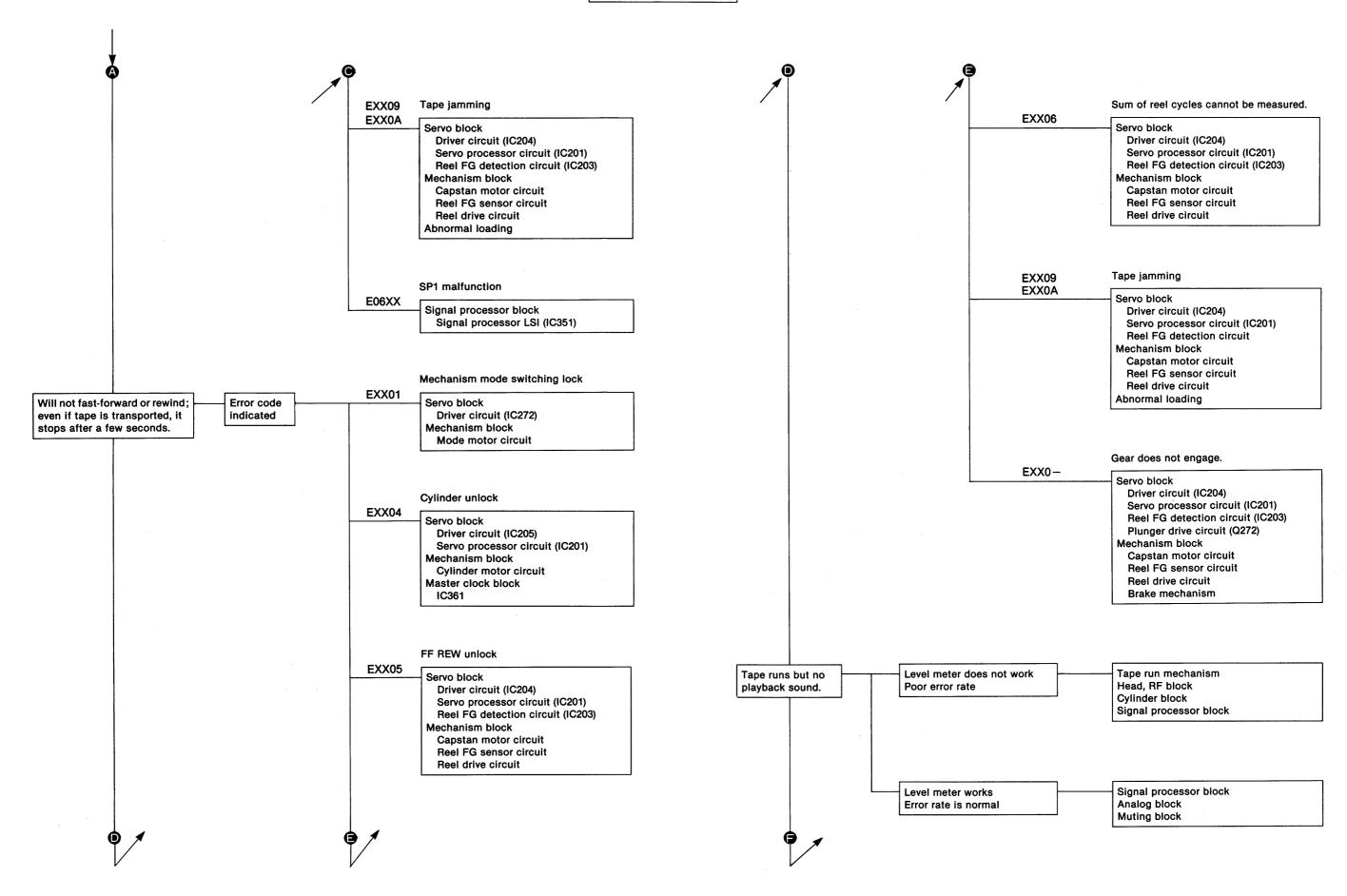
SERVO P.C.B. (RFKBV3700-0)

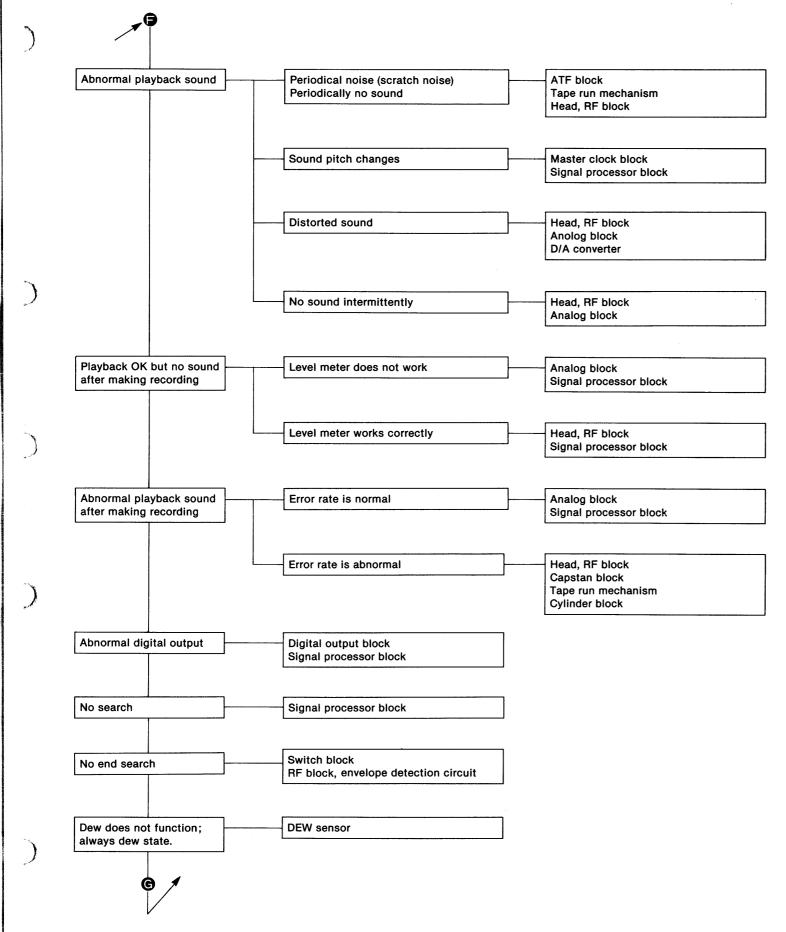


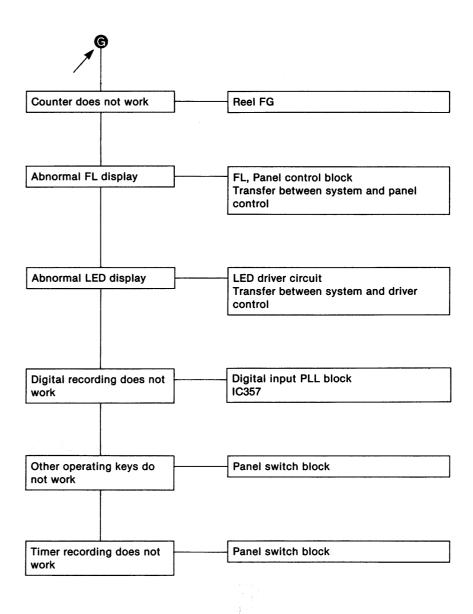
■ TROUBLESHOOTING











KEY POINTS FOR TROUBLESHOOTING

Mechanism block Loading mechanism Post roller Tension regulator Pinch roller Brake lever Brake mechanism Brake lever Solenoid Solenoid driver Mechanism switch block Tape hole detection switch Cassette detection switch

Holder switch

Detection LED

FPC & FPC connector

Reel FG block

Mode switch

Mode motor block Mode motor

Mode motor driver circuit

Detection photo transistor

Reel FG amp (servo P.C.B.)

Master clock block 28 MHz oscillator 16 MHz, 22 MHz, 24 MHz oscillate and select circuit

Panel switch block Switch Panel control IC

Head, RF block Head FPC & FPC connector Head dirty Head cracked or damaged RF recording current Playback eye pattern

Tape begin/end detection block Begin/end detection photo transistor Begin/end detection LED Comparator circuit FPC & FPC connector

Power supply block Power supply regulator output Fuse

Capstan block Capstan FG FG amp Motor driver output Motor current

Cylinder block Cylinder FG Cylinder PG FG amp PG amp Motor driver output Motor current

ATF block RF ATF output ATF SYNC output ATF select circuit ATF gate allay

Signal processor block Data & clock to D/A Data & clock to A/D All clocks

Digital output block Digital output PB

Panel control block Panel control block Transfer between panel and system control Panel control reset

Analog block Input amplifier Output amplifier Muting circuit A/D converter D/A converter

ABOUT THE ERROR RATE

If the error rate is normal, it can be judged that everything up to signal processing, meaning the operation of the RF head mechanism, is normal.

Thus, when there is a problem with playback, if the error is normal, it can be assumed that the origin of the problem is in the analog system.

ABOUT THE LEVEL METER

Just as for the error rate, if the level meter is operating normally, it indicates that the signal is reaching signal processing.

In other words, if there is no problem with the level meter during playback, it indicates that the head and the RF are outputting the signal.

In addition, if there is no problem with the level meter during recording, it indicates that the analog system (input amplifier and AD) is functioning normally.

ERROR RATE DISPLAY FUNCTION

1. Setting the error rate display mode

Simultaneously press the counter mode key, the counter reset key, and the pause key.

If the counter reset key is released before the other keys, the mode will not change to the error rate display

(1) The digital input mode will be displayed:

If "AES" is displayed, it indicates the AES/EBU mode. If "IEC-2" mode is displayed, it indicates the IEC Type II mode.

2. Changing to the following displays

Press the counter mode key successively.

- (2) The error rates for head A and head B will be displayed: FLs A and B will light up (max 9984).
- (3) The error rate for head A will be displayed: FL A will light up (max 4992).
- (4) The operation modes for the system control (left) and the mechanism control (right) will be displayed.
- (5) The error codes for the system control (left) and the mechanism control (right) will be displayed. (If the tray is open, the error codes will be cleared.)
- (6) The display will return to (1).

3. Returning to the normal mode

Press the counter reset key: the mode will change to the A time display mode.

ERROR CODE TABLE

Error code	Sys	tem control error code	Mech	anism control error code
(Note. 1)	Processing (Note 2)	Contents	Processing (Note 2)	Contents
1	Test operation	R3CP clock malfunction	Unload	Mechanism mode switching lock
2	Test operation	HSW clock malfunction	Unload	Tape loading lock
3	Transmission omitted	Faulty transmission of the mechanism control	Unload	Capstan unlock
4	Unload	Still protection during operation	Unload	Cylinder unlock
5	Unload	Broken tape	Unload	Reel unlock
6	Unload	Faulty transmission of SP1	Unload	Sum of reel cycles cannot be measured.
7			Unload	In-cassette rewind time exceeded.
. 8			Unload	Slack tape-up time exceeded.
9			Unload	Tape jamming (Supply side)
Α			Unload	Tape jamming (Take-up side)
B (-)			Unload	Gear does not engage.
С			Tray stop	Initial tray setting not possible.
0				No error
FF (blank)		No error		

Note 1: Display mode

E X1 X2

E: Indicates that mode is the error rate display mode.

X1: System control error codeX2: Mechanism control error code

ode

Test operation:
Internal clock of the system control temporarily connects for operation.

Transmission omitted:

Transmission processing stopped.

Note 2: Processing when an error occurs

Unload:

Tape is unloaded.

■ REPLACEMENT PARTS LIST

Notes: * Important safety notice:

Components identified by A mark have special characteristics important for safety. When replacing any of these components use only manufacturer's specified parts.

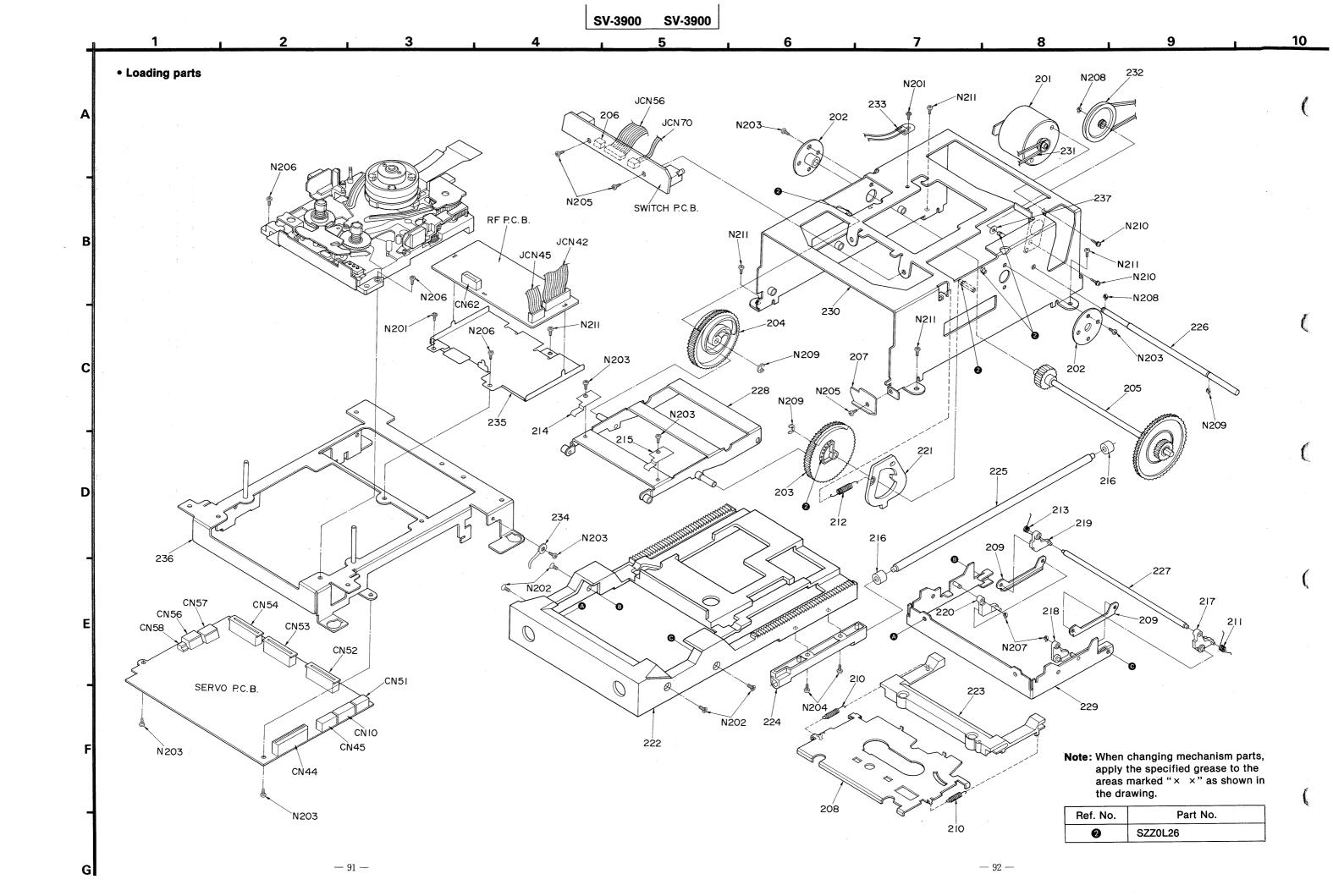
* The parenthesized indications in the Remarks columns specify the areas. (Refer to the cover page for area.)

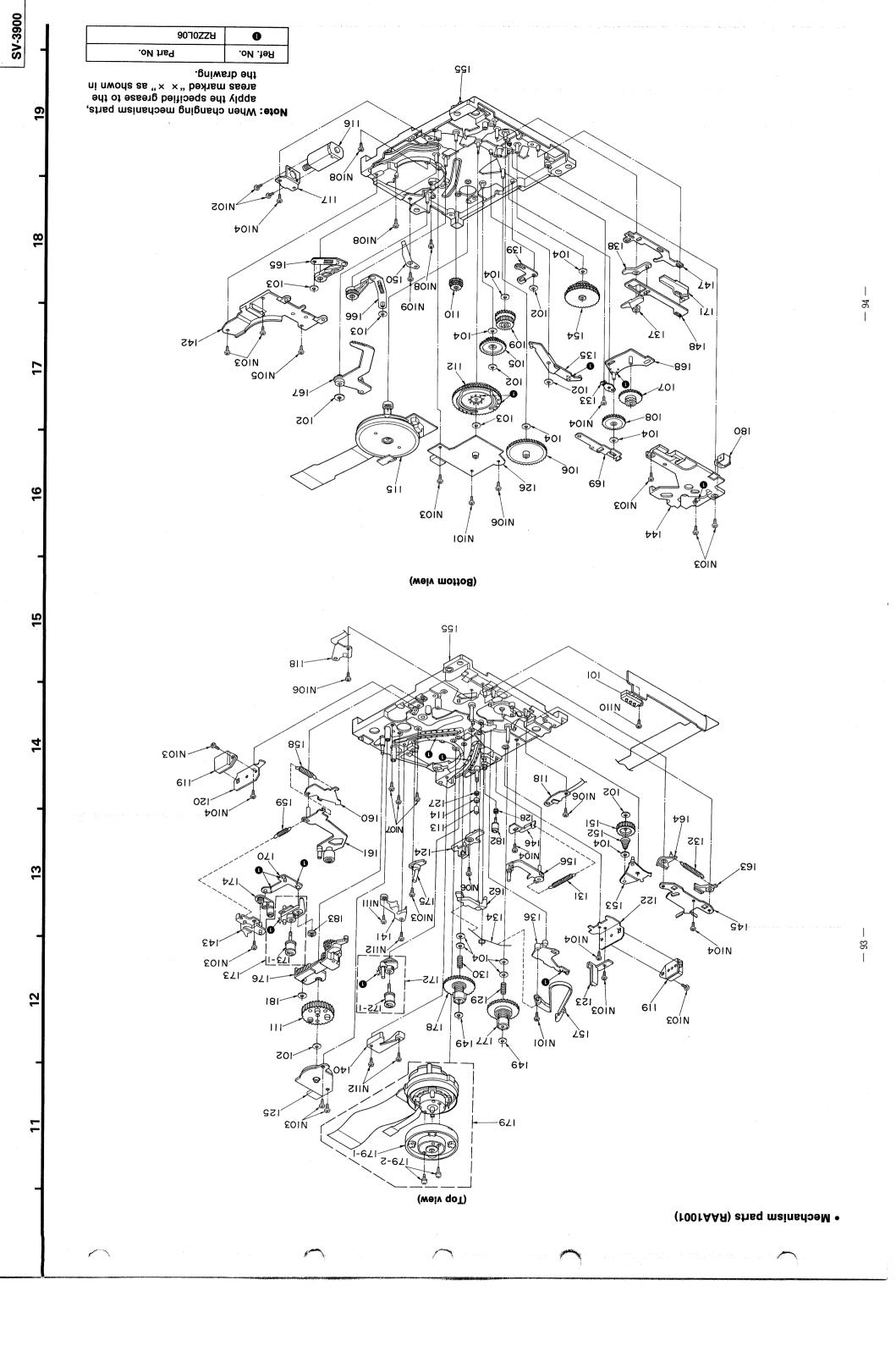
Parts without these indications can be used for all areas.

Ref. No.	Part No.	Part Name & Description	Remarks	Ref. No.	Part No.	Part Name & Description	Remarks
				IC603	UPD74HC04G	IC, INVERTER	
		INTEGRATED CIRCUIT (S)		IC801	M37450M4-356	IC, MICROCOMPUTER	
				IC802	M5M34050P	IC, DUAL TRANSCEIVER	
IC11	AN7812F	IC, REGULATOR		IC803, 804	UPD4021BC	IC, DATA DETECTOR	
IC12	AN7805F	IC, REGULATOR		IC805, 806	UPD4094BC	IC, DATA DETECTOR	
IC13	AN79M2OF	IC, REGULATOR		IC807	M74HC541P	IC, 3-STATE BUFFER	
IC14	AN79L20	IC, REGULATOR		IC808	M74HC393P	IC, 4-BIT BINARY COUNTER	
IC15	M5F78M12L	IC, REGULATOR		IC809	M74HC00P	IC, NAND GATE	
IC16	M5F79M12L	IC, REGULATOR		IC810	M74HC04P	IC, INVERTER	
IC71	AN79N05	IC, REGULATOR		IC811	M74HC4040P	IC, RIPPLE COUNTER	
IC101	AN7030SE2	IC, RF AMP		IC812	M74HC153P	IC, DATA SELECTOR	
IC102	AN7035SCE2	IC, PLAYBACK PLL		IC901, 902	NJM5532MDT	IC, BUFFER AMP	
IC201	MN6742SDR	IC, SERVO PROCESSOR		IC903, 904	M5219FPE1	IC, BUFFER AMP	
IC202	MN53020SDQ	IC, ATF		IC905, 906	NJM5532MDT	IC, SAMPLING HOLD	
IC203	AN8320NFA	IC, LINEAR SERVO		IC907, 908	NJM5532DD	IC, BUFFER AMP	
IC204, 205	AN3841SR	IC, MOTOR DRIVE		IC909	UPD4066BG-T1	IC, ANALOG SWITCH	
IC206	M5228FPE2	IC, ANALOG SWITCH		IC910	M5M34050FTP2	IC, AES/EBU SELECTOR	
IC207	UPD74HC04GE2	IC, INVERTER		IC911	UPD74HC04GE2	IC, INVERTER	
IC208	MN4066BS-T2	IC, ANALOG SWITCH		IC912	T74HCU04AFTP	IC, INVERTER	
IC209	AN78LO5ME2	IC, REGULATOR					
IC271	MN17541SDN2	IC, MECHANISM CONTROL				TRANSISTOR (S)	
IC272	ANGGO7NSE2	IC, MOTOR DRIVE					
IC273	AN1339SE2	IC, VOLTAGE CONTROL	W **** *** *** *** *** *** *** *** ***	Q102-105	UN5216-Q	TRANSISTOR	
IC274	TA7291S	IC, TRAY MOTOR CONTROL		Q106, 107	2SC3937TW	TRANSISTOR	
IC275	TC4S81FTX	IC, AND GATE		Q109	UN5216-Q	TRANSISTOR	
IC301	MN188161SDL	IC, SYSTEM CONTROL		Q201	2SB956R	TRANSISTOR	
IC302	MN1281R-TA	IC, RESET GENERATOR	***************************************	Q271	2SD1280STW	TRANSISTOR	
IC351	MN6624	IC, DIGITAL SIGNAL PROCESSOR		Q272	DTA123JKTW	TRANSISTOR	
IC352	SRM20256LM10	IC, S, RAM		Q274	2SB709RTW	TRANSISTOR	
IC357	M5238FPE1	IC. DIGITAL INPUT PLL		Q275	DTC124EKTW	TRANSISTOR	
IC358, 359	MN74HC00S	IC, NAND GATE		Q276	DTB113ZKTW	TRANSISTOR	
IC360	CD74HC4046A	IC, DF PLL		Q277	DTA114EKTW	TRANSISTOR	
IC361	MN74HCU04S	IC, INVERTER		Q301	XN1212TW	TRANSISTOR	
IC362	UPD74HC163G	IC, BINARY COUNTER		Q311-313	2SC3315CTA	TRANSISTOR	
IC405	AK5326-VP	IC. A/D CONVERTER		Q314, 315	XN1112TW	TRANSISTOR	
IC407	M5F78M05L	IC, REGULATOR		Q351	XN1212TW	TRANSISTOR	
IC408	M5F79M05L	IC, REGULATOR		Q352	2SA1309A-R	TRANSISTOR	
IC501	YM3404B	IC, DIGITAL FILTER		Q353	2SC1047DTA	TRANSISTOR	
IC502	MN53010PEH	IC, SERIAL/PARALLEL CONV.		Q401	UN4212TA	TRANSISTOR	
IC503-506	PCM56P-J	IC, D/A CONVERTER		Q501	UN4124TA	TRANSISTOR	
IC507, 508	UPD4053BGT	IC, SAMPLING HOLD		Q502	XN1112TW	TRANSISTOR	
IC507, 508	M5238FPE1	IC, OP. AMP		Q503, 504	2SC3315CTA	TRANSISTOR	
				Q509, 510	2SC3315CTA	TRANSISTOR	
IC511 IC513, 514	M5219FPE1	IC, BUFFER		Q509, 510 Q511	2SC3313C1A 2SC3311A-Q	TRANSISTOR TRANSISTOR	
······································	SVHHAF0614	IC, L. P. F.		Q511 Q512	2SA1309A-R	TRANSISTOR TRANSISTOR	
IC551, 552	M5218L	IC, CLASS AA AMP		Q512 Q551-554	2SD1450RTA	TRANSISTOR	
IC601	M50754-165FP	IC, PANEL CONTROL		Q601-605		TRANSISTOR TRANSISTOR	
1C602	AN6873S	IC, INVERTER		1 K001_002	UN4111	TIMINOTOTOR	

Ref. No.	Part No.	Part Name & Description	Remarks	Ref. No.	Part No.	Part Name & Description	Remarks
Q790	UN4111	TRANSISTOR					
Q1010	UN4114TA	TRANSISTOR				COIL (S)	
		DIODE (S)		L1, 2	SLQX400-D	COIL	<u> </u>
				L101	ELJFA470KF	COIL	
11-15	1SR35200TB	DIODE	Δ	L104	ELJFA180KF	COIL	
16	SVDS2V20	DIODE	Δ	L106	ELJFA101KF	COIL	
17	1SR35200TB	DIODE	Δ	L108	RLQZB471KT-D	COIL	
18	SVDS2V20	DIODE	Δ	L109, 110	ELJFA180KF	COIL	
19	1SR35200TB	DIODE	Δ	L120	RLQZB101KT-D	COIL	
79	1SR35200TB	DIODE	Δ	L202	RLM9R001-Z	COIL	
201	MA701TX	DIODE		L203	RLQZB101KT-D	COIL	
203	1N4606TR	DÍODE		L351	RLQZB2R2KT-D	COIL	
204	MA151ATW	DIODE		L352	RL03B002-M	COIL	
271	1S2473TR	DIODE		L550-552	EXCELDR35V	COMBINATION PART	
351	RVDSVC321	DIODE		L801-807	EXCELDR35V	COMBINATION PART	
401	MA165	DIODE		L901-908	EXCELDR35V	COMBINATION PART	
503-506	MA165	DIODE		L919-922	EXCELDR35V	COMBINATION PART	
509, 510	MA719TA	DIODE		-	LINGLIDITOOT	COMPLICATION LYIN	
	LN28RCPP-JF	L. E. D.				TRANSFORMER(S)	
605	LN31GPH-JF2	L. E. D.				TIMINGLORMER (3)	
	LN49YPH-JF1	L. E. D.		- $ $ _{T1}	SLT5M523	DOWED TRANSFORMED	_
	LN29RPH-JF1	L. E. D.		T901		POWER TRANSFORMER	Δ
	MA165	DIODE		1901	RLZ0006-0	TRANSFORMER	
	MA165	DIODE				0001114000 (0)	
	MA165	DIODE				OSC ILLATOR (S)	
	MA165	DIODE		- V001	DOLLOO TO A TO	OGGILL LEGO	
	MA165	DIODE		X201		OSC ILLATOR	
	MA165	DIODE		X202	RSXY8MOOMO1T	OSCILLATOR	
				X301	RSXY8MOOM01T	OSC ILLATOR	
	MA165	DIODE		X351	RSXC16M3J01	OSC ILLATOR	
	MA165	DIODE		X352	RSXC22M5J01	OSCILLATOR	
	MA165	DIODE	·	X353	RSXC24M5J01	OSCILLATOR	
	MA165	DIODE		X354	RSXA28M2J01	OSCILLATOR	
		DIODE		X601	RSXY6MOOMO1T	OSCILLATOR	
915-918	MA719TA	DIODE		X801	RSXB9M83M01	OSC ILLATOR	
		VARIABLE RESISTOR(S)				DISPLAY TUBE	
		· ·		1		DISTRICT TODE	
		V. R, RF RECORDING LEVEL ADJ.		FL601	RSL0062-F	DISPLAY TUBE	
		V. R, RF RECORDING LEVEL ADJ.					
		V. R, ATF GAIN ADJ.				FUSE (S)	
		V. R, PG PHASE ADJ.					
		V. R, TAPE BEGIN/END DET. ADJ.		F2	XBA2C04TB0S	FUSE, 250V 400mA	Δ
		V. R, REC BALANCE		F3	XBA2CO2TBOS	FUSE, 250V 200mA	Δ
		V. R, REC LEVEL					
501, 502	EVNDXAA00B53	V. R, DAC OUTPUT BALANCE ADJ.				SWITCH(ES)	
503, 504	EVNDXAA00B15	V. R, DAC OFF-SET ADJ.					
551 I	EVU57A022A14	V. R, HEADPHONES LEVEL		S1	ESB8249V	SW, POWER	Δ
				S2			\triangle
		COMPONENT COMBINATION (S)				SW, SAMPLING FREQ. SELECTOR	
						SW, ANALOG/DIGITAL SELECTOR	
01, 902 F	XCEMT471BT	COMPONENT COMBINATION	*****			SW, AUTO/MANUAL	***************************************

Ref. No.	Part No.	Part Name & Description	Remarks	Ref. No.	Part No.	Part Name & Description	Remarks
S614	EVQQTG05R	SW, RESET		CN57	RJP6G28ZA	CONNECTOR (6P)	
S616	EVQQTG05R	SW, COUNTER RESET		CN58	RJT036W002	CONNECTOR (2P)	
S618	EVQQTG05R	SW, REW		CN62	RJS7Q11ZA	CONNECTOR (7P)	
S619	EVQQTG05R	SW, FF		CN67	RHR190ZA	CONNECTOR (3P)	
S622	EVQQTG05R	SW, LOCAL/REMOTE		CN801	SJSD1205	CONNECTOR (12P)	
S623	EVQQTG05R	SW, COUNTER MODE		CN802, 803	RJS1A5009	CONNECTOR (9P)	
S624	EVQQTG05R	SW, REVERSE SKIP		CN804	RJS1A5115	CONNECTOR (15P)	
S625	EVQQTG05R	SW, FORWARD SKIP		CN805	RJS5T4ZA	CONNECTOR (5P)	
S626	EVQQTG05R	SW, PAUSE		CN806	RHR193ZA	CONNECTOR (5P)	
S627	EVQQTG05R	SW, REC		CN808	RJS3T4ZA	CONNECTOR (3P)	
S628	EVQQTG05R	SW, AUTO REC MUTE		CN901, 902	RJJ93MS02	ANALOG IN TERMINAL	
S631	EVQQTG05R	SW, PLAY		CN903, 904	RJJ93MS01	ANALOG OUT TERMINAL	
S632	EVQQTG05R	SW, STOP		CN905	RJJ93MS02	AES/EBU IN TERMINAL	A STATE OF THE STA
S635	EVQQTG05R	SW, OPEN/CLOSE		CN906	RJJ93MS01	AES/EBU OUT TERMINAL	
S701	EVQWR4002	SW, TAPE HOLE DET.		CN907	SJF3057-7A-1	IEC TYPE2 IN/OUT TERMINAL	
S702	EVQWXN001	SW, LOAD DET.		CN911, 912	REZ0294A	CONNECTOR (6P)	
S703	EVQWXM001	SW, MODE DET.		CN913	RHR190ZA	CONNECTOR (3P)	
S751, 752	SSPD18-1	SW, LEAF (OPEN/CLOSE)		CN1003	REE0295	CONNECTOR (10P)	
S801	RST1H001	SW, DIP		CN1003A	RHR199ZA	CONNECTOR (12P)	The state of the s
				CN1003A	RHR191ZA	CONNECTOR (4P)	
S802	RST1D001	SW, DIP		001004	IUIIIIIIII	OOMILOTOR(41)	
S901	SSS189	SW, OUTPUT LEVEL SELECTOR				METER	
S902	RST1D001	SW, DIP				METER	
		JACK (S) &SOCKET (S)		HM1002	RSE0001	HOUR METER	
JK1	SJVD06	AC INLET	Δ			CONNECTOR ASS' Y	
JK550	SJJD19	HEADPHONES	22	l			
JK1002	QJS1955H	PARALLEL REMOTE		JCN1	RWJ0710130QQ	CONNECTOR ASS' Y(10P)	<u> </u>
SK801	RJS1E0164	IC SOCKET		JCN3		CONNECTOR ASS' Y(10P)	
31/001	IMOTEOTOA	TO SOUNCE		JCN5	RWJ0706200QQ	CONNECTOR ASS' Y(6P)	
		CONNECTOD (C) & TEDMINAL (C)		JCN8	RWJ1006400KQ	CONNECTOR ASS' Y(6P)	Market Control of the
	,	CONNECTOR (S) & TERMINAL (S)		JCN9	REZ0121A	CONNECTOR ASS' Y(6P)	
	DVID4 0074	CONTRACTOR (10D)		JCN16		CONNECTOR ASS' Y(5P)	
CN1	RHR197ZA	CONNECTOR (10P)		+		CONNECTOR ASS' Y(5P)	
CN2	SJSD1005	CONNECTOR (10P)		JCN30			
CN3, 4	RHR197ZA	CONNECTOR (10P)		JCN42	REZ0125A	CONNECTOR ASS' Y (10P)	
CN5	RHR193ZA	CONNECTOR (6P)		JCN45	REZ0126A	CONNECTOR ASS' Y (5P)	
CN6, 7	RJS6T4ZA	CONNECTOR (6P)		JCN56	REZ0127A	CONNECTOR ASS' Y (6P)	
CN10-12	RJP6G27ZA	CONNECTOR (6P)		JCN67	RWJ0703230QQ	CONNECTOR ASS' Y (3P)	
CN13	RJS3T4ZA	CONNECTOR (3P)		JCN70	REZ0138A-1	CONNECTOR ASS' Y (2P)	
CN16	RHR192ZA	CONNECTOR (5P)		JCN806	RWJ0705230QQ	CONNECTOR ASS' Y (5P)	
CN18	RJS5T4ZA	CONNECTOR (5P)		JCN913	RWJ0703230QQ		
CN21, 22	RJS6Q8ZA	CONNECTOR (6P)		JCN1003		CONNECTOR ASS' Y (10P)	
CN23	RJS4T4ZA	CONNECTOR (4P)		JCN1003A	RWJ0712120QQ		
CN26	RJP8G27ZA	CONNECTOR (8P)		JCN1004	RWJ0704150QQ	CONNECTOR ASS' Y (4P)	
CN28	RJP10G27ZA	CONNECTOR (10P)					
CN29	RJS5T4ZA	CONNECTOR (5P)				RELAY (S)	
CN41	RJP10G27ZA	CONNECTOR (10P)					
CN43, 44	SJSD1721	CONNECTOR (17P)		RLY401	AG80239	RELAY	
CN45	RJP5G27ZA	CONNECTOR (5P)		RLY901, 902	AG80239	RELAY	
CN51	RJP3G27ZA	CONNECTOR (3P)					
CN52-54	RJU051W015	CONNECTOR (15P)					
L	RJP4G28ZA	CONNECTOR (4P)	+	11	+		





■ REPLACEMENT PARTS LIST

Notes: * Important safety notice:

Components identified by △ mark have special characteristics important for safety. When replacing any of these components use only manufacturer's specified parts.

* The parenthesized indications in the Remarks columns specify the areas. (Refer to the cover page for area.)

Parts without these indications can be used for all areas.

			<u> </u>		1	T	
Ref. No.	Part No.	Part Name & Description	Rema rks	Ref. No.	Part No.	Part Name & Description	Remarks
				N5	XTB3+16JFR	SCREW	
		CABINET AND CHASSIS		N6	XSN26+4FZ	SCREW	
				N7	XTBS3+8FFZ1	SCREW	
	RMY0030	RADIATOR		N8	XTV3+6G	SCREW	-
	SNE1004-1	GND PLATE		N9	XTW3+6T	SCREW	
	SMN2080	EARTH PLATE		N10	XVE3A4FP	SCREW	
	RMA0202	FL HOLDER		N11	XNS12	NUT	
	REZ0136A	CONNECTOR		N12	XNS7S	NUT	
	REZ0137A	CONNECTOR		N13	XTB3+6F	SCREW	
	RGK0165A-H	TRAY ORNAMENT		N14	XNS9	NUT	
	RGQ0059	CARD SPACER		N15	XWC3B	WASHER	
	RGQ0046	WIRE SADDLE		N16	XWC4B	WASHER	
0	RFKHV3900EBH	REAR PANEL ASS' Y	(EB)	N17	XTB3+8GFR	SCREW	
.0	RFKHV3900EGH	REAR PANEL ASS'Y	(EG)	N18	XYA3+CJ16FR	SCREW	
1	RGU0030A	BUTTON, POWER		N19	XTB4+8F	SCREW	
2	RGU0235-H	BUTTON, OPERATION A		N20	XTB3+10GFR	SCREW	
3	RGU0236A-H	BUTTON, OPERATION B		N21	XSB3+8	SCREW	
4	SHE181	P. C. B. SUPPORT		N22	XTB3+20JFR	SCREW	
.5	RGU0238	BUTTON, SWITCH					
.6	RGV0033	KNOB, SAMPLING FREQUENCY				MECHANISM PARTS	
7	RGW0055	KNOB, BALANCE/H. P. LEVEL					
18	RKA0009-K	FOOT		101	EVQWR4002	CASSETTE SW.	-
19	RKF0089	HOLDER (SERVO P. C. B.)		102	QBW2008	WASHER	
20	RKM0076-H	CABINET		103	QBW2030	WASHER	
21	RMA0192	HOLDER (HEADPHONES)		104	QBW2059	WASHER	
22	RMA0193	HOLDER (BALANCE/REC LEVEL)		105	RDG0066-1	MAIN GEAR A	
23	RMG0134	MECHANISM SUPPORT		106	RDG0067	MAIN GEAR B	
24	RMK0068-2	CHASSIS		107	RDG0068	IDLER GEAR (P)	
25	RFKGV3900PPH	FRONT PANEL ASS' Y		108	RDG0069	IDLER GEAR (F)	
26	RYQ0049-1	KNOB, REC LEVEL		109	RDG0070	COUNTER GEAR	
27	SHE185-1	P. C. B. SUPPORT		110	RDG0073-1	MODE REPEATING GEAR	
28	RMA0408	ANGLE		111	RDK0006-1	LOAD CAM	
29	RMY0047-1	RADIATOR		112	RDK0007-1	MODE CAM	
30	RMC0117	EARTH PLATE		113	RDP0020	FIXED POST	
31	RMZ0095-1	INSULATION SHEET (A)		114	RDP0021	FIXED POST FLANGE	
32	RMZ0096	INSULATION SHEET (B)		115	REM0001	CAPSTAN UNIT	
33	SHR330	BINDER		116	REM0009	MODE MOTOR ASS'Y	
34	SHR9815	RIVET		117	RMN0028	HOLDER, MODE MOTOR	
35	SJT347	FUSE HOLDER		118	REQ0012	INTERFACE P. C. B.	
36	SJT345	HOUR METER HOLDER		119	REQ0014	BEGIN/END DET. SENSOR ASS'Y	
				120	RMN0030	BEGIN DET. ANGLE	
		SCREWS		122	RMN0029	END DET. ANGLE	
				123	RMQ0059	LEAD OPENER	
N1	XTB3+8J	SCREW		124	REQ0018	BEGIN/END DET. LED ASS' Y	
√2	SHDD4-1	SCREW		125	EVQWXN001	LOAD SW ASS' Y	
√3	SNE2095-5	SCREW		126	EVQWXM001	MODE SW ASS'Y	·
N4	XTBS3+8JFZ1	SCREW		127	RMB0061	SPRING, FIXED POST	

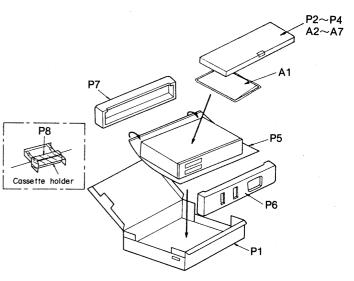
Ref. No.	Part No.	Part Name & Description	Remarks	Ref. No.	Part No.	Part Name & Description	Remarks
28	RMB0063	SPRING, GUIDE ROLLER		178	RXR0007	T. REEL ASS' Y	
29	RMB0071	SPRING, S. REEL		179	VEG0752-1	CYLINDER UNIT	
30	RMB0073-1	SPRING, T. REEL		179-1	VEH0460	UPPER CYLINDER	
31	RMB0074	SPRING, TENSION		179-2	VHD0593	SCREW	
32	RMB0075	SPRING, BRAKE		180	RSJ0006	PLUNGER	
33	RMC0034	ANGLE		181	QBW2081A	WASHER	
34	RME0037-1	SPRING, BT		182	RXP0031	GUIDE ROLLER	
35	RML0088	LEVER, PINCH		183	RHW12009	GUIDE WASHER	
36	RML0090	LEVER, TENSION					
37	RML0094	LEVER, S. BRAKE				SCREWS	
38	RML0095	LEVER, T. BRAKE					
39	RML0103	LEVER LOAD SELECT		N101	QHQ1371	SCREW	
40	RMQ0052	S. STOPPER		N102	XQN14+C16	SCREW	-
41	RMQ0053	T. STOPPER		N103	RHQ0014	SCREW	-
42	RMQ0055	LOAD GUIDE HOLDER		N104	RHQ0007	SCREW	
43	RMQ0056-1	GUIDE ARM STOPPER	-	N105	RHQ0015	SCREW	
44	RMQ0058	MODE GUIDE PLATE		N106	RHQ0016	SCREW	3
45	RMQ0062	IDLER GUIDE		N107	RHQ0017	SCREW	
46	RMQ0063	TENSION SPRING HOOK		N108	XQN16+A45T	SCREW	
47	RMQ0064-1	S. BRAKE DRIVE PLATE		N109	XQN2+A2	SCREW	
48	RMQ0065-1	T. BRAKE DRIVE PLATE		N110	RHQ0018	SCREW	
49	RNW172ZA	WASHER		N111	RHQ0019	SCREW	
	RUS740ZA	EARTH ANGLE		N112	RHQ0020	SCREW	
50	ļ			N112	MIQUUZU	SOILEN	
51	RDG0071	IDLER GEAR	· 			LOADING DADTO	
152	RMB0069	IDLER SPRING				LOADING PARTS	
153	RXL0051	IDLER ARM ASS' Y		-	DEMONIDATO IV	MOTOD AGG! V	<u> </u>
54	RXG0011-2	DRIVE GEAR		201	RFKPVDA10-K	MOTOR ASS' Y	
155	RXK0019	CHASSIS UNIT		202	RDB0032	HOLDER, GEAR SHAFT MAIN GEAR	
.56	RXL0035-4	TENSION ARM ASS' Y		203	RDG0120-2		
57	RXL0036	TENSION BAND ASS'Y		204	RDG0122-1	MAIN GEAR	
158	RMB0066	PIN-PRESSURE SPRING		205	RFKNVDA10BK	GEAR SHAFT ASS' Y	
159	RMB0067	PINCH ROLLER SPRING		206	RJP2G27ZA	CONNECTOR (CN72)	
60	RMM0036	PIN-PRESSURE LINK		207	RMA0194	SHAFT FRAME	
161	RXL0046	PINCH ARM ASS'Y		208	RMA0486	ANGLE, CASSETTE HOLDER	
162	RXL0048	BT LEVER ASS' Y		209	RMA0200	HOLDER ARM	
163	RXL0049	S. BRAKE ASS' Y		210	RMB0110	SPRING	
164	RXL0050	T. BRAKE ASS' Y		211	RMB0111	SPRING	
165	RXL0052-1	S. LOAD ARM		212	RMB0131	SPRING	
166	RXL0054-1	T. LOAD ARM		213	RMB0144	SPRING	
167	RXL0056-2	LOAD LEVER		214	RMC0050-1	ANGLE	
168	RXL0057	P. F. IDLER ASS' Y		215	RMC0051-1	ANGLE	
169	RXL0058	LEVER, P. F. SELECTION	-	216	RMG0090	RUBBER	
170	RXM0018	GUIDE LINK ASS' Y		217	RML0139-1	HOLDER, SHAFT	
71	RXM0019	PLUNGER LINK ASS' Y		218	RML0140-1	HOLDER	
72	RXP0016-2	S. POST ROLLER ASS' Y		219	RML0141-1	HOLDER, SHAFT	
72-1	RXP0008	POST ROLLER		220	RML0142-1	HOLDER	
173	RXP0017-1	T. POST ROLLER ASS' Y		221	RML0150-1	HOLDER, MAIN GEAR	
73-1	RXP0008	POST ROLLER		222	RMR0206-H	TRAY	
74	RXP0020-1	T. GUIDE ROLLER		223	RMR0207-H	CASSETTE HOLDER	
75	RXQ0057	T. INCLINED BASE ASS' Y		224	RMR0209-1	SHAFT ANGLE	
	+	LOAD HOLDER ASS' Y		225	RMS0158-1	SHAFT	
76	RXQ0079-1 RXR0006	S. REEL ASS' Y		226	RMS0160	SHAFT	

Ref. No.	Part No.	Part Name & Description	Remarks
227	RMS0165	SHAFT	
228	RXA0046	SUB FRAME	
229	RXA0047	CASSETTE HOLDER	
230	RXK0059	FRAME	
231	SMQ20025	BELT	
232	SMQ40032	PULLY GEAR	
233	EYHS78R	DEW SENSOR	
234	SHE36-3	EARTH TERMINAL	
235	RSC0066	SHIELD PLATE	
236	RXA0060	MECHANISM FRAME	
237	RMX0044	WASHER	
		SCREWS	,
N201	XSN2+3	SCREW	
N202	XSS26+4FZ	SCREW	
N203	XTB3+6J	SCREW	
N204	XTN3+10G	SCREW	
N205	XTN3+6B	SCREW	
N206	XTN3+8F	SCREW	
N207	XUC15FT	WASHER	
N208	XUC2FT	WASHER	
N209	XUC3FT	WASHER	
N210	XYN26+C33	SCREW	
N210 N211	XTB3+6F	SCREW	
		PACKING MATERIAL	
P1	RPG0936	PACKING CASE	
P2	RPN0391-1	PAD	
P3	RPN0392	PAD	
P4	XZB09X10C03	PROTECTION BAG (SCREW)	
P5	RPF0017	PROTECTION BAG (UNIT)	
P6	RPN0221	PAD (FRONT)	
P7	RPN0222	PAD (BACK)	
P8	RPH0065-1	TRAY COVER	
		ACCESSORIES	
A1	RQF1179	INSTRUCTION MANUAL	1
12	SJPD19-1E	COAXIAL CABLE	
A 3	SJAD8	AC POWER SUPPLY CORD	△ (EB)
4 3	RJA0003-K	AC POWER SUPPLY CORD	<u>∧</u> (EG)
\4	RYQ0059-1	RACK MOUNT KIT	
N5	RYQ0060-1	RACK MOUNT KIT	
76	XYN3+F10FZ	SCREW	
17	RT-RCLP	CLEANING TAPE	
		<pre><pre><pre><pre><pre><pre><pre><pre></pre></pre></pre></pre></pre></pre></pre></pre>	
		BOARDS ASS' Y>	
CB1	REP0814D	MAIN P. C. B. ASS' Y	(NLA)

Part No.	Part Name & Description	Remarks
RFKBV3900EBA	PARALLEL REMOTE P. C. B.	(NLA)
	DIGITAL TERMINAL P. C. B.	(NLA)
	AC IN P. C. B.	(NLA)
	POWER SWITCH P. C. B.	(NLA)
	POWER TRANSFORMER P. C. B.	(NLA)
	ANALOG TERMINAL P.C.B.	(NLA)
	BALANCE/REC LEVEL P. C. B.	(NLA)
	OUTPUT LEVEL P. C. B.	(NLA)
	VOLTAGE ADJ. P. C. B.	(NLA)
RFKBV3900EBI	MAIN P. C. B.	(NLA)
REP0693B	REGULATOR IC P. C. B.	(NLA)
	POWER SUPPLY P. C. B.	(NLA)
REP0694B	OPERATION P. C. B.	(NLA)
	HEADPHONES JACK P. C. B.	(NLA)
REP0417A	RF P. C. B., SERVO P. C. B. ASS' Y	(NLA)
RFKBV3700-N	RF P. C. B.	(NLA)
RFKBV3700-0	SERVO P. C. B.	(NLA)
REP0421A	TRAY MOTOR P. C. B.	(NLA)
	SWITCH P.C.B.	(NLA)
REQ0012	CONNECTOR P. C. B.	(NLA)
REP0737A	INTERFACE P. C. B.	(NLA)
	<mechanism ass'y=""></mechanism>	
RAA1001	MECHANISM UNIT	(NLA)
RAA1001	MECHANISM UNIT	(NLA)
	RFKBV3900EBA RFKBV3900EBI REP0693B REP0694B REP0417A RFKBV3700-N RFKBV3700-O REP0421A REQ0012 REP0737A	RFKBV3900EBA PARALLEL REMOTE P. C. B. DIGITAL TERMINAL P. C. B. AC IN P. C. B. POWER SWITCH P. C. B. POWER TRANSFORMER P. C. B. ANALOG TERMINAL P. C. B. BALANCE/REC LEVEL P. C. B. OUTPUT LEVEL P. C. B. VOLTAGE ADJ. P. C. B. REP0693B REGULATOR IC P. C. B. POWER SUPPLY P. C. B. REP0694B OPERATION P. C. B. REP0417A RF P. C. B., SERVO P. C. B. ASS' Y RFKBV3700-N RF P. C. B. RFKBV3700-O SERVO P. C. B. REP0421A TRAY MOTOR P. C. B. SWITCH P. C. B. REQ0012 CONNECTOR P. C. B. CMECHANISM ASS' Y> CMECHANISM ASS' Y>

Note: Printed circuit board assembly and mechanism assembly with mark (NLA) is no longer available after discontinuation of the product.

PACKING



RESISTORS & CAPACITORS

Notes : * Capacity values are in microfarads (uF) unless specified otherwise, P=Pico-farads(pF) F=Farads(F) * Resistance values are in ohms, unless specified otherwise, 1K=1,000(OHM) , 1M=1,000k(OHM)

Ref. No.	Part No.	Values & Remarks	Ref. No.	Part No.	Values & Remark	ß	Ref. No.	Part No.	Val	ues & Remarks
			R209	ERJ6GEYJ153V	1/10W 15K		R294-296	ERJ6GEYJ473V	1/10W	47K
		RESISTORS	R210	ERJ6GEYJ102V	1/10W 1K		R297	ERJ6GEYJ220	1/10W	22
			R212	ERJ6GEYJ105	1/10W 1M		R298	ERJ6GEYJ473V	1/10W	47K
R11	ERDS2TJ392T	1/4W 3.9K	R213	ERJ6GEYJ274V	1/10W 270K		R301-306	ERJ6GEYJ332V	1/10W	3. 3K
R12	ERD25FJ102	1/4W 1K 🛆	R214	ERJ6GEYJ682V	1/10W 6.8K		R309-312	ERJ6GEYJ332V	1/10W	3. 3K
R13, 14	ERG1ANJP560S	1W 56	R215	ERJ6GEYJ184V	1/10W 180K		R313	ERJ6GEYJ331V	1/10W	330
R101, 102	ERJ6GEYJ221V	1/10W 220	R216	ERJ6GEYJ101V	1/10W 100		R314-320	ERJ6GEYJ561V	1/10W	560
R103, 104	ERJ6GEYJ681V	1/10W 680	R217, 218	ERJ6GEYJ472V	1/10W 4.7K		R321	ERJ6GEYJ331V	1/10W	330
R112, 113	ERJ6GEYJ332V	1/10W 3.3K	R219, 220	ERJ6GEYJ103V	1/10W 10K		R322	ERJ6GEYJ561V	1/10W	560
R115	ERJ6GEYJ332V	1/10W 3.3K	R221, 222	ERJ6GEYJ474V	1/10W 470K		R323	ERJ6GEYJ473V	1/10W	47K
R117	ERJ6GEYJ102V	1/10W 1K	R223, 224	ERJ6GEYJ222V	1/10W 2. 2K		R324	ERJ6GEYJ331V	1/10W	330
R118	ERJ6GEYJ471V	1/10W 470	R225, 226	ERJ6GEYJ184V	1/10W 180K		R325	ERJ6GEYJ561V	1/10W	560
R123	ERJ6GEYJ822V	1/10W 8. 2K	R227	ERJ6GEYJ102V	1/10W 1K		R326	ERJ6GEYJ332V	1/10W	3. 3K
R124	ERJ6GEYJ152V	1/10W 1.5K	R228	ERJ6GEYJ222V	1/10W 2. 2K		R331-333	ERJ6GEYJ105	1/10W	1M
R126	ERJ6GEYJ152V	1/10W 1.5K	R229	ERJ6GEYJ103V	1/10W 10K		R334-336	ERJ6GEYJ561V	1/10W	560
R128	ERJ6GEYJ822V	1/10W 8. 2K	R230	ERJ6GEYJ681V	1/10W 680		R337-339	ERJ6GEYJ102V	1/10W	1K
R131	ERJ6GEYJ331V	1/10W 3.2K	R231	ERJ6GEYJ821V	1/10W 820		R340	ERJ6GEYJ332V	1/10W	3. 3K
			R232	ERJ6GEYJ333V	1/10W 33K		R341	ERJ6GEYJ561V	1/10W	560
R132, 133	ERJ6GEYJ221V	1/10W 220	ļ	 					1/10W	10K
R134	ERJ6GEYJ102V	1/10W 1K	R233	ERJ6GEYJ393V	1/10W 39K		R342	ERJ6GEYJ103V		330
R135, 136	ERJ6GEYJ471V	1/10W 470	R234	ERJ6GEYJ333V	1/10W 33K		R343	ERJ6GEYJ331V	1/10W	
R150	ERJ6GEYJ102V	1/10W 1K	R235	ERJ6GEYJ473V	1/10W 47K		R344	ERJ6GEYJ223V	1/10W	22K
R151, 152	ERJ6GEYJ471V	1/10W 470	R236	ERJ6GEYJ332V	1/10W 3. 3K		R351, 352	RRJ6GCAD822T	1/10W	8. 2K
R153	ERJ6GEYJ223V	1/10W 22K	R237	ERX12SJR33E	1/2W 0.33		R354	RRJ6GCAD822T	1/10W	8. 2K
R154, 155	ERJ6GEYJ471V	1/10W 470	R240	ERJ6GEYJ151V	1/10W 150		R355	ERJ6GEYJ102V	1/10W	1K
R156, 157	ERJ6GEYJ222V	1/10W 2.2K	R241	ERJ6GEYJ333V	1/10W 33K		R356-358	ERJ6GEYJ103V	1/10W	10K
R158	ERJ6GEYJ102V	1/10W 1K	R242	ERJ6GEYJ103V	1/10W 10K		R359	ERJ6GEYJ102V	1/10W	1K
R159	ERJ6GEYJ223V	1/10W 22K	R243	ERJ6GEYJ473V	1/10W 47K		R360	RRJ6GCAD183T	1/10W	18K
R160	ERJ6GEYJ221V	1/10W 220	R244	ERJ6GEYJ102V	1/10W 1K		R361	RRJ6GCAD153T	1/10W	15K
R162	ERJ6GEYJ472V	1/10W 4.7K	R245	ERX12SJR33E	1/2W 0.33		R362	ERJ6GEYJ331V	1/10W	330
R164, 165	ERJ6GEYJ472V	1/10W 4.7K	R247	ERJ6GEYJ331V	1/10W 330		R363	ERJ6GEYJ103V	1/10W	10K
R167	ERJ6GEYJ332V	1/10W 3.3K	R248	ERJ6GEYJ822V	1/10W 8.2K		R364	ERJ6GEYJ105	1/10W	1M
R168	ERJ6GEYJ102V	1/10W 1K	R249	ERJ6GEYJ472V	1/10W 4.7K		R365	ERJ6GEYJ331V	1/10W	330
R169	ERJ6GEYJ471V	1/10W 470	R250	ERJ6GEYJ153V	1/10W 15K		R366	RRJ6GCAD683T	1/10W	68K
R172	ERJ6GEYJ102V	1/10W 1K	R251	ERJ6GEYJ104V	1/10W 100K		R367	ERJ6GEYJ331V	1/10W	330
R173, 174	ERJ6GEYJ222V	1/10W 2.2K	R271, 272	ERJ6GEYJ103V	1/10W 10K		R368	ERJ6GEYJ561V	1/10W	560
R175	ERJ6GEYJ151V	1/10W 150	R273	ERJ6GEYJ681V	1/10W 680		R409, 410	ERDAS3J102T	1/4W	1K
R176	ERJ6GEYJ104V	1/10W 100K	R274	ERSB39JR82U	1/4W 0.82		R411, 412	ERDAS3J273T	1/4W	27K
R178	ERJ6GEYJ820V	1/10W 82	R275	ERJ6GEYJ473V	1/10W 47K		R413, 414	ERDAS3J472T	1/4W	4. 7K
R181	ERJ6GEYJ331V	1/10W 330	R277, 278	ERJ6GEYJ333V	1/10W 33K		R415, 416	ERDAS3J273T	1/4W	27K
R193-195	ERJ6GEYJ102V	1/10W 1K	R279	ERJ6GEYJ683V	1/10W 68K		R417	ERJ6GEYJ331V	1/10W	330
R196	ERJ6GEYJ103V	1/10W 10K	R280	ERJ6GEYJ333V	1/10W 33K		R419, 420	ERJ6GEYJ331V	1/10W	330
R197		1/10W 1K	R281	ERJ6GEYJ332V	1/10W 3. 3K		R421-424	ERJ6GEYJ225V	1/10W	2. 2M
R201	ERJ6GEYJ102V		R282	ERJ6GEYJ682V	1/10W 6.8K		R447	ERJ6GEYJ561V	1/10W	560
R202, 203	ERJ6GEYJ223V	 	R284	 	1/10W 10K		R500	ERJ6GEYJ102V	1/10W	1K
R204	ERJ6GEYJ103V	 	R285	ERJ6GEYJ104V	1/10W 100K		R503-508	ERJ6GEYJ102V	1/10W	1K
R205	ERJ6GEYJ683V		R286	ļ			R509, 510	ERJ6GEYJ473V	1/10W	47K
R206	ERJ6GEYJ684V		R287	ERJ6GEYJ684V			R517-520	ERDAS3J472T	1/4W	4. 7K
R207	ERJ6GEYJ472V	 	R289, 290	ERJ6GEYJ473V	1/10W 47K		R521, 522	ERDS2TJ682T	1/4W	6. 8K
	ļ	<u> </u>						ERDS2TJ103	1/4W	10K
R208	ERJ6GEYJ683V	1/10W 68K	R292, 293	ERJ6GEYJ563V	1/10W 56K		R523, 524	FUND 19109	1/47	TOIL

Ref. No.	Part No.	Val	ues & Remarks	Ref. No.	Part No.	Val	ues & Remarks	Ref. No.	Part No.	Va	lues & Remarks
R525, 526	ERDAS3J472T	1/4W	4. 7K	R925, 926	ERJ6GEYJ105	1/10W	1M	C29	ECEA1VU330	35V	33U
R527, 528	ERDAS3J332T	1/4W	3. 3K	R927, 928	ERDAS3J332T	1/4W	3. 3K	C30, 31	ECKR1H103ZF5	50V	0. 01U
R529, 530	ERDAS3J152T	1/4W	1.5K	R929, 930	ERDAS3J562T	1/4W	5. 6K	C32	ECEA1EU470	25V	47U
R531-534	ERDAS3J332T	1/4W	3. 3K	R931-938	EROS2TKF1002	1/4W	10K	C34	ECKR1H103ZF5	50V	0. 01U
R535, 536	ERDAS3J152T	1/4W	1.5K	R939, 940	EROS2TKF3903	1/4W	390K	C79	ECKR1H103ZF5	50V	0. 01U
R537, 538	ERJ6GEYJ105	1/10W	1M	R941-944	EROS2TKF2002	1/4W	20K	C101, 102	ECUV1H222KBN	50V	2200P
R569-572	ERJ6GEYJ124V	1/10W	120K	R945, 946	ERDAS3J122T	1/4W	1. 2K	C103, 104	ECUV1H471KCN	50V	470P
R575, 576	ERDS2TJ102	1/4W	1K	R947, 948	EROS2TKF5103	1/4W	510K	C105	ECUV1H470JCN	50V	47P
R577	ERDAS3J332T	1/4W	3. 3K	R949, 950	EROS2TKF2202	1/4W	22K	C107	ECUV1H222KBN	50V	2200P
R577A	ERDS2TJ103	1/4W	10K	R951-954	EROS2TKF75RO	1/4W	75. 0	C110	ECEAOJK221B	6. 3V	220U
R578	ERDAS3J332T	1/4W	3. 3K	R955, 956	EROS2TKF5103	1/4W	510K	C123	ECUV1C105ZFM	16V	1 U
R578A	ERDS2TJ103	1/4W	10K	R957, 958	EROS2TKF2202	1/4W	22K	C124, 125	ECUV1H472KBN	50V	4700P
R579	ERDAS3J332T	1/4W	3. 3K	R959-962	ERDAS3J103T	1/4W	10K	C126, 127	ECUV1H470JCN	50V	47P
R579A	ERDS2TJ123	1/4W	12K	R963, 964	ERJ6GEYJ473V	1/10W	47K	C129, 130	ECEAOJKS220B	6. 3V	22U
R580	ERDAS3J332T	1/4W	3. 3K	R965	ERDS2TJ221	1/4W	220	C131	ECUV1H222KBN	50V	2200P
R580A	ERDS2TJ123	1/4W	12K	R967, 968	ERDAS3J472T	1/4W	4. 7K	C150	ECUV1H471KCN	50V	470P
R581A	ERDS2TJ104	1/4W	100K	R969-972	ERDS2TJ331	1/4W	330	C151	ECUV1H222KBN	50V	2200P
R581	ERJ6GEYJ472V	1/10W	4. 7K	R973, 974	ERJ6GEYJ153V	1/10W	15K	C152	ECUV1H102KBN	507	1000P
R582A	ERDS2TJ104	1/4W	100K	R975, 976	ERJ6GEYJ101V	1/10W	100	C154	ECUV1H220JCN	50V	22P
R582	ERJ6GEYJ472V	1/10W	4. 7K	R977	ERD25FJ101	1/4W	100 △	C155, 156	ECUV1H102KBN	50V	1000P
R583A	ERDAS3G100T	1/4W	10	R978, 979	ERJ6GEYJ330V	1/10W	33	C157	ECUV1C105ZFM	16V	1U
R583	ERJ6GEYJ472V	1/10W	4. 7K	R980	ERJ6GEYJ331V	1/10W	330	C159	ECUV1H22OJCN	50V	22P
R584A	ERDAS3G100T	1/4W	10	R981	ERJ6GEYJ334V	1/10W	330K	C161	ECUV1H562KBN	50V	5600P
R584	ERJ6GEYJ472V	1/10W	4. 7K	R982	ERJ6GEYJ473V	1/10W	47K	C163	ECUV1H562KBN	50V	5600P
R585A	ERDAS3G330T	1/4W	33	R983	ERJ6GEYJ332V	1/10W	3. 3K	C166	ECUV1E153MBN		0. 015U
R585	ERJ6GEYJ472V	1/10W	4. 7K	R984, 985	ERDS2TJ750	1/4W	75	C167	ECUV1H102KBN	50V	1000P
R586A	ERDAS3G330T	1/4W	33	R986	ERJ6GEYJ223V	1/10W	22K	C168	ECUV1H270JCN	50V	27P
R586	ERJ6GEYJ472V	1/10W	4. 7K	R987-990	ERDAS3J392T	1/4W	3. 9K	C169	ECUV1H100JCN	50V	10P
R587A	ERDAS3G332T	1/4W	3. 3K	R991, 992	ERDAS3J101T	1/4W	100	C171, 172	ECUV1H680JCN	50V	68P
R587	ERJ6GEYJ472V	1/10W	4. 7K	R992A	ERJ6GEYJ100	1/10W	100	C173	ECUV1H471KCN	50V	470P
R588A	ERDAS3G332T	1/4W	3. 3K	R993, 994	ERDAS3J101T	1/4W	100	C174	ECUV1E104MBN	25V	0. 1U
R588	ERJ6GEYJ472V	1/10W	4, 7K	R995, 996		1/4W	47K	C175	ECUV1H472KBN	50V	4700P
R589, 590	ERDAS3G102T	1/4W	1K	R997	ERJ6GEYJ223V	ļ	22K	C176	ECUV1F472RDN ECUV1E333MDN		0. 033U
R591, 592	ERDS2EJ121	1/4W		R998	ERJ6GEYJ101V	ļ		C177, 178			
R593-596	ERDS2EJ121	1/4W	120 2. 2K	R999		1/10W	100		ECUV1H101KCN	50V	100P
R601-604		 			ERJ6GEYJ223V	1/10W	22K	C179	ECUV1E104MBN	25V	0. 10
R605	ERDS2TJ472	1/4W	4. 7K	R1021		1/10W	2. 7K	C181	ECUV1C105ZFM	16V	1U
	ERDS2TJ100	1/4W	10	R1022	ļ	1/10W	270	C185, 186	ECUV1H331KCN	50V	330P
R606-608	ERDS2TJ102	1/4₩	1K	R1023	ERJ6GEYJ394V	1/10W	390K	C187	ECUV1H470JCN	50V	47P
R610, 611	ERDS2TJ471	1/4W	470					C188	ECUV1H331KCN	50V	330P
R612	ERDS2TJ102	1/4W	1K			CAPACIT	ORS	C190	ECUV1H180JCN	50V	18P
R613-619	ERDS2TJ103	1/4W	10K					C201	ECEAOJK470	6. 3V	47U
R620	ERDS2TJ471	1/4₩	470	C1	ECKWKC103PF2		0.01U A	C202	ECUV1E104ZFN	25V	0. 1U
R621	ERDS2TJ105T	1/4W	1M	C11-19	ECKR1H103ZF5		0. 01U	C203, 204	ECUV1H180JCN	50V	18P
	ERDS2TJ472	1/4W	4. 7K	C20	ECEA1EPZ332E		3300U	C205, 206	ECUV1E104ZFN	25V	0. 1U
R821	ERDS2TJ105T	1/4W	1M ·	C21	ECEA1CU100	16V	100	C207	ECUV1H271KCN	50V	270P
R825-831	ERDS2TJ472	1/4W	4. 7K	C22	ECEA1EPZ332E		3300U	C208	ECUV1H221KCN	50V	220P
	ERDAS3J392T	1/4W	3. 9K	C23	ECEA1CU100	16V	10U	C209, 210	ECUV1E104MBN	25V	0. 1U
	ERDAS3J102T	1/4W	1K	C24	ECES1EU682G	25V	6800U	C211	ECUV1H221KCN	50V	220P
	ERDAS3J392T	1/4W	3. 9K	C25	ECEA1CU100	16V	10U	C212, 213	ECEA1CK220	16V	22U
R913-920	ERDAS3J332T	1/4W	3. 3K	C26	ECEA1CU472E	16V	4700U	C214, 215	ECUV1H101KCN	50V	100P
R921, 922	ERDAS3J102T	1/4W	1K	C27	ECEA1CU100	16V	100	C216, 217	ECUV1C105ZFM	16V	1U
R923, 924	ERD25FJ331	1/4W	330 ⚠	C28	ECEA1HU221B	50V	220U	C218, 219	ECUV1H390KCN	50V	39P

Ref. No.	Part No.	Values & Remarks	Ref. No.	Part No.	Values & Remarks	Ref. No.	Part No.	Values & Remarks
220, 221	ECUV1H121KCN	50V 120P	C371	ECUV1E104ZFN	25V 0. 1U	C656	ECBT1H102KB5	50V 1000P
222, 223	ECUV1C105ZFM	16V 1U	C372	ECEAOJU330	6. 3V 33U	C657	ECEAOJK470	6. 3V 47U
224	ECQV1H683JZ3	50V 0. 68U	C373	ECUV1H103ZFN	50V 0.01U	C658	ECBT1E103ZF	25V 0. 01U
225, 226	ECUV1H332MBN	50V 3300P	C374	ECEAOJU330	6. 3V 33U	C659	ECEA1VK100B	35V 10U
227	ECUV1E333MDN	25V 0.033U	C376	ECUV1E103KBN	25V 0.01U	C660	ECBT1E103ZF	25V 0. 01U
228	ECUV1H181KCN	50V 180P	C377, 378	ECUV1E223MBN	25V 0. 022U	C694	ECBT1E103ZF	25V 0. 01U
229	ECEAOJK221B	6. 3V 220U	C379	ECUV1E104ZFN	25V 0.1U	C751	ECKW1H103ZF5	50V 0. 01U
230	ECEAOJK470	6. 3V 47U	C385	ECUV1H103ZFN	50V 0.01U	C801, 802	ECKR1H103ZF5	50V 0. 01U
231	ECEA1AU470	10V 47U	C389	ECUV1E104ZFN	25V 0. 1U	C802A	ECQV1H104JZ3	50V 0. 1U
232	ECEA1CK220	16V 22U	C390	ECUV1H103ZFN	50V 0.01U	C803	ECKR1H103ZF5	50V 0.01U
233	ECUV1E104ZFN	25V 0.1U	C391	ECUV1E103KBN	25V 0.01U	C803A	ECQV1H104JZ3	50V 0.1U
234	ECEA1CK470	16V 47U	C392-395	ECUV1H103ZFN	50V 0.01U	C804	ECKR1H103ZF5	50V 0. 01U
235, 236	ECUV1E104ZFN	25V 0.1U	C396	ECUV1H390JCN	50V 39P	C804A	ECQV1H104JZ3	50V 0.1U
237	ECUV1C105ZFM	16V 1U	C397	ECUV1H103ZFN	50V 0.01U	C805-813	ECKR1H103ZF5	50V 0. 01U
238	ECUV1E223MBN	25V 0. 022U	C401	ECUV1E104ZFN	25V 0. 1U	C814	ECEA1CU220	16V 22U
239	ECUV1E683MBN	25V 0. 068U	C404, 405	ECUV1H220JCN	50V 22P	C815, 816	ECCR1H470K5	50V 47P
240	ECUV1E153MBN	25V 0. 015U	C407, 408	ECUV1H103ZFN	50V 0.01U	C817	ECCR1H100K5	50V 10P
241	ECUV1E104MBN	25V 0. 0130	C411, 412	ECEA1CPZ470B	16V 47U	C818-820	ECKR1H103ZF5	50V 0. 01U
242-246	ECUV1E104ZEN	25V 0. 1U	C425-430	ECUV1E104ZFN	25V 0. 1U	C901-904	ECEA1CPZ470B	16V 47U
247, 248	ECUV1E333MDN	25V 0. 033U	C431, 432	ECEAOJPZ221B	6. 3V 220U	C905-912	ECUV1H22OJCN	50V 22P
249	ECUV1E153MBN	25V 0. 015U	C433, 434	ECQB1H272JZ	50V 2700P	C913, 914	ECEA1CPZ470B	16V 47U
250	ECUV1E104MBN	25V 0. 0130	C435, 434	ECUV1E104ZFN	25V 0. 1U	C915, 916	ECEA1CPZ101B	16V 100U
251-253	ECUV1E104MBN	25V 0. 1U	C438	ECEAOJU101B	6. 3V 100U	C917, 918	ECUV1H103ZFN	50V 0. 01U
260	ECUV1E334ZFM	25V 0. 33U	C439, 440	ECEAOGUTOTB ECEA1CU220	16V 22U	C919, 920	ECUV1H220JCN	50V 22P
		25V 0. 330	C500			C921-924		50V 22F
268 269	ECUV1C105ZFM			ECUV1H102KBN		C925-928	ECUV1H150JCN	16V 220U
	ECUV1E103KBN ECEA0JK470	25V 0. 01U	C501, 502	ECHR1H271JZ3			ECEA1CPZ221B	50V 0. 01U
270 271		6. 3V 47U 25V 0. 1U	C503, 504 C505, 506	ECUV1H103ZFN		C929, 930 C931	ECUV1H103ZFN ECEA0JU330	6. 3V 33U
	ECUV1E104ZFN			ECHR1H103JZ3		C932, 933		
272	ECUV1E223MBN	25V 0. 022U	C507, 508	ECEA1CU101	16V 100U		ECUV1E224ZFN	
273	ECUV1H101KCN	50V 100P	C509, 510	ECEA1CPZ101B	16V 100U	C934-937	ECUV1H470KCN	50V 47P
274	ECUV1E103KBN	25V 0. 01U	C511-514	ECEAOJPZ221B	6. 3V 220U	C938, 939	ECUV1E224ZFN	25V 0. 22U
275	ECUV1C105ZFM	16V 1U	C515-518	ECUV1H22OJCN	50V 22P	C940-942	ECUV1H103ZFN	50V 0. 01U
	ECUV1H221KCN	50V 220P	C519, 520	ECEA1CU101	16V 100U	C943	ECUV1H470KCN	50V 47P
280	ECUV1C334ZFN	16V 0. 33U	C521	ECUV1E104ZFN	25V 0. 1U	C944	ECUV1H103ZFN	50V 0. 01U
301-307	ECUV1H103ZFN	50V 0.01U	C523, 524	ECUV1H103ZFN	50V 0.01U	C945	ECKW1H103ZF5	50V 0. 01U
341	ECUV1E104ZFN	25V 0.1U	C525	ECEAOJU101B	6. 3V 100U	C946	ECUV1H101KCN	50V 100P
342	ECUV1H103ZFN	50V 0.01U	C526	ECUV1H103ZFN	50V 0.01U	C947~950	ECUV1H103ZFN	50V 0, 01U
350	ECUV1H103ZFN	50V 0.01U	C545, 546	ECUV1H22OJCN	50V 22P	C953-960	ECHR1H221JZ3	50V 220P
351	ECUV1E473MBN	25V 0.047U	C547, 548	ECUV1H103ZFN	50V 0.01U	C966	ECUV1H103ZFN	50V 0.01U
352	ECUV1H820JCN	50V 82P	C549	ECUV1H470KCN	50V 47P	C990	ECUV1H103ZFN	50V 0.01U
353	ECUV1H471JCN	50V 470P	C550A	ECQV1H104JZ3	50V 0. 1U	C991, 992	ECEA1CU331	16V 330U
354, 355	ECUV1H221JCN	50V 220P	C550	ECUV1H470KCN	50V 47P	C995, 996	ECUV1H103ZFN	50V 0. 01U
356	ECUV1H151JCN	50V 150P	C551, 552	ECQV1H104JZ3	50V 0.1U	C1020-1028	ECUV1H103ZFN	50V 0. 01U
357	ECEAOJU330	6. 3V 33U	C553, 554	ECKT1H101KB	50V 100P	1		
358-361	ECUV1H180JCN	50 V 18P	C555, 556	ECQV1H104JZ3	50V 0.1U			
362, 363	ECUV1H150JCN	50V 15P	C561, 562	ECEA1CU220	16V 22U			
364	ECUV1H050CCN	50V 5P	C563, 564	ECEA1CN100SB	16V 10U			
365	ECUV1H102KBN	50V 1000P	C602, 603	ECEAOJK470	6. 3V 47U			
366	ECUV1H103ZFN	50V 0.01U	C604, 605	ECBT1E103ZF	25V 0.01U			
367, 368	ECEAOJU330	6. 3V 33U	C652	ECBT1H102KB5	50V 1000P			
369	ECEA0JU221	6. 3V 220U	C654	ECBT1H102KB5	50V 1000P			
370	ECUV1H150JCN	50V 15P	C655	ECEA1VK100B	35V 10U	1		