

Service Manual

ORDER NO.
RRV4491

DJ MIXER

DJM-900SRT

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Model	Type	Power Requirement	Remarks
DJM-900SRT	LSYXJ8	AC 110V to 240 V	
DJM-900SRT	UXJCB	AC 120 V	
DJM-900SRT	XJCN5	AC 220 V to 240 V	

The DJM-900SRT Serato DJ Edition is based on the DJM-900NXS, and the plugin DJ software "Serato DJ" is added. The main differences of the DJM-900SRT from the DJM-900NXS are as follows:

Serato DJ installer, a Control Vinyl, and a Control CD are provided.

Noncontact fader is adopted for the crossfader.

Color changes: Control panel, fader panel, CDC panel, COLOR controls, fader knobs, and LED illumination

"LINE" terminal of CH2 and CH3 of the rear panel is changed to "PHONO" terminal.

In this service manual, any difference from the DJM-900NXS is described in each corresponding article.

For the matters not described in this manual, refer to the service manual of the DJM-900NXS.

Model No.	Order No.	Remarks
DJM-900NXS/SYXJ8	RRV4170	

For SPECIFICATIONS and PANEL FACILITIES, refer to the operating instructions.

NOTE: The model name of this product is DJM-900SRT Serato DJ Edition.

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1. SERVICE PRECAUTIONS

1.2 NOTES ON REPLACING

The part listed below is difficult to replace as a discrete component part.

A When the part listed in the table is defective, replace whole Assy.

Assy Name	PCB Assy Part No.	Parts that is Difficult to Replace			
		Ref No.	Function	Part No.	Remarks
MAIN Assy	DWX3502	IC1406	12V → 1.2V DC/DC converter	BD9326EFJ	IC with heat-pad
		IC1407	12V → 3.3V DC/DC converter	BD9326EFJ	IC with heat-pad
		IC501	ETHER UCOM	R5S76700B200BG	BGA
		IC1801	USB UCOM	ADSP-BF524BBCZ-3A	BGA
		IC2214	FPGA	XC3S50A-4FTG256C	BGA
		IC701	DSP	D810K013DZKB400	BGA
		Q3405	Transistor	2SD1760F5 (R)	Transistor with heat-pad
		Q3406	Transistor	2SB1184F5 (R)	Transistor with heat-pad
		Q3407	Transistor	2SD1760F5 (R)	Transistor with heat-pad
		Q3408	Transistor	2SB1184F5 (R)	Transistor with heat-pad
CDCB Assy	DWX3191	IC3601	CDC SENSOR	AD7147ACPZ500RL7	IC with heat-pad

1.3 SERVICE NOTICE

VOLTAGE MONITORING

This unit always monitors for power failure and will shut itself off immediately after an error is detected.

A power failure is indicated with flashing of the LFO FORM LED (Intervals: 250 ms [Lit 125 ms/Unlit 125 ms]).

All the LEDs other than LFO FORM will be unlit, and all the switches and VRs will be disabled.

Repair the unit according to the diagnostic procedures described in "5.4 Information on Power Diagnostics" on the Service Manual for DJM-900NXS.

CONFIRMATION OF USER-SETTING

This product has user- and club-setting data. Be sure to confirm those data before starting repair, although changing them may not have a large effect. Use the Check Sheet in "8.5," to which you can transcribe the settings, as required.

The settings are stored in Flash ROM (IC502) on the Main Assy.

To display the [USER SETUP] screen, hold [ON/OFF (UTILITY)] pressed for at least 1 sec.

To display the [CLUB SETUP] screen, press [POWER] (ON) while holding [ON/OFF (UTILITY)] pressed.

FLASH ROM ON THE MAIN Assy

Never replace the Flash ROM (IC502) on the MAIN Assy during servicing.

If the FLASH ROM is assumed to be defective, replace the whole MAIN Assy.

This FLASH ROM contains data that can only be written in at the factory.

An IEEE 802.3-based MAC address specific to this unit has been written.

CROSS FADER ASSY replacement

Noncontact faders are adopted for the cross faders with this product. Compared with conventional contact-type cross faders, noncontact faders offer dozens of times the durability.

Because high accuracy is required for assembly of the fader section, the service part of this section will be supplied as a whole Assy. Use the CROSS FADER Assy (DXA2257) for replacement.

After replacement, be sure to perform "FDR SET" in Cross Fader Calibration mode of Test mode. If you don't, the unit may not start up properly.

Note that performing "FDR SET" in Cross Fader Calibration mode is also required after replacement of the MAIN Assy.

Handling of the Serato DJ Control Vinyl and Control CD

As a Control CD and Control Vinyl for Serato DJ provided with this product are consumables, they are not assigned as service parts. Users will have to purchase them.

If there is an inquiry about that, inform the user of the following:

- Control Vinyl
Purchase from an agent of Serato DJ or the online store of Serato (<http://serato.com/>).
- Control CD
Refer to information at the Pioneer DJ support site (<http://pioneerdj.com/support/>).

2. SPECIFICATIONS

2.1 SPECIFICATIONS

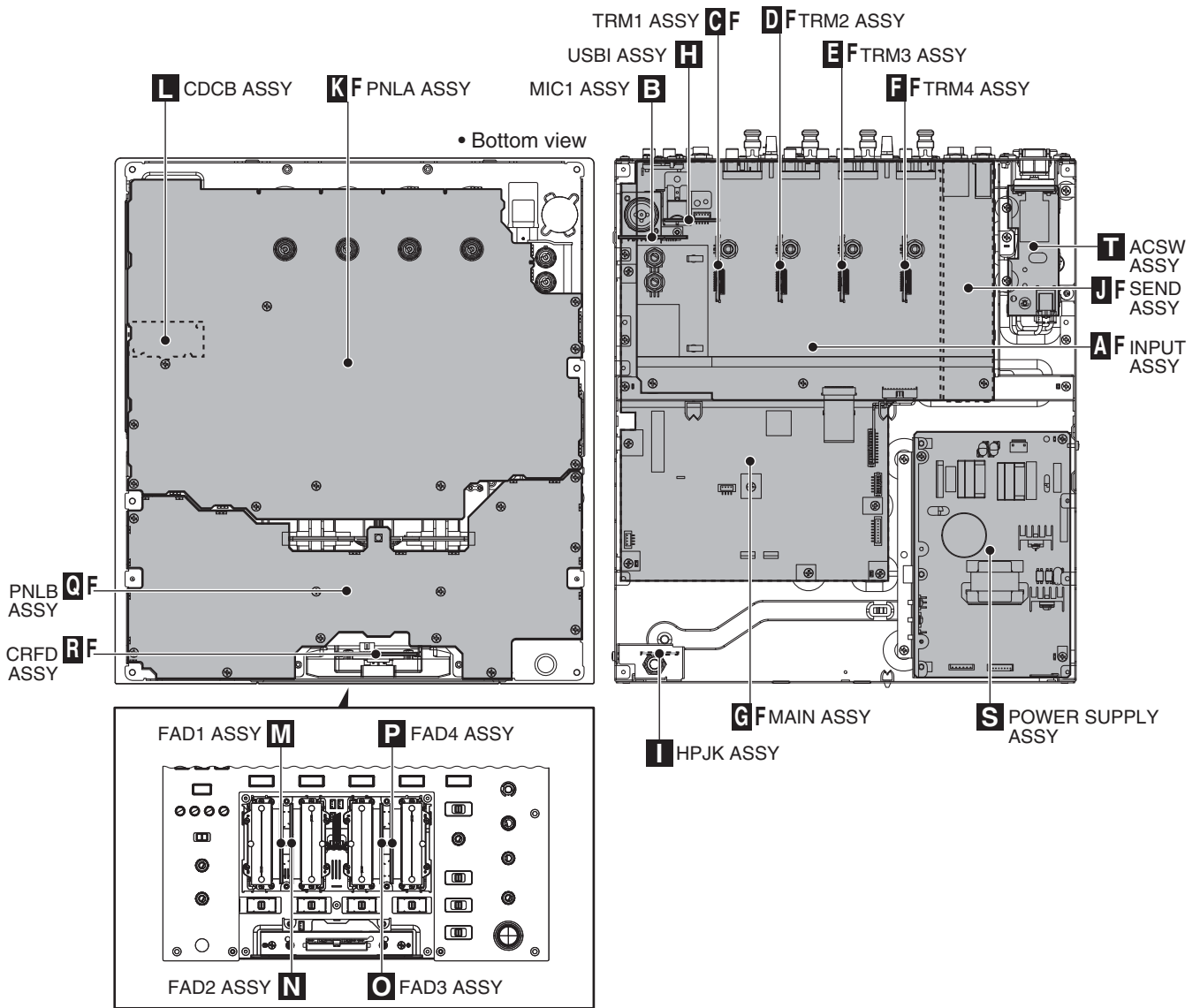
Refer to Operating instructions for DJM-900SRT

Accessories

- Record (CONTROL VINYL)
- CD-ROM (CONTROL CD)
- CD-ROM (DXX2757 : Installation Disc)
- USB cable (DDE1128)
- Power cord(LSYXJ8: ADG7062, UXJCB: DDG1108, XJCN5: ADG7105)
- Operating instructions(LSYXJ8: DRB1701, UXJCB: DRB1702, XJCN5: DRB1703)

3. BASIC ITEMS FOR SERVICE

3.3 PCB LOCATIONS




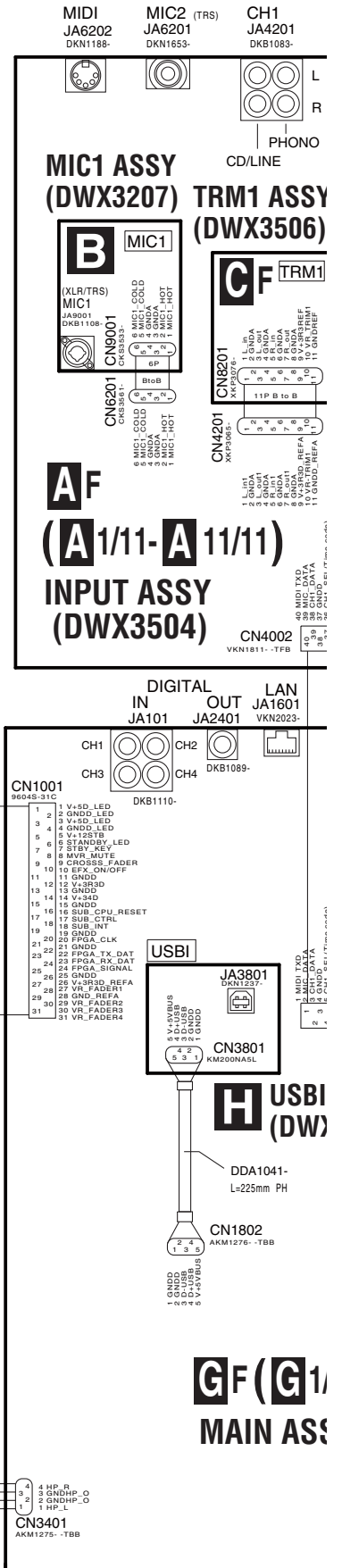
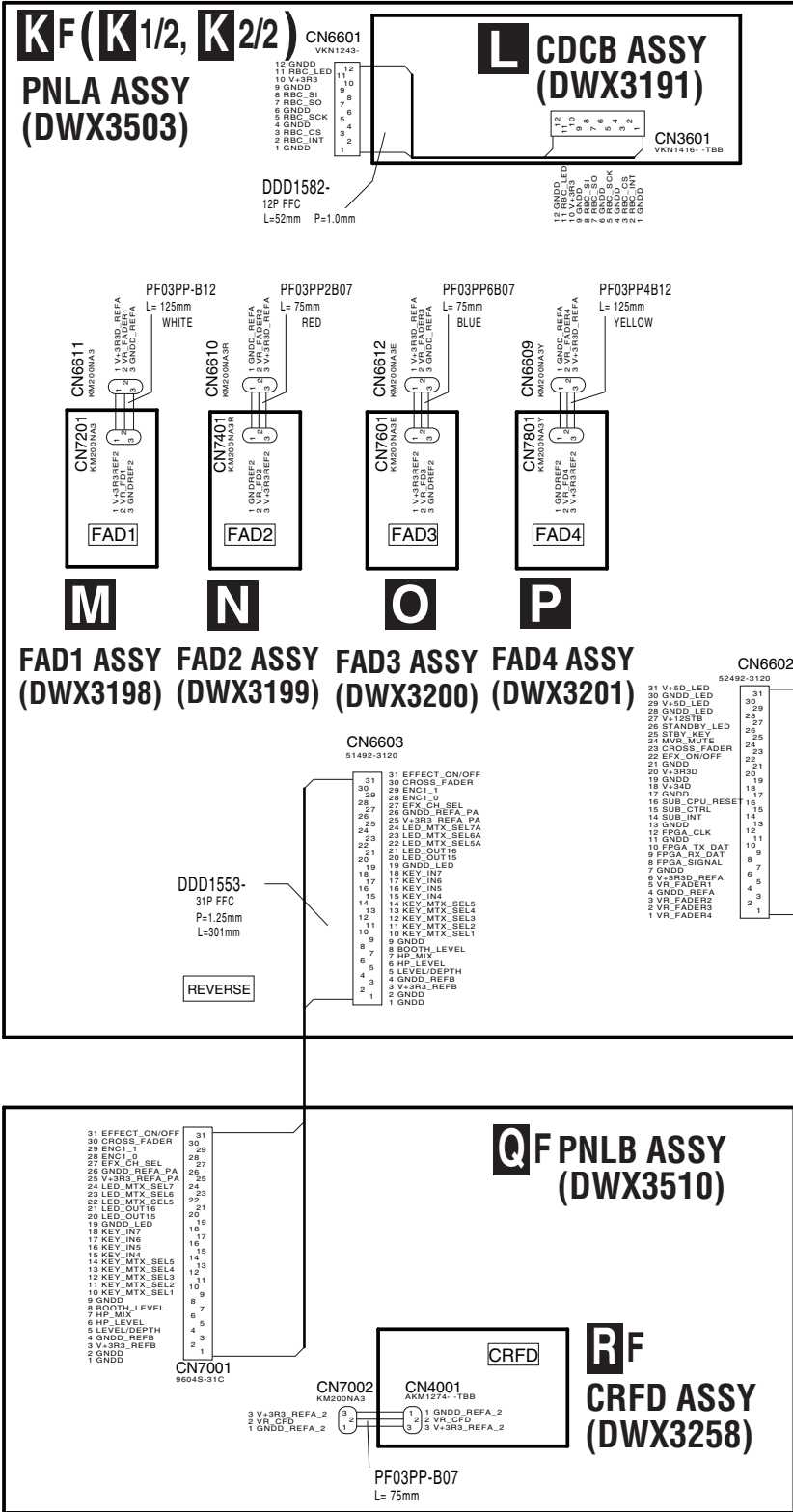
NOTES: ● Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.
 ● The ⚠ mark found on some component parts indicates the importance of the safety factor of the part.
 Therefore, when replacing, be sure to use parts of identical designation.

Mark No.	Description	Part No.	Mark No.	Description	Part No.
LIST OF ASSEMBLIES					
NSP	1..MOTHER ASSY	DWM2511	NSP	1..SUB ASSY	DWM2420
	2..MAIN ASSY	DWX3502		2..ACSW ASSY	DWR1490
	2..CDCB ASSY	DWX3191		2..PNLB ASSY	DWX3510
	2..USBI ASSY	DWX3192		2..FAD1 ASSY	DWX3198
				2..FAD2 ASSY	DWX3199
NSP	1..AUDIO ASSY	DWM2512		2..FAD3 ASSY	DWX3200
	2..INPUT ASSY	DWX3504		2..FAD4 ASSY	DWX3201
	2..SEND ASSY	DWX3505		2..MIC1 ASSY	DWX3207
	2..TRM1 ASSY	DWX3506		2..HPJK ASSY	DWX3208
	2..TRM2 ASSY	DWX3507			
	2..TRM3 ASSY	DWX3508		1..PNLA ASSY	DWX3503
	2..TRM4 ASSY	DWX3509	⚠	1..POWER SUPPLY ASSY	DWR1492
			NSP	1..CROSS FADER ASSY	DXA2257
				2..CRFD ASSY	DWX3258

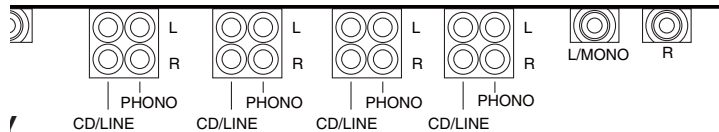
4. BLOCK DIAGRAM

4.1 OVERALL WIRING DIAGRAM

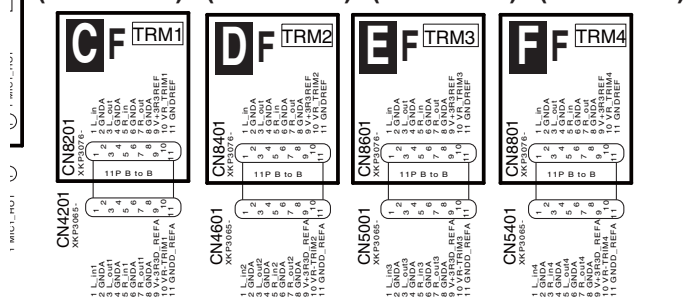
- When ordering service parts, be sure to refer to "EXPLODED VIEWS and PARTS LIST" or "PCB PARTS LIST".
- The Δ mark found on some component parts indicates the importance of the safety factor of the part.
- Therefore, when replacing, be sure to use parts of identical designation.
-  : The power supply is shown with the marked box.



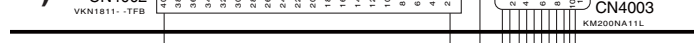
201 (TRS) CH1 CH2 CH3 CH4 RETURN (TS)
 653- JA4201 JA4601 JA5001 JA5401 JA5801 JA5802
 DKB1083- DKB1083- DKB1083- DKB1083- DKN1654- DKN1652-



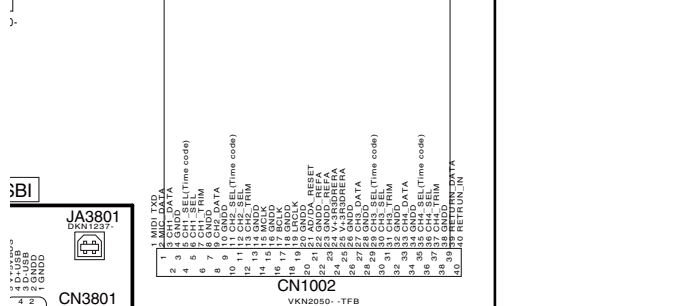
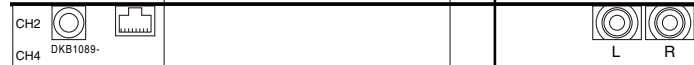
TRM1 ASSY (DWX3506) TRM2 ASSY (DWX3507) TRM3 ASSY (DWX3508) TRM4 ASSY (DWX3509)



A 11/11)
SY
4)



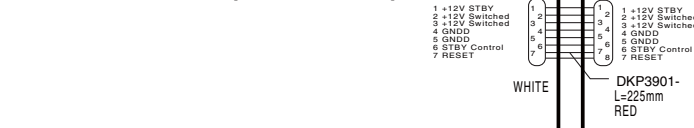
SIGNAL OUT LAN JA1601 LAN
 JA2401 VKN2023- 40P FFC L=90mm P=0.5mm



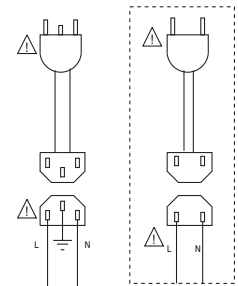
H USBI ASSY (DWX3192)



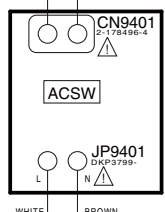
G (G 1/21 - G 21/21)
MAIN ASSY (DWX3502)



AC POWER CORD UXJCB : DDG1108-
 AC POWER CORD LSYXJ8:ADG7062-
 XJCN5:ADG7105-
 AC INLET ASSY UXJCB : DKP3927-
 AC INLET ASSY LSYXJ8/XJCN5:DKP3782-

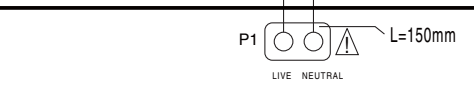
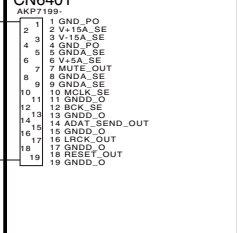


T ACSW ASSY (DWR1490)



The mark found on some component parts should be replaced with same parts(safety regulation authorized) of identical designation

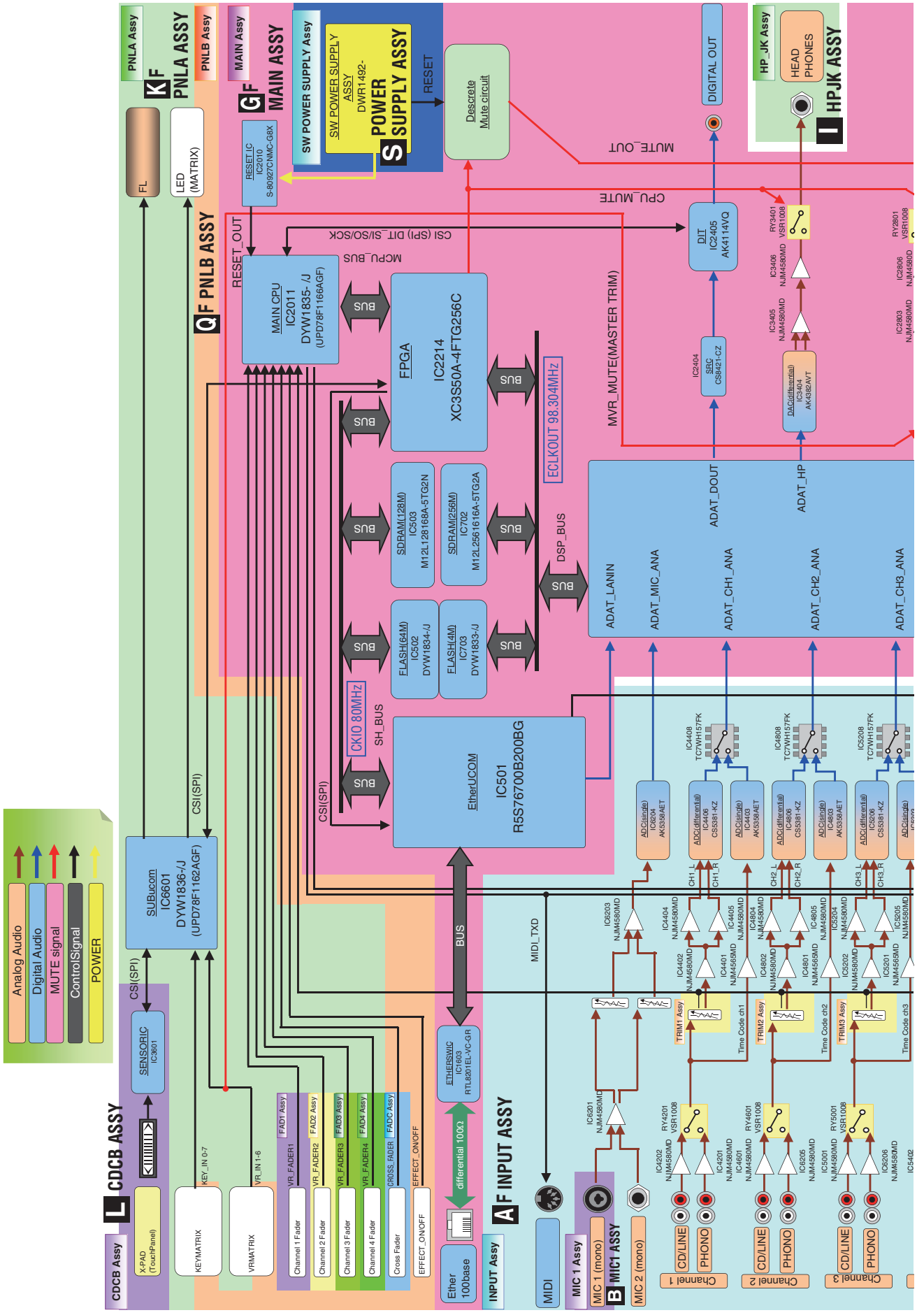
MASTER1 MASTER2 SEND (TS)
 JA2801 JA2802 JA6401 JA6402
 DKB1093- DKB1093- DKN1652- DKN1652-

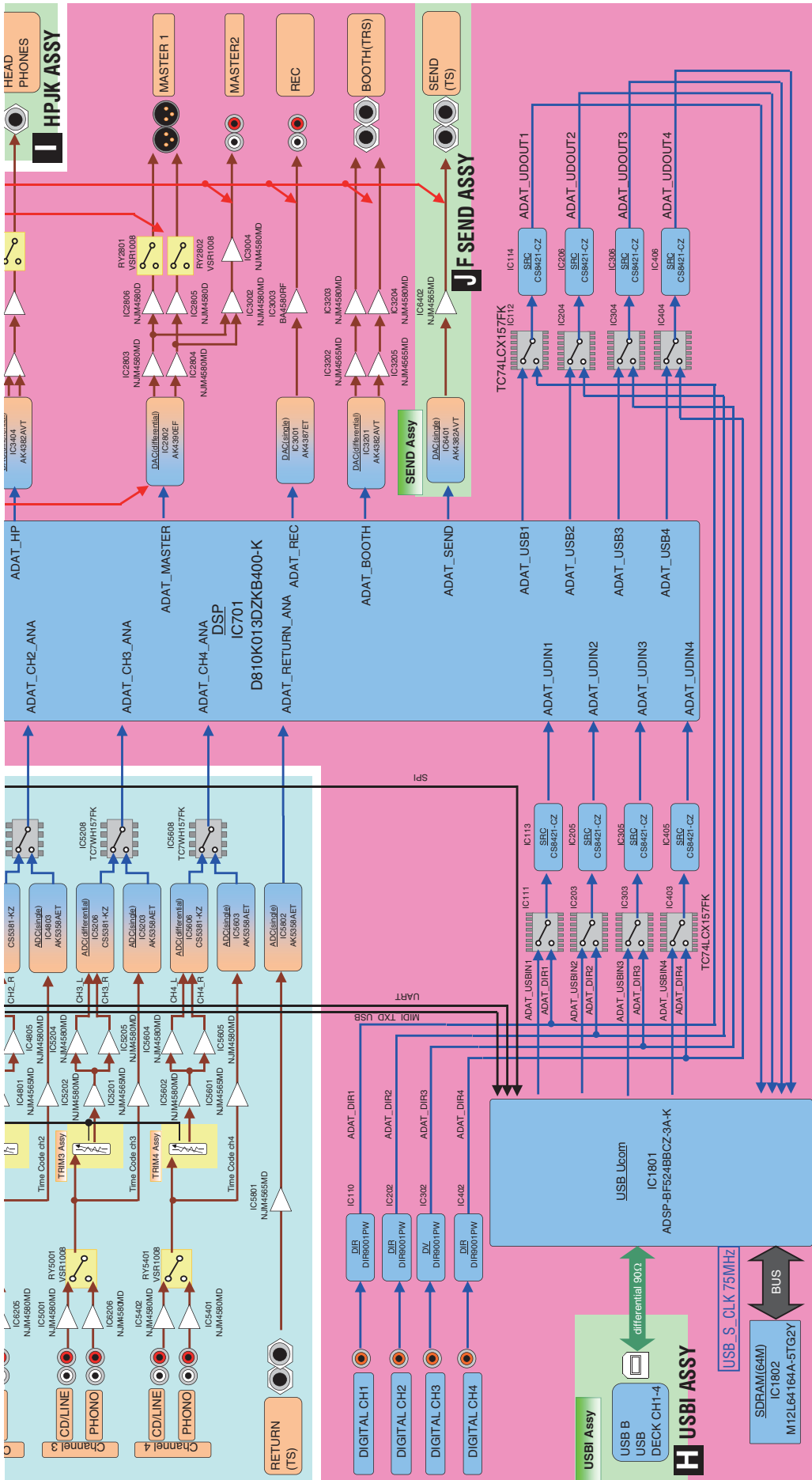


S POWER SUPPLY ASSY (DWR1492)



4.2 AUDIO SYSTEM BLOCK DIAGRAM





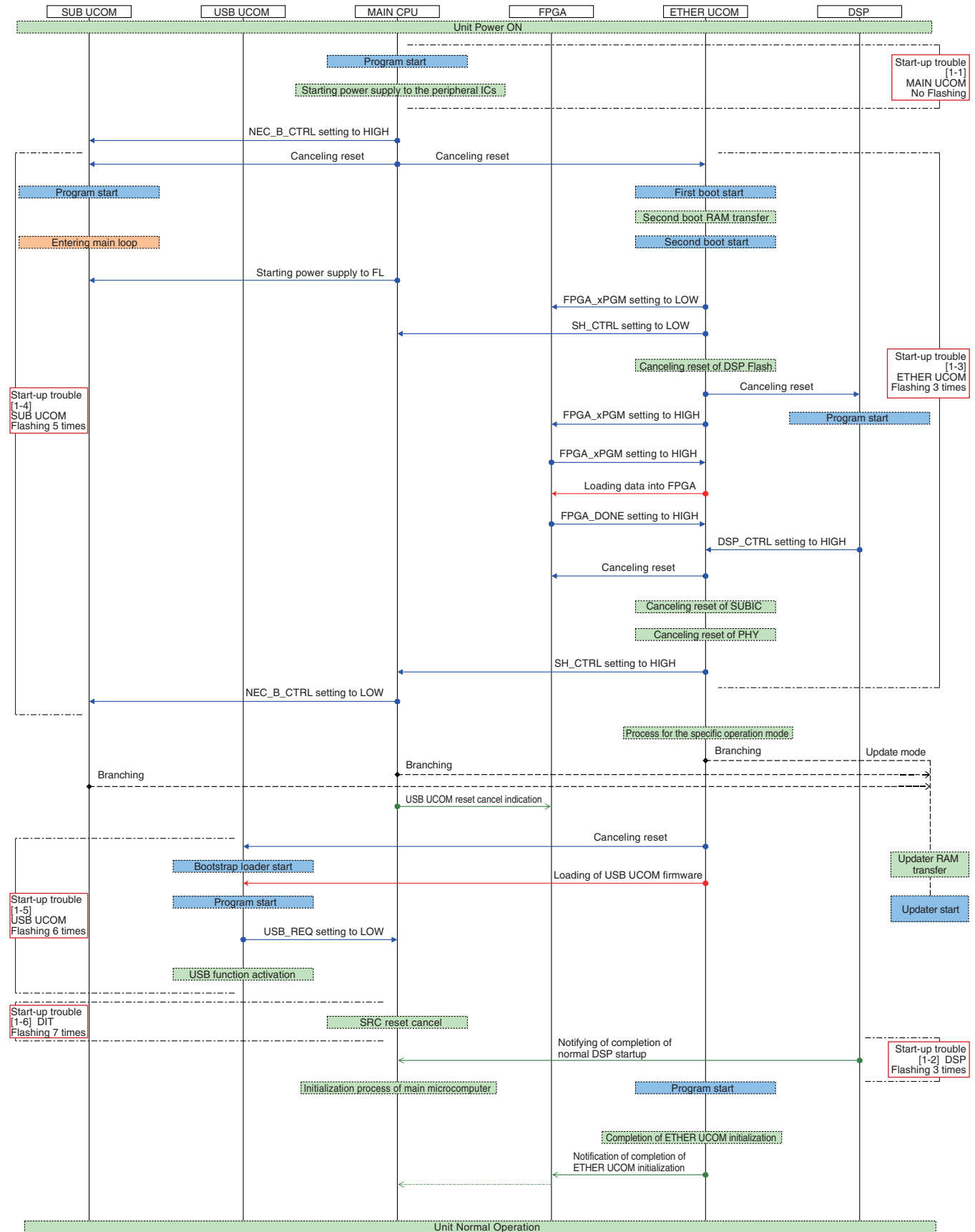
DJM-900SRT

A B C D E F

5. DIAGNOSIS

5.1 POWER ON SEQUENCE

Seaquece Diagram



5.2 TROUBLESHOOTING

Contents

- [0] Prior Confirmation
- [0-1] Checking Internal Cables
 - [0-2] Prior confirmation of power
- [1] Failure in Startup
- [1-1] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) button does not flash.
 - [1-2] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) button flashes once.
 - [1-3] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) flashes three times.
 - [1-4] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) flashes five times.
 - [1-5] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) flashes six times.
 - [1-6] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) flashes seven times.
 - [1-7] The unit does not turn on, and the LED flashes endlessly.
- [2] AUDIO INPUT
- [2-1] No signal is input to the CD/LINE, PHONO connectors.
 - [2-2] No signal is input to the DIGITAL connector.
 - [2-3] No signal is input to the MIC1/MIC2 connectors.
 - [2-4] No signal is input to the RETURN connector.
- [3] AUDIO OUTPUT
- [3-1] No signal is output from the MASTER1/MASTER2 connectors.
 - [3-2] No signal is output from the REC connector.
 - [3-3] No signal is output from the BOOTH connector.
 - [3-4] No signal is output from the SEND connector.
 - [3-5] No signal is output from the PHONES connector.
 - [3-6] No signal is output from the DIGITAL MASTER OUT connector.
- [4] DVS
- [4-1] A device connected to the USB connector cannot be recognized.
 - [4-2] The unit cannot function with a control disc.
- [5] FL
- [5-1] The FL does not light.
- [6] X-PAD
- [6-1] Pressing on the JOG dial not be detected.
 - [6-2] The X-PAD LEDs not light.
- [7] LAN
- [7-1] No LAN communication.
- [8] CROSS FADER
- [8-1] Abnormal function of the crossfader
 - [8-2] The crossfader is inoperable.
- *Point to be checked – Assys are classified with prefix.
- [1-**] MAIN Assy, PNLA Assy, POWER SUPPLY Assy
 - [2-**] INPUT Assy, MAIN Assy
 - [3-**] SEND Assy, MAIN Assy, HPJK Assy
 - [4-**] USBI Assy, MAIN Assy
 - [5-**] PNLA Assy
 - [6-**] PNLA Assy
 - [7-**] MAIN Assy
 - [8-**] CRFD Assy, PNLB Assy, PNLA Assy, MAIN Assy

[0] Prior Confirmation

[0-1] Checking Internal Cables

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Disconnection, breakage, or loose connection of internal cables	Relevant part	Check that all the cables are securely connected. Check that there is no breakage in the cables.	Securely connect the cables. If a cable is broken, replace it.	4.1 OVERALL CONNECTION DIAGRAM

[0-2] Prior confirmation of power

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Failure in the power source corresponding to the defective location	Power to the IC, etc. that is assumed to be defective	Check if there is a problem in power supplied to the location to be diagnosed.	If there is any problem, check the power source, referring to the "POWER BLOCK DIAGRAM" then repair.	4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS

A [1] Failure in Startup

[1-1] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) button does not flash.

A V+12STBY power failure or MAIN_CPU (IC2011) startup error may be suspected.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Power failure	MAIN Assy CN1202-1pin (1-1)	Check for the V+12STBY power.	<ul style="list-style-type: none"> If the V+12STBY power cannot be confirmed, go to [2]. If the V+12STBY power can be confirmed, go to [3]. 	4.3 POWER BLOCK DIAGRAM
2	Power failure Wire defect	POWER SUPPLY P3-1pin (5-1)	Check for the V+12STBY power.	<ul style="list-style-type: none"> If the V+12STBY power can be confirmed, the wire DKP3901 may be defective. Replace it. If the V+12STBY power cannot be confirmed, the POWER SUPPLY Assy may be defective. Replace it. 	5.3 INFORMATION ON POWER DIAGNOSTICS
3	Power failure	MAIN Assy IC1404-4pin (1-3)	Check for the V+3R3E power.	<ul style="list-style-type: none"> If the V+3R3E power can be confirmed, go to [4]. If the V+3R3E power cannot be confirmed, the 3.3V REGULATOR (IC1404) block may be defective. Check for the status of soldering and replace it. 	
4	RESET signal error	MAIN Assy R2023 (1-39)	Check for the CPU_RESET signal.	<ul style="list-style-type: none"> If an output signal can be confirmed, go to [5]. If an output signal cannot be confirmed, the RESET IC (IC2010) block may be defective. Check for the status of soldering and replace it. 	10.41 WAVEFORMS MAIN ASSY (1-39)
5	20 MHz CLK error	MAIN Assy R2019 (1-40)	Check for the 20M_CLK signal.	<ul style="list-style-type: none"> If an output signal cannot be confirmed, the crystal (X2001) block may be defective. Check for the status of soldering and replace it. 	10.41 WAVEFORMS MAIN ASSY (1-40)
6	MAIN_CPU defective	MAIN Assy	If the symptom persists after the above corrections.	The MAIN CPU (IC2011) may be defective. Replace it.	—————

C

[1-2] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) button flashes once.

Communication error between the MAIN_CPU (IC2011) and DSP (IC701).

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	DSP (IC701) does not start. Devices • DSP (IC701) • FLASH (IC703) • SDRAM (IC702) • FPGA (IC2214)	MAIN Assy [POWER] L702 (1-55) L701 (1-56) L703 (1-57) R782 (1-58) [RESET] R671 (1-26) R674 (1-27) [CLK] R896 (1-59)	[POWER] V+3R3D_DSP V+1R2D_DSP V+1R2D_DPLL V+3R3D_DSP_MEM [RESET] DSP_RESET Check that the signal level of DFLASH_RST is High. [CLK] Check if the 24 MHz clock is input to 24.5M_CLK_DSP.	[POWER] If an error is detected, check for the power supply source on the power supply block diagram, and repair it. [RESET] If the signal level is not High, check for the signal supply source of the RESET signal, and repair it. [CLK] If no signal is input, check for the signal supply source of the CLK signal and repair it.	[POWER] 4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS [RESET] 10.41 WAVEFORMS MAIN ASSY (1-26) (1-27) [CLK] 10.41 WAVEFORMS MAIN ASSY (1-59)

D

[1-3] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) flashes three times.

Communication error between MAIN_CPU (IC2011) and ETHER_ICOM (IC501).

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	ETHER_UCOM (IC501) does not start. Devices • ETHER_UCOM (IC501) • FLASH (IC502) • SDRAM (IC503)	MAIN Assy [POWER] L502 (1-23) L501 (1-24) L503 (1-25) R613 (1-22) [RESET] R532 (1-43) [CLK] R2020 (1-41)	[POWER] Check that power at each point is normal. V+1R2D_LAN V+3R3D_LAN V+1R2D_LAN_PLL V+3R3D_LAN_MEM [RESET] Check that the signal level of SUB_SH_RESET is High. [CLK] Check if the 20 MHz clock is input to 20M_CLK_SH.	[POWER] If an error is detected, check for the power supply source on the power supply block diagram, and repair it. [RESET] If the signal level is not High, check for the signal supply source of the RESET signal, and repair it. [CLK] If no signal is input, check for the signal supply source of the CLK signal and repair it.	[POWER] 4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS [RESET] 10.41 WAVEFORMS MAIN ASSY (1-43) [CLK] 10.41 WAVEFORMS MAIN ASSY (1-41)
2	FPGA (IC2214) configuration is not completed. Devices • FPGA (IC2214) • FLASH (IC502)	MAIN Assy [POWER] R2239 (1-50) R2268 (1-51) R2280 (1-52) [RESET] R717 (1-30) [CLK] R2304 (1-53) [Communication line] R2335 (1-54)	[POWER] V+3R3D_FPGA V+1R2D_FPGA V+3R3D_FPGA_AUX [RESET] Check that the signal level of SH_FPGA_RST_X is High. [CLK] Check if the 20 MHz clock is input to 24M_CLK_FPGA. [Communication line] Check that the signal level of SH_FPGA_DONE is High.	[POWER] If an error is detected, check for the power supply source on the power supply block diagram, and repair it. [RESET] If the signal level is not High, check for the signal supply source of the RESET signal, and repair it. [CLK] If no signal is input, check for the signal supply source of the CLK signal and repair it.	[POWER] 4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS [RESET] 10.41 WAVEFORMS MAIN ASSY (1-30) [CLK] 10.41 WAVEFORMS MAIN ASSY (1-53) [Communication line] 10.41 WAVEFORMS MAIN ASSY (1-54)

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No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
3	DSP (IC701) does not start. Devices • DSP (IC701) • FLASH (IC703) • SDRAM (IC702)	MAIN Assy [POWER] L702 1-55 L701 1-56 L703 1-57 R782 1-58 [RESET] R671 1-26 R674 1-27 [CLK] R896 1-59	[POWER] V+3R3D_DSP V+1R2D_DSP V+1R2D_DPLL V+3R3D_DSP_MEM [RESET] DSP_RESET Check that the signal level of DFLASH_RST is High. [CLK] Check if the 24 MHz clock is input to 24.5M_CLK_DSP.	[POWER] If an error is detected, check for the power supply source on the power supply block diagram, and repair it. [RESET] If the signal level is not High, check for the signal supply source of the RESET signal, and repair it. [CLK] If no signal is input, check for the signal supply source of the CLK signal and repair it.	[POWER] 4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS [RESET] 10.41 WAVEFORMS MAIN ASSY 1-26 1-27 [CLK] 10.41 WAVEFORMS MAIN ASSY 1-59

[1-4] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) flashes five times.

Communication error between MAIN_CPU (IC2011) and SUB_UCOM (IC6601).

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	SUB_UCOM (IC6601) does not start. Devices • SUB_UCOM (IC6601) • FPGA (IC2214)	PNLB Assy [POWER] R6615 4-19 [RESET] R6611 4-17 [CLK] X6601 4-18	[POWER] V+3R3D_PA [RESET] Check that the signal level of SUB_CPU_RESET is High. [CLK] Check if the 20 MHz clock is input. As the drive circuit is in an IC, a normal waveform check cannot be performed. Simply check whether or not it oscillates.	[POWER] If an error is detected, check for the power supply source on the power supply block diagram, and repair it. [RESET] If the signal level is not High, check for the signal supply source of the RESET signal, and repair it. [CLK] If no signal is input, check for the signal supply source of the CLK signal and repair it.	[POWER] 4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS [RESET] 10.41 WAVEFORMS PNLB ASSY 4-17 [CLK] 10.41 WAVEFORMS PNLB ASSY 4-18

[1-5] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) flashes six times.

Communication error between MAIN_CPU (IC2011) and USB_UCOM (IC1801).

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	USB_UCOM (IC1801) does not start. Devices • USB_UCOM (IC1801) • SDRAM (IC1802)	MAIN Assy [POWER] L1802 1-31 L1801 1-32 R1806 1-33 R1817 1-34 [RESET] C1831 1-36 [CLK] R2304 1-35	[POWER] V+1R2D_USB V+3R3D_USB V+3R3D_USB_MEM V+2R5D_USB_PLL [RESET] Check that the signal level of USB_RESET is High. [CLK] Check if the 24 MHz clock is input to 24M_CLK_USB.	[POWER] If an error is detected, check for the power supply source on the power supply block diagram, and repair it. [RESET] If the signal level is not High, check for the signal supply source of the RESET signal, and repair it. [CLK] If no signal is input, check for the signal supply source of the CLK signal and repair it.	[POWER] 4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS [RESET] 10.41 WAVEFORMS MAIN ASSY 1-36 [CLK] 10.41 WAVEFORMS MAIN ASSY 1-35

[1-6] The unit does not turn on, and the LED of the LFO FORM (WAKE UP) flashes seven times.

Communication error between MAIN_CPU (IC2011) and DIT (IC2405).

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Power Signal line	MAIN Assy [POWER] R2425 1-73 [RESET] C2411 1-69 [CLK] R2409 1-68	[POWER] Check for the voltage of the V+3R3D_DIT power. [RESET] Check that the signal level of DIT_RESET is High. [CLK] Check that 24M CLK is input.	[POWER] If an error is detected, check for the power supply source on the power supply block diagram, and repair it. [RESET] If the signal level is not High, check for the signal supply source of the RESET signal, and repair it. [CLK] If no signal is input, check for the signal supply source of the CLK signal and repair it.	[POWER] 4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS [RESET] 10.41 WAVEFORMS MAIN ASSY 1-69 [CLK] 10.41 WAVEFORMS MAIN ASSY 1-68
2	Defective part	MAIN Assy DIT (IC2405)	If the symptom persists after the above corrections.	DIT (IC2405) may be defective. Replace it.	—

[1-7] The unit does not turn on, and the LED flashes endlessly.

The unit is shut down in error by the Power Monitoring Circuit.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Power failure	See "5.4 VOLTAGE MONITORING CIRCUIT."	Check for power at each point, following "5.4 VOLTAGE MONITORING CIRCUIT."	Repair the defective parts.	5.4 VOLTAGE MONITORING CIRCUIT

A [2] AUDIO INPUT

[2-1] No signal is input to the CD/LINE, PHONO connectors.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
0	Prior Confirmation	DIGITAL, CD/LINE, PHONO, USB DECK* selector / TRIM	Confirm on the screen that the selector is set properly.	_____	Operating instructions
1	Loose connection /defective parts	INPUT Assy IC4408/IC4808/ IC5208/IC5608 6 pin Representative CH1 2-32	[Checking the Audio Timecode select block] Check that the TC_SEL* signal level is Low.	<ul style="list-style-type: none"> • If it is low, go to [2]. • If it is high, check for loose connection with the MAIN Assy, improper part-mounting status for communication, or improper mounting status of the MAIN CPU. If no problem is still found, the MAIN CPU (IC201) may be defective. Replace it. 	10.41 WAVEFORMS INPUT ASSY 2-32
2	Loose connection /defective parts	INPUT Assy IC4406(CH1) IC4806(CH2) IC5206(CH3) IC5606(CH4) 16, 17, 20, 21 pin Representative CH1_R- 2-20 /CH1_R+ 2-21	[Checking the ADC block] 10.41 WAVEFORMS INPUT ASSY 2-32 Check for an audio signal at the input connector of ADC.	<p>If no audio signal is input to ADC, a problem in an analog circuit at a previous stage may be suspected.</p> <ul style="list-style-type: none"> • If no audio signal is input, go to [5]. • If an audio signal is input, go to [3]. 	10.41 WAVEFORMS INPUT ASSY 2-20 2-21
3	Loose connection /defective parts	INPUT Assy IC4406(CH1) IC4806(CH2) IC5206(CH3) IC5606(CH4) - 1 pin Representative CH1 2-22	Check for the RESET signal level of ADC is High.	<ul style="list-style-type: none"> • If the RESET signal level is L, go to [9]. • If the RESET signal level is H, go to [4]. 	10.41 WAVEFORMS INPUT ASSY 2-22
4	Loose connection /defective parts	INPUT Assy IC4406(CH1) IC4806(CH2) IC5206(CH3) IC5606(CH4) LRCK: 3 pin 2-23 Representative BCLK: 4 pin 2-24 Representative MCLK: 5 pin 2-25 Representative	10.41 WAVEFORMS INPUT ASSY 2-32 Check for the CLK signal of ADC.	<ul style="list-style-type: none"> • If no CLK signal is input, go to [11]. • If the CLK signal is input, ADC must be improperly soldered or defective. 	10.41 WAVEFORMS INPUT ASSY 2-23 2-24 2-25
5	Loose connection /defective parts	INPUT Assy IC4402(CH1) IC4802(CH2) IC5202(CH3) IC5602(CH4) Input: 2, 3, 5, 6 pin Representative CH1 2-18 after TRIM Output: 1, 7 pin Representative CH1 R 2-19	[Checking an Intermediate Buffer block] Check for the signal input to and output from the Intermediate Buffer block.	<ul style="list-style-type: none"> • If a signal is input but not output, this block must be improperly soldered or defective. • If no signal is input, go to [6]. • If a signal is input and output, Balanced Direct ADC Driver block must be improperly soldered or defective. 	10.41 WAVEFORMS INPUT ASSY 2-18 2-19
6	Loose connection /defective parts	INPUT Assy CN4201(CH1) CN4601(CH2) CN5001(CH3) CN5401(CH4) 1 pin, 3 pin, 5 pin, 7 pin Representative CH1 R 2-17 2-18	[Checking the TRIM] Check for an audio signal at the periphery of TRIM.	<ul style="list-style-type: none"> • If a signal is interrupted at the periphery of TRIM, check for the mounting status or soldering status of TRIM. If no problem is found, replace the TRIM Assy. • If there is no signal before TRIM, go to [7]. 	10.41 WAVEFORMS INPUT ASSY 2-17 2-18
7	Loose connection /defective parts	INPUT ASSY IC4201(CH1 PHONO) IC4202(CH1 CD) IC6205(CH2 PHONO) IC4602(CH2 CD) IC6206(CH3 PHONO) IC5002(CH3 CD) IC5401(CH4 PHONO) IC5402(CH4 CD) 1 pin, 7 pin Representative CH1 CD R 2-16	[Checking the Input Buffer block] Check if an audio signal is output from the operation amplifier.	<ul style="list-style-type: none"> • If no signal is output, this block must be improperly soldered or defective. • If an audio signal is output, go to [8]. 	10.41 WAVEFORMS INPUT ASSY 2-16

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No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
8	Loose connection /defective parts	INPUT Assy/ MAIN Assy LINE_SEL* Representative LINE_SEL1 2-14	[Checking the RELAY block] Check for the LINE_SEL* signal. If the input selector is set to PHONO, the LINE_SEL* signal level is H. If the input selector is set to CD/LINE, the LINE_SEL* signal level is L.	<ul style="list-style-type: none"> If a LINE_SEL* signal is not output properly, check for loose connection with the MAIN Assy, improper part mounting status for communication, or improper mounting of the MAIN CPU. If there is no problem, the MAIN CPU (IC2011) may be defective. Replace it. If a LINE_SEL* signal is output properly, this block must be improperly soldered or defective. 	10.41 WAVEFORMS INPUT ASSY 2-14
9	Loose connection /defective parts	INPUT Assy IC4009/IC4010 2 pin, 4 pin Representative RESET_IN 2-9 RESET_IN1 2-8	[Checking the RESET signal path] Check for the RESET signal level is High.	<ul style="list-style-type: none"> If the signal level at Pin 2 is H and that at Pin 4 is L, IC4009/IC4010 must be soldered improperly or defective. If the signal level at Pin 2 is L, go to [10]. 	10.41 WAVEFORMS INPUT ASSY 2-9 2-8
10	Loose connection /defective parts	INPUT Assy IC4005 2 pin, 3 pin AD/DA_RESET 2-7 BCLK 2-12	[Checking the RESET signal path] Check for the RESET signal level is High. Check for the BCLK signal.	<ul style="list-style-type: none"> If the signal level at Pin 2 is L, go to [12]. If the signal level at Pin 3 is not normal, check for BCLK in [13]. If the signal level at Pin 2 is H and that at Pin 3 is normal, IC4005 is soldered improperly or defective. 	10.41 WAVEFORMS INPUT ASSY 2-7 2-12
11	Loose connection /defective parts	INPUT Assy LRCLK: IC4002/IC4006 BCLK: IC4003/IC4007 MCLK: IC4004/IC4008 2 pin, 4 pin Representative LRCLK_IN_2 2-6 LRCLK 2-13 BCLK_IN_2 2-3 BCLK 2-12 MCLK_IN_2 2-2 MCLK 2-11	[Checking the CLK signal path] Check for the LRCLK, BCLK, and MCLK signals.	<ul style="list-style-type: none"> If the signal level at Pin 2 is normal and Pin 4 has no signal, IC4002/IC4006/IC4003/IC4007/IC4004 10.41 WAVEFORMS 2-6 2-13 2-3 2-12 2-2 2-11 INPUT ASSY 2-18 2-19 /IC4008 must be improperly soldered or defective. If the signal at Pin 2 is not normal, go to [13]. 	10.41 WAVEFORMS INPUT ASSY 2-6 2-13 2-3 2-12 2-2 2-11
12	Loose connection /defective parts	MAIN Assy R709 1-28	[Checking the RESET signal path] Check for the RESET signal level is High.	If no problem is found, ETHER UCOM (IC501) must be improperly soldered or defective.	10.41 WAVEFORMS MAIN ASSY 1-28
13	Loose connection /defective parts	MAIN Assy LRCLK: IC2206 2,4 pin 1-44 1-49 BCLK: IC2209 2,4 pin 1-45 1-48 MCLK: IC2210 2,4 pin 1-46 1-47	[Checking the CLK signal path] Check for the LRCLK, BCLK, and MCLK signals.	<ul style="list-style-type: none"> If the signal at Pin 2 is normal and Pin 4 has no signal, IC2206/IC2209/IC2210 must be improperly soldered or defective. If the signal at Pin 2 is not normal, FPGA (IC2214) must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-44 1-49 1-45 1-48 1-46 1-47

[2-2] No signal is input to the DIGITAL connector.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
0	Prior Confirmation	DIGITAL, CD/LINE, PHONO, USB DECK*selector / TRIM	Confirm on the screen that the selector is set properly.	_____	_____
1	Loose connection /defective parts	MAIN Assy IC101(CH1) IC201(CH2) IC301(CH3) IC401(CH4) -5 pin Representative CH1_SPDIF 1-6	Check if a signal is input to DIGITAL properly.	<ul style="list-style-type: none"> If any of the signals is not input, IC101(CH1) /IC201(CH2)/IC301(CH3)/IC401(CH4) may be defective. If there is a signal, go to [2]. 	10.41 WAVEFORMS MAIN ASSY 1-6
2	Loose connection /defective parts	MAIN Assy IC110(CH1) IC202(CH2) IC302(CH3) IC402(CH4) -21 pin Representative DIR_RESET 1-11	Check that the RESET signal level of the DIR is High.	<ul style="list-style-type: none"> If it is H, go to [3]. If it is L and IC504 has no problem, ETHER UCOM (IC501) must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-11
3	Loose connection /defective parts	MAIN Assy IC110(CH1) IC202(CH2) IC302(CH3) IC402(CH4) • LRCKO: 10 pin • BCKO: 11 pin • SCKO: 4 pin • DOUT: 12 pin Representative 1-8 1-9 1-7 1-10	Check for the LRCKO, BCKO, SCKO, DOUT signal output from DIR.	<ul style="list-style-type: none"> If a signal is output, go to [4]. If any of the signals is not output, IC110(CH1) /IC202(CH2)/IC302(CH3)/IC402(CH4) may be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-8 1-9 1-7 1-10

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No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
4	Loose connection /defective parts	MAIN Assy IC111(CH1) IC203(CH2) IC303(CH3) IC403(CH4) -9 pin Representative ADATIN_SRC1 1-16	Check for the digital audio signal at DIGI/USB SW.	<ul style="list-style-type: none"> • If there is a signal, go to [6]. • If there is no signal, go to [5]. 	10.41 WAVEFORMS MAIN ASSY 1-16
5	Loose connection /defective parts	MAIN Assy IC111(CH1) IC203(CH2) IC303(CH3) IC403(CH4) -9 pin Representative DIGI_SEL1 1-12	Check that the DIGI_SEL* signal level is Low. IC111(CH1)/IC203(CH2)/IC303(CH3)/ IC403(CH4) 1 pin	<ul style="list-style-type: none"> • If it is L, but no signal is output, IC111(CH1) /IC203(CH2)/IC303(CH3)/IC403(CH4) must be improperly soldered or defective. • If it is not L, MAIN CUP: IC2011 must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-12
6	Loose connection /defective parts	MAIN Assy UDIN1 UDIN2 UDIN3 UDIN4 Test land Representative 1-17	Check for the output signal at SRC.	<ul style="list-style-type: none"> • If a signal is output, DSP (IC701) must be improperly soldered or defective. • If a signal I not output, go to [7]. 	10.41 WAVEFORMS MAIN ASSY 1-17
7	Loose connection /defective parts	MAIN Assy IC113(CH1) IC205(CH2) IC305(CH3) IC405(CH4) 5 pin Representative 1-13	Check that the CH*SRC_RESET signal level is High.	<ul style="list-style-type: none"> • If it is L, Ether UCOM (IC501) must be improperly soldered or defective. • If it is H, go to [8]. 	10.41 WAVEFORMS MAIN ASSY 1-13
8	Loose connection /defective parts	MAIN Assy IC113(CH1) IC205(CH2) IC305(CH3) IC405(CH4) -7, 8 pin Representative 1-14 1-15	Check for the CLK signal (ILRCK, ISCLK) at SRC.	<ul style="list-style-type: none"> • If there is no CLK signal, IC111(CH1)/IC203(CH2)/IC303(CH3)/IC403(CH4) must be improperly soldered or defective. • If there is a CLK signal, IC113(CH1)/IC205(CH2)/IC305(CH3)/IC405(CH4) must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-14 1-15

[2-3] No signal is input to the MIC1/MIC2 connectors.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
0	Prior Confirmation	OFF, ON, TALK OVER switch	Confirm on the screen that the selector is set properly.	_____	_____
1	Loose connection /defective parts	INPUT Assy IC6204 MIC1: 2 pin 2-41 MIC2: 1 pin 2-44	Check for an audio signal at the input connector of the ADC.	<ul style="list-style-type: none"> • If no audio signal is input to the ADC, a problem in an analog circuit at a previous stage may be suspected. • If no signal is input, go to [2]. • If a signal is input, go to [3]. 	10.41 WAVEFORMS INPUT ASSY 2-41 2-44
2	Loose connection /defective parts	INPUT Assy VR6202 2 pin 2-39 3 pin 2-40 VR6201 2 pin 2-42 3 pin 2-43	Check for an audio signal at the periphery of the TRIM.	<ul style="list-style-type: none"> • If a signal is interrupted at the periphery of the TRIM, VR6202/VR6201 must be improperly soldered or defective. • If there is no signal before the TRIM, the MIC1 /MIC2 AMP block must be improperly soldered or defective. 	10.41 WAVEFORMS INPUT ASSY 2-39 2-40 2-42 2-43
3	Loose connection /defective parts	INPUT Assy IC6204 13 pin 2-45	Check for the RESET signal level of the ADC is High.	<ul style="list-style-type: none"> • If the signal level is L, go to [5]. • If the signal level is H, go to [4]. 	10.41 WAVEFORMS INPUT ASSY 2-45
4	Loose connection /defective parts	INPUT Assy LRCK: IC6204 10 pin 2-48 BCLK: IC6204 12 pin 2-46 MCLK: IC6204 11 pin 2-47	Check for the CLK signal of the ADC. • LRCK • BCLK • MCLK	<ul style="list-style-type: none"> • If there is no CLK signal, go to [7]. • If there is a CLK signal but no signal is output from the ADC, the ADC must be improperly soldered or defective. 	10.41 WAVEFORMS INPUT ASSY 2-48 2-46 2-47
5	Loose connection /defective parts	INPUT Assy IC4010 2 pin 2-9 4 pin 2-10	Check for the RESET signal level is High.	<ul style="list-style-type: none"> • If the signal level at Pin 2 is H and that at Pin 4 is L, IC4010 must be improperly soldered or defective. • If the signal level at Pin 2 is L, go to [6]. 	10.41 WAVEFORMS INPUT ASSY 2-9 2-10
6	Loose connection /defective parts	INPUT Assy IC4005 2 pin, 3 pin 2-7 2-12	Check for the RESET signal level is High.	<ul style="list-style-type: none"> • If the signal level at Pin 2 is L, go to [8]. • If the signal level at Pin 3 is not normal, check for BCLK in [7]. • If a signal at Pin 2 is H and that at Pin 3 is normal, IC4005 must be improperly soldered or defective. 	10.41 WAVEFORMS INPUT ASSY 2-7 2-12

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
7	Loose connection /defective parts	INPUT Assy • LRCK: IC4006 2 pin 2-11 , 4 pin 2-2 • BCLK: IC4007 2 pin 2-12 , 4 pin 2-4 • MCLK: IC4008 2 pin 2-13 , 4 pin 2-6	Check for the LRCLK, BCLK, and MCLK signals.	<ul style="list-style-type: none"> If the signal at Pin 2 is normal and Pin 4 has no signal, IC4006/IC4007/IC4008 must be improperly soldered or defective. If the signal at Pin 2 is not normal, go to [9]. 	10.41 WAVEFORMS INPUT ASSY 2-11 2-2 2-12 2-4 2-13 2-6
8	Loose connection /defective parts	MAIN Assy R709 1-28	[Checking the RESET signal path] Check for the RESET signal level is High.	<ul style="list-style-type: none"> If no problem with the signal is found, check for the cable connection with the MAIN Assy and the parts mounted along the communication line. If no problem is found, ETHER UCOM (IC501) may be improperly soldered. 	10.41 WAVEFORMS MAIN ASSY 1-28
9	Loose connection /defective parts	MAIN Assy LRCLK: IC2206 2, 4 pin 1-44 1-49 BCLK: IC2209 2, 4 pin 1-45 1-48 MCLK: IC2210 2, 4 pin 1-46 1-47	[Checking the CLK signal path] Check for the LRCLK, BCLK, and MCLK signals.	<ul style="list-style-type: none"> If the signal at Pin 2 is normal and Pin 4 has no signal, IC2206/IC2209/IC2210 must be improperly soldered or defective. If a signal at Pin 2 is not normal, FPGA (IC2214) must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-44 1-49 1-45 1-48 1-46 1-47

[2-4] No signal is input to the RETURN connector.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
0	Prior Confirmation	DELAY, ECHO, SPIRAL, REVERB, TRANS, FILTER, FLANGER, PHASER, ROBOT, MELODIC, SLIP ROLL, ROLL, REV ROLL, SND/RTN (MIDI LFO) SW /EFFECT ON/OFF button	<ul style="list-style-type: none"> Check that the type for BEAT EFFECT is set to SND/RTN. Check that the BEAT EFFECT is set to ON. <p>*No signal is output from the RETURN connector if a signal is input only to the R channel. Even when the R channel is to be diagnosed, be sure to input a signal to the L channel, as well.</p>		
1	Loose connection /defective parts	INPUT Assy CN4002 1 pin 2-15	Check that the RETURN IN signal level is H.	<ul style="list-style-type: none"> If it is L, JA5801 must be improperly soldered or defective. If it is H, go to [2]. 	10.41 WAVEFORMS INPUT ASSY 2-15
2	Loose connection /defective parts	INPUT Assy IC5802 1 pin 2-34 , 2 pin 2-33	Check for an audio signal at the input connector of the ADC.	<ul style="list-style-type: none"> If no audio signal is input to the ADC, a problem in an analog circuit at a previous stage may be suspected. If no signal is input, the Input Buffer block must be improperly soldered or defective. If a signal is input, go to [3]. 	10.41 WAVEFORMS INPUT ASSY 2-34 2-33
3	Loose connection /defective parts	INPUT Assy IC5802 13 pin 2-35	Check for the RESET signal level of the ADC is High.	<ul style="list-style-type: none"> If the signal level is L, go to [5]. If the signal level is H, go to [4]. 	10.41 WAVEFORMS INPUT ASSY 2-35
4	Loose connection /defective parts	INPUT Assy • LRCK: IC5802 10 pin 2-38 • BCLK: IC5802 12 pin 2-36 • MCLK: IC5802 11 pin 2-37	Check for the CLK signal of the ADC. LRCK, BCLK, MCLK	<ul style="list-style-type: none"> If there is no signal, go to [7]. If there is a CLK signal but no signal is output from the ADC, the ADC must be improperly soldered or defective. 	10.41 WAVEFORMS INPUT ASSY 2-38 2-36 2-37
5	Loose connection /defective parts	INPUT Assy IC4009 2 pin 2-9 , 4 pin 2-8 RESET_IN1	Check for Pins 2 and 4 of IC4009.	<ul style="list-style-type: none"> If a signal at Pin 2 is H and that at Pin 4 is L, IC4009 must be improperly soldered or defective. If the signal level at Pin 2 is L, go to [6]. 	10.41 WAVEFORMS INPUT ASSY 2-9 2-8
6	Loose connection /defective parts	INPUT Assy IC4005 2 pin, 3 pin 2-7 2-12	Check for Pins 2 and 3 of IC4005.	<ul style="list-style-type: none"> If the signal level at Pin 2 is L, go to [8]. If a signal at Pin 3 is not normal, check for BCLK in [7]. If the signal level at Pin 2 is H and that at Pin 3 is normal, IC4005 must be improperly soldered or defective. 	10.41 WAVEFORMS INPUT ASSY 2-7 2-12
7	Loose connection /defective parts	INPUT Assy • LRCK: IC4002 2 pin 2-11 , 4 pin 2-1 • BCLK: IC4003 2 pin 2-12 , 4 pin 2-3 • MCLK: IC4004 2 pin 2-13 , 4 pin 2-5	Check for the LRCLK, BCLK, and MCLK signals.	<ul style="list-style-type: none"> If a signal at Pin 2 is normal and Pin 4 has no signal, IC4002/IC4003/IC4004 must be improperly soldered or defective. If a signal at Pin 2 is not normal, go to [9]. 	10.41 WAVEFORMS INPUT ASSY 2-11 2-1 2-12 2-3 2-13 2-5
8	Loose connection /defective parts	MAIN Assy R709 2-28	[Checking the RESET signal path] Check for the RESET signal level is High.	<ul style="list-style-type: none"> If no problem with the signal is found, check for the cable connection with the MAIN Assy and the parts mounted along the communication line. If no problem is found, ETHER UCOM (IC501) may be improperly soldered. 	10.41 WAVEFORMS MAIN ASSY 2-28

A

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
9	Loose connection /defective parts	MAIN Assy LRCLK: IC2206 2,4 pin 1-44 1-49 BCLK: IC2209 2,4 pin 1-45 1-48 MCLK: IC2210 2,4 pin 1-46 1-47	[Checking the CLK signal path] Check for the LRCLK, BCLK, and MCLK signals.	<ul style="list-style-type: none"> If the signal at Pin 2 is normal and Pin 4 has no signal, IC2206/IC2209/IC2210 must be improperly soldered or defective. If a signal at Pin 2 is not normal, FPGA (IC2214) must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-44 1-49 1-45 1-48 1-46 1-47

[3] AUDIO OUTPUT

[3-1] No signal is output from the MASTER1/MASTER2 connectors.

B

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Output Confirmation	MASTER1 / MASTER2	Identify the connector(s) that do(es) not output signals.	<ul style="list-style-type: none"> If neither MASTER 1 nor 2 connector outputs, go to [2]. If only the MASTER 1 connector does not output, go to [11]. If only the MASTER2 connector does not output, go to [13]. 	—————
2	Loose connection /defective parts	MAIN Assy IC2802-10,11,20, 21 pin 1-92 1-93 1-90 1-91 IC2803 1,7 pin 1-94 1-95 /IC2804-1,7 pin 1-96 1-97	[Checking the Shaping Noise Filter] Check for the DAC differential audio signal output.	<ul style="list-style-type: none"> If a signal is output from the DAC output (Pins 10, 11, 20, and 21 of IC2802) but no signal is output from Pins 1 and 7 of IC2803/IC2804, the Shaping Noise Filter block must be improperly soldered or defective. If no signal is output from the DAC output (Pins 10, 11, 20, and 21 of IC2802), go to [3]. 	10.41 WAVEFORMS MAIN ASSY 1-92 1-93 1-90 1-91 1-94 1-95 1-96 1-97
3	Loose connection /defective parts	MAIN Assy IC2802-29 pin 1-89	Check that a signal is output from the ADAT_MASTER_OUT.	<ul style="list-style-type: none"> If no signal is output, the DSP(IC701) must be improperly soldered or defective. If a signal is output, go to [4]. 	10.41 WAVEFORMS MAIN ASSY 1-89
4	Loose connection /defective parts	MAIN Assy IC2802-1 pin 1-84	Check that the MVR_MUTE_OUT signal level is Low.	<ul style="list-style-type: none"> If it is H, failure of the PNLA may be suspected. Check for the MASTER LEVEL operation in test mode. If it is L, go to [5]. 	10.41 WAVEFORMS MAIN ASSY 1-84
5	Loose connection /defective parts	MAIN Assy IC2802-27 pin 1-85	Check that the RESET_OUT signal level is High.	<ul style="list-style-type: none"> If it is H, go to [8]. If it is L, go to [6]. 	10.41 WAVEFORMS MAIN ASSY 1-85
6	Loose connection /defective parts	MAIN Assy IC2601-2,4 pin 1-78 1-82	Check for the RESET_OUT signal level is High.	<ul style="list-style-type: none"> If a signal level at Pin 2 is H and that at Pin 4 is L, IC2601 must be improperly soldered or defective. If the signal level at Pin 2 is L, go to [7]. 	10.41 WAVEFORMS MAIN ASSY 1-78 1-82
7	Loose connection /defective parts	MAIN Assy R709 1-28	Check for the mounting status of the RESET signal line R709.	<ul style="list-style-type: none"> If no problem is found, ETHER UCOM (IC501) must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-28
8	Loose connection /defective parts	MAIN Assy IC2802 LRCLK 30 pin BCLK 28 pin MCLK 24 pin 1-88 1-87 1-86	Check for the CLK signal of IC2802. LRCK, BCLK, MCLK	<ul style="list-style-type: none"> If [3], [4], [5], and [8] are normal but a signal is not output from the DAC (Pins 10, 11, 20, and 21 of IC2802), IC2802 must be improperly soldered or defective. If no CLK signal is found, go to [9]. 	10.41 WAVEFORMS MAIN ASSY 1-88 1-87 1-86
9	Loose connection /defective parts	MAIN Assy LRCK: IC2604 2,4 pin 1-77 1-81 BCLK: IC2603 2,4 pin 1-76 1-80 MCLK: IC2602 2,4 pin 1-75 1-79	Check for Pins 2 and 4 of LRCK: IC2604 BCLK: IC2603 MCLK: IC2602	<ul style="list-style-type: none"> If the signal at Pin 2 is normal and Pin 4 has no signal, IC2604/IC2603/IC2602 must be improperly soldered or defective. If a signal at Pin 2 is not normal, go to [10]. 	10.41 WAVEFORMS MAIN ASSY 1-77 1-81 1-76 1-80 1-75 1-79
10	Loose connection /defective parts	MAIN Assy LRCK: IC2205 2,4 pin 1-49 1-44 BCLK: IC2207 2,4 pin 1-48 1-45 MCLK: IC2208 2,4 pin 1-47 1-46	Check for Pins 2 and 4 of LRCK: IC2205 BCLK: IC2207 MCLK: IC2208	<ul style="list-style-type: none"> If the signal at Pin 2 is normal and Pin 4 has no signal, IC2205/IC2207/IC2208 must be improperly soldered or defective. If a signal at Pin 2 is not normal, FPGA (IC2214) must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-49 1-44 1-48 1-45 1-47 1-46
11	Loose connection /defective parts	MAIN Assy IC2806-1,7 pin 1-98 1-99 IC2805-1,7 pin 1-100 1-101	Check for the Balanced Output AMP.	<ul style="list-style-type: none"> If no signal is output, IC2806/IC2805 must be improperly soldered or defective. If a signal is output, go to [12]. 	10.41 WAVEFORMS MAIN ASSY 1-98 1-99 1-100 1-101
12	Loose connection /defective parts	MAIN Assy MUTE_M1 1-102	Check that the MUTE_M1 (test land) signal level is Low.	<ul style="list-style-type: none"> If it is H, Q2836/Q2837 must be improperly soldered or defective. If a CPU_MUTE_OUT signal is not output properly, go to [13]. If it is L, RY2801/RY2802 must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-102

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No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
13	Loose connection /defective parts	MAIN Assy IC2011 86 pin 1-38	Check that the signal level at Pin 86 of CPU_MUTE: MAIN_CPU (IC2011) is Low.	<ul style="list-style-type: none"> If it is H, IC2011 must be improperly soldered or defective. If it is L, Q2001/R2014/R2608 must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-38
14	Loose connection /defective parts	MAIN Assy MUTE_O 1-83	[No audio output from the MASTER2] Check that the MUTE_OUT signal level is Low.	<ul style="list-style-type: none"> If it is L, Q3007/Q3008 must be improperly soldered or defective. If it is H, Q2601/Q2602/Q2603 must be improperly soldered or defective. If the CPU_MUTE signal is not supplied properly, go to [13]. 	10.41 WAVEFORMS MAIN ASSY 1-83
15	Loose connection /defective parts	MAIN Assy 1-108 1-109	[No audio output from the MASTER2] Check the signal at the MASTER2_L/ MASTER2_R (test land).	<ul style="list-style-type: none"> If no signal is found, the Bal -> Unbal AMP block must be improperly soldered or defective. If there is a signal but no final output signal, the Output AMP block must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-108 1-109

[3-2] No signal is output from the REC connector.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Loose connection /defective parts	MAIN Assy MUTE_O 1-83	Check that the MUTE_OUT signal level is Low.	<ul style="list-style-type: none"> If it is L, Q3005/Q3006 must be improperly soldered or defective. If it is H, Q2601/Q2602/Q2603 must be improperly soldered or defective. If the CPU_MUTE signal level is not L, go to [13] of [3-1] No signal is output from the MASTER1/MASTER2 connectors. 	10.41 WAVEFORMS MAIN ASSY 1-83
2	Loose connection /defective parts	MAIN Assy AOUTL_REC AOUTR_REC REC_L REC_R 1-110 1-111 1-112 1-113	Check the audio signals input to and output from IC3003.	<ul style="list-style-type: none"> If a signal is interrupted at the periphery of IC3003, IC3003 or peripheral parts must be improperly soldered or defective. If no signal is input to IC3003, go to [3]. 	10.41 WAVEFORMS MAIN ASSY 1-110 1-111 1-112 1-113
3	Loose connection /defective parts	MAIN Assy IC3001 3 pin 1-105	Check that a signal is supplied to the ADAT_REC_OUT.	<ul style="list-style-type: none"> If no signal is found, DSP(IC701) must be improperly soldered or defective. If a signal is found, go to [4]. 	10.41 WAVEFORMS MAIN ASSY 1-105
4	Loose connection /defective parts	MAIN Assy IC3001 5 pin 1-107	Check that the RESET_OUT signal level is High.	<ul style="list-style-type: none"> If it is H, go to [5]. If it is L, go to [6] of [3-1] No signal is output from the MASTER1/MASTER2 connectors. 	10.41 WAVEFORMS MAIN ASSY 1-107
5	Loose connection /defective parts	MAIN Assy IC3001 LRCK: 4 pin BCLK: 2 pin MCLK: 1 pin 1-106 1-104 1-103	Check for the CLK signal of the IC3001.	<ul style="list-style-type: none"> If [3], [4], and [5] are normal but a signal is not output from the DAC (Pins 10 and 11 of IC3001), IC3001 must be improperly soldered or defective. If a CLK signal is not found, go to [9] of [3-1] No signal is output from the MASTER1/MASTER2 connectors. 	10.41 WAVEFORMS MAIN ASSY 1-106 1-104 1-103

[3-3] No signal is output from the BOOTH connector.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Loose connection /defective parts	MAIN Assy MUTE_O 1-83	Check that the MUTE_OUT signal level is Low.	<ul style="list-style-type: none"> If it is L, Q3205/Q3206/Q3207/Q3208 must be improperly soldered or defective. If it is H, Q2601/Q2602/Q2603 must be improperly soldered or defective. If the CPU_MUTE signal level is not L, go to [13] of [3-1] No signal is output from the MASTER1/MASTER2 connectors. 	10.41 WAVEFORMS MAIN ASSY 1-83
2	Loose connection /defective parts	MAIN Assy IC3203 1-123 1-124 1-127 1-128 IC3204 1-125 1-126 1-129 1-130	Check the audio signals input to and output from IC3203/IC3204.	<ul style="list-style-type: none"> If a signal is interrupted at the periphery of IC3203/IC3204, IC3203/IC3204 or peripheral parts must be improperly soldered or defective. If no signal is input to IC3203/IC3204, go to [3]. 	10.41 WAVEFORMS MAIN ASSY 1-123 1-124 1-127 1-128 1-125 1-126 1-129 1-130

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No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
3	Loose connection /defective parts	MAIN Assy IC3202 1-119 1-120 1-123 1-124 IC3205 1-121 1-122 1-125 1-126	Check the audio signals input to and output from IC3202/IC3205.	<ul style="list-style-type: none"> If a signal is interrupted at the periphery of IC3202/IC3205, IC3202/IC3205 or peripheral parts must be improperly soldered or defective. If no signal is input to IC3202/IC3205, go to [4]. 	10.41 WAVEFORMS MAIN ASSY 1-119 1-120 1-123 1-124 1-121 1-122 1-125 1-126
4	Loose connection /defective parts	MAIN Assy IC3201-3 pin 1-116	Check that a signal is supplied to the ADAT_BOOTH_OUT.	<ul style="list-style-type: none"> If no signal is found, DSP(IC701) must be improperly soldered or defective. If a signal is found, go to [5]. 	10.41 WAVEFORMS MAIN ASSY 1-116
5	Loose connection /defective parts	MAIN Assy IC3201-5 pin 1-118	Check that the RESET_OUT signal level is High.	<ul style="list-style-type: none"> If a signal level is High, go to [6] of [3-1] No signal is output from the MASTER1/MASTER2 connectors. 	10.41 WAVEFORMS MAIN ASSY 1-118
6	Loose connection /defective parts	MAIN Assy IC3201 LRCK: 4 pin 1-117 BCLK: 2 pin 1-115 MCLK: 1 pin 1-114	Check for the CLK signal of the IC3001. LRCK, BCLK, MCLK	<ul style="list-style-type: none"> If a CLK signal is not found, go to [9] of [3-1] No signal is output from the MASTER1/MASTER2 connectors. 	10.41 WAVEFORMS MAIN ASSY 1-117 1-115 1-114

B

[3-4] No signal is output from the SEND connector.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Loose connection /defective parts	MAIN Assy CN1003/ SEND Assy CN6401	Check the connection status of CN1003/ CN6401.	Connect a connector correctly.	—————
2	Loose connection /defective parts	MAIN Assy / SEND Assy MUTE_OUT MUTE_O 1-83	Check that the MUTE_OUT signal level is Low.	<ul style="list-style-type: none"> If it is L, Q6403/Q6404 must be improperly soldered or defective. If it is H, Q2601/Q2602/Q2603 must be improperly soldered or defective. If the CPU_MUTE signal level is not L, go to [13] of [3-1] No signal is output from the MASTER1/ MASTER2 connectors. 	10.41 WAVEFORMS MAIN ASSY 1-83
3	Loose connection /defective parts	SEND Assy IC6402 3-6 3-7 3-8 3-9	Check the audio signals input to and output from IC6402.	<ul style="list-style-type: none"> If a signal is interrupted at the periphery of IC6402, IC6402 or peripheral parts must be improperly soldered or defective. If no signal is input to IC6402, go to [4]. 	10.41 WAVEFORMS SEND ASSY 3-6 3-7 3-8 3-9
4	Loose connection /defective parts	SEND Assy IC6401-3 pin 3-4	Check that a signal is supplied to the ADAT_SEND_OUT.	<ul style="list-style-type: none"> If no signal is found, DSP(IC701) must be improperly soldered or defective. If a signal is found, go to [5]. 	10.41 WAVEFORMS SEND ASSY 3-4
5	Loose connection /defective parts	SEND Assy IC6401 5 pin	Check that the RESET_OUT signal level is High.	<ul style="list-style-type: none"> If it is H, go to [6]. If it is L, check for R709. If no problem is found, ETHER UCOM (IC501) must be improperly soldered or defective. 	—————
6	Loose connection /defective parts	SEND Assy LRCK: 4 pin 3-3 BCLK: 2 pin 3-2 MCLK: 1 pin 3-1	Check for the CLK signal of the IC6401.	<ul style="list-style-type: none"> If a CLK signal is not found, go to [9] of [3-1] No signal is output from the MASTER1/MASTER2 connectors. 	10.41 WAVEFORMS SEND ASSY 3-3 3-2 3-1

C

D

[3-5] No signal is output from the PHONES connector.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Loose connection /defective parts	MAIN Assy / HPJK Assy	Check for the connection between the MAIN Assy and HPJK Assy.	Correct any power connections.	—————
2	Loose connection /defective parts	HPJK Assy CN9201-1 pin,4 pin 5-1 5-2	Check for the signals at Pins 1 and 4 of CN9201.	<ul style="list-style-type: none"> If a signal is found, the HPJK ASSY or wires are defective. Replace them. If no signal is found, go to [3]. 	10.41 WAVEFORMS HPJK ASSY 5-1 5-2
3	Loose connection	MAIN Assy CN3401 1 pin 1-146 , 4 pin 1-147	Check for the signals at Pins 1 and 4 of CN3401.	<ul style="list-style-type: none"> If a signal is found, wires are defective. Replace them. If no signal is found, go to [4]. 	10.41 WAVEFORMS MAIN ASSY 1-146 1-147
4	Loose connection /defective parts	MAIN Assy RY3401-3 pin,8 pin 1-144 1-145	Check for the signals at Pins 3 and 8 of RY3401.	<ul style="list-style-type: none"> If a signal is found, go to [5]. If no signal is found, go to [6]. 	10.41 WAVEFORMS MAIN ASSY 1-144 1-145
5	Loose connection /defective parts	MAIN Assy CPU_MUTE 1-38	Check that the CPU_MUTE signal level is High.	<ul style="list-style-type: none"> If it is H, Q3409 must be improperly soldered or defective. If the CPU_MUTE signal is not supplied properly, go to [13] of [3-1] No signal is output from the MASTER1/MASTER2 connectors. If no problem is found, RY3401 must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-38

E

F

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
6	Loose connection /defective parts	MAIN Assy IC3406 HP_L1/HP_L2 1-140 1-142 HP_R1/HP_R2 1-141 1-143	Check the audio signals input to and output from IC3406.	<ul style="list-style-type: none"> If a signal is interrupted at the periphery of IC3406, IC3406 or peripheral parts must be improperly soldered or defective. If no signal is input to IC3406, go to [7]. 	10.41 WAVEFORMS MAIN ASSY 1-140 1-142 1-141 1-143
7	Loose connection /defective parts	MAIN Assy IC3405 HP_L+/L-/L1 1-136 1-137 1-140 HP_R+/R-/R1 1-139 1-138 1-141	Check the audio signals input to and output from IC3405.	<ul style="list-style-type: none"> If a signal is interrupted at the periphery of IC3405, IC3405 or peripheral parts must be improperly soldered or defective. If no signal is input to IC3405, go to [8]. 	10.41 WAVEFORMS MAIN ASSY 1-136 1-137 1-140 1-139 1-138 1-141
8	Loose connection /defective parts	MAIN Assy IC3404-3 pin 1-131	Check that a signal is supplied to the ADAT_HP.	<ul style="list-style-type: none"> If no signal is found, DSP(IC701) must be improperly soldered or defective. If a signal is found, go to [9]. 	10.41 WAVEFORMS MAIN ASSY 1-131
9	Loose connection /defective parts	MAIN Assy IC3404-5 pin 1-132	Check that the DIR_ADC_DAC_RESET signal level is High.	<ul style="list-style-type: none"> If it is H, go to [10]. If it is L, check for R709. If no problem is found, ETHER UCOM (IC501) must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-132
10	Loose connection /defective parts	MAIN Assy LRCK: IC3201-4 pin 1-135 BCLK: IC3201-2 pin 1-134 MCLK: IC3201-1 pin 1-133	Check for the CLK signal of the IC3404.	<ul style="list-style-type: none"> If a CLK signal is not found, go to [9] of [3-1] No signal is output from the MASTER1/MASTER2 connectors. 	10.41 WAVEFORMS MAIN ASSY 1-135 1-134 1-133

[3-6] No signal is output from the DIGITAL MASTER OUT connector.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Loose connection /defective parts	MAIN Assy IC2405-16 pin 1-74	Check for the signal at Pin 16 of DIT (IC2405).	<ul style="list-style-type: none"> If a signal is output, parts between IC2405 and JA2401 must be improperly soldered or defective. If no signal is output, go to [2]. 	10.41 WAVEFORMS MAIN ASSY 1-74
2	Loose connection /defective parts	MAIN Assy DIT_RESET: 31 pin 1-69 LRCK_DOUT: 24 pin 1-71 BCLK_DOUT: 26 pin 1-72 DAUX: 28 pin 1-70 XTI: 30 pin 1-68 DIT_TXD: 33 pin 1-65 DIT_SCK: 34 pin 1-66 DITD_CS: 35 pin 1-67	<p>Check for input signal at each points of IC2405.</p> <ul style="list-style-type: none"> A signal level at Pin 31 of the DIT_RESET must be High. LRCK_DOUT: 24 pin BCLK_DOUT: 26 pin DAUX: 28 pin XTI: 30 pin DIT_TXD: 33 pin DIT_SCK: 34 pin DITD_CS: 35 pin 	<ul style="list-style-type: none"> If Pin 31 of DIT_RESET is not High, check for R710. If no problem is found, Ether UCOM (IC501) must be improperly soldered or defective. If the signals at Pin 24 of LRCK, Pin 26 of BCLK, and Pin 28 of DAUX are not normal, go to [3]. If the signal at Pin 30 of XTI is not normal and no problem is found on IC2403, FPGA (IC2214) must be improperly soldered or defective. If signals at Pin 33 of the DIT_TXD, Pin 34 of the DIT_SCK, and Pin 35 of the DITD_CS are not normal, MAIN UCOM (IC2011) must be improperly soldered or defective. If both are normal and no signal is detected at Pin 16 of IC2405, IC2405 must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-69 1-71 1-72 1-70 1-68 1-65 1-66 1-67
3	Loose connection /defective parts	MAIN Assy DOUT_SRC_RESET: 5 pin 1-63 SRC_LRCK: 7 pin 1-61 SRC_BCLK: 8 pin 1-62 ADAT_DOUT: 9 pin 1-60 MS_SEL: 15 pin 1-64	<p>Check for input signal at each points of DOUT SRC (IC2404).</p> <ul style="list-style-type: none"> A signal level at Pin 5 of the DOUT_SRC_RESET must be High. SRC_LRCK: 7 pin SRC_BCLK: 8 pin ADAT_DOUT: 9 pin MS_SEL: 15 pin 	<ul style="list-style-type: none"> If Pin 5 of DOUT_SRC_RESET is not High, check for R708. If no problem is found, Ether UCOM (IC501) must be improperly soldered or defective. If the signals at Pin 7 of the LRCK and Pin 8 of the SCLK are not normal but no problem is found on IC2401/IC2402, FPGA (IC2214) must be improperly soldered or defective. If no signal is found at Pin 9 of the ADAT_DOUT, DSP (IC701) must be improperly soldered or defective. If the signal at Pin 15 of the MS_SEL is not normal and Q2403/Q2402/Q2401 do not function properly, they must be improperly soldered or defective. If the DOUT_MS_SEL signal is not supplied properly, check for R711. If no problem is found, Ether UCOM (IC501) must be improperly soldered or defective. If both are normal and no signal is output from IC2404, IC2404 must be improperly soldered or defective. 	10.41 WAVEFORMS MAIN ASSY 1-63 1-61 1-62 1-60 1-64

A [4] DVS

[4-1] A device connected to the USB connector cannot be recognized.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
0	Prior Confirmation	DIGITAL, CD/LINE, PHONO, USB DECK*selector /PC settings	<ul style="list-style-type: none"> • Check that the selector is set to USB DECK.* • Install the latest version of the driver. • Check that the settings for the DJM-900SRT Utility of the PC are correct. 	If there is any problem in the checked items, make corrections.	Operating instructions
1	Defective parts	USB1 Assy [Power supply] V+5VBUS CN3801 5 pin 1-165 [D±USB] D+USB, D-USB L3801, CN3801 3 pin 1-166 4 pin 1-167	[Power] Check that the C voltage is about 5 V (4.75–5.25) while the unit is connected with a PC. [D±USB] Check that a waveform can be observed while the unit is connected with a PC.	[Power] If the voltage is not normal, replace the USB1 Assy. If the voltage is normal, go to [2]. [D±USB] If there is an abnormality, soldering of the part may be defective. Repair the part or replace it with a good one. • If the above items are OK, go to [2].	10.41 WAVEFORMS MAIN ASSY 1-165 1-166 1-167
2	Loose connection	MAIN Assy [Power supply] V+5VBUS CN1802 5 pin 1-37 [D±USB] D+USB, D-USB CN1802 3 pin 1-168 4 pin 1-169	[Power] Check that the C voltage is about 5 V (4.75–5.25) while the unit is connected with a PC. [D±USB] Check that a waveform can be observed while the unit is connected with a PC.	[Power] If the voltage is not normal, there may be a break in a cable. If the voltage is normal, go to [3]. [D±USB] If no signal is output, there may be a break in a cable. Replace the part. • If the symptom persists after the above corrections, go to [3].	10.41 WAVEFORMS MAIN ASSY 1-37 1-168 1-169
3	Defective parts/ USB_UCOM defective	MAIN Assy [Power supply] V+5VBUS R1877	[Power] As R1877 is placed on the B side, you cannot inspect it with power ON. Remove the MAIN Assy to check the mounting status.	<ul style="list-style-type: none"> • [Power] If defective soldering of the resistor is identified, resolder it or replace the part. • If the symptom persists after the above corrections Soldering of the USB_UCOM (IC1801) may be defective. Replace the Assy. 	—————

[4-2] The unit cannot function with a control disc.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
0	Prior Confirmation	DIGITAL, CD/LINE, PHONO, USB DECK*selector /PC settings	<ul style="list-style-type: none"> • Check that the selector is set to USB DECK.* • Install the latest version of the driver. • Check that the settings for the DJM-900SRT Utility of the PC are correct. • Check that the signal from a control CD is input to the CD connector and that that from a control record is input to the PHONO connector. • Check that the settings are properly made, referring to the Quick Start Guide and the operating instructions of the application. 	If there is any problem in the checked items, make corrections.	Operating instructions, Quick start guide
1	Loose connection /defective parts	INPUT Assy MAIN Assy TC_SEL*	[Checking the Audio Timecode Select block] Check that the TC_SEL* signal from Pin 6 of IC4408/IC4808/IC5208/IC5608 is high (3.3 V).	<ul style="list-style-type: none"> • If the signal is not high (3.3 V), soldering of the MAIN_CPU (IC2011) or the IC itself is defective. • If the signal is high, go to [2]. 	—————
2	Loose connection /defective parts	INPUT Assy CH*_ADAT_TIME 2-27	Check that a signal is input to Pin 2 of IC4408/IC4808/IC5208/IC5608.	<ul style="list-style-type: none"> • If no signal is input, go to [3]. • If a signal is input, soldering of IC4408/IC4808 /IC5208/IC5608 or the IC itself is defective. 	10.41 WAVEFORMS INPUT ASSY 2-27
3	Loose connection /defective parts	INPUT Assy Representative CH1_TIME_R 2-26 IC4403 (CH1)/ IC4803 (CH2)/ IC5203 (CH3)/ IC5603 (CH4) 1 pin, 2 pin	[Checking the Timecode ADC block] Check that a signal is input to the Timecode ADC INPUT connector.	If no signal is input, the analog circuitry prior to the ADC block can be deemed as being in failure. <ul style="list-style-type: none"> • If no signal is input, go to [4]. • If a signal is input, go to [5]. 	10.41 WAVEFORMS INPUT ASSY 2-26
4	Loose connection /defective parts	INPUT Assy Before TRIM/ CH*_TIME_L /CH*_TIME_R 2-26 IC4401 (CH1)/ IC4801 (CH2)/ IC5201 (CH3)/ IC5601 (CH4)	[Checking the Timecode Buffer block] Measure a signal before and after the Timecode Buffer block.	<ul style="list-style-type: none"> • If a signal is interrupted around the Timecode Buffer block, soldering of the block or a part is defective. • If a signal is not interrupted, go to [5]. 	10.41 WAVEFORMS INPUT ASSY 2-26

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
5	Loose connection /defective parts	INPUT Assy IC4403 (CH1)/ IC4803 (CH2)/ IC5203 (CH3)/ IC5603 (CH4) RESET: Representative CH1_RESET: 13 pin 2-28 LRCK: Representative 10 pin 2-31 MCLK: Representative 11 pin 2-30 BCLK: Representative 12 pin 2-29	[Checking the Timecode ADC block] Check each signal of the ADC block.	<ul style="list-style-type: none"> If a signal at Pin 13 of RESET_IN* is not high (3.3 V), go to [2-1-9]. If no signal is input to LRCK, MCLK, or BCLK, go to [2-1-11]. If no problem is found with the above locations but if no signal is input to Pin 9, soldering of IC4403 (CH1), IC4803 (CH2), IC5203 (CH3), or IC5603 (CH4) is defective or the IC itself is defective. 	10.41 WAVEFORMS INPUT ASSY 2-28 2-31 2-30 2-29

[5] Indicator (FL/LED) not lit

SUB UCOM (IC6601) controls the FL/LED.

MAIN_CPU controls only LFO FORM (WAKE UP) button LED and EFFECT ON/OFF button LED.

[5-1] The FL does not light.

Driver power to the FL is supplied within the MAIN Assy.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Power failure	PNLA Assy V6601 24 pin 4-12 1 pin 4-10 25 pin 4-9 MAIN Assy IC1407 V+3R3D Block 1-2 IC1405 V+5D Block 1-5 IC1401 V+34D Block 1-4	Check for the voltage of the FL (V+3R3D_PA/V+5D_LED_PA/V+34D_PA).	<ul style="list-style-type: none"> If no loose connection is detected with the FL of the PNLA Assy but power voltage is not detected, check the mounting status of the DC-DC converter IC and peripheral parts for each voltage point of the MAIN Assy. If an error is detected, repair the defective parts. If the voltage is normal, go to [2]. 	4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS 10.41 WAVEFORMS PNLA ASSY 4-12 4-10 4-9 MAIN ASSY 1-2 1-5 1-4
2	Filament voltage error of the FL	PNLA Assy Voltage between pins 1 to 32 of V6601 4-11	Check that the FL filament voltage is $2.3\text{ V} \pm 20\%$.	<ul style="list-style-type: none"> If the voltage is outside the normal range, a bias circuit error of the FL may be suspected. Check the mounting status of Q6601, Q6602, and peripheral parts. If there is no problem, Q6601 or Q6602 may be defective. Replace them. If the voltage is normal, go to [3]. 	10.41 WAVEFORMS PNLA ASSY 4-11
3	Signal errors	PNLA Assy V6601 22 pin 4-14 21 pin 4-13 23 pin 4-15 24 pin 4-16	Check for the output signal of the FL communication line and the cable connection status in the PNLA Assy. <ul style="list-style-type: none"> FL_SCK FL_TXD FL_LAT FL_BK 	<ul style="list-style-type: none"> If no signal is output, check for the mounting status of SUB UCOM (IC6601). If there is no problem, the port may be defective. Replace it. If soldering is improper, resolder it. If the signal is normal, go to [4]. 	10.41 WAVEFORMS PNLA ASSY 4-14 4-13 4-15 4-16
4	Defective FL	PNLA Assy	If the symptom persists after the above corrections.	FL may be defective. Replace it.	—

[6] X-PAD

SUB UCOM (IC6601) controls the X-PAD.

[6-1] Pressing on the JOG dial not be detected

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Loose connection	PNLA Assy CDCB Assy	Check for loose connection on the signal line from the power source and SUB UCOM (IC6601) to the touch sensor IC (IC3601).	<ul style="list-style-type: none"> If any connection on the power or signal line is improper, correct it. If the signal is normal, go to [2]. 	—
2	Signal errors	PNLA Assy CN6601 10 pin 4-2 5 pin 4-5 8 pin 4-3 7 pin 4-4 3 pin 4-6 2 pin 4-7	Check for input and output signals of the communication line of the touch sensor IC in the PNLA Assy when power is supplied and the unit is turned on. <ul style="list-style-type: none"> V+3R3 (power) RBC_SCK RBC_SI RBC_SO RBC_CS RBC_INT 	<ul style="list-style-type: none"> If there is no signal when the unit is turned on, check the mounting status of SUB UCOM (IC6601). If there is no problem, the port may be defective. Replace it. If the signal is normal, go to [3]. 	4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS 10.41 WAVEFORMS PNLA ASSY 4-5 4-3 4-4 4-6 4-7
3	Touch sensor IC defective	PNLA Assy (the same points checked in No.2)	Check that the input and output signals of the touch sensor IC communication line in the PNLA Assy with the X-PAD touched in normal operation. (the same points checked in No.2)	If there are no input or output signals, the touch sensor IC (IC3601) may be defective. As this part cannot be replaced, replace the Assy.	—

A [6-2] The X-PAD LEDs not light

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Loose connection	PNLA Assy CN6602 27 pin 4-8 /MAIN Assy	Check for drive voltage of V+12_STB of the LED.	<ul style="list-style-type: none"> If any connection on the power line is improper, correct it. If the signal is normal, go to [2]. 	4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS 10.41 WAVEFORMS PNLA ASSY 4-8
2	Signal errors	PNLA Assy IC6601 79 pin 4-21 80 pin 4-20	Check if the LED drive signal is output from SUB uCOM (IC6601 Pins 79 and 80).	<ul style="list-style-type: none"> If the touch sensor functions normally but no signal is output, check the mounting status of the periphery of Q6651 to Q6654. If there is no problem, SUB UCOM (IC6601) port may be defective. Replace it. If the signal is normal, go to [3]. 	10.41 WAVEFORMS PNLA ASSY 4-21 4-20
3	Defective LED	PNLA Assy CN6601 11↔12 pin 4-1	Check for the voltage (2.7–4.2 V) at both electrodes of the LED.	If no voltage is detected, the LED may be defective. Replace it.	10.41 WAVEFORMS PNLA ASSY 4-1

B [7] LAN

[7-1] No LAN communication

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1			Perform the Mode 13:Device Test in Test mode, and check if normal status is displayed for LAN, MACAd, IPAdd, and Subn.	<ul style="list-style-type: none"> If all show normal status, go to [2]. If LAN is NG, go to [3]. <p>If MACAd, IPAdd, and Subn are abnormal, data stored in the Flash (IC502) for the ETHER UCOM may be damaged. Replace the entire MAIN Assy. Note: The Flash (IC502) DYW1798 for the ETHER UCOM is not a service part for MAC address administration. Replace it with the MAIN Assy.</p>	6.1 TEST MODE
2	Error between the LAN connectors (JA1601) and ETHER-PHY (IC1603)	MAIN Assy 1-18	Check that the status of TRANS (T1601), Filters (F1601, F1602), etc.	<ul style="list-style-type: none"> Repair the defective parts. If the signal is normal, go to [4]. 	10.41 WAVEFORMS MAIN ASSY 1-18
3	<ul style="list-style-type: none"> Part defective Signal error 	MAIN Assy [POWER] L1601 1-20 [CLK] R1629 1-21 [Communication line] Representative R1631 1-19	Error between ETHER-PHY (IC1603) and ETHER Ucom (IC501) may be suspected. [POWER] <ul style="list-style-type: none"> Power V+3R3D [CLK] <ul style="list-style-type: none"> 25M CLK * (25M_CLK_PHY) [Communication line] <ul style="list-style-type: none"> Check for the MII BUS line. 	<ul style="list-style-type: none"> Repair the defective parts. If the signal is normal, go to [4]. 	4.3 POWER BLOCK DIAGRAM 5.3 INFORMATION ON POWER DIAGNOSTICS 10.41 WAVEFORMS MAIN ASSY 1-20 1-21 1-19
4	Part defective	MAIN Assy	If the symptom persists after the above corrections.	ETHER-PHY (IC1603) may be defective. Replace it.	

C [8] CROSS FADER

[8-1] Abnormal function of the crossfader

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	Calibration	Calibration	Perform calibration in Test mode to check if the crossfader functions properly.	If calibration is not possible or fails, go to [8-2].	6.1 TEST MODE

[8-2] The crossfader is inoperable.

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
1	The CRFD Assy is not functioning.	CRFD Assy [Power supply] V+3R3REF IC4001 5 pin [GND] GNDREF IC4001 2 pin [AD_CRS_FADER signal] IC4001 4 pin, R4002, R4003 8-1	[Power] Check that AC power is supplied properly. [GND] Check that GNDREF is connected with GNDD in the MAIN Assy. [AD_CRS_FADER signal] Check the AD_CRS_FADER signal at each diagnostic point while operating the crossfader.	<ul style="list-style-type: none"> [Power] If there is any abnormality, soldering of IC4001 may be defective. If the symptom persists after resoldering the defective part, go to [2]. [GND] If there is any abnormality, soldering of IC4001 may be defective. If the symptom persists after resoldering the defective part, go to [2]. [AD_CRS_FADER signal] If no signal is output, soldering of the resistor or IC4001 may be defective. Repair or replace the part with a good one. If no problem is found with the above parts, go to [2]. 	10.41 WAVEFORMS CRFD ASSY 8-1

No.	Cause	Diagnostics Point	Item to be Checked	Corrective Action	Reference
2	Loose connection	PNLB Assy [Power supply] V+3R3REF2 CN7002 3 pin [GND] GNDREF2 CN7002 1 pin [CROSS_FADER signal] CN7002 2 pin same as 8-1	[Power] Check that AC power is supplied properly. [GND] Check that GNDREF2 is connected with GNDD in the MAIN Assy. [CROSS_FADER signal] Check the CROSS_FADER signal at the diagnostics point while operating the crossfader.	<ul style="list-style-type: none"> [Power] If power is supplied properly, there may be a break in a cable. If power is not supplied properly, go to [3]. [GND] If connection is properly made, there may be a break in a cable. If connection is not properly made, go to [3]. [CROSS_FADER signal] If no signal is output, there may be a break in a cable. Replace the part. If the symptom persists after the above corrections, go to [3]. 	10.41 WAVEFORMS CRFD ASSY 8-1
3	Loose connection	PNLB Assy [Power supply] V+3R3_REFA CN7001 25 pin [GND] GNDD_REFA CN7001 26 pin [CROSS_FADER signal] CN7001 30 pin same as 8-1	[Power] Check that AC power is supplied properly. [GND] Check that GNDD_REFA is connected with GNDD in the MAIN Assy. [CROSS_FADER signal] Check the CROSS_FADER signal at the diagnostics point while operating the crossfader.	<ul style="list-style-type: none"> [Power] If power is supplied properly, there may be a break in a cable inside the board. If power is not supplied, go to [4]. [GND] If connection is proper, there may be a break in a cable inside the board. If connection is not properly made, go to [4]. [CROSS_FADER signal] If no signal is output, there may be a break in a cable inside the board. Replace the Assy. If the symptom persists after the above corrections, go to [4]. 	10.41 WAVEFORMS CRFD ASSY 8-1
4	Loose connection	PNLA Assy [Power supply] V+3R3_REFA_PA CN6603 25 pin [GND] GNDD_REFA_PA CN6603 26 pin [CROSS_FADER signal] CN6603 30 pin same as 8-1	[Power] Check that AC power is supplied properly. [GND] Check that GNDD_REFA is connected with GNDD in the MAIN Assy. [CROSS_FADER signal] Check the CROSS_FADER signal at the diagnostics point while operating the crossfader.	<ul style="list-style-type: none"> [Power] If power is supplied properly, there may be a break in a cable inside the board. If power is not supplied, go to [5]. [GND] If connection is proper, there may be a break in a cable inside the board. If connection is not properly made, go to [5]. [CROSS_FADER signal] If no signal is output, there may be a break in a cable inside the board. Replace the Assy. If the symptom persists after the above corrections, go to [5]. 	10.41 WAVEFORMS CRFD ASSY 8-1
5	Loose connection	PNLA Assy [Power supply] V+3R3D_REFA CN6602 6 pin [GND] GNDD_REFA CN6602 4 pin [CROSS_FADER signal] CN6602 23 pin same as 8-1	[Power] Check that AC power is supplied properly. [GND] Check that GNDD_REFA is connected with GNDD in the MAIN Assy. [CROSS_FADER signal] Check the CROSS_FADER signal at the diagnostics point while operating the crossfader.	<ul style="list-style-type: none"> [Power] If power is supplied properly, there may be a break in a cable inside the board. If power is not supplied, go to [6]. [GND] If connection is proper, there may be a break in a cable inside the board. If connection is not properly made, go to [6]. [CROSS_FADER signal] If no signal is output, there may be a break in a cable inside the board. Replace the Assy. If the symptom persists after the above corrections, go to [6]. 	10.41 WAVEFORMS CRFD ASSY 8-1
6	Loose connection	MAIN Assy [Power supply] V+3R3D_REFA CN1001 26 pin [GND] GNDREF_A CN1001 28 pin [VR_FADER_CRS signal] CN1001 9 pin same as 8-1	[Power] Check that AC power is supplied properly. [GND] Check that GNDREF_A is connected with GNDD in the MAIN Assy. [VR_FADER_CRS signal] Check the VR_FADER_CRS signal at the diagnostics point while operating the crossfader.	<ul style="list-style-type: none"> [Power] If power is supplied properly, there may be a break in a cable inside the board. If power is not supplied, go to [7]. [GND] If connection is proper, there may be a break in a cable inside the board. If connection is not properly made, go to [7]. [VR_FADER_CRS signal] If no signal is output, soldering of the resistor or IC2011 may be defective. Repair or replace the part with a good one. If the symptom persists after the above corrections, go to [7]. 	10.41 WAVEFORMS CRFD ASSY 8-1
7	Loose connection/ MAIN_CPU defective	MAIN Assy [VR_FADER_CRS signal] R1012, R2062, IC2011 56 pin same as 8-1	[VR_FADER_CRS signal] Check the VR_FADER_CRS signal at the diagnostics point while operating the crossfader.	[VR_FADER_CRS signal] If no signal is output, soldering of the resistor or IC2011 may be defective. Repair or replace the part with a good one.	10.41 WAVEFORMS CRFD ASSY 8-1

6. SERVICE MODE

6.1 TEST MODE

1. Description of Test Modes

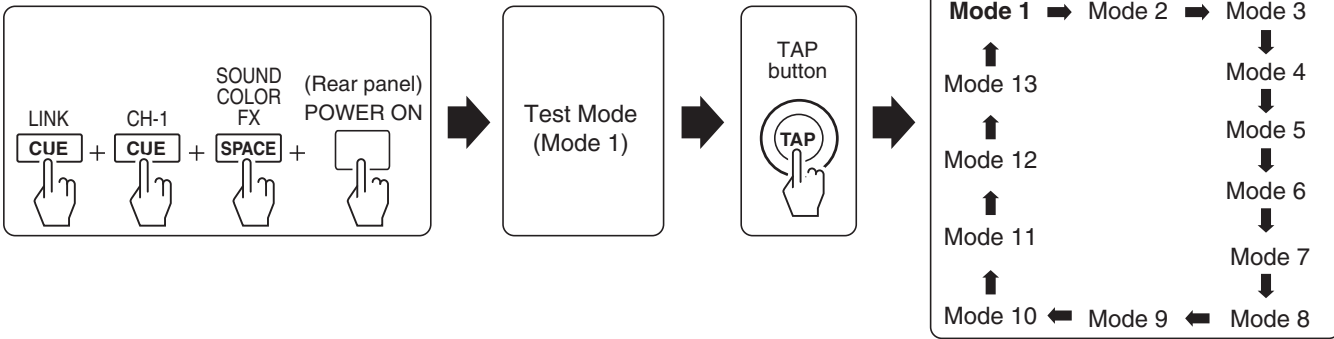
The Following 12 test modes are provided for this unit:

- ① Mode 1 : Confirmation of software version. "Version"
- ② Mode 2 : CROSS Fader Calibration mode. "FDR SET"
- ③ Mode 3 : All LEDs & FL display "OFF" mode. "ALL CLR"
- ④ Mode 4 : ALL LEDs & FL display "ON" mode. "ALL SET"
- ⑤ Mode 5 : KEY operating test. "KEY TEST"
- ⑥ Mode 6 : SELECT SW Operating test. "SW TEST"
- ⑦ Mode 7 : Rotary VRs value test. "VOLTEST"
- ⑧ Mode 8 : Fader test. "FDRTEST"
- ⑨ Mode 9 : Channel Level Indicator LED test. "LEDTEST"
- ⑩ Mode 10 : X-PAD value test. "RBNTST"
- ⑪ Mode 11 : AD values of the rotary VRs test. "VOL AD"
- ⑫ Mode 12 : AD values of the fader test. "FDR AD"
- ⑬ Mode 13 : Device test. "DEVICE"

2. Test Mode

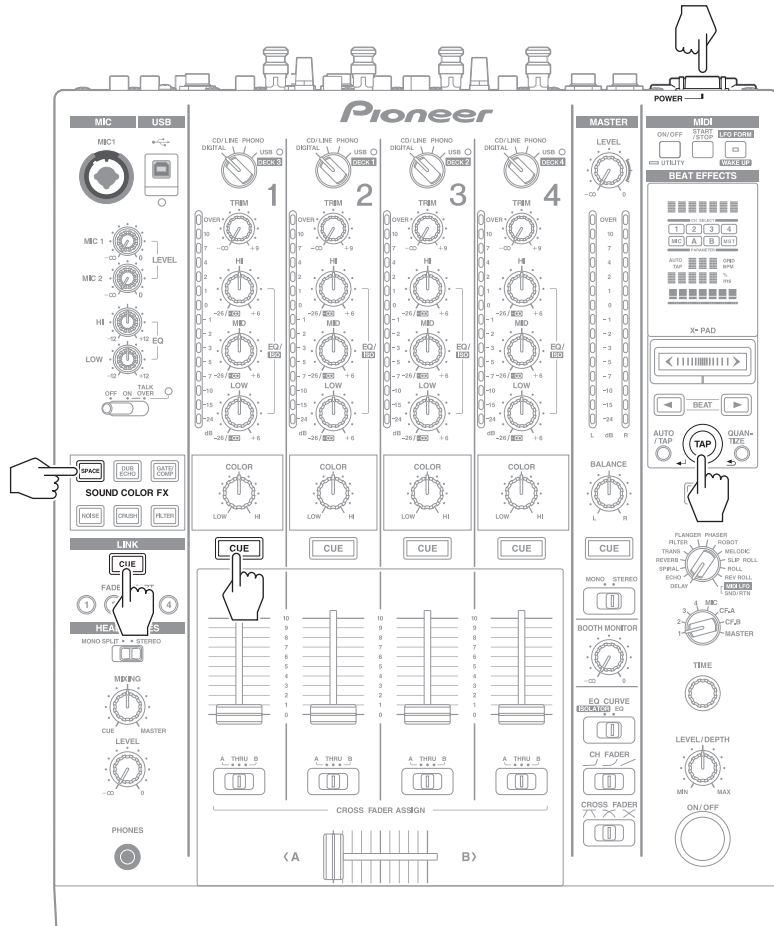
Test Mode : ON

Cyclic operation



Test Mode : CANCEL

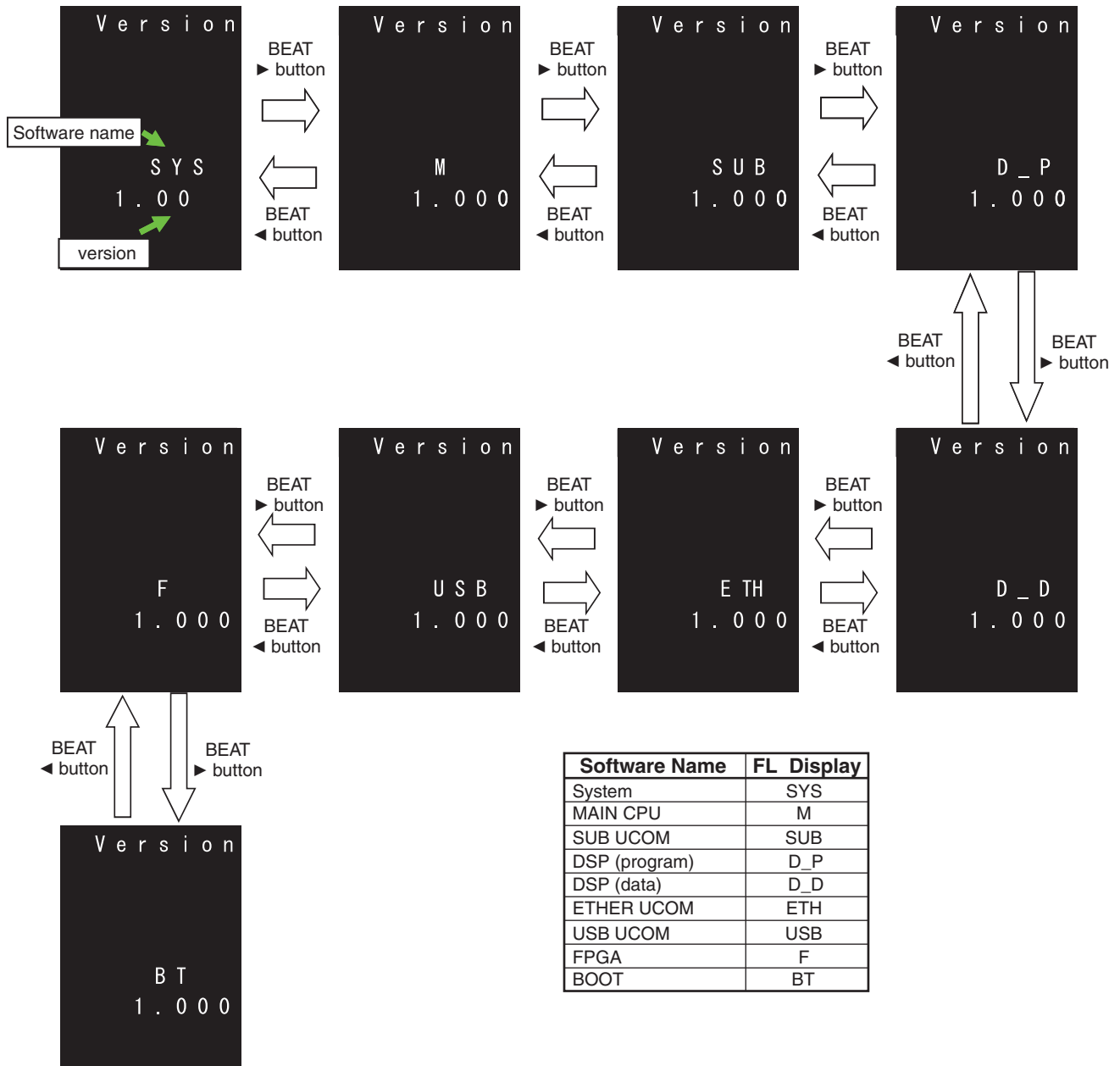
(Rear panel)
POWER OFF



3. Test mode Contents

① Mode 1 : Confirmation of software version

- Mode for confirmation of the versions of the system, MAIN CPU, SUB UCOM, DSP (program), DSP (data), ETHER UCOM, USB UCOM, FPGA and BOOT.
When this mode is entered, "Version" is displayed at the top of the FL display and the TAP key lights.
- Mode 1 consists of 9 pages for indicating the versions of the system, MAIN CPU, SUB UCOM, DSP (program), DSP (data), ETHER UCOM, USB UCOM, FPGA and BOOT.
The pages can be changed by pressing the BEAT ◀, ▶ button.
When Mode 1 is entered, the System Version Display mode is automatically entered.



A ② Mode 2 : CROSS Fader Calibration mode "FDR SET"

[Function outline]

Mode for obtaining then setting the minimum and maximum A/D values for the crossfader

[Mode name to be displayed]

FDR SET

[Operating elements]

[TAP] : For changing modes

[CH1 CUE] [CH4 CUE] : For setting an A/D value

[CH2 CUE] [CH3 CUE] : For starting setting and storing a setting value

B

[How to operate]

(1) Starting setting

Press the CH2 CUE and CH3 CUE buttons simultaneously.

Setting of the minimum and maximum A/D values starts.

The CH SELECT data are displayed on the FL display.

(2) Setting of the minimum value

Slide the crossfader to its leftmost position then press the CH1 CUE button.

As soon as the value is determined, the frame for "A" of the CH SELECT data on the FL display lights.

(3) Setting of the maximum value

Slide the crossfader to its rightmost position then press the CH4 CUE button.

As soon as the value is determined, the frame for "B" of the CH SELECT data on the FL display lights.

C

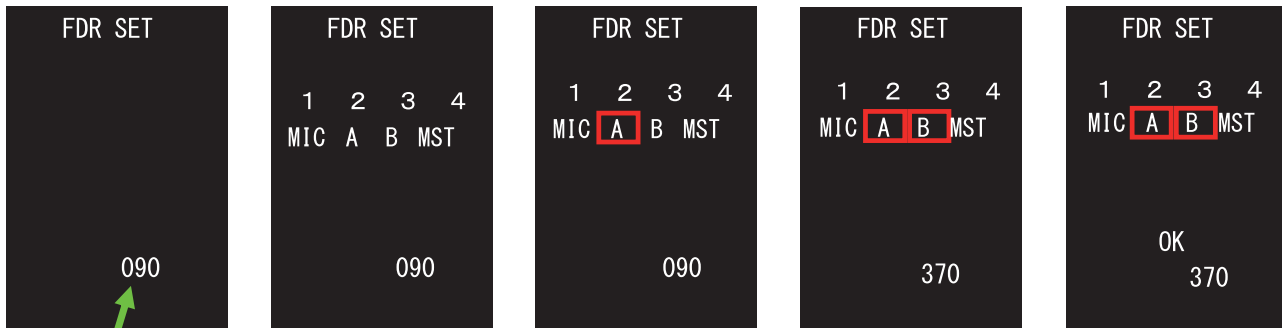
(4) Storing a setting value

Press the CH2 CUE and CH3 CUE buttons simultaneously.

Confirm that storage succeeded with the status code to be displayed on the FL display.

[Display change]

D



A/D value for the crossfader

CH2 CUE

CH3 CUE

A ————— B

CH1 CUE

A ————— B

CH4 CUE

CH2 CUE

CH3 CUE

[Error codes]

E

Description of the status codes to be displayed on the FL display

OK	Storage completed
NG	Parameter error
ERR	Storage failed.
E10	A/D-value acquisition error on the A side
E20	A/D-value acquisition error on the B side

[Special instructions]

- If the minimum and maximum A/D values of the crossfader are not set, the CH1 CUE, CH2 CUE, CH3 CUE, and CH4 CUE buttons flash when the unit is started in Normal mode.
- If the amplitude value becomes larger during acquisition of an A/D value because of noise or erroneous operation, that session is deemed to be a failure. An acquisition process is reattempted up to 3 times. If an A/D value cannot be obtained after the 3rd retry, an acquisition error results.
- The A/D values set in this mode will become effective after restart of the mixer.

F

③ Mode 3 : All LEDs & FL display “OFF” mode

- Mode for extinguishing all the LEDs and FL segments.
Only after this mode is entered, “ALL CLR” is displayed for 1 second at the top of the FL display.

④ Mode 4 : All LEDs & FL display “ON” mode

- Mode for lighting all the LEDs and FL segments.
Only after this mode is entered, “ALL SET” is displayed for 1 second at the top of the FL display.

⑤ Mode 5 : Key operating test

- Mode for indicating a pressed key with lighting of an LED and indication on the FL display.
When this mode is entered, “KEYTEST” is displayed at the top of the FL display and the TAP key lights.
- The name of a pressed key is indicated on the FL display.

Operating Keys	Lighting LED	FL Display	Remark
FADER START 1 button	FADER START 1 button LED	FSCH1	
FADER START 2 button	FADER START 2 button LED	FSCH2	
FADER START 3 button	FADER START 3 button LED	FSCH3	
FADER START 4 button	FADER START 3 button LED	FSCH4	
CH1 CUE button	CH1 CUE button LED	CUE_1	
CH2 CUE button	CH2 CUE button LED	CUE_2	
CH3 CUE button	CH3 CUE button LED	CUE_3	
CH4 CUE button	CH4 CUE button LED	CUE_4	
LINK CUE button	LINK CUE button LED	CUE_L	
MASTER CUE button	MASTER CUE button LED	CUE_M	
EFFECT CUE button	EFFECT CUE button LED	CUE_E	
SOUND COLOR FX SPACE button	SOUND COLOR FX SPACE button LED	CFX_1	
SOUND COLOR FX DUB ECHO button	SOUND COLOR FX DUB ECHO button LED	CFX_2	
SOUND COLOR FX GATE/COMP button	SOUND COLOR FX GATE/COMP button LED	CFX_3	
SOUND COLOR FX NOISE button	SOUND COLOR FX NOISE button LED	CFX_4	
SOUND COLOR FX CRUSH button	SOUND COLOR FX CRUSH button LED	CFX_5	
SOUND COLOR FX FILTER button	SOUND COLOR FX FILTER button LED	CFX_6	
LFO FORM (WAKE UP) button	LFO FORM (WAKE UP) button LED	LFO	
ON/OFF (UTILITY) button	ON/OFF (UTILITY) button LED	MD_ON	
START/STOP button	None	MD_ST	
BEAT ◀ button	None	BEAT<	
BEAT ▶ button	None	BEAT>	
AUTO/TAP button	None	AUTO	
QUANTIZE button	QUANTIZE button LED	GRID	
ON/OFF button	ON/OFF button LED	EFXon	

⑥ Mode 6 : SELECT SW Operating test. "SW TEST"

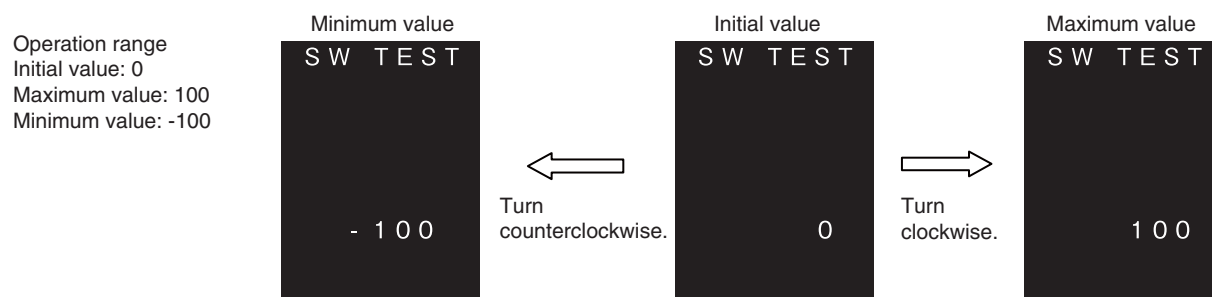
- When this mode is entered, "SW TEST" is displayed at the top of the FL display and the TAP key lights.

Operating Switches	Lighting LED	Remark
DIGITAL, CD/ LINE, PHONO, LINE, USB 1/2 selector switch CH1	: DIGITAL	4dB
	: CD/LINE	7dB
	: PHONO	10dB
	: USB DECK3	OVER
DIGITAL, CD/ LINE, PHONO, LINE, USB 3/4 selector switch CH2	: DIGITAL	4dB
	: CD/LINE	7dB
	: PHONO	10dB
	: USB DECK1	OVER
DIGITAL, CD/ LINE, PHONO, LINE, USB 5/6 selector switch CH3	: DIGITAL	4dB
	: CD/LINE	7dB
	: PHONO	10dB
	: USB DECK2	OVER
DIGITAL, CD/ LINE, PHONO, LINE, USB 7/8 selector switch CH4	: DIGITAL	4dB
	: CD/LINE	7dB
	: PHONO	10dB
	: USB DECK4	OVER
CROSS FADER ASSIGN (A, THRU, B) selector switch CH1	: Assign A	-24dB
	: THRU	-15dB
	: Assign B	-10dB
CROSS FADER ASSIGN (A, THRU, B) selector switch CH2	: Assign A	-24dB
	: THRU	-15dB
	: Assign B	-10dB
CROSS FADER ASSIGN (A, THRU, B) selector switch CH3	: Assign A	-24dB
	: THRU	-15dB
	: Assign B	-10dB
CROSS FADER ASSIGN (A, THRU, B) selector switch CH4	: Assign A	-24dB
	: THRU	-15dB
	: Assign B	-10dB
OFF, ON, TALK OVER selector switch	: OFF	-1dB
	: ON	0dB
	: TALK OVER	1dB
MONO SPLIT, STEREO selector switch	: MONO SPLIT	-5dB
	: STEREO	-3dB
MONO, STEREO selector switch	: MONO	0dB
	: STEREO	1dB
EQ CURVE (ISOLATOR, EQ) selector switch	: ISOLATOR	-3dB
	: EQ	-2dB
CH FADER (↵, ↷, ↶) selector switch	: Left	-1dB
	: MID	0dB
	: Right	1dB
CROSS FADER (↵, ↷, ↶) selector switch	: Left	-1dB
	: MID	0dB
	: Right	1dB

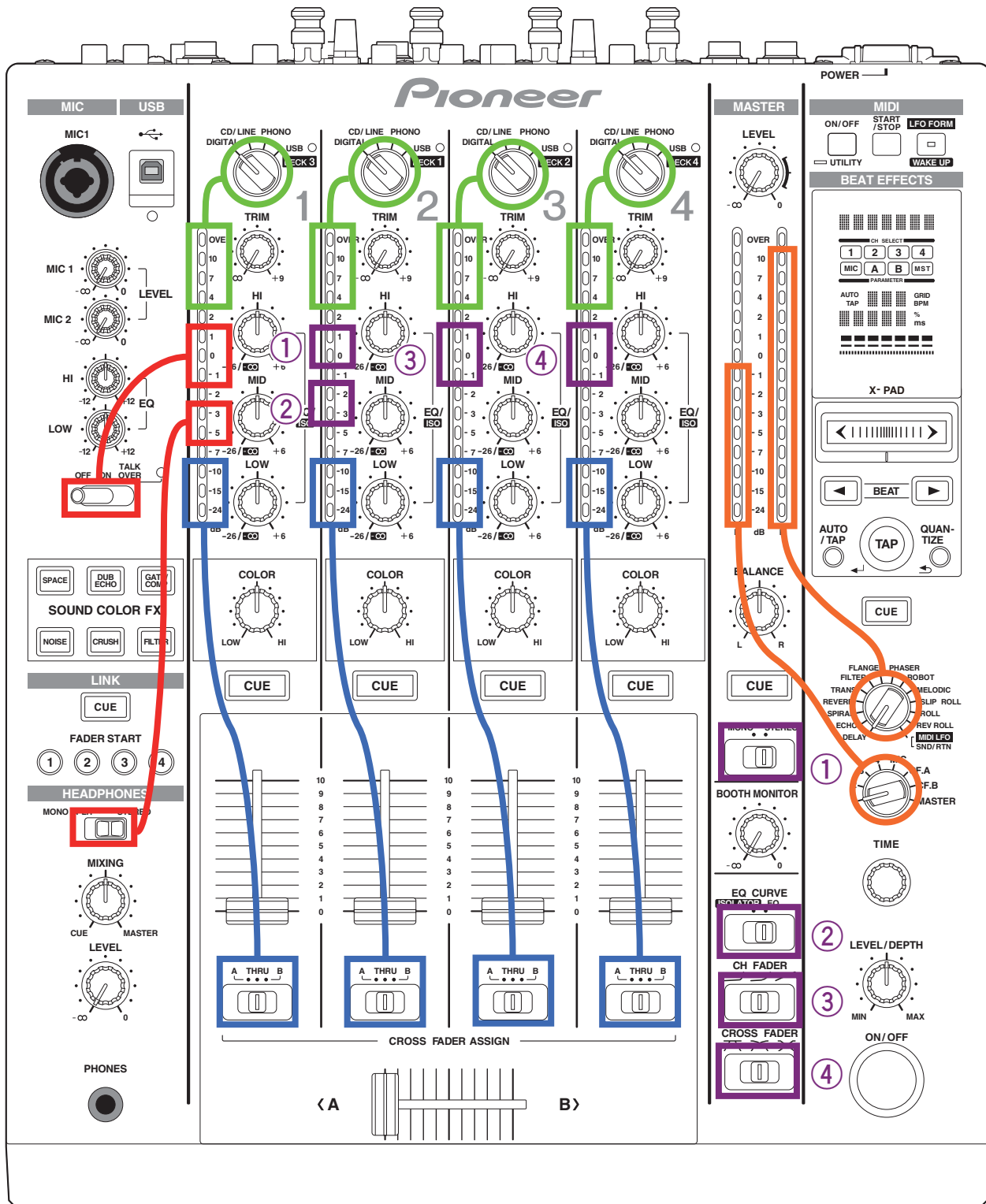
Operating Switches		Lighting LED		Remark
1, 2, 3, 4, MIC, CF.A, CF.B, MASTER selector switch	: 1	Master Level Indicator LED L CH	-24dB	
	: 2		-15dB	
	: 3		-10dB	
	: 4		-7dB	
	: MIC		-5dB	
	: CF.A		-3dB	
	: CF.B		-2dB	
	: MASTER		-1dB	
DELAY, ECHO, SPIRAL, REVERB, TRANS, FILTER, FLANGER, PHASER, ROBOT, MELODIC, SLIP ROLL, ROLL, REV ROLL, SND/RTN (MIDI LFO) selector switch	: DELAY	Master Level Indicator LED R CH	-24dB	
	: ECHO		-15dB	
	: SPIRAL		-10dB	
	: REVERB		-7dB	
	: TRANS		-5dB	
	: FILTER		-3dB	
	: FLANGER		-2dB	
	: PHASER		-1dB	
	: ROBOT		0dB	
	: MELODIC		1dB	
	: SLIP ROLL		2dB	
	: ROLL		4dB	
	: REV ROLL		7dB	
	: SND/RTN (MIDI LFO)		10dB	

This mode is also used to check operation of the TIME control.

The value displayed on the FL display increases/decreases as you turn the TIME control:

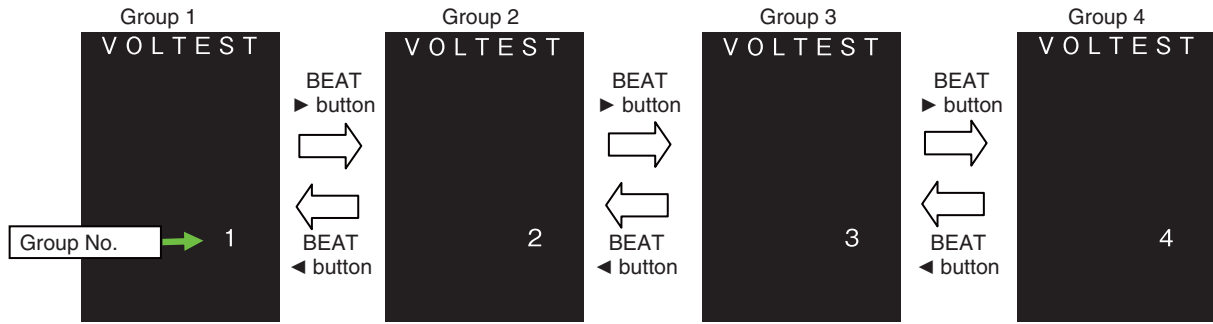


Correspondence diagram of Mode 6 : SELECT SW Operating test. "SW TEST"



⑦ Mode 7 : Rotary VRs value test. "VOLTEST"

- Mode for confirmation of the AD conversion value for each rotary VR on the operation panel with lighting of a channel level indicator LED or the segments on the FL display
When this mode is entered, "VOLTEST" is displayed at the top of the FL display and the TAP key lights.
- To indicate the AD conversion values for several rotary VRs with a single level meter, the rotary VRs are divided into 4 groups.
The group pages can be changed by pressing the BEAT ◀, ▶ button.



[Use of this mode during repair]

- For failure judgment of the rotary VRs
- For operation check of a rotary VR after replacement

• Group 1

Rotary VRs whose AD conversion values can be confirmed:

- CH1 TRIM control, CH2 TRIM control, CH3 TRIM control, CH4 TRIM control, EQ HI control, EQ LOW control, CH1 COLOR control

VR to be tested	Lit LED or FL	Remarks
CH1 TRIM control	CH1 Channel Level Indicator LED	"-∞": Lights off "+9": Full Illuminate
CH2 TRIM control	CH2 Channel Level Indicator LED	"-∞": Lights off "+9": Full Illuminate
CH3 TRIM control	CH3 Channel Level Indicator LED	"-∞": Lights off "+9": Full Illuminate
CH4 TRIM control	CH4 Channel Level Indicator LED	"-∞": Lights off "+9": Full Illuminate
EQ HI control	Master Level Indicator L LED	"-12": Lights off "+12": Full Illuminate
EQ LOW control	Master Level Indicator R LED	"-12": Lights off "+12": Full Illuminate
CH1 COLOR control	At the bottom of the FL display	"LOW": Lights off "HIGH": Full Illuminate

A

• Group 2

Rotary VRs whose AD conversion values can be confirmed:

- CH1 EQ/ISO HI control, CH2 EQ/ISO HI control, CH3 EQ/ISO HI control, CH4 EQ/ISO HI control, MIXING control, LEVEL control, CH2 COLOR control

VR to be tested	Lit LED or FL	Remarks
CH1 EQ/ISO HI control	CH1 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
CH2 EQ/ISO HI control	CH2 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
CH3 EQ/ISO HI control	CH3 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
CH4 EQ/ISO HI control	CH4 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
MIXING control	Master Level Indicator L LED	"CUE": Lights off "MASTER": Full Illuminate
LEVEL control	Master Level Indicator R LED	"-∞": Lights off "0": Full Illuminate
CH2 COLOR control	At the bottom of the FL display	"LOW": Lights off "HIGH": Full Illuminate

B

C

• Group 3

Rotary VRs whose AD conversion values can be confirmed:

- CH1 EQ/ISO MID control, CH2 EQ/ISO MID control, CH3 EQ/ISO MID control, CH4 EQ/ISO MID control, MASTER LEVEL control, BALANCE control, CH3 COLOR control

VR to be tested	Lit LED or FL	Remarks
CH1 EQ/ISO MID control	CH1 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
CH2 EQ/ISO MID control	CH2 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
CH3 EQ/ISO MID control	CH3 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
CH4 EQ/ISO MID control	CH4 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
MASTER LEVEL control	Master Level Indicator L LED	"-∞": Lights off "0": Full Illuminate
BALANCE control	Master Level Indicator R LED	"L": Lights off "R": Full Illuminate
CH3 COLOR control	At the bottom of the FL display	"LOW": Lights off "HIGH": Full Illuminate

D

E

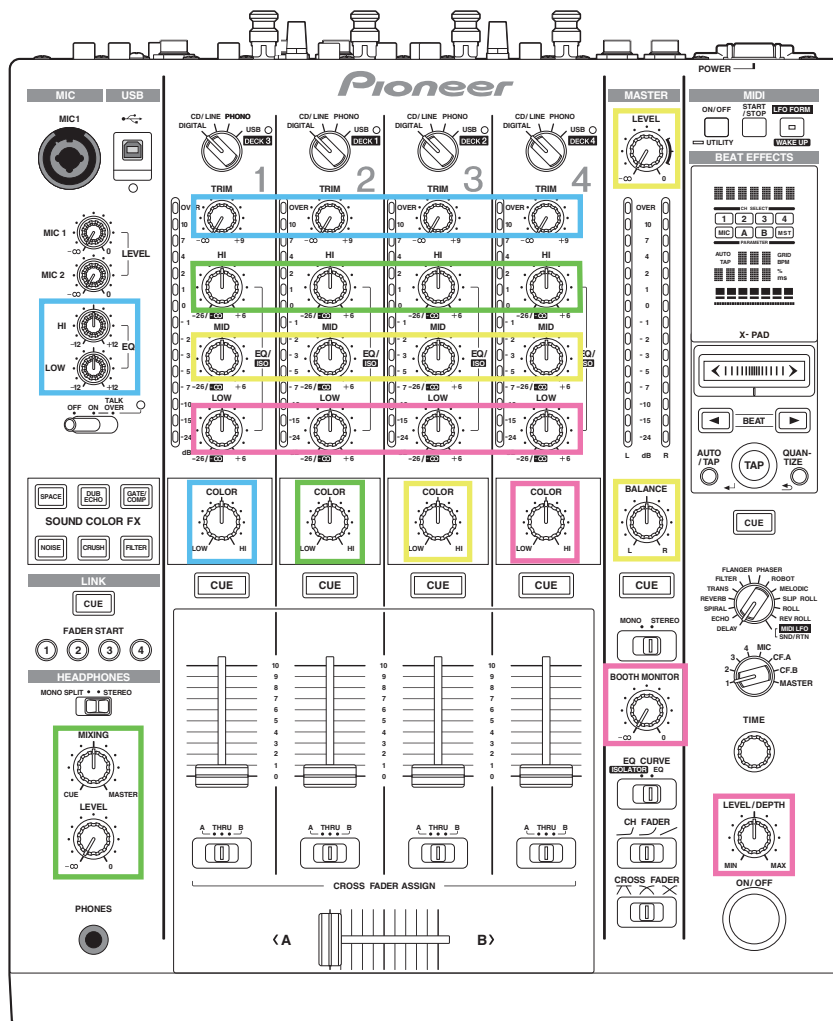
F

• Group 4

Rotary VRs whose AD conversion values can be confirmed:

- CH1 EQ/ISO LOW control, CH2 EQ/ISO LOW control, CH3 EQ/ISO LOW control, CH4 EQ/ISO LOW control, BOOTH MONITOR control, LEVEL/DEPTH control, CH4 COLOR control

VR to be tested	Lit LED or FL	Remarks
CH1 EQ/ISO LOW control	CH1 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
CH2 EQ/ISO LOW control	CH2 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
CH3 EQ/ISO LOW control	CH3 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
CH4 EQ/ISO LOW control	CH4 Channel Level Indicator LED	"-∞/-26": Lights off "+6": Full Illuminate
BOOTH MONITOR control	Master Level Indicator L LED	"-∞": Lights off "0": Full Illuminate
LEVEL/DEPTH control	Master Level Indicator R LED	"MIN": Lights off "MAX": Full Illuminate
CH4 COLOR control	At the bottom of the FL display	"LOW": Lights off "HIGH": Full Illuminate



- : Group 1
- : Group 2
- : Group 3
- : Group 4

⑧ Mode 8 : Fader test. "FDRTEST"

- Mode for confirmation of the values of CH1–4 Channel FADER and CROSS FADER with the channel level indicator LEDs. When this mode is entered, "FDRTEST" is displayed at the top of the FL display and the TAP key lights.

[Use of this mode during repair]

- For failure judgment of the faders
- For operation check of a fader after replacement

Fader to be tested	Lit LED	Remarks
CH1 Channel Fader	CH1 Channel Level Indicator LED	"0": Lights off "10": Full Illuminate
CH2 Channel Fader	CH2 Channel Level Indicator LED	"0": Lights off "10": Full Illuminate
CH3 Channel Fader	CH3 Channel Level Indicator LED	"0": Lights off "10": Full Illuminate
CH4 Channel Fader	CH4 Channel Level Indicator LED	"0": Lights off "10": Full Illuminate
Cross Fader	Master Level Indicator L LED	"A": Full Illuminate "B": Lights off

[Special instructions]

For the crossfader, the LEDs light with reference to the minimum and maximum A/D values that have been stored in CROSS Fader Calibration mode "FDR SET".

If the A/D values are not stored, the immediate A/D values will be used.

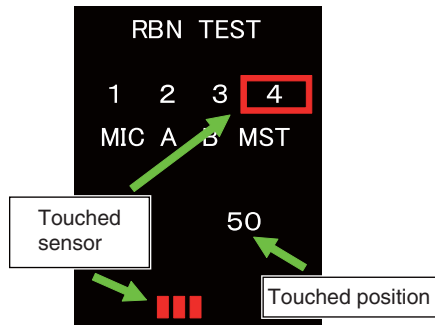
⑨ Mode 9 : Channel Level Indicator LED test. "LEDTEST"

- Mode for confirming lighting of the Channel Level Indicator LEDs
When this mode is entered, "LEDTEST" is displayed at the top of the FL display and the TAP key lights.
The LEDs for CH1 CUE button, CH2 CUE button, CH3 CUE button, CH4 CUE button, MASTER CUE button, and EFFECT CUE button light.
- Each time the CUE button is pressed, the channel level indicator LEDs for each channel light one by one from the bottom. At first, all the LEDs are unlit.
If the CUE button is pressed after it was pressed 15 times (when the top LED is lit), all the LEDs become unlit again.

Button to be tested	Lit Meter	Remarks
CH1 CUE button	CH1 Channel Level Indicator LED	
CH2 CUE button	CH2 Channel Level Indicator LED	
CH3 CUE button	CH3 Channel Level Indicator LED	
CH4 CUE button	CH4 Channel Level Indicator LED	
MASTER CUE button	Master Level Indicator L LED	
EFFECT CUE button	Master Level Indicator R LED	

⑩ Mode 10 : X-PAD value test. "RBNTTEST"

- Mode for confirmation of the X-PAD value with a channel level indicator LED and the FL display
When this mode is entered, "RBNTTEST" is displayed at the top of the FL display.
- The data item for a touched position on the X-PAD is expressed with a figure in the range of 0 to 127 and displayed in the middle of the FL display. The LEDs light according to the position touched on the X-PAD.
- The X-PAD consists of 8 sensors.
A touched sensor is indicated on the FL display so that you can judge if it is functioning properly.
The indication method is as shown below.



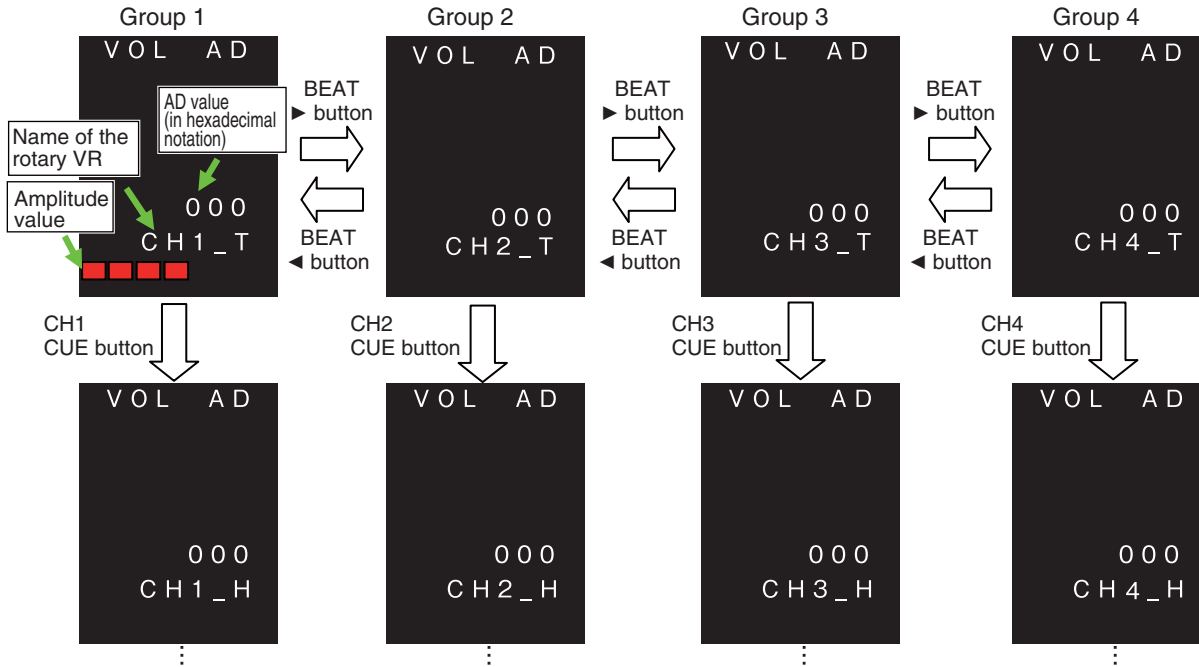
Operating Section	Lit LED or FL	Remarks
X-PAD	CH1 Channel Level Indicator LED	Left: Lights off Right: Full Illuminate
X-PAD	FL: BPM section on the FL display	Left: 0 Right: 127
X-PAD Sensor 1	CH and X-PAD sections on the FL display	CH section: Red frame around 1 X-PAD section: The 1st, 2nd, and 3rd segments light.
X-PAD Sensor 2	CH and X-PAD sections on the FL display	CH section: Red frame around 2 X-PAD section: The 5th, 6th, and 7th segments light.
X-PAD Sensor 3	CH and X-PAD sections on the FL display	CH section: Red frame around 3 X-PAD section: The 9th, 10th, and 11th segments light.
X-PAD Sensor 4	CH and X-PAD sections on the FL display	CH section: Red frame around 4 X-PAD section: The 13th, 14th, and 15th segments light.
X-PAD Sensor 5	CH and X-PAD sections on the FL display	CH section: Red frame around MIC X-PAD section: The 17th, 18th, and 19th segments light.
X-PAD Sensor 6	CH and X-PAD sections on the FL display	CH section: Red frame around A X-PAD section: The 21th, 22th, and 23th segments light.
X-PAD Sensor 7	CH and X-PAD sections on the FL display	CH section: Red frame around B X-PAD section: The 25th, 26th, and 27th segments light.
X-PAD Sensor 8	CH and X-PAD sections on the FL display	CH section: Red frame around MST X-PAD section: The 29th, 30th, and 31th segments light.

⑪ Mode 11 : AD values of the rotary VRs test. "VOL AD"

- Mode for displaying the AD values of rotary VRs on the FL display for confirmation. When this mode is entered, "VOL AD" is displayed at the top of the FL display. To indicate the AD values for several rotary VRs with a single level meter, the rotary VRs are divided into 4 groups. The group pages can be changed by pressing the BEAT ◀, ▶ button. When the groups are switched, the name of the VR at the top of the group list is displayed at the bottom of the FL display. The rotary VRs in a group can be changed by pressing the CH1-4 CUE button.
- The maximum amplitude value among AD conversion values for a rotary VR being tested can be confirmed on each page of this mode. The value is expressed with the number of segments lit at the bottom of the FL display. The measurement procedure is as indicated below. Turn the rotary VR you wish to test to a desired position then press the EFFECT CUE key to start measurement. To reset, press the EFFECT CUE key again. During measurement, the maximum amplitude value will be continuously displayed. The EFFECT CUE key is lit during measurement and goes dark when reset. Details on the amplitude value display are shown in the figures below.



An amplitude value in the range of ±1 to ±6 with regard to the VR value at the beginning of measurement is displayed. For a value greater than +7 or less than -7, all the 7 segments are lit.



Note:

If the CUE button is pressed while the last VR on each group list is displayed, the top VR on that group list will then be displayed. When the BEAT ◀, ▶ button is pressed for a layer lower than the 2nd one, the top VR on the list of the next group will be displayed.

[Use of this mode during repair]

- For failure judgment of the rotary VRs
As a guide, amplitude values higher than +4 or lower than -4 may be judged as failure.
The VRs can be set to any position during measurement. Possible symptoms are shown below.
 - The volume changes arbitrarily.
 - Interrupted sound leakage occurs even if the volume is decreased to the minimum at the Master or ZONE.
 - The MIDI signal is output even if the corresponding VR is not operated.
- For operation check of a rotary VR after replacement

• Group 1

Rotary VRs whose AD conversion values can be confirmed:

- CH1 TRIM control, CH1 EQ/ISO HI control, CH1 EQ/ISO MID control, CH1 EQ/ISO LOW control, CH1 COLOR control, EQ HI control, EQ LOW control

VR to be tested	FL Display Name	Remarks
CH1 TRIM control	CH1_T	"-∞": 000 "+9": 3FE
CH1 EQ/ISO HI control	CH1_H	"-∞/-26": 000 "+6": 3FE
CH1 EQ/ISO MID control	CH1_M	"-∞/-26": 000 "+6": 3FE
CH1 EQ/ISO LOW control	CH1_L	"-∞/-26": 000 "+6": 3FE
CH1 COLOR control	CH1_C	"LOW": 000 "HIGH": 3FE
EQ HI control	MIC_H	"-12": 000 "+12": 3FE
EQ LOW control	MIC_L	"-12": 000 "+12": 3FE

*As an AD value that is displayed on the FL display is the one before a hysteresis removal process, and as it has an amplitude, an error of about ± 2 may be produced.

• Group 2

Rotary VRs whose AD conversion values can be confirmed:

- CH2 TRIM control, CH2 EQ/ISO HI control, CH2 EQ/ISO MID control, CH2 EQ/ISO LOW control, CH2 COLOR control, MIXING control, LEVEL control

VR to be tested	FL Display Name	Remarks
CH2 TRIM control	CH2_T	"-∞": 000 "+9": 3FE
CH2 EQ/ISO HI control	CH2_H	"-∞/-26": 000 "+6": 3FE
CH2 EQ/ISO MID control	CH2_M	"-∞/-26": 000 "+6": 3FE
CH2 EQ/ISO LOW control	CH2_L	"-∞/-26": 000 "+6": 3FE
CH2 COLOR control	CH2_C	"LOW": 000 "HIGH": 3FE
MIXING control	HP_MX	"CUE": 000 "MASTER": 3FE
LEVEL control	HP_LV	"-∞": 000 "0": 3FE

*As an AD value that is displayed on the FL display is the one before a hysteresis removal process, and as it has an amplitude, an error of about ± 2 may be produced.

A

• Group 3

Rotary VRs whose AD conversion values can be confirmed:

- CH3 TRIM control, CH3 EQ/ISO HI control, CH3 EQ/ISO MID control, CH3 EQ/ISO LOW control, CH3 COLOR control, MASTER LEVEL control, BALANCE control

VR to be tested	FL Display Name	Remarks
CH3 TRIM control	CH3_T	"-∞": 000 "+9": 3FE
CH3 EQ/ISO HI control	CH3_H	"-∞/-26": 000 "+6": 3FE
CH3 EQ/ISO MID control	CH3_M	"-∞/-26": 000 "+6": 3FE
CH3 EQ/ISO LOW control	CH3_L	"-∞/-26": 000 "+6": 3FE
CH3 COLOR control	CH3_C	"LOW": 000 "HIGH": 3FE
MASTER LEVEL control	MSTLv	"-∞": 000 "0": 3FE
BALANCE control	MST_B	"L": 000 "R": 3FE

B

■

C

*As an AD value that is displayed on the FL display is the one before a hysteresis removal process, and as it has an amplitude, an error of about ± 2 may be produced.

• Group 4

Rotary VRs whose AD conversion values can be confirmed:

- CH4 TRIM control, CH4 EQ/ISO HI control, CH4 EQ/ISO MID control, CH4 EQ/ISO LOW control, CH4 COLOR control, BOOTH MONITOR control, LEVEL/DEPTH control

VR to be tested	FL Display Name	Remarks
CH4 TRIM control	CH4_T	"-∞": 000 "+9": 3FE
CH4 EQ/ISO HI control	CH4_H	"-∞/-26": 000 "+6": 3FE
CH4 EQ/ISO MID control	CH4_M	"-∞/-26": 000 "+6": 3FE
CH4 EQ/ISO LOW control	CH4_L	"-∞/-26": 000 "+6": 3FE
CH4 COLOR control	CH4_C	"LOW": 000 "HIGH": 3FE
BOOTH MONITOR control	BOOTH	"-∞": 000 "0": 3FE
LEVEL/DEPTH control	LV/DP	"MIN": 000 "MAX": 3FE

D

■

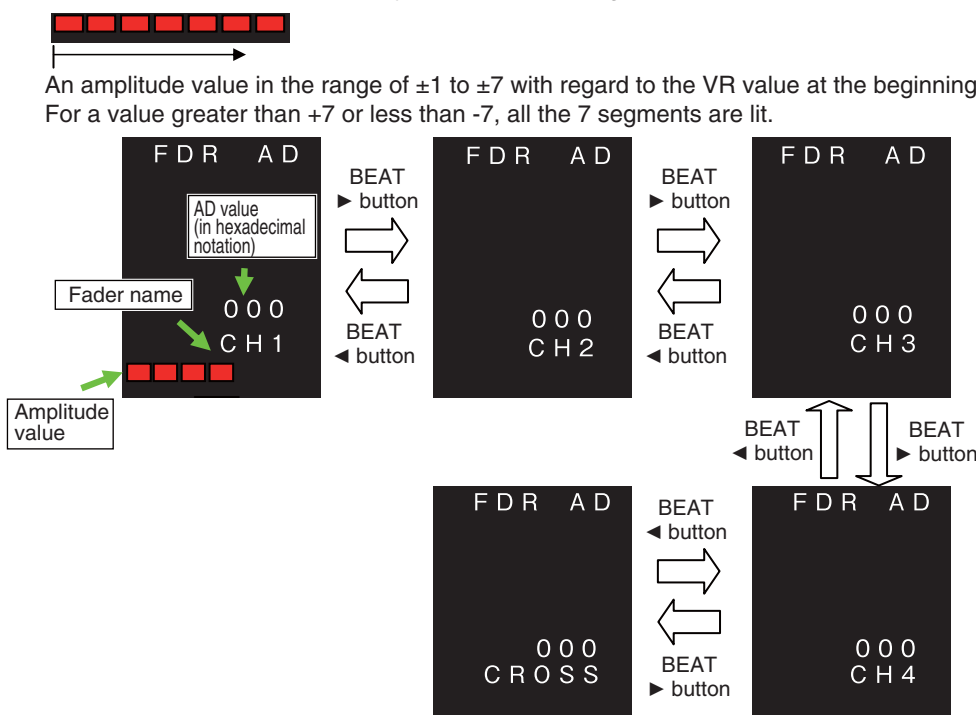
E

*As an AD value that is displayed on the FL display is the one before a hysteresis removal process, and as it has an amplitude, an error of about ± 2 may be produced.

F

⑫ Mode 12 : AD values of the fader test. "FDR AD"

- Mode for displaying the AD values of the faders on the FL display for confirmation
When this mode is entered, "FDR AD" is displayed at the top of the FL display and the TAP key lights.
- Mode 11 consists of 5 pages for indicating the values of 5 faders.
The pages can be changed by pressing the BEAT ◀, ▶ button.
When the groups are switched, the name of the VR at the top of the group list is displayed at the bottom of the FL display and its AD value is displayed in the middle.
- The maximum amplitude value among AD conversion values for a fader being tested can be confirmed on each page of this mode. The value is expressed with the number of segments lit at the bottom of the FL display. The measurement procedure is as indicated below.
Set the fader you wish to test to a desired position then press the EFFECT CUE key to start measurement. To reset, press the EFFECT CUE key again. During measurement, the maximum amplitude value will be continuously displayed.
The EFFECT CUE key is lit during measurement and goes dark when reset.
Details on the amplitude value display are shown in the figures below.



[Use of this mode during repair]

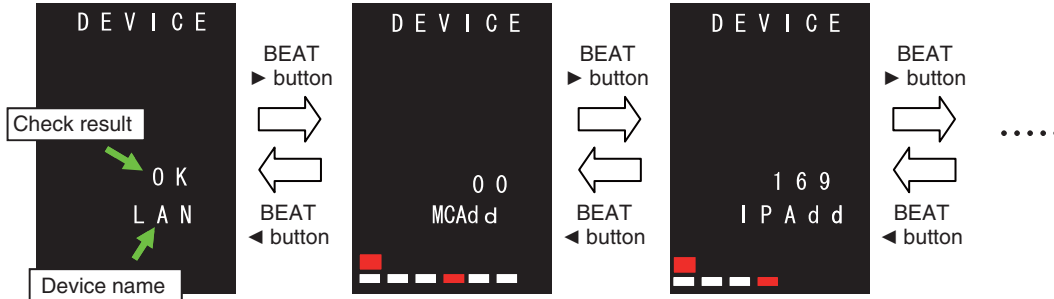
- For failure judgment of the faders
As a guide, amplitude values higher than $+4$ or lower than -4 may be judged as failure.
The VRs can be set to any position during measurement. Possible symptoms are shown below.
 - The volume changes arbitrarily.
 - Interrupted sound leakage occurs even if the volume is decreased to the minimum at the fader.
 - The MIDI signal is output even if the corresponding VR is not operated.
- For operation check of a fader after replacement

Faders that can be confirmed

Fader to be tested	FL Display Name	Remarks
CH1 Channel Fader	CH1	"0": 000 "10": 3FE
CH2 Channel Fader	CH2	"0": 000 "10": 3FE
CH3 Channel Fader	CH3	"0": 000 "10": 3FE
CH4 Channel Fader	CH4	"0": 000 "10": 3FE
Cross Fader	CROSS	"A": 000 "B": 3FE

13 Mode 13 : Device test. "DEVICE"

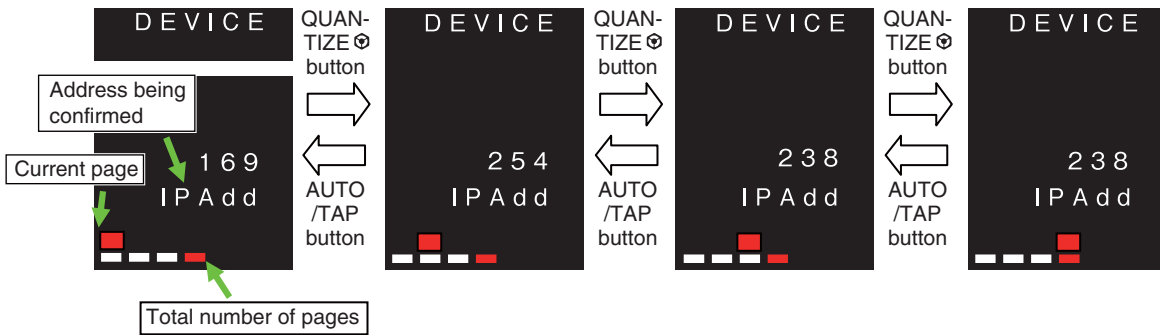
- Mode for displaying the device information for confirmation
When this mode is entered, "DEVICE" is displayed at the top of the FL display and the TAP button lights.
- Mode 12 consists of 5 pages for indicating the statuses of DSP SDRAM, LAN, IP address, MAC address, and subnet mask.
The pages can be changed by pressing the BEAT ◀, ▶ button.
- "OK" is displayed in the middle of the FL display when the device is operating properly, and "NG" is displayed when it is not properly operating.



- You can also check the address data for a device.
The address data that can be confirmed are MAC address, IP address, and IP address, and they are indicated by 3 digits at a time in the middle of the FL display.
If an address cannot be obtained, "EE" is displayed.
The address data pages can be changed with the AUTO/TAP or QUANTIZE button.
On the address data page, the current page No. and the total number of pages are displayed with lit segments at the bottom of the FL display.

Note: With the QUANTIZE button, the pages can be changed cyclically, but with the AUTO/TAP button, the pages change only in one direction (the page will not change if you press the key on Page 1).

Example: When the IP address is "169.254.238.238"



Devices and addresses that can be confirmed

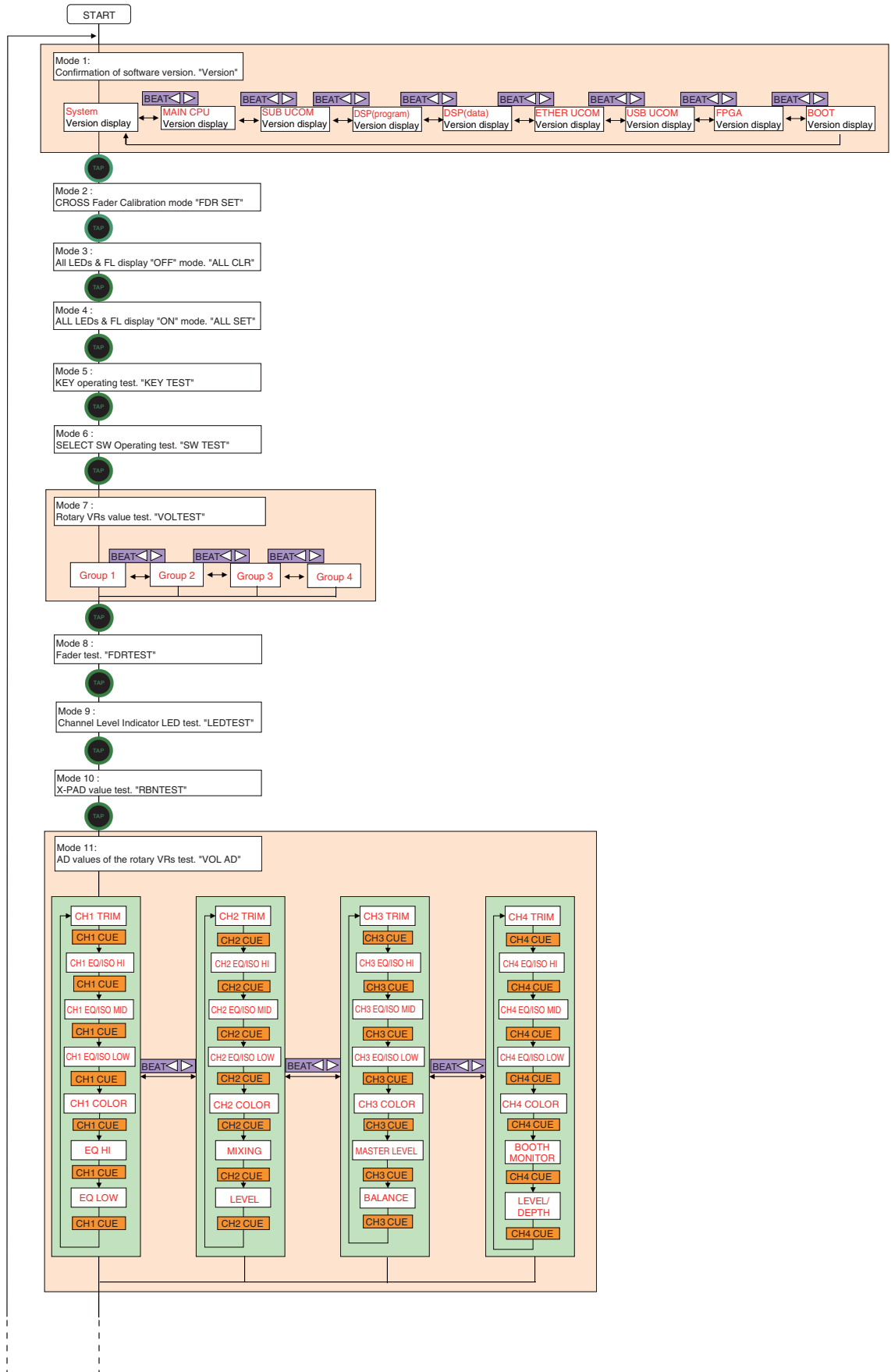
Device, Address	FL Display Name	Remarks
DSP SDRAM	SDRAM	During check: CHK, Properly operating: OK, Not properly operating: NG
LAN	LAN	Properly operating: OK, Not properly operating: NG
LAN (MAC Address)	MCAdd	: Address value
LAN (IP Address)	IPAdd	Properly operating: Address value, Not properly operating: EE *1
LAN (Subnet Mask)	Subn	Properly operating: Address value, Not properly operating: EE *2

*1 "EE" is also displayed while the IP address is being obtained. If "EE" is displayed for more than 15 seconds, something is wrong.
*2 "EE" is also displayed before the subnet mask data are obtained.

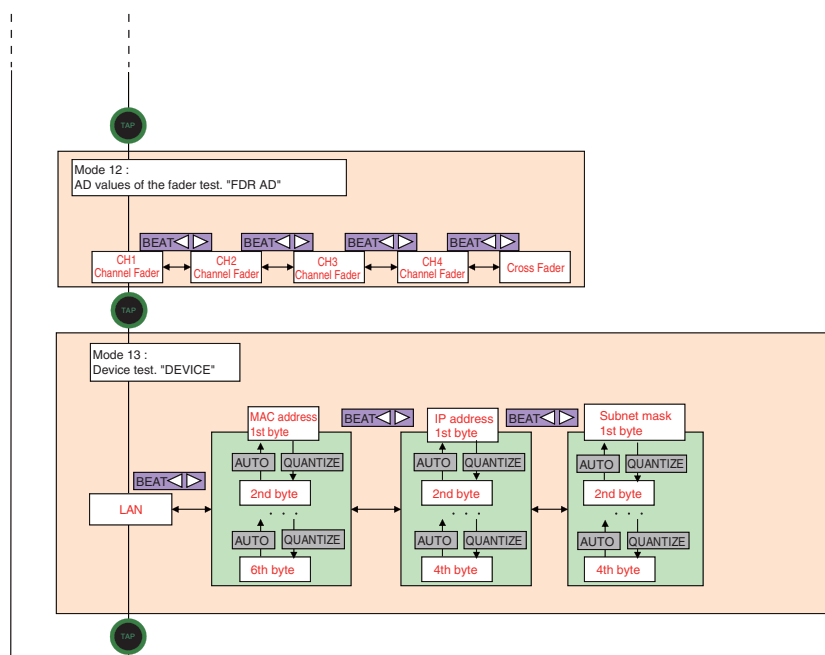
[Special instructions]

For acquisition of the IP address and subnet mask, the unit must be connected with a PC via a LAN cable.

4. Mode transition flowchart



A



B

C

6.2 ABOUT THE DEVICE

D

Device Name	Function	Part No.	Reference No.	Assy
MAIN CPU	Main control	DYW1835	IC2011	MAIN Assy
FPGA	For RAM, Digital input distinction, clock divider	XC3S50A-4FTG256C	IC2214	MAIN Assy
ETHER UCOM	CONTROLLER for Ether net, Main control support	R5S76700B200BG	IC501	MAIN Assy
FLASH (64M)	Memory for Ether UCOM (Firmware in MAC Address)	DYW1834	IC502	MAIN Assy
SDRAM (128M)	Memory for Ether UCOM (Work)	M12L128168A-5TG2N	IC503	MAIN Assy
DSP	Audio DSP	D810K013DZKB400	IC701	MAIN Assy
FLASH (4M)	Memory for DSP (Firmware)	DYW1833	IC703	MAIN Assy
SDRAM (256M)	Memory for DSP (Work)	M12L2561616A-5TG2A	IC702	MAIN Assy
USB UCOM	CONTROLLER for USB	ADSP-BF524BBCZ-3A	IC1801	MAIN Assy
SDRAM (64M)	Memory for USB UCOM (Work)	M12L64164A-5TG2Y	IC1802	MAIN Assy
ETHER PHY	PHY for Ether net LINK	RTL8201EL-VC-GR	IC1603	MAIN Assy
SUB UCOM	LED, FL, KEY, VR control	DYW1836	IC6601	PNLA Assy
CDC SENSOR	Contact position detection of a X-PAD	AD7147ACPZ500RL7	IC3601	CDCB Assy

E

Two or more FLASH and SDRAM are mounted in this unit.
Please judge the device which you should diagnose in reference to this list.

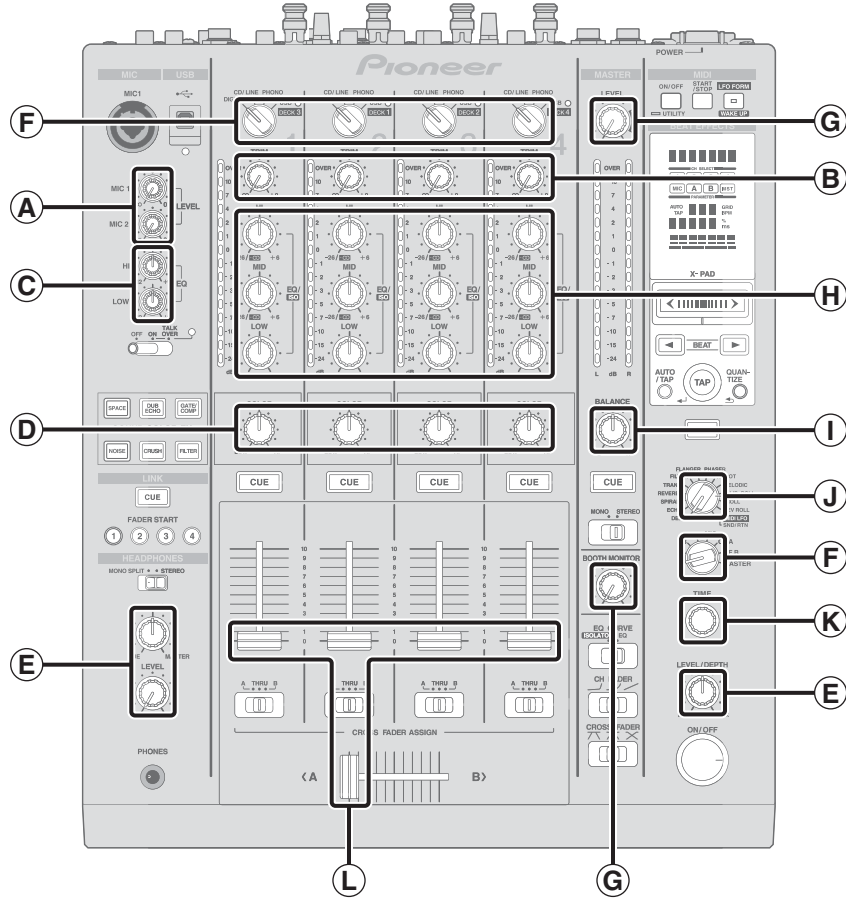
F

7. DISASSEMBLY

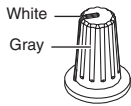
DISASSEMBLY for DJM-900SRT is the same as DJM-900NXS except for the following:

Note: Even if the unit shown in the photos and illustrations in this manual may differ from your product, the procedures described here are common.

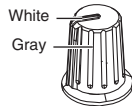
Knobs and Volumes Location



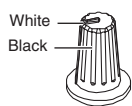
A DAA1308 ×2



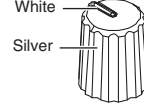
B DAA1321 ×4



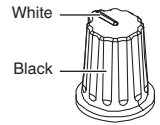
C DAA1307 ×2



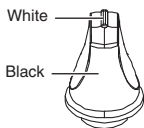
D DAA1320 ×4



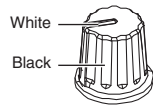
E DAA1220 ×3



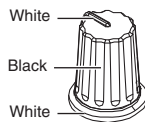
F DAA1213 ×5



G DAA1198 ×2



H DAA1305 ×12

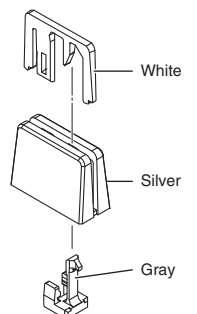


L DAC2935 ×5 + DAC2685 ×5 + DNK5888 ×5

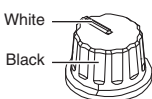
DAC2685

DAC2935

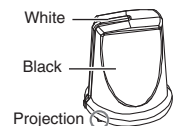
DNK5888



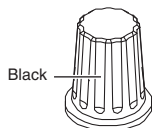
I DAA1265 ×1



J DAA1205 ×1



K DAA1180 ×1

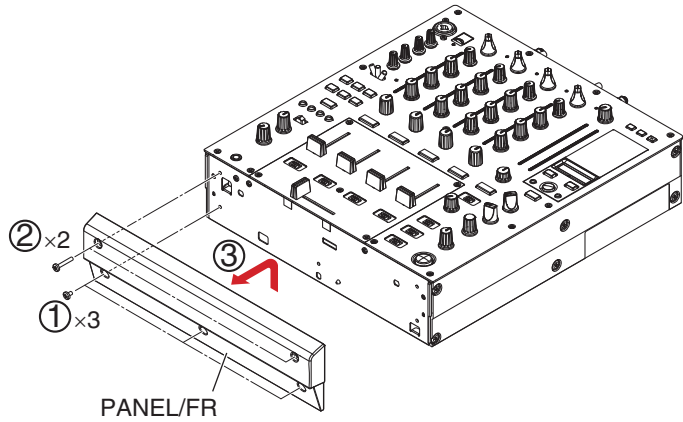


A Disassembly

[1] Exterior Section

[1-1] Exterior section

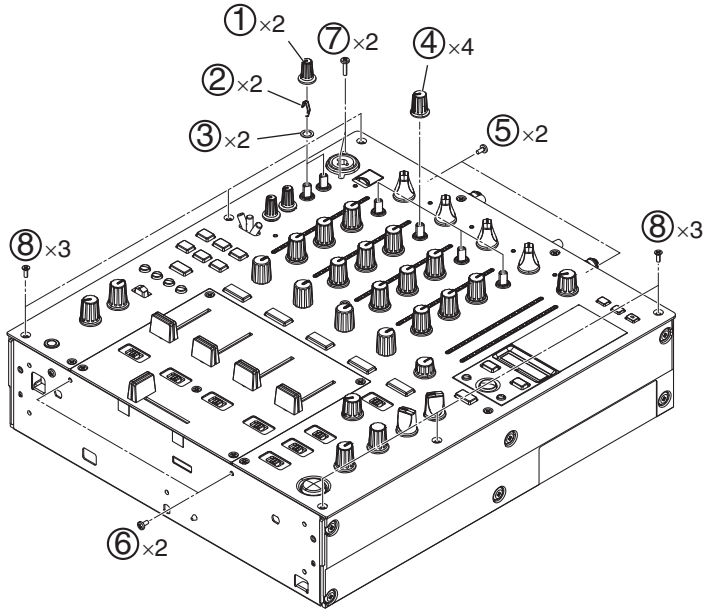
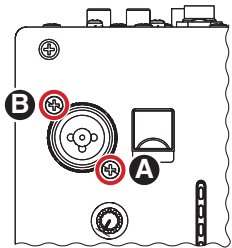
- (1) Remove the three screws. (BCZ40P060FTB)
- (2) Remove the two screws. (BSZ40P220FTB)
- (3) Remove it to the front direction while lifting PANEL/FR on the top.



[1-2] Control panel section

- (1) Remove the two KNOB/RSW. (DAA1308)
- (2) Remove the two springs. (DBH1788)
- (3) Remove the two washers. (WA62D095D025)
- (4) Remove the four Knob/CH. (DAA1321)
- (5) Remove the two screws. (BBZ30P060FTB)
- (6) Remove the two screws. (BBZ30P060FTC)
- (7) Remove the two screws. (BPZ30P120FTB)
- (8) Remove the six screws. (CCZ30P080FTB)

Screw tightening order



B

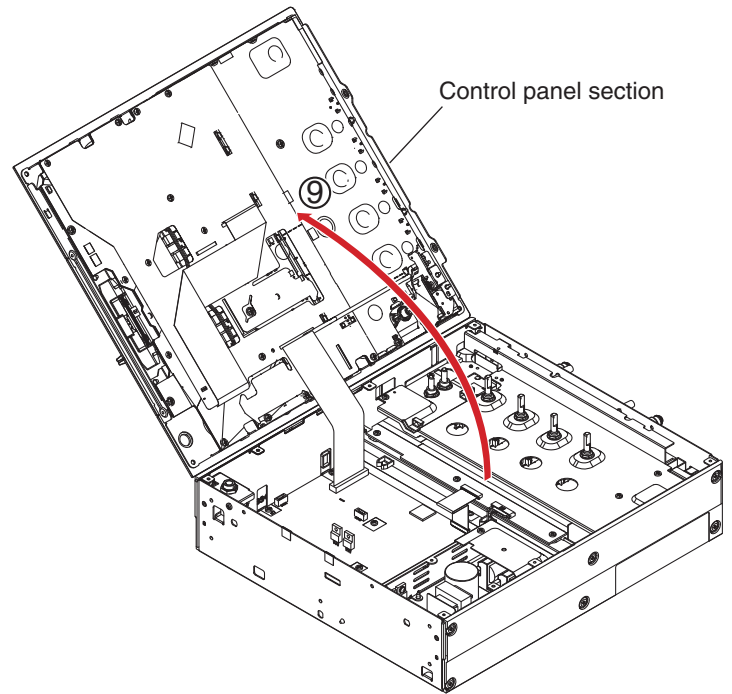
C

D

E

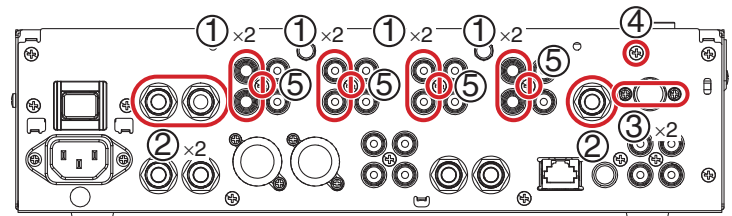
F

(9) Remove the control panel section.



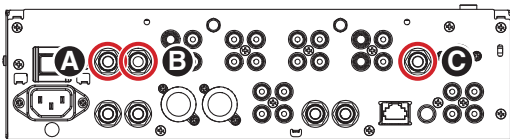
[2] INPUT Assy

- (1) Disconnect the eight short pin plugs.
- (2) Remove the three washers and three nuts.
- (3) Remove the two screws. (PMH30P100FTB)
- (4) Remove the one screw. (BBZ30P060FTB)
- (5) Remove the four screws. (BPZ30P080FTB)



• Rear view

Nut tightening order

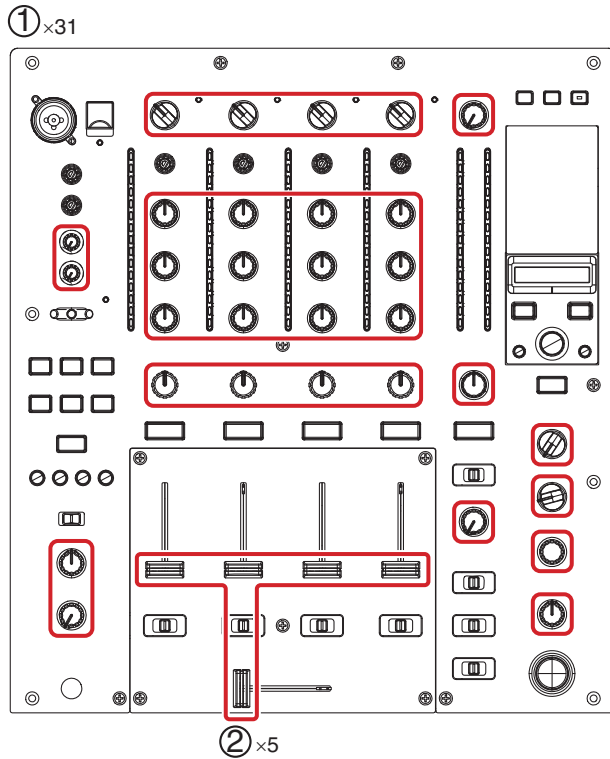
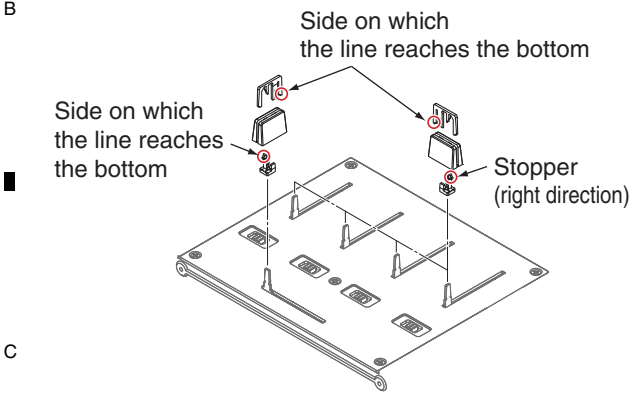


A [3] Panel Section

[3-1] Control panel and Fader panel

- (1) Remove the all knobs.
- (2) Remove the five DAC2685, five DAC2935, five DNK5888.
(See below.)

The reference of the direction



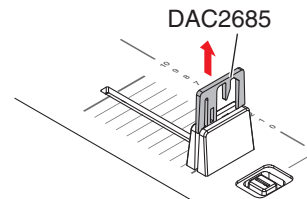
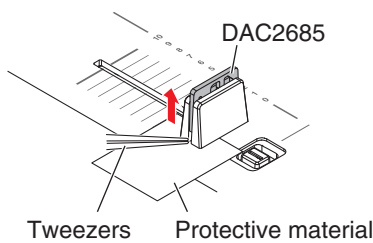
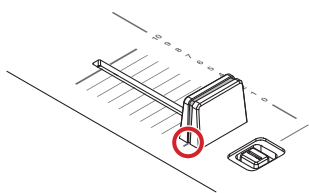
• Disassembly of the slider knob

The new slider knob adopted by this product is designed so that it is not pulled out easily. Therefore, the method for removing the slider knob is different from the conventional method; it can only be pulled out after DAC2685 is removed.

① Find the side on which the line reaches the bottom.

② Insert a pair of tweezers etc. beneath the line then push the DAC2685 upward. To protect the panel from being scratched, use protective material.

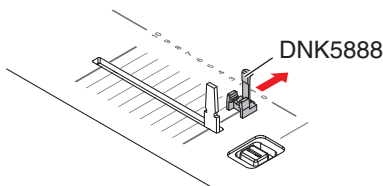
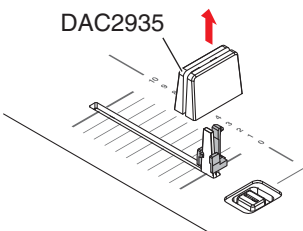
③ Remove the DAC2685.



*: During reassembly, fully push down DAC2685 until it is dented into Slider knob 1.

④ Remove the DAC2935.

⑤ Remove the DNK5888.

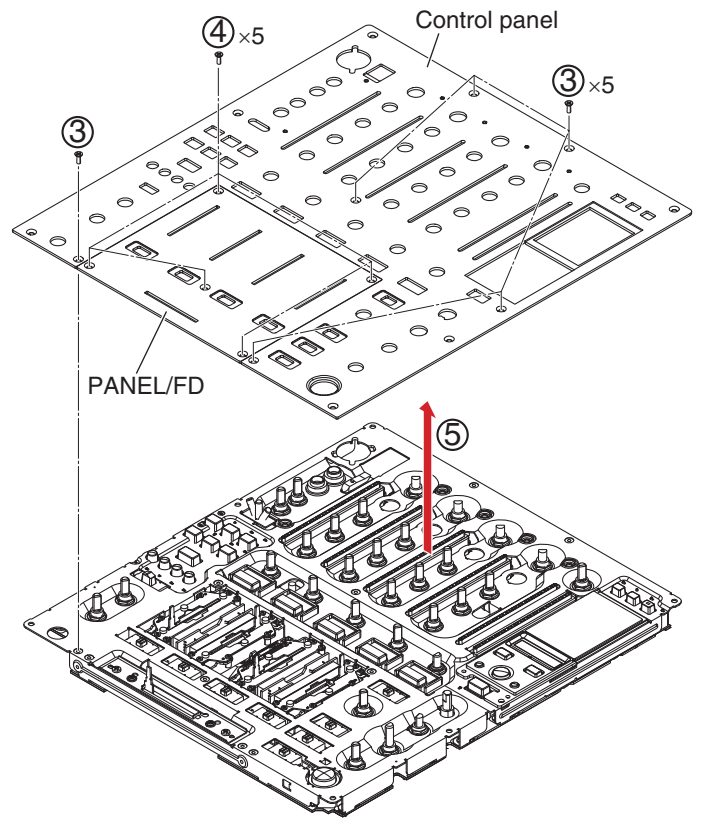
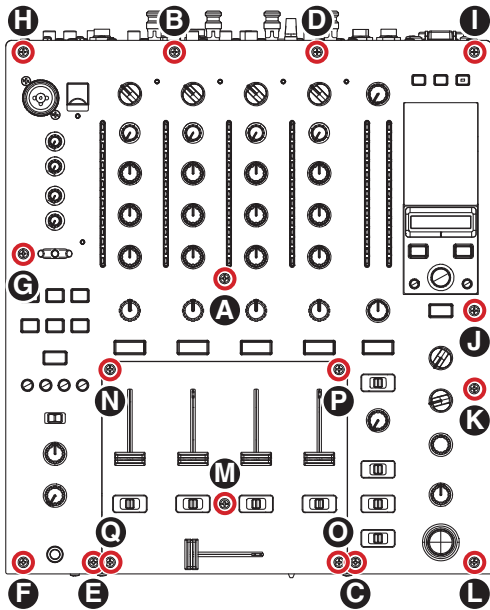


- (3) Remove the 6 screws. (CCZ30P080FTB)
- (4) Remove the 5 screws. (CCZ30P080FTB)
- (5) Remove the Control panel and PANEL/FD.

Note:

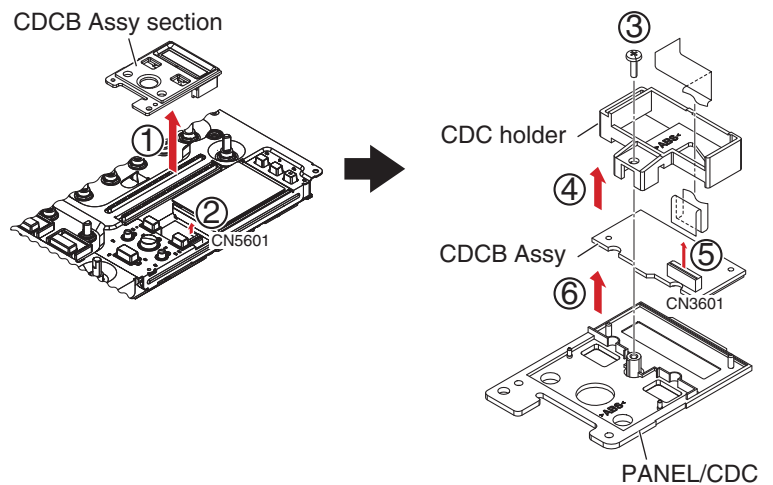
When you remove only PANEL/FD, it is a process of step (2), (4).

Screw tightening order
(for MAIN unit)



[3-2] CDCB Assy

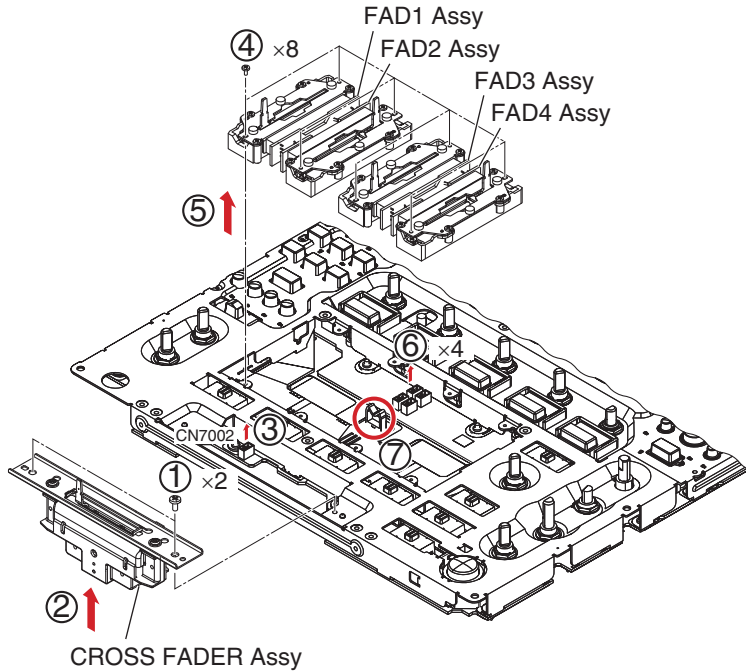
- (1) Remove the CDCB Assy section.
- (2) Disconnect the one flexible cable. (CN5601)
- (3) Remove the one screw. (BPZ26P080FTC)
- (4) Remove the CDC holder.
- (5) Disconnect the one flexible cable. (CN3601)
- (6) Remove the CDCB Assy.



• Bottom view

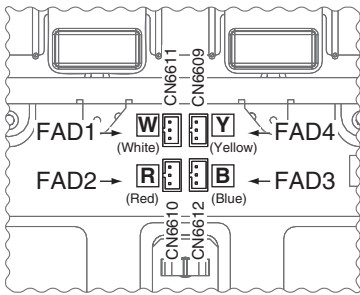
A [3-3] Fader section

- (1) Remove the two screws. (BBZ30P060FTC)
- (2) Remove the CROSS FADER Assy.
- (3) Disconnect the one connector. (CN7002)
- (4) Remove the eight screws. (BSZ20P040FTB)
- (5) Remove the FAD1 to FAD4 Assemblies.
- (6) Disconnect the four connectors. (CN6609 to 6612)
- (7) Release the jumper wire from clamer.



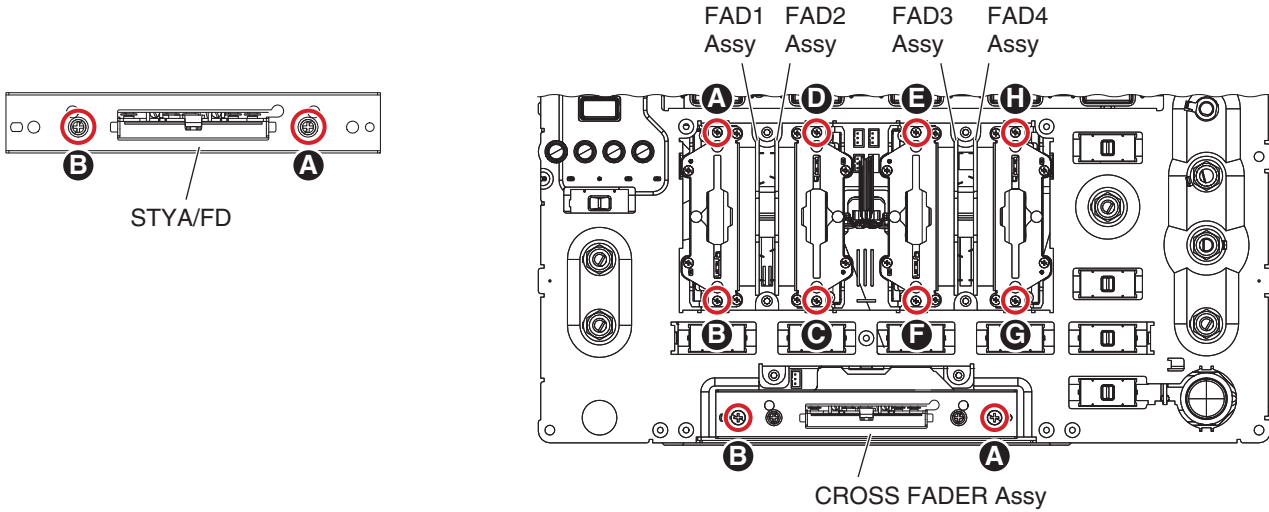
B • Connectors color

Match the color of a connected connector.



C

Screw tightening order



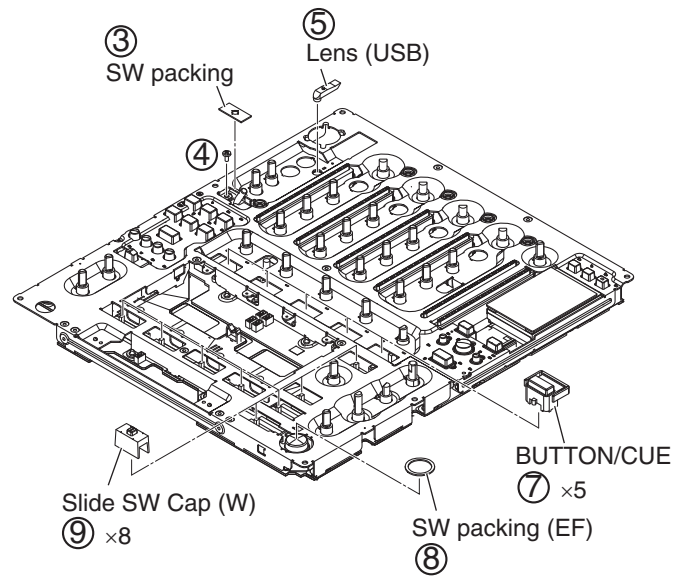
D

E

F

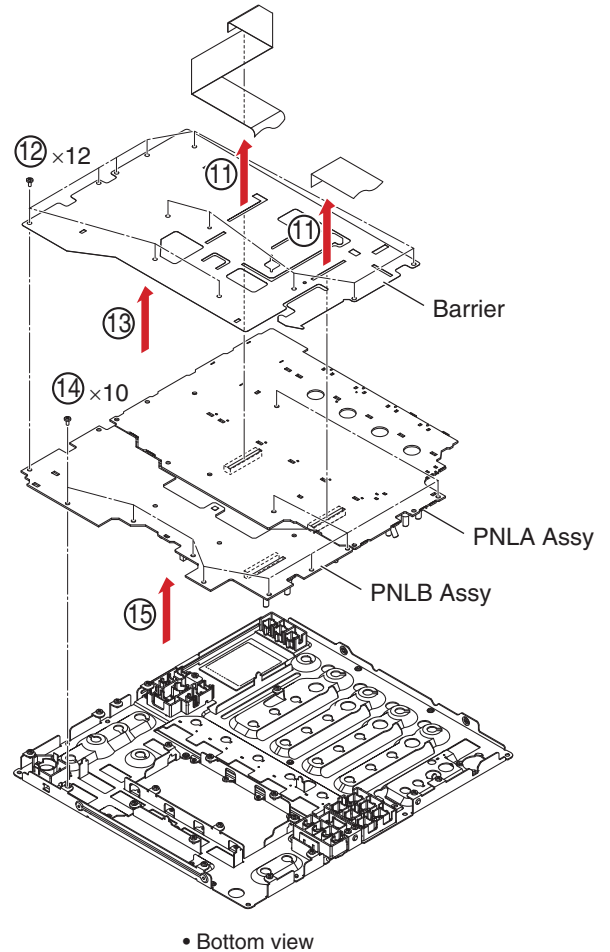
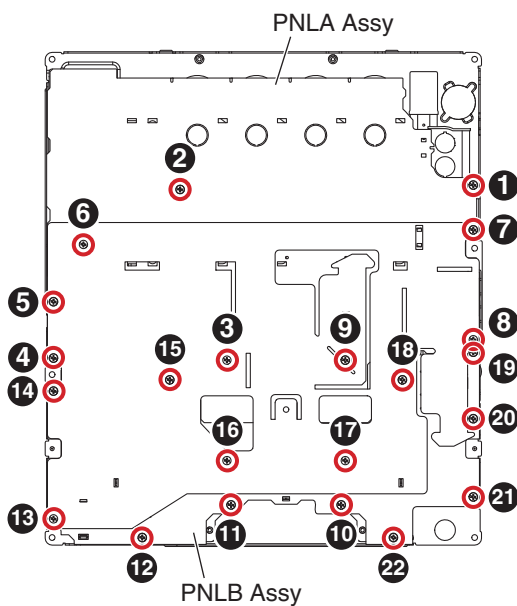


- (3) Remove the SW packing.
- (4) Remove the one screw. (AMZ26P040FTC)
- (5) Remove the lens (USB).
- (6) Remove the six bushes.
- (7) Remove the five BUTTON/CUE.
- (8) Remove the SW packing (EF).
- (9) Remove the eight slide SW caps (W).



- (10) Reverse the control panel section.
- (11) Disconnect the two flexible cables.
- (12) Remove the 12 screws. (BBZ30P060FTC)
- (13) Remove the barrier.
- (14) Remove the 10 screws. (BBZ30P060FTC)
- (15) Remove the PNLA and PNLB assemblies.

Screw tightening order



8. EACH SETTING AND ADJUSTMENT

8.1 NECESSARY ITEMS TO BE NOTED

A After repairing, be sure to check the version of the firmware (see “Mode 1: For confirmation of the version” in “TEST MODE”), and if it is not the latest one, update to the latest version.

Perform the each item when the following parts are replaced.

- MAIN Assy
(MAIN CPU: IC2011, FLASH MEMORY IC: IC703) ⇒
 - Confirmation of the version of the firmware
 - Updating to the latest version of the firmware
 - SERVICE MODE Mode 2 :
CROSS Fader Calibration mode "FDR SET"
- PNLA Assy
(SUB UCOM: IC6601) ⇒
 - Confirmation of the version of the firmware
 - Updating to the latest version of the firmware
- B • CROSS FADER ASSY ⇒
 - SERVICE MODE Mode 2 :
CROSS Fader Calibration mode "FDR SET"

8.2 UPDATING OF THE FIRMWARE

UPDATING OF THE FIRMWARE for DJM-900SRT is the same as DJM-900NXS except for the following:

[1] Downloading and Confirmation of the Updater Files

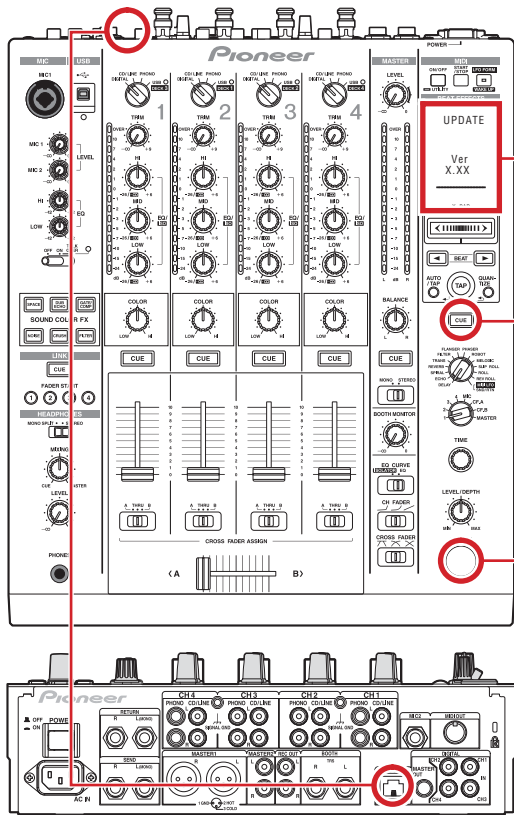
1. Download the zipped updater file for the latest firmware from Niis.
2. Unzip the downloaded file.

Unzipping the ZIP file generates the “DJM900SRT_Vxxx_Win” folder.

Confirm that the following files are contained in the folder:

- C ① DJM-900SRT_vxxx.exe
- The above xxx denotes the version of new firmware.
 - The extension (.exe) may not be displayed, depending on the setting of the computer.

[2] Preparation for Updating on the Main Unit



<STEP1>
Press [CUE (BEAT EFFECT)] and [ON/OFF (BEAT EFFECT)] with power button.
Move to update mode.

<STEP2>
Check the current version of your firmware by "current version" on VFD. (No need to update it if current version shows as x.xx. It is the latest firmware.)

<STEP3>
Connect your computer with this unit by LAN cable.

Connect LAN cable to the LINK terminal on rear on this unit.

[5] Executing Updating

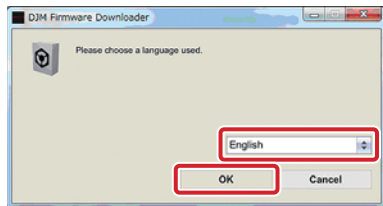
- * Close all of applications before updating.
- * If you were setting firewall by security software, it would not be able to transfer the data to mixer. Please read the operation manual of the security software or cancel the firewall.

<STEP 1>

Double click "DJM900SRT_vXXX.exe".
Start update tool.

<STEP 2>

Select language.



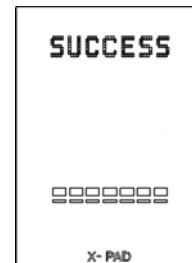
<STEP 3>

Start updating by pressing "Start".
* NEVER remove power/LAN cable on updating.
* If the waiting picture remains more than 30 seconds, the network connection might not work. Press Cancel button and update again after connecting it.



<STEP 4>

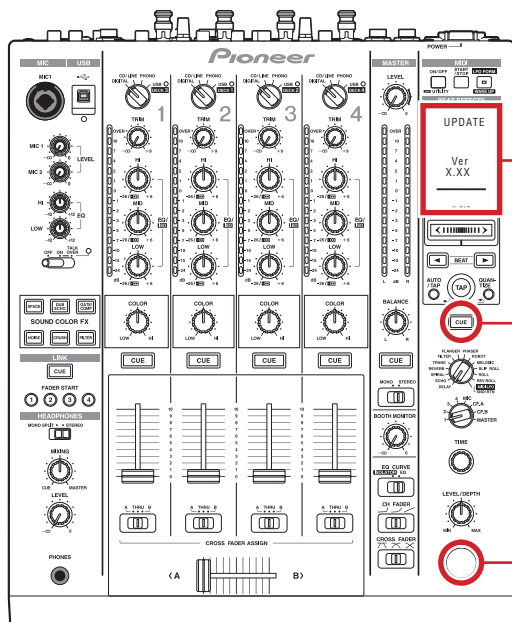
Confirm the updating completed.
Updating completed when "SUCCESS" message shows.



<STEP 5>

Restart this unit.
* Please adjust network setting as original if you adjusted it in 3-1.

[6] Confirmation of the Firmware Version



<STEP1>
Press [CUE (BEAT EFFECT)] and [ON/OFF (BEAT EFFECT)] with power button.
Move to update mode.

<STEP2>
Check it is the latest version.
Update was completed if it shows "x.xx".

8.4 USER SETTABLE ITEMS

This unit is provided with user settable items, as shown below.

	Setting Item	FL Display	Setting Area	Initial Value (Factory settings)	Part No.	Part Name	Ref No.	Assy	Content to be Stored
A USER SETUP	Fader Start	F.S.	ON/OFF	OFF	DYW1834	Flash ROM	IC502	MAIN	UTILITY setting
	MIDI CH	MIDI CH	1 to 16	1					
	MIDI Button Type	MIDI BT	Toggle/Trigger	Toggle					
	TALK OVER MODE	TLK MOD	ADVANCED / NORMAL	ADVANCED					
	TALK OVER LEVEL	TLK LVL	-24dB/-18dB/-12dB/-6dB	-18dB					
B CLUB SETUP	Digital Master Out Level	DOUT LV	-19dB/-15dB/-10dB/-5dB	-19dB					
	Digital Out Sampling Rate	DOUT FS	48kHz/96kHz	96kHz					
	MASTER ATT.	MST ATT	-6dB/-3dB/0dB	0dB					
	Auto Standby	AUTOSTB	ON/OFF	ON					
	Mic Output To Booth Monitor	MIC BTH	ON/OFF	ON					
	PC Utility	PC UTLY	ON/OFF	ON					
	Peak Limiter	PKLIMIT	ON/OFF	ON					
	Factory Reset	INITIAL	YES/NO	NO					

8.5 SHEET FOR CONFIRMATION OF THE USER SETTING

It is recommended that you take note of settings of USER SETUP and CLUB SETUP before starting repair.
(For procedure details, refer to the operating instructions "Changing the Settings" of the unit.)

USER SETUP																			
Fader Start		MIDI CH														MIDI Button Type			
ON	OFF	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	TGL (TOGGLE)	TRG (TRIGGER)
TALK OVER MODE						TALK OVER LEVEL													
ADV(ADVANCED)		NOR(NORMAL)				-6dB		-12dB		-18dB		-24dB							

CLUB SETUP												
Digital Master Out Level				Digital Master Out Sampling Rate				MASTER ATT.			Auto Standby	
-19dB	-15dB	-10dB	-5dB	48kHz		96kHz		-6dB	-3dB	0dB	ON	OFF
Mic Output To Booth Monitor				PC UTILITY		Peak Limiter		Factory Reset				
ON		OFF		ON	OFF	ON	OFF	YES	NO			

9. EXPLODED VIEWS AND PARTS LIST

NOTES: ● Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.

● The \triangle mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.

● Screws adjacent to ∇ mark on product are used for disassembly.

● For the applying amount of lubricants or glue, follow the instructions in this manual.

(In the case of no amount instructions, apply as you think it appropriate.)

● Nos. indicate the pages and Nos. in the service manual for the base model.

9.1 PACKING SECTION

DJM-900NXS/SYXJ8, DJM-900SRT/LSYXJ8, DJM-900SRT/UXJCB and DJM-900SRT/XJCN5 are constructed the same except for the following:

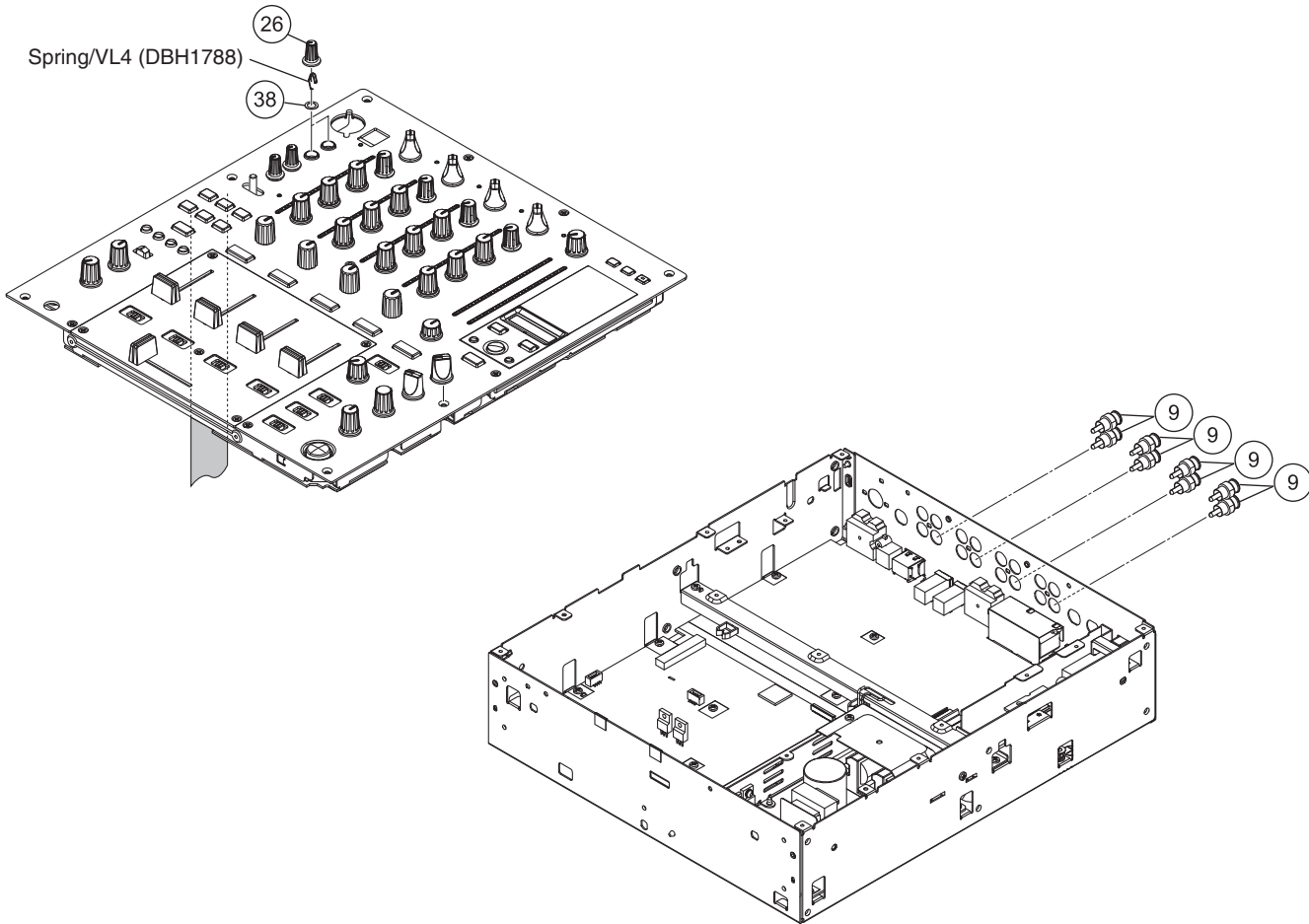
Mark	No.	Symbol and Description	DJM-900NXS /SYXJ8	DJM-900SRT /LSYXJ8	DJM-900SRT /UXJCB	DJM-900SRT /XJCN5	Remarks
		PACKING SECTION					
	P89-1	Power Cord	ADG7062	ADG7062	DDG1108	ADG7105	Installation Disc Serato DJ CONTROL CD
NSP	P89-3	CD-ROM	DXX2694	DXX2757	DXX2757	DXX2757	
		CD-ROM	Not used	DXX2734	DXX2734	DXX2734	
	P89-4	Operating Instructions (En/Fr/De/It/Nl/Es/Ru)	DRB1569	Not used	Not used	Not used	
	P89-4	Operating Instructions (En/Fr/De/It/Nl/Es/Ru)	Not used	DRB1701	Not used	Not used	
	P89-5	Operating Instructions (En)	Not used	Not used	DRB1702	Not used	
	P89-8	Operating Instructions (Zhc)	Not used	Not used	Not used	DRB1703	
NSP	P89-9	Warranty Card	ARY7158	ARY7158	Not used	Not used	
NSP	P89-10	Flier	DRH1108	Not used	Not used	Not used	
	P89-13	Pad A	DHA1854	Not used	Not used	Not used	
	P89-13	Pad/F	Not used	DHA1896	DHA1896	DHA1896	
	P89-14	Pad B	DHA1855	Not used	Not used	Not used	
	P89-14	Pad/R	Not used	DHA1897	DHA1897	DHA1897	
NSP	P89-16	Packing Case Leaflet/FPC	DHG3077 Not used	DHG3291 DRM1377	DHG3292 Not used	DHG3294 DRM1377	
NSP		Leaflet/FIR	Not used	DRM1378	DRM1378	DRM1378	
NSP		Leaflet/QSG	Not used	DRM1380	DRM1380	DRM1380	
NSP		Leaflet/ERT	Not used	DRM1384	DRM1384	DRM1384	
		Packing Board	Not used	DHB1002	DHB1002	DHB1002	
NSP		Poly Bag	Not used	AHG7082	AHG7082	AHG7082	
NSP		Record Assy/DVS	Not used	DXX2733	DXX2733	DXX2733	Serato DJ CONTROL VINYL
NSP		Leaflet/SUF	DRH1204	Not used	Not used	Not used	
NSP		Leaflet/TEN	DRY1262	Not used	Not used	Not used	
NSP		Leaflet/TFR	DRY1263	Not used	Not used	Not used	
NSP		Label	Not used	Not used	DRW2594	Not used	

9.2 EXTERIOR SECTION

DJM-900NXS/SYXJ8, DJM-900SRT/LSYXJ8, DJM-900SRT/UXJCB and DJM-900SRT/XJCN5 are constructed the same except for the following:

Mark	No.	Symbol and Description	DJM-900NXS /SYXJ8	DJM-900SRT /LSYXJ8	DJM-900SRT /UXJCB	DJM-900SRT /XJCN5	Remarks
EXTERIOR SECTION							
	P91-1	INPUT Assy	DWX3193	DWX3504	DWX3504	DWX3504	
	P91-3	TRM1 Assy	DWX3203	DWX3506	DWX3506	DWX3506	
	P91-4	TRM2 Assy	DWX3204	DWX3507	DWX3507	DWX3507	
	P91-5	TRM3 Assy	DWX3205	DWX3508	DWX3508	DWX3508	
	P91-6	TRM4 Assy	DWX3206	DWX3509	DWX3509	DWX3509	
	P91-9	Short Pin Plug	AKM7008	Not used	Not used	Not used	
	P91-9	Plug/PIN	Not used	DKM1024	DKM1024	DKM1024	
	P91-17	Extension Shaft A	DNK5365	Not used	Not used	Not used	
B	P91-23	Front Panel	DNK5845	Not used	Not used	Not used	
	P91-23	Panel/FR	Not used	DNK6260	DNK6260	DNK6260	
	P91-26	Rotary SW Knob S (B)	DAA1178	Not used	Not used	Not used	
	P91-26	KNOB/RSW	Not used	DAA1308	DAA1308	DAA1308	
	P91-27	Rotary SW Knob S (C)	DAA1204	Not used	Not used	Not used	
	P91-27	Knob/CH	Not used	DAA1321	DAA1321	DAA1321	
NSP	P91-29	Label	Not used	Not used	DRW1975	Not used	
NSP	P91-30	Name Label	DRW2463	DAL1270	DAL1263	DAL1265	
NSP		Caution Label (L)	Not used	DRW2430	Not used	Not used	
NSP		Name Label/Ru	DRW2486	Not used	Not used	Not used	
C	NSP	Label/FCC	Not used	Not used	DRW2595	Not used	

EXTERIOR SECTION



9.3 BOTTOM SECTION

DJM-900NXS/SYXJ8, DJM-900SRT/LSYXJ8, DJM-900SRT/UXJCB and DJM-900SRT/XJCN5 are constructed the same except for the following:

Mark	No.	Symbol and Description	DJM-900NXS /SYXJ8	DJM-900SRT /LSYXJ8	DJM-900SRT /UXJCB	DJM-900SRT /XJCN5	Remarks
		BOTTOM SECTION					
	P93-1	MAIN Assy	DWX3190	DWX3502	DWX3502	DWX3502	
	P93-2	SEND Assy	DWX3195	DWX3505	DWX3505	DWX3505	
⚠	P93-5	AC Inlet Assy (2P)	DKP3762	DKP3762	Not used	DKP3762	
⚠	P93-5	AC Inlet Assy (3P)	Not used	Not used	DKP3927	Not used	
	P93-9	Rear Panel	DNC2020	DNC2107	DNC2107	DNC2108	
	P93-34	Screw	Not used	Not used	PMH40P080FTC	Not used	

BOTTOM SECTION

*Caution : About No.17 Silicon Rubber D5 L DEB1456

During MAIN Assy disassembly, if the silicone sheet is found to be deformed, replace it with a new one.

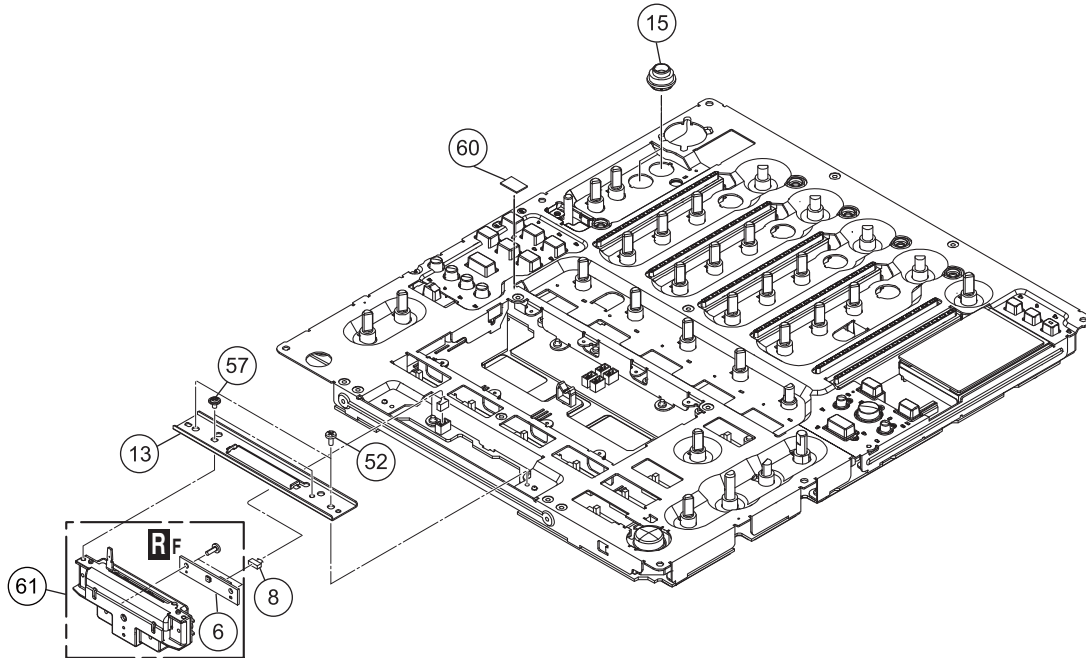
Looking at the bottom panel of the Looking at the bottom panel of the chassis from the outside, check that the hole is covered by a silicon sheet and that the MAIN Assy is not seen through the hole.

9.4 CONTROL PANEL SECTION (1/2)

DJM-900NXS/SYXJ8, DJM-900SRT/LSYXJ8, DJM-900SRT/UXJCB and DJM-900SRT/XJCN5 are constructed the same except for the following:

Mark	No.	Symbol and Description	DJM-900NXS /SYXJ8	DJM-900SRT /LSYXJ8	DJM-900SRT /UXJCB	DJM-900SRT /XJCN5	Remarks
CONTROL PANEL SECTION (1/2)							
NSP	P95-6	FADC Assy	DWX3202	Not used	Not used	Not used	
	P95-6	CRFD ASSY	Not used	DWX3258	DWX3258	DWX3258	
	P95-13	CRF Stay	DNF1870	Not used	Not used	Not used	
	P95-13	Stay/CRF	Not used	DNF1927	DNF1927	DNF1927	
	P95-15	Bush	DNK5974	Not used	Not used	Not used	
B	P93-26	Rotary SW Knob (B)	DAA1176	Not used	Not used	Not used	
	P93-26	Knob/RSW	Not used	DAA1305	DAA1305	DAA1305	
	P93-27	Rotary SW Knob S (A)	DAA1177	Not used	Not used	Not used	
	P93-27	Knob/RSW	Not used	DAA1307	DAA1307	DAA1307	
	P95-33	Rotary SW Knob (HM)	DAA1256	Not used	Not used	Not used	
C	P95-33	Knob/CL	Not used	DAA1320	DAA1320	DAA1320	
	P95-36	Button (CUE)	DAC2503	Not used	Not used	Not used	
	P95-36	Button/CUE	Not used	DAC2882	DAC2882	DAC2882	
	P95-37	Slider Knob 1	DAC2684	Not used	Not used	Not used	
	P95-37	Knob/SLD	Not used	DAC2935	DAC2935	DAC2935	
C	P95-40	Fader Panel	DAH2830	Not used	Not used	Not used	
	P95-40	Panel/FD	Not used	DAH2971	DAH2971	DAH2971	
	P95-42	Control Panel	DNB1186	DNB1233	DNB1233	DNB1233	
	P95-44	CDC Panel	DNK5846	Not used	Not used	Not used	
	P95-44	Panel/CDC	Not used	DNK6258	DNK6258	DNK6258	
	P95-57	Screw	IMZ30P040FTC	BPZ30P080FTB	BPZ30P080FTB	BPZ30P080FTB	
	P95-61	Cross Fader Assy	Not used	DXA2257	DXA2257	DXA2257	

CONTROL PANEL SECTION (1/2)



9.5 CONTROL PANEL SECTION (2/2)

DJM-900NXS/SYXJ8, DJM-900SRT/LSYXJ8, DJM-900SRT/UXJCB and DJM-900SRT/XJCN5 are constructed the same except for the following:

Mark	No.	Symbol and Description	DJM-900NXS /SYXJ8	DJM-900SRT /LSYXJ8	DJM-900SRT /UXJCB	DJM-900SRT /XJCN5	Remarks
		CONTROL PANEL SECTION (2/2)					
	P97-1	PNLA ASSY	DWX3196	DWX3503	DWX3503	DWX3503	
	P97-2	PNLB Assy	DWX3197	DWX3510	DWX3510	DWX3510	
	P97-7	Barrier	DEC3382	DEC3512	DEC3512	DEC3512	
	P97-11	Button (TAP)	DAC2653	Not used	Not used	Not used	
	P97-11	Button/TAP	Not used	DAC2931	DAC2931	DAC2931	
	P97-12	Button (LINK)	DAC2654	Not used	Not used	Not used	
	P97-12	Button/FDS	Not used	DAC2932	DAC2932	DAC2932	

10. SCHEMATIC DIAGRAM

10.1 INPUT ASSY(1/11)

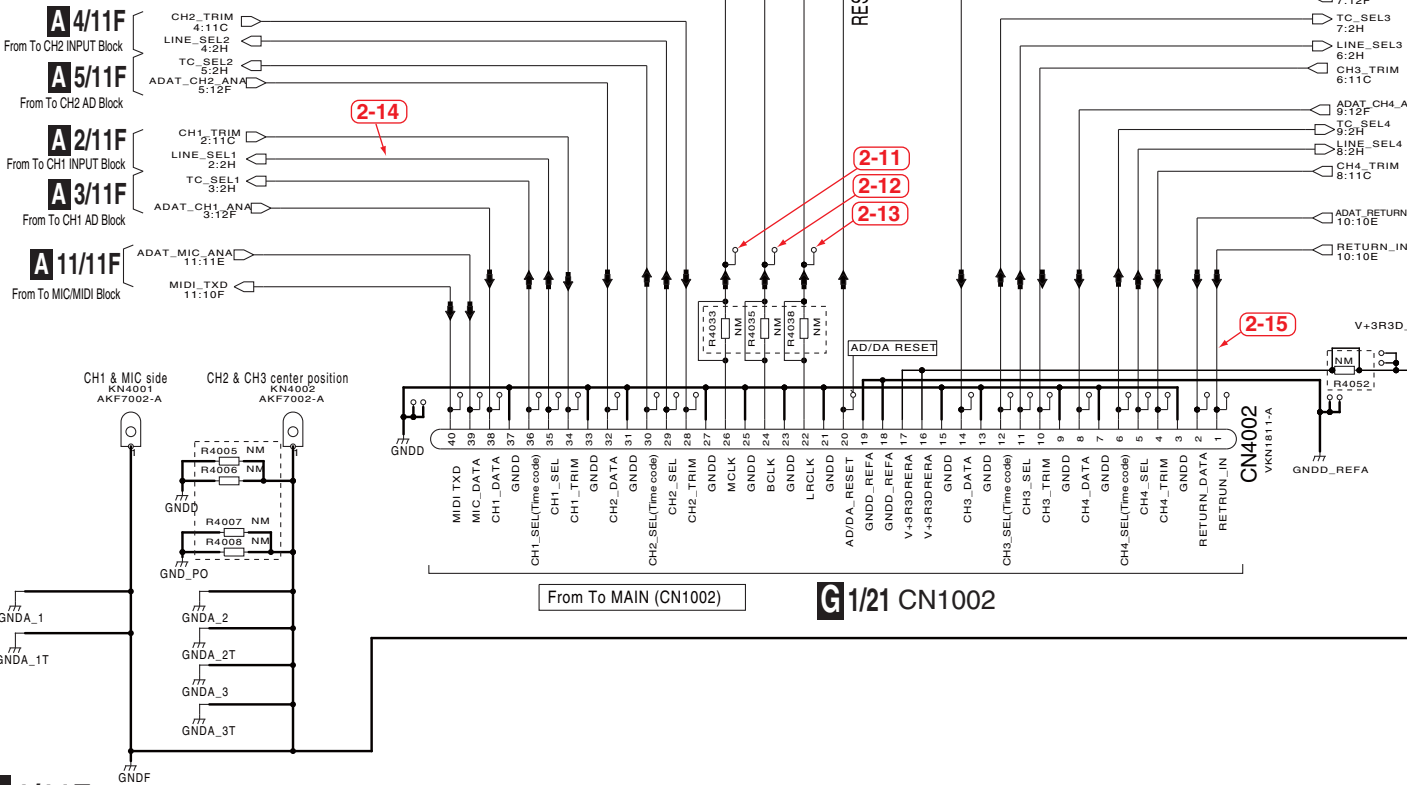
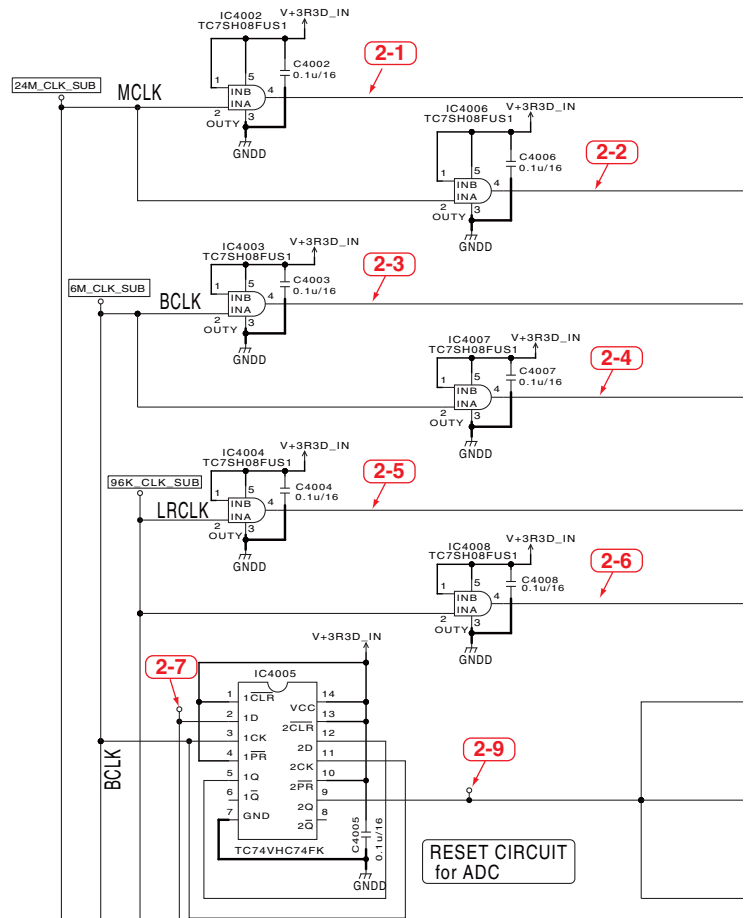
A1/11F INPUT ASSY (DWX3504)

BOARD IF

NOTES NM1 is STBY

- RS1/10SR***J
- RS1/16SS***J
- RS1/4SA***J
- D □ RS1/10SE***D
- RN □ RN1/16SE***D
- |—| CKSRYB
- |—| CKSSYB
- |—| CKSYB
- HXS —|—| CFHXSQ
- CH —|—| CCSRCH
- |—| CCSSCH
- LA —|—| CFTLA
- JQ —|—| CEJQ
- NP —|—| CEANP
- |—| CEAT
- HAT —|—| CEHAT
- ZL —|—| CEHAZL

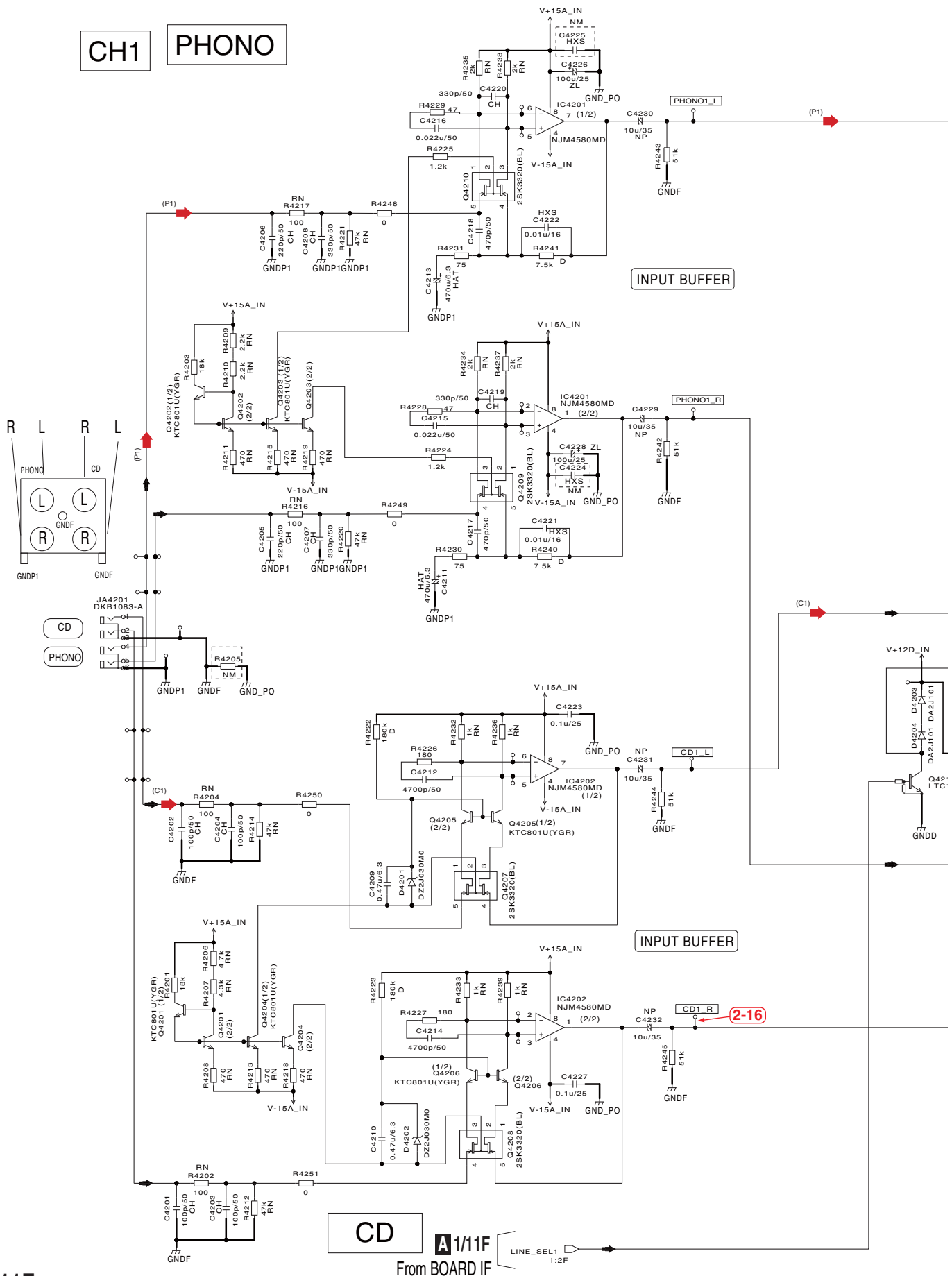
- : Voltage measuring point
- : Waveform measuring point



A1/11F

10.2 INPUT ASSY(2/11)

CH1 PHONO

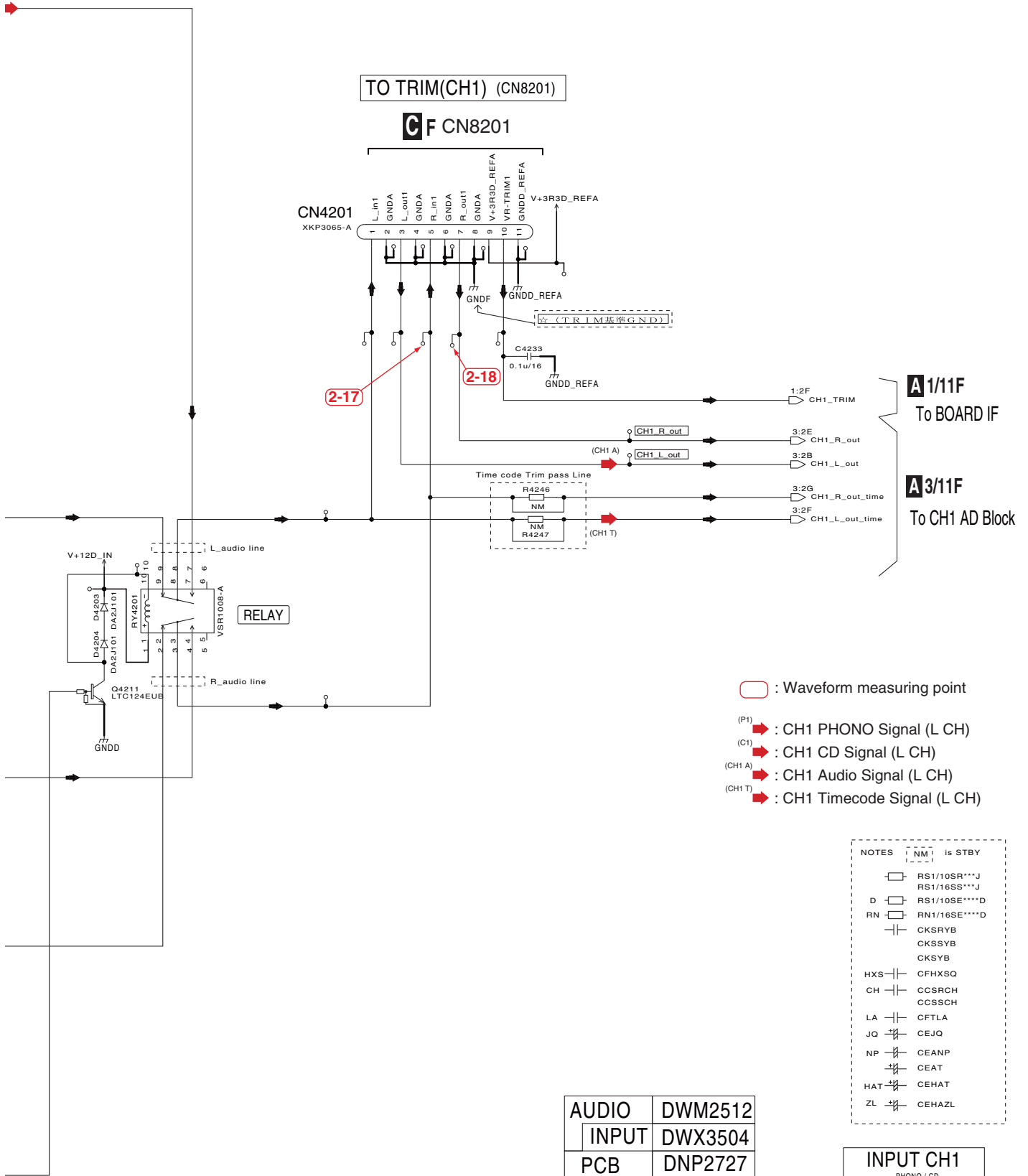


CD
PHONO

CD **A1/11F**
From BOARD IF

A2/11F

A 2/11F INPUT ASSY (DWX3504)



10.3 INPUT ASSY(3/11)

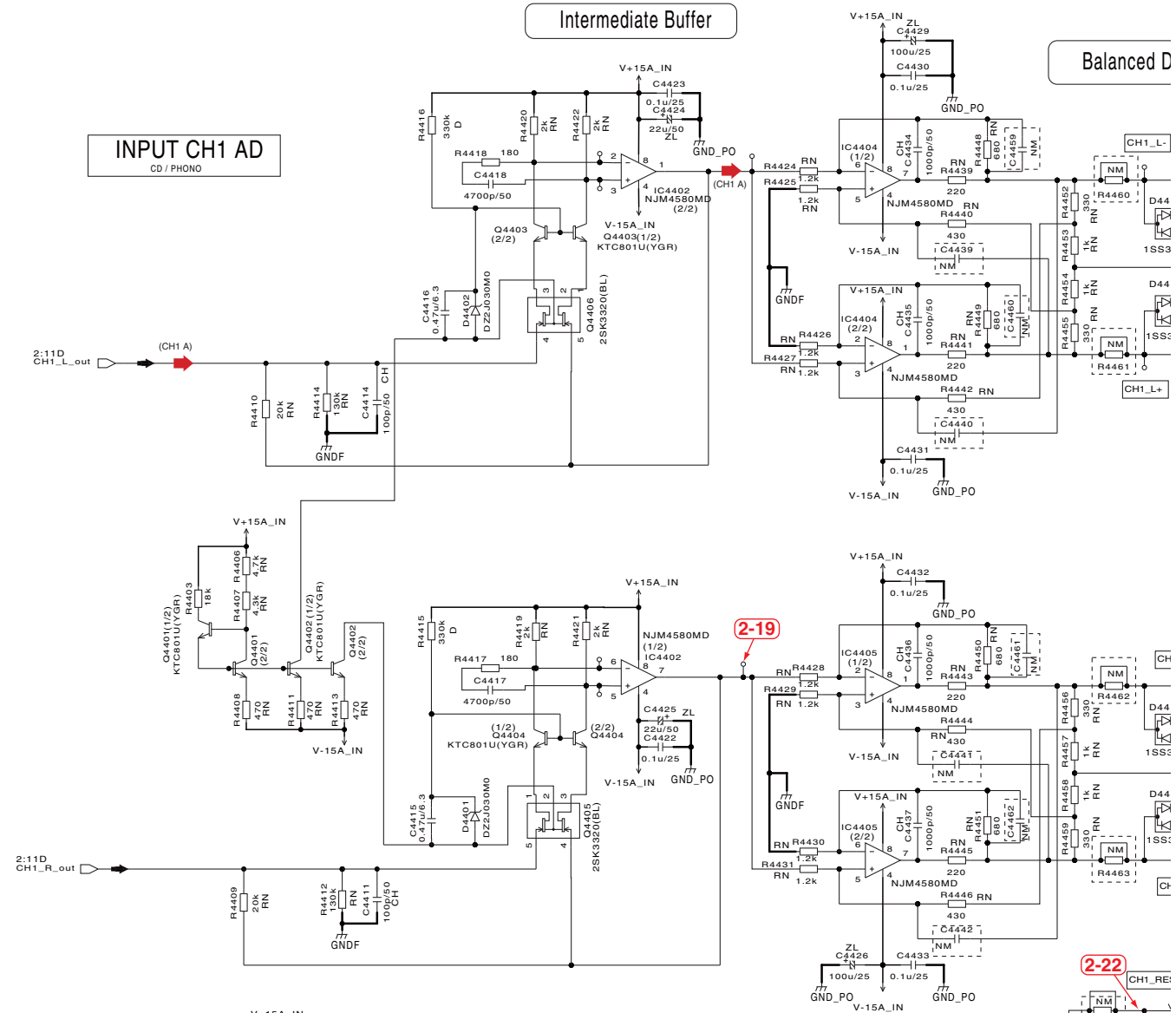
A
B
C
D
E
F

From CH1 INPUT

INPUT CH1 AD
CD / PHONO

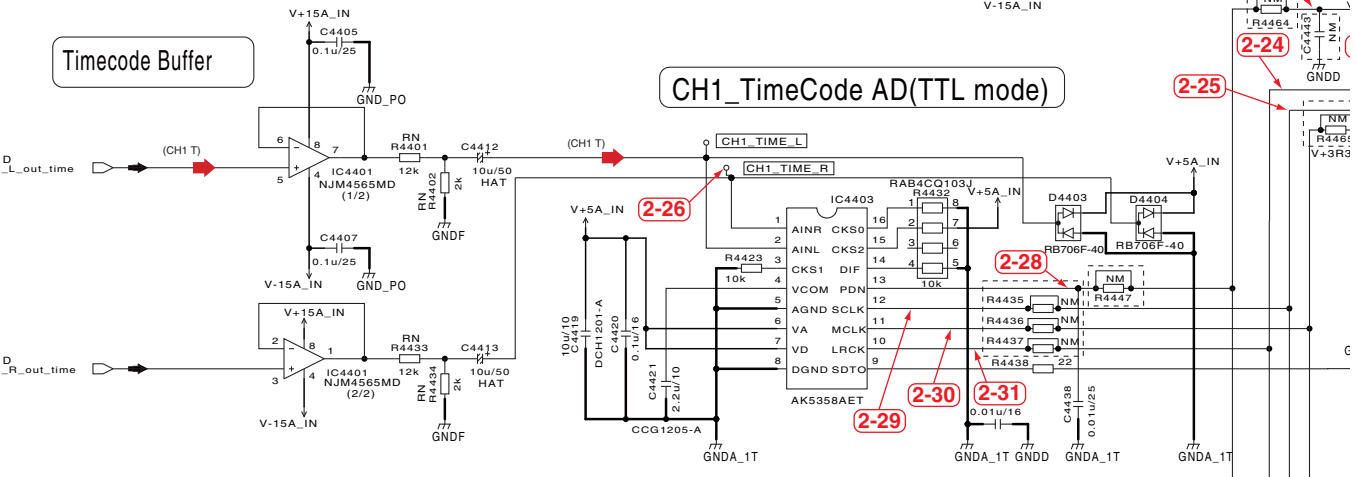
Intermediate Buffer

Balanced D



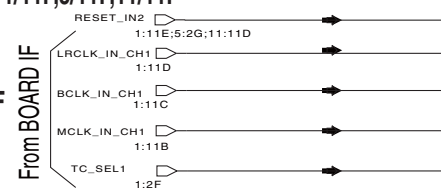
Timecode Buffer

CH1_TimeCode AD(TTL mode)



A1/11F,5/11F,11/11F

A1/11F

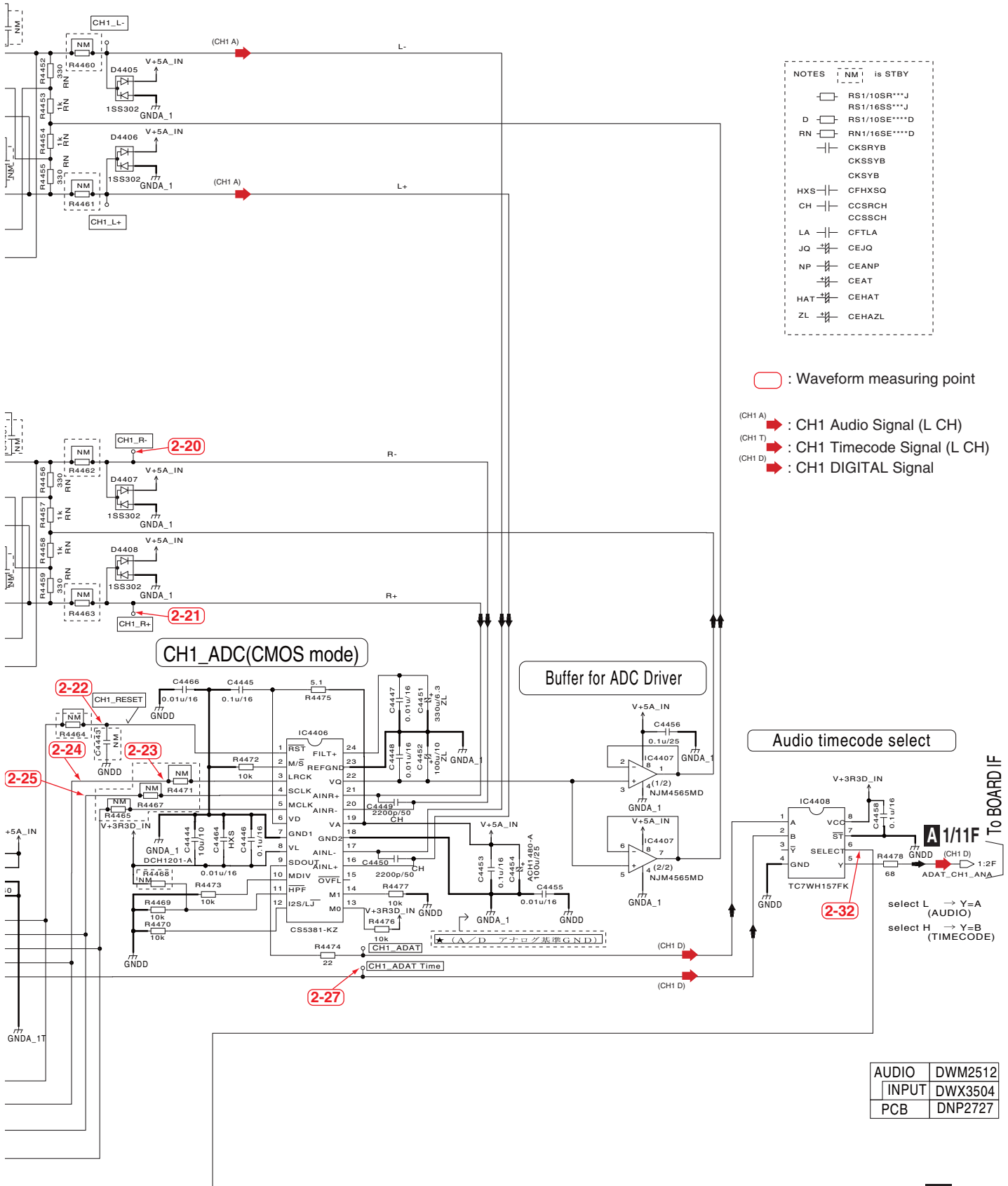


A3/11F

DJM-900SRT

A3/11F INPUT ASSY (DWX3504)

Balanced Direct ADC Driver



- NOTES NM1 is STBY
- RS1/10SR***J
 - RS1/16SS***J
 - D □ RS1/10SE****D
 - RN RN1/16SE****D
 - CKSRYB
 - CKSSYB
 - CKSYB
 - HXS □ CFHXSQ
 - CH □ CCSRCH
 - CCSSCH
 - LA □ CFTLA
 - JO □ CEJQ
 - NP □ CEANP
 - CEAT
 - HAT □ CEHAT
 - ZL □ CEHAZL

○ : Waveform measuring point

- (CH1 A) → CH1 Audio Signal (L CH)
- (CH1 T) → CH1 Timecode Signal (L CH)
- (CH1 D) → CH1 DIGITAL Signal

CH1_ADC(CMOS mode)

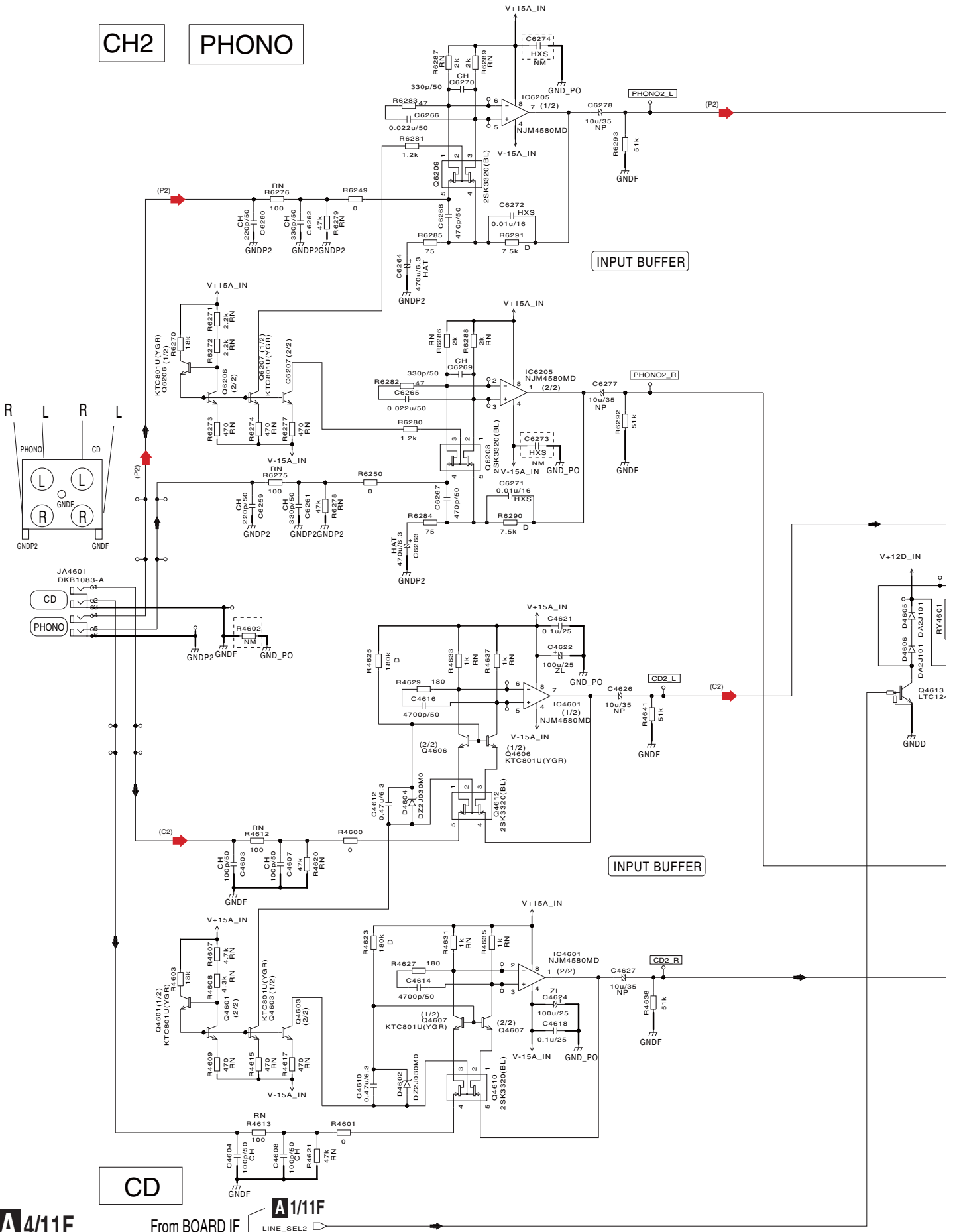
Buffer for ADC Driver

Audio timecode select

AUDIO	DWM2512
INPUT	DWX3504
PCB	DNP2727

10.4 INPUT ASSY(4/11)

CH2 PHONO

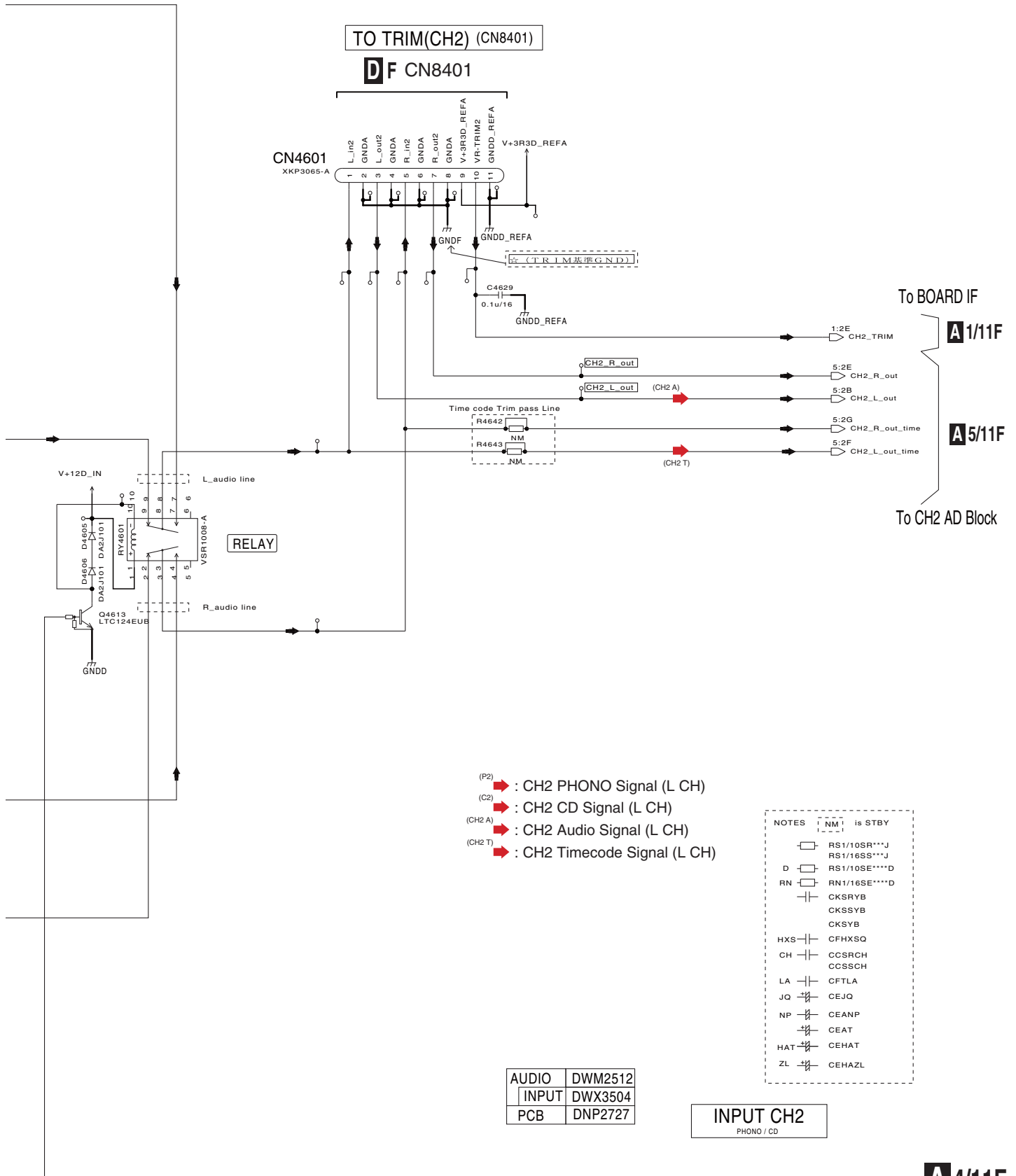


CD

From BOARD IF **A** 1/11F

DJM-900SRT

A 4/11F INPUT ASSY (DWX3504)



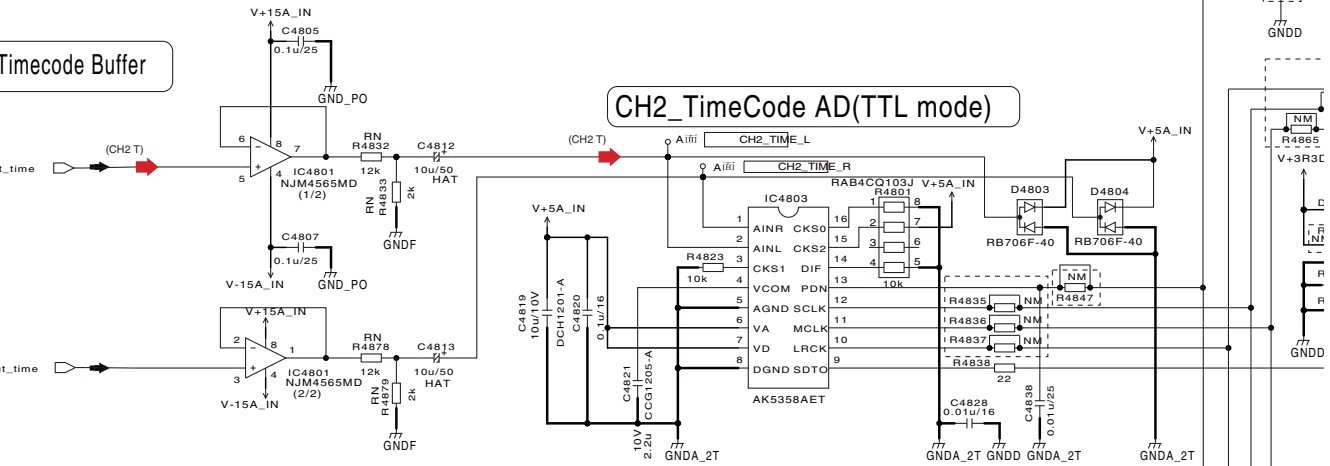
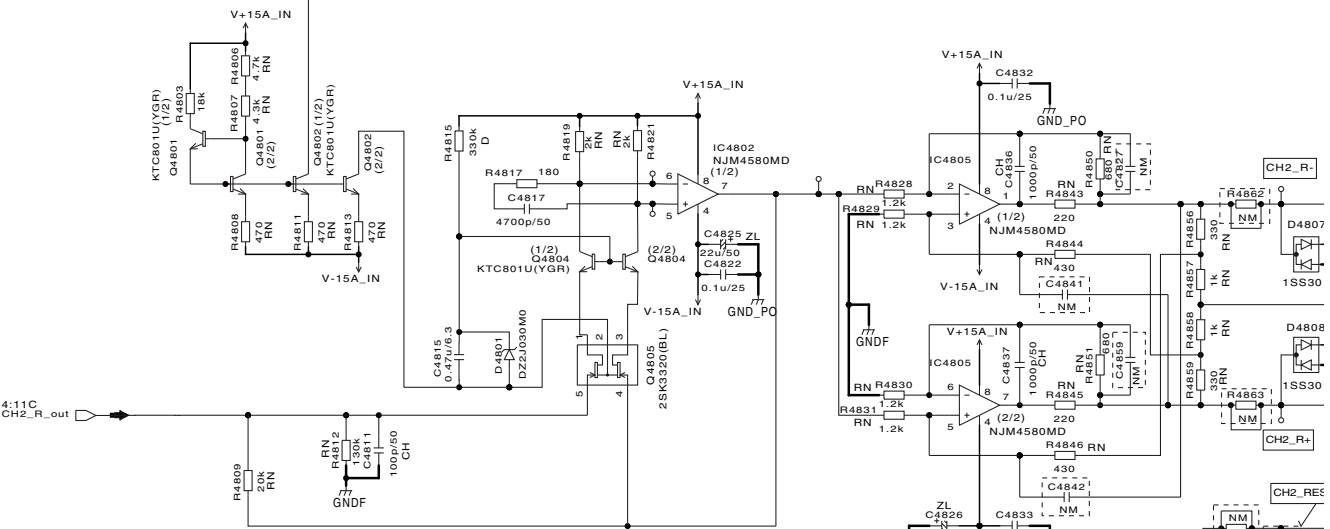
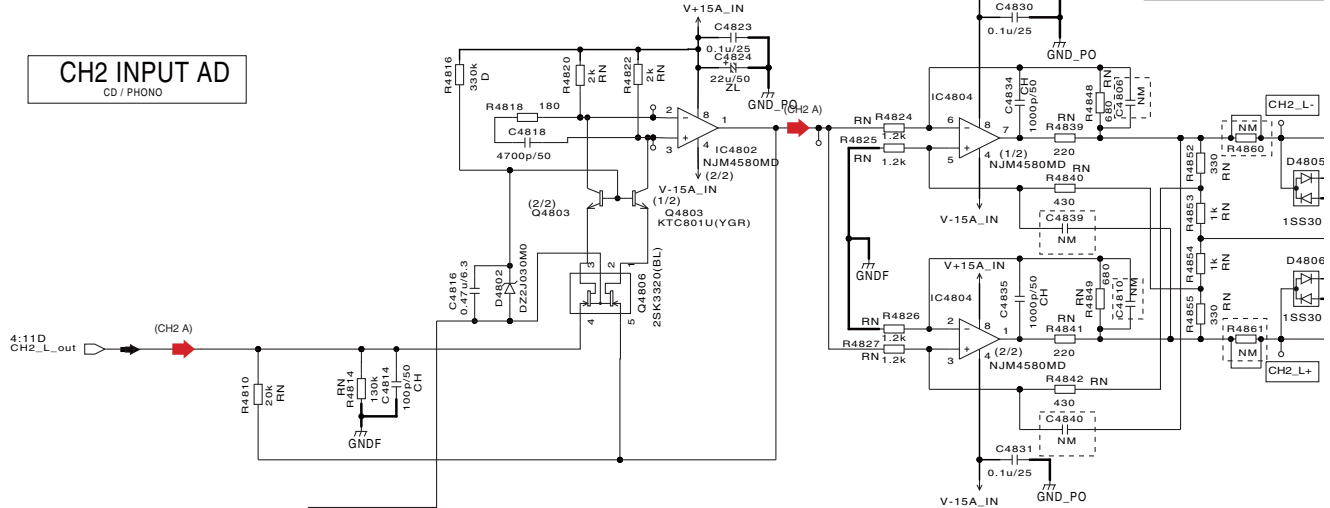
10.5 INPUT ASSY(5/11)

A

CH2 INPUT AD
CD / PHONO

Intermediate Buffer

Balanced Direct



B

C

D

E

F

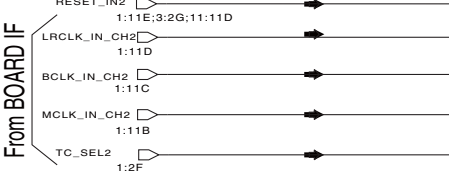
From CH2 INPUT

A 4/11F

A 1/11F, 3/11F, 11/11F

A 1/11F

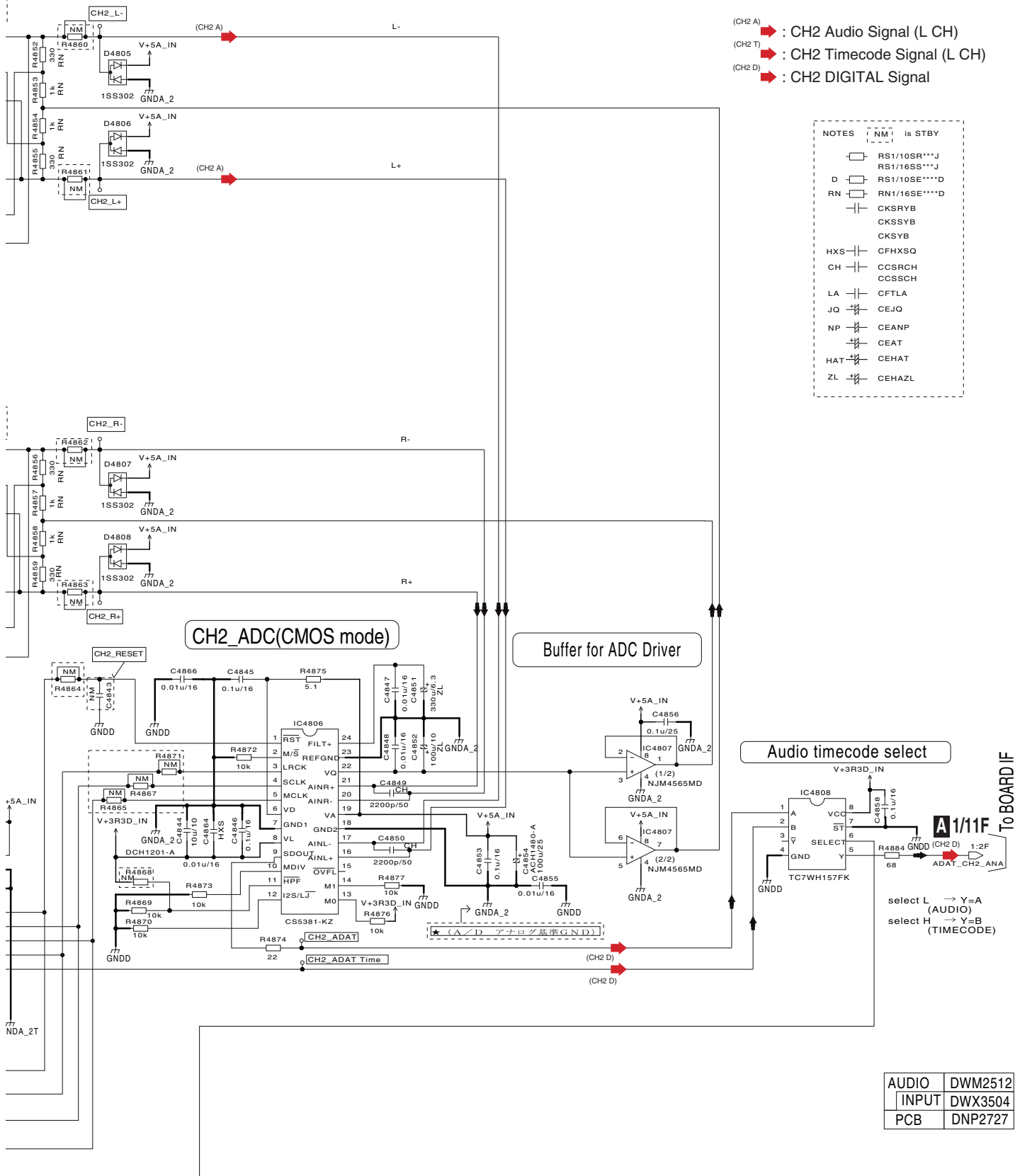
A 5/11F



DJM-900SRT

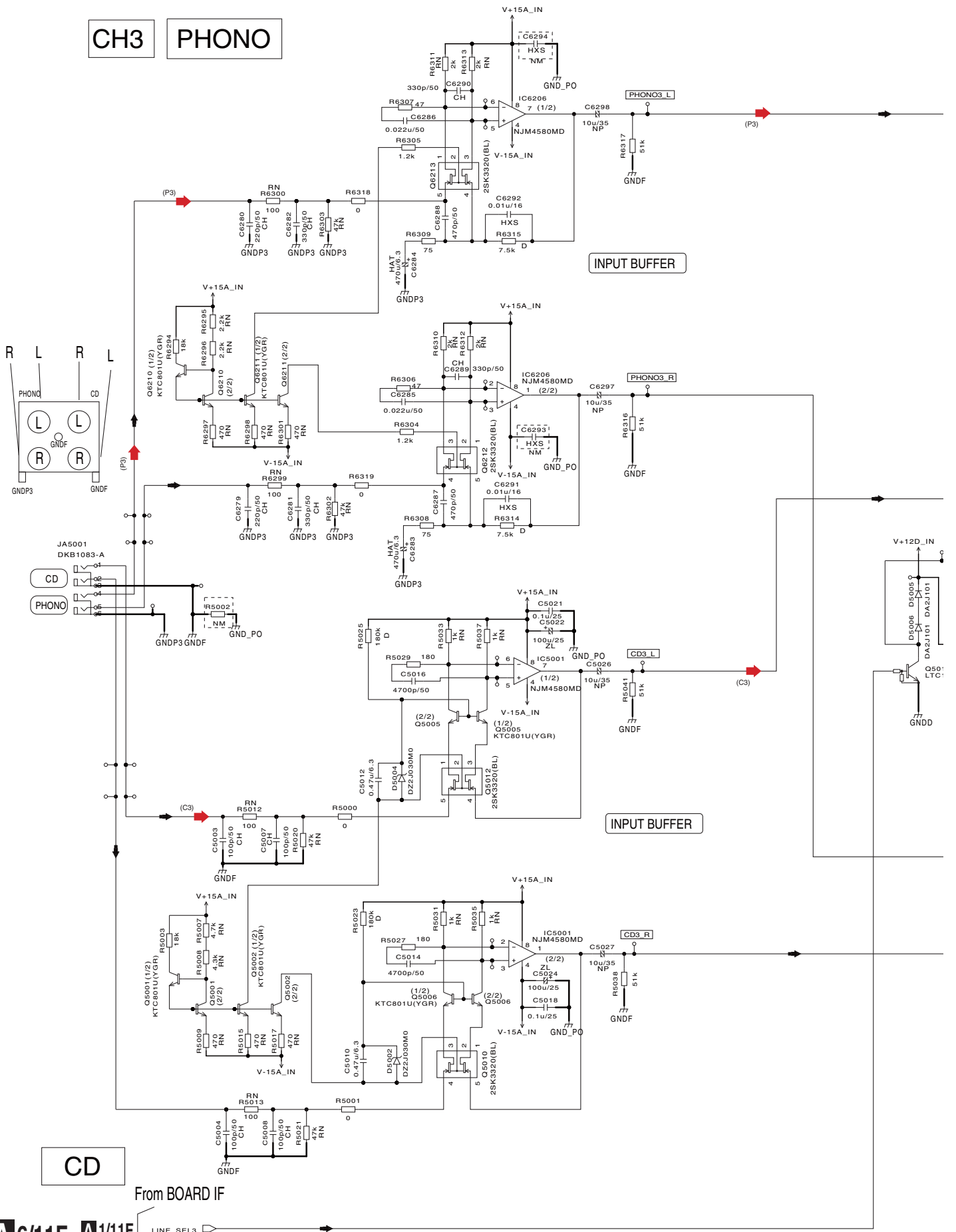
Balanced Direct ADC Driver

A5/11F INPUT ASSY (DWX3504)



10.6 INPUT ASSY(6/11)

CH3 PHONO



CD

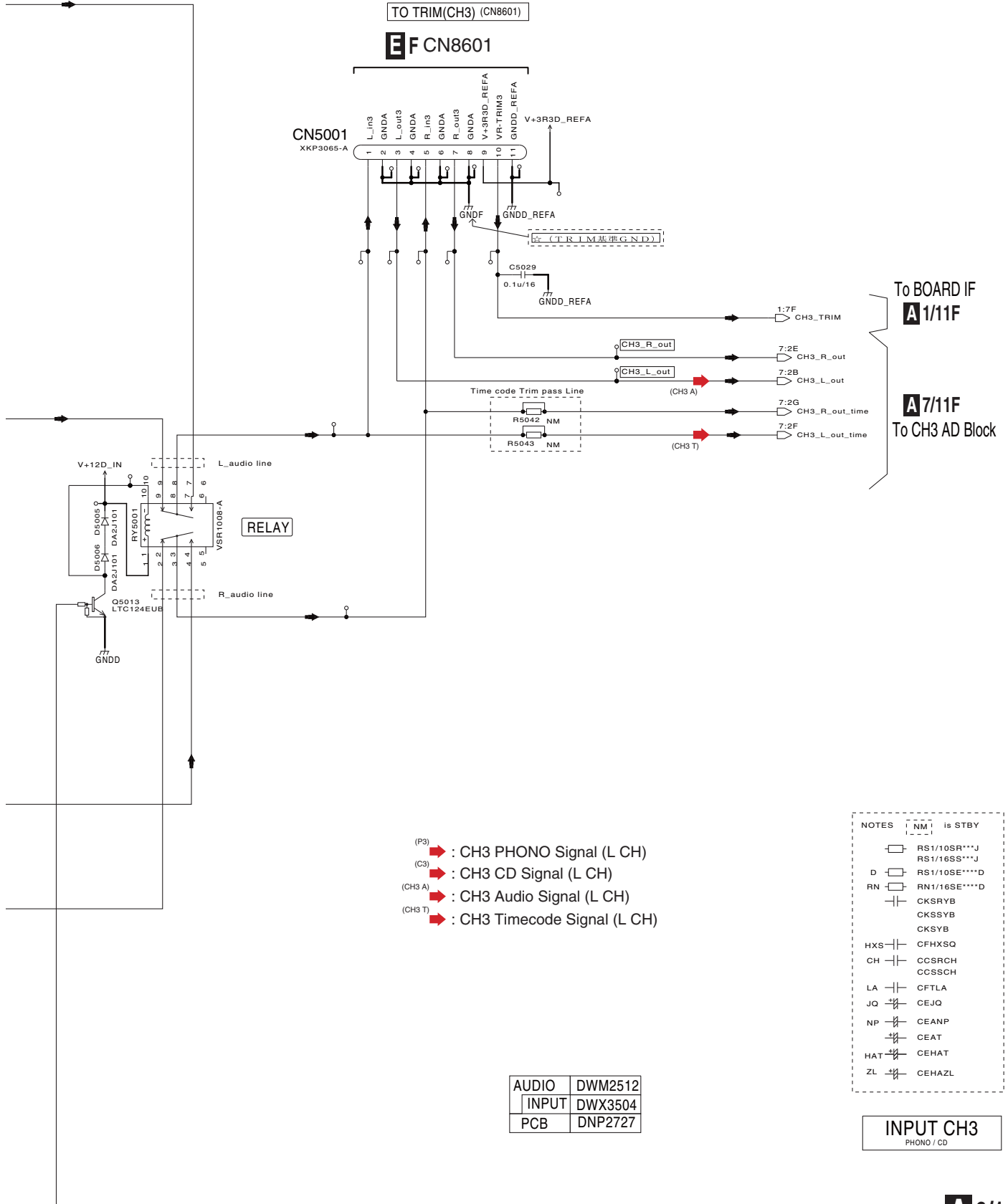
From BOARD IF

A6/11F A1/11F

LINE_SEL3 1.7E

DJM-900SRT

A 6/11F INPUT ASSY (DWX3504)



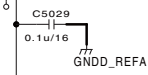
TO TRIM(CH3) (CN8601)

E F CN8601

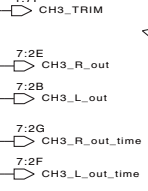
CN5001
XKP3065-A



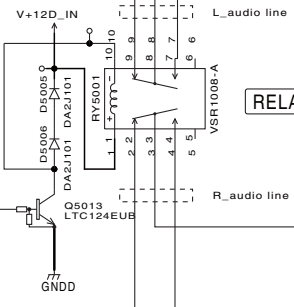
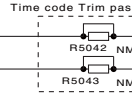
TRIMMER GND



To BOARD IF
A 1/11F



A 7/11F
To CH3 AD Block



- (P3) : CH3 PHONO Signal (L CH)
- (C3) : CH3 CD Signal (L CH)
- (CH3 A) : CH3 Audio Signal (L CH)
- (CH3 T) : CH3 Timecode Signal (L CH)

- NOTES: NM is STBY
- RS1/10SR****J
 - RS1/16SS****J
 - D RS1/10SE****D
 - RN RN1/16SE****D
 - CKSRYB
 - CKSSYB
 - CKSYB
 - HXS CFHXSQ
 - CH CC SRCH
 - CCSSCH
 - LA CFTLA
 - JO CEJO
 - NP CEANP
 - CEAT
 - HAT CEHAT
 - ZL CEHAZL

AUDIO	DWM2512
INPUT	DWX3504
PCB	DNP2727

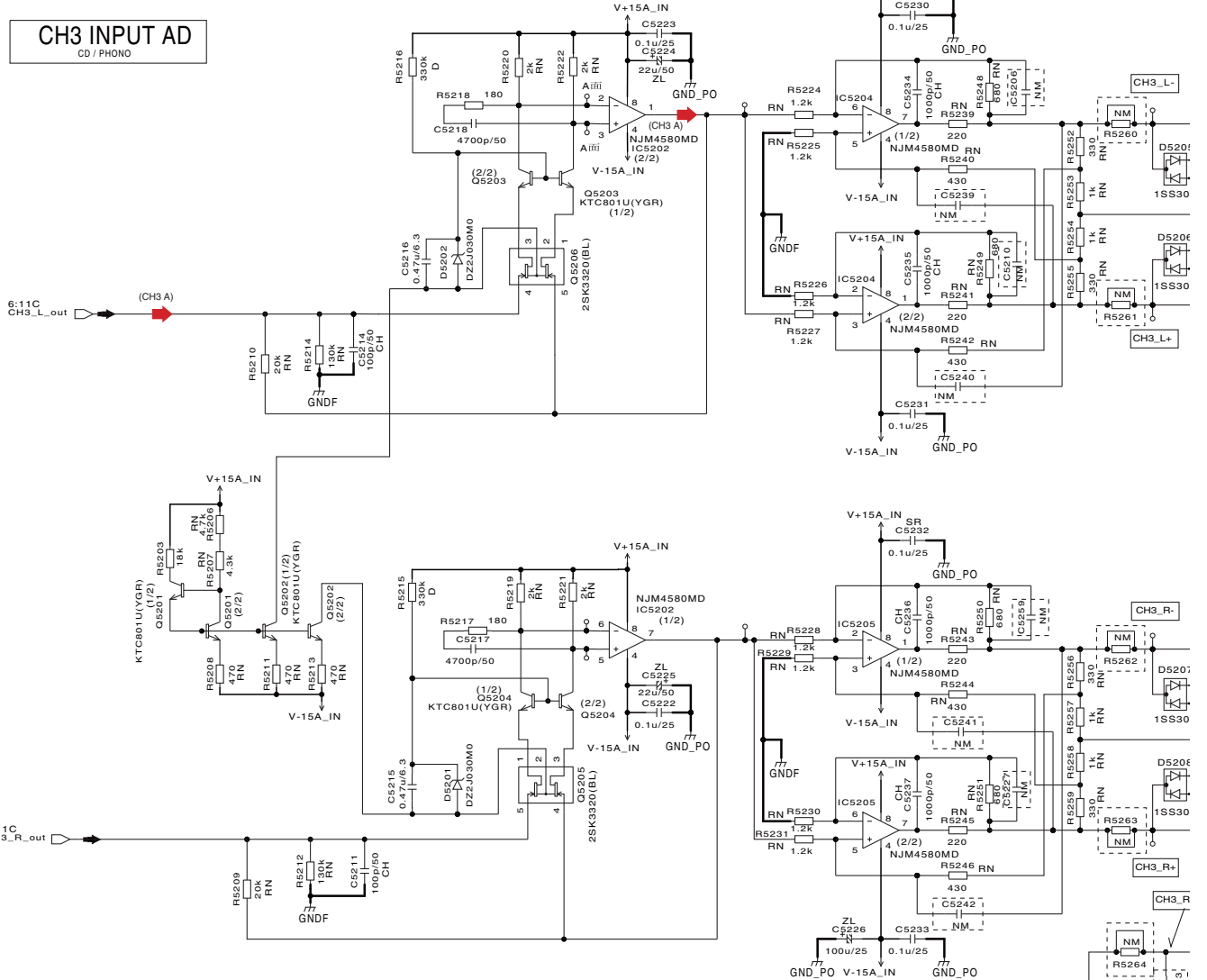
INPUT CH3
PHONO / CD

10.7 INPUT ASSY(7/11)

CH3 INPUT AD
CD / PHONO

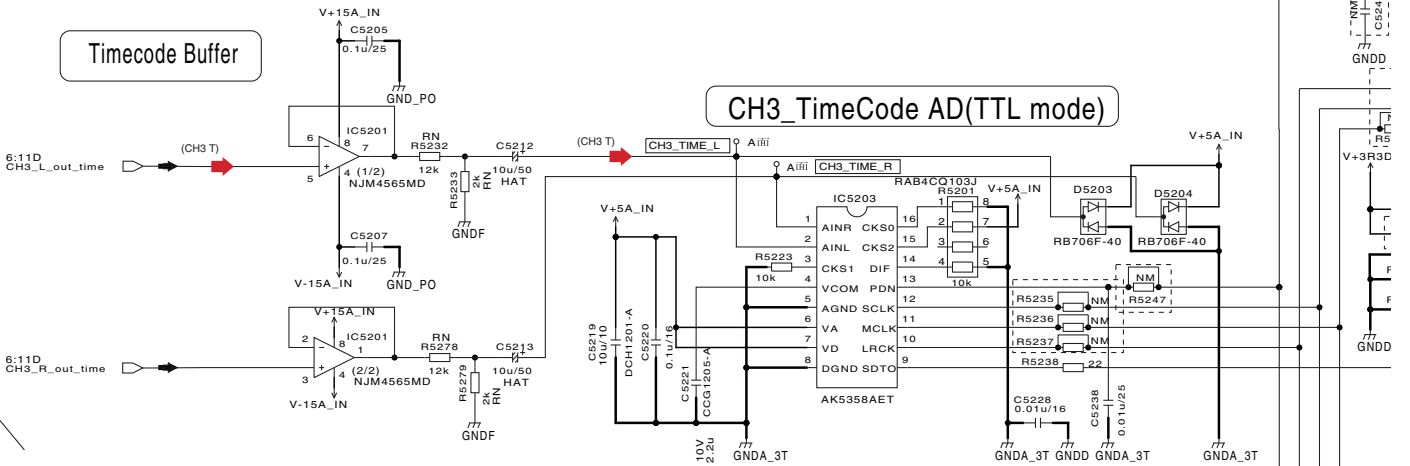
Intermediate Buffer

Balanced Direct AD



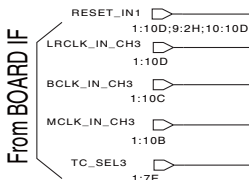
Timecode Buffer

CH3_TimeCode AD(TTL mode)



A 1/11F, 9/11F, 11/11F

A 1/11F

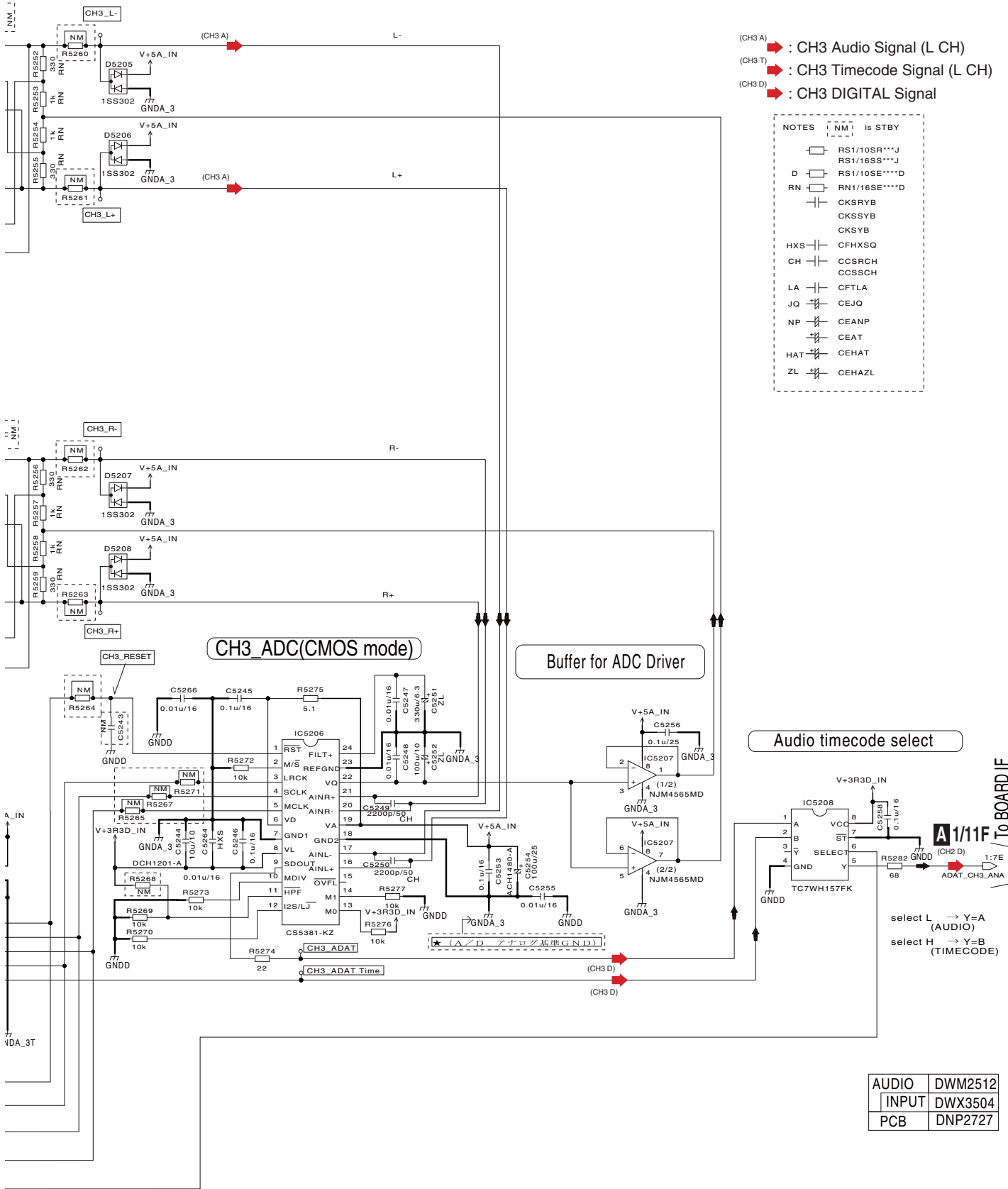


DJM-900SRT

A 7/11F

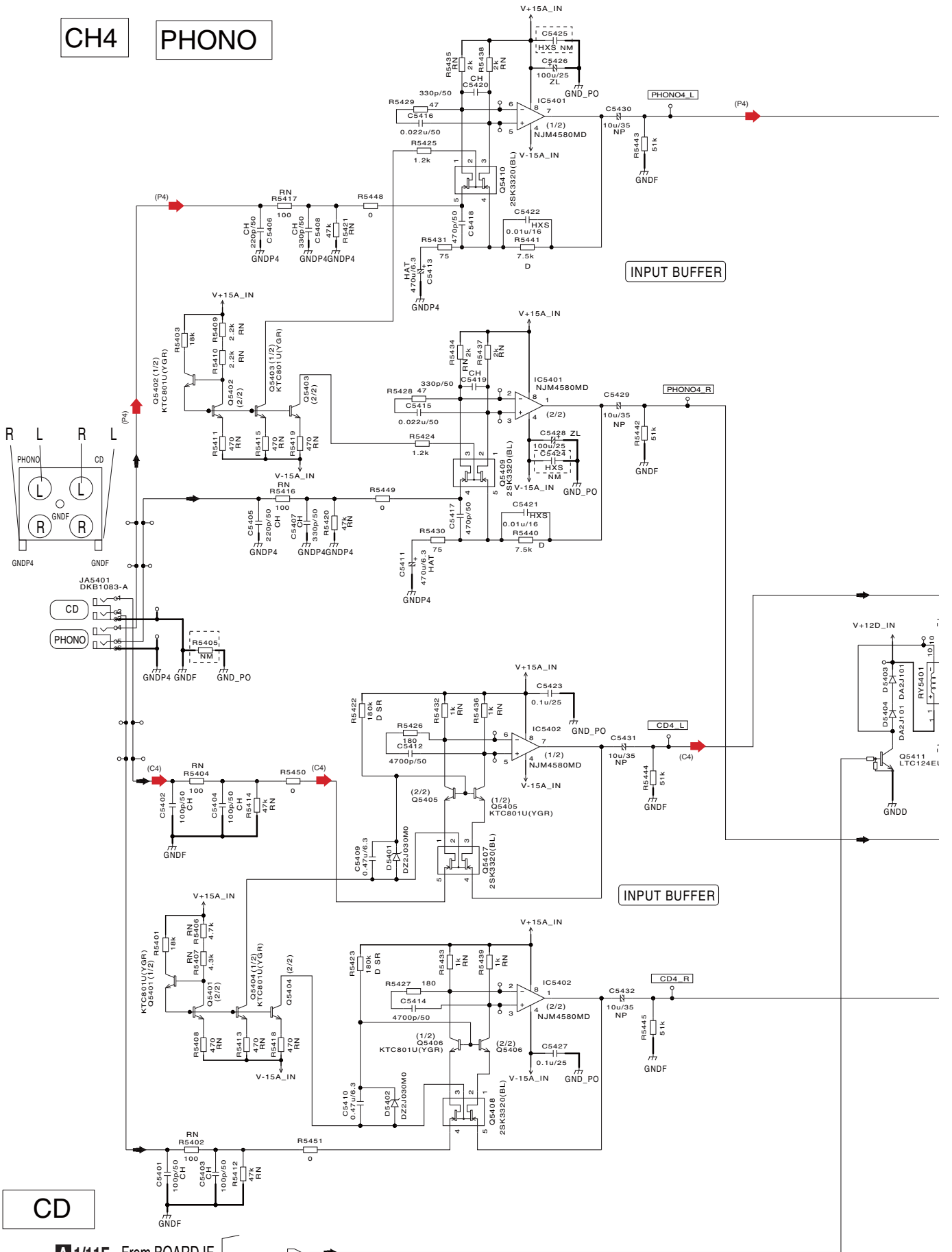
Balanced Direct ADC Driver

A7/11F INPUT ASSY (DWX3504)



10.8 INPUT ASSY(8/11)

CH4 PHONO



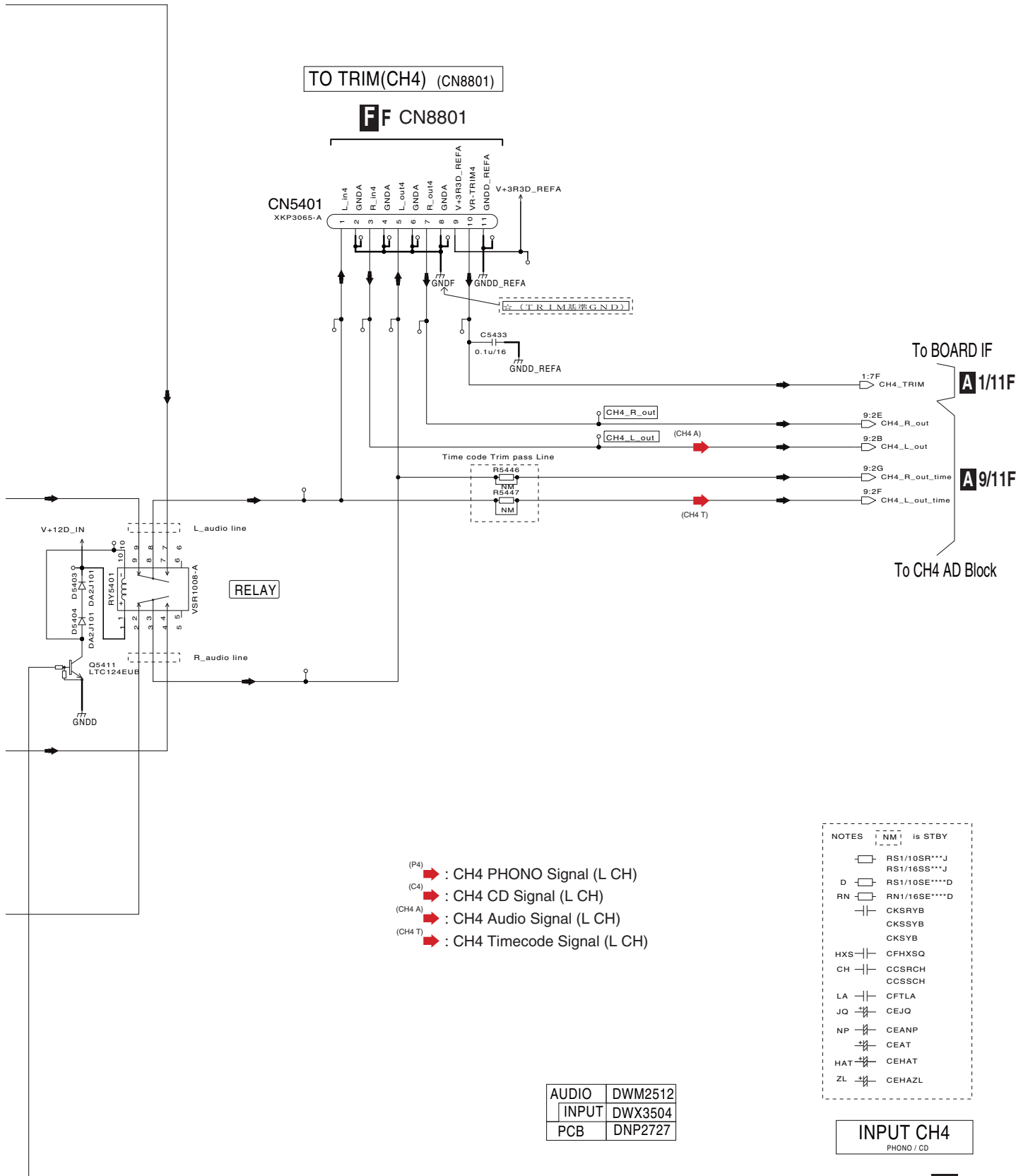
CD

A8/11F A1/11F From BOARD IF

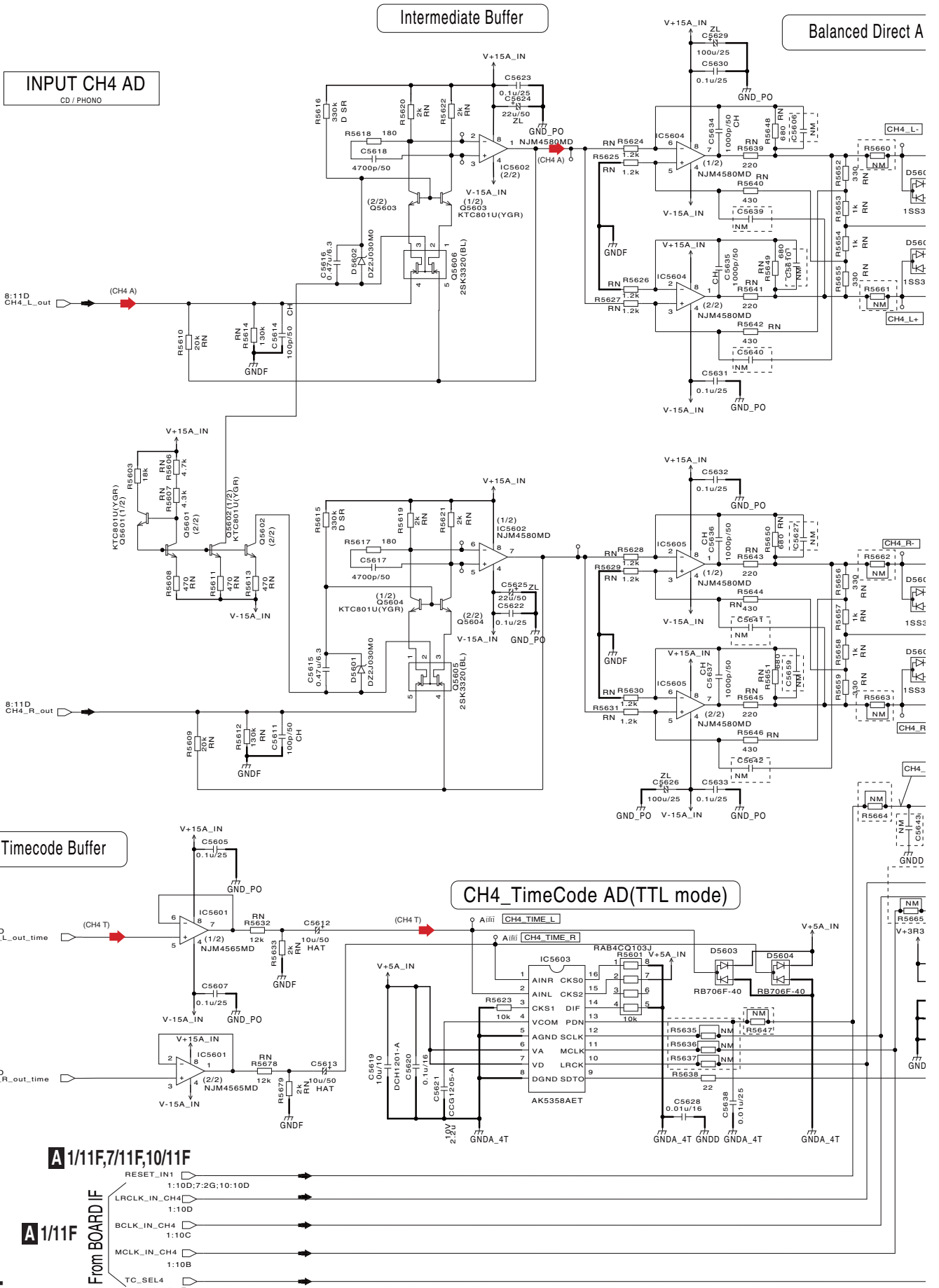
LINE_SEL4 1:7F

DJM-900SRT

A 8/11F INPUT ASSY (DWX3504)



10.9 INPUT ASSY(9/11)

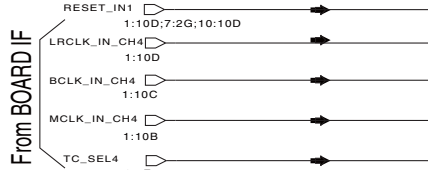


From CH4 INPUT
A 8/11F

A 1/11F, 7/11F, 10/11F

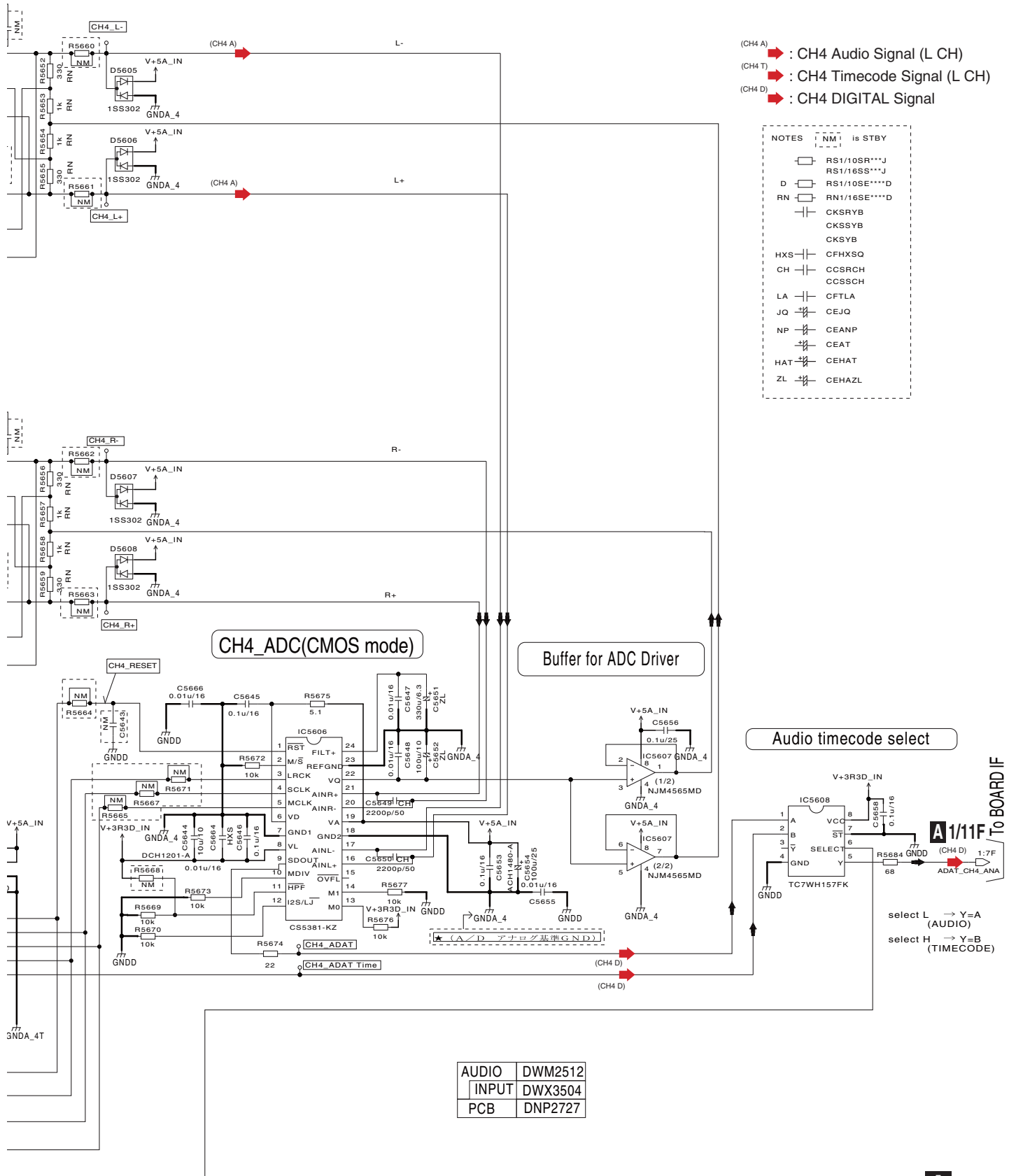
A 1/11F

A 9/11F



Balanced Direct ADC Driver

A9/11F INPUT ASSY (DWX3504)



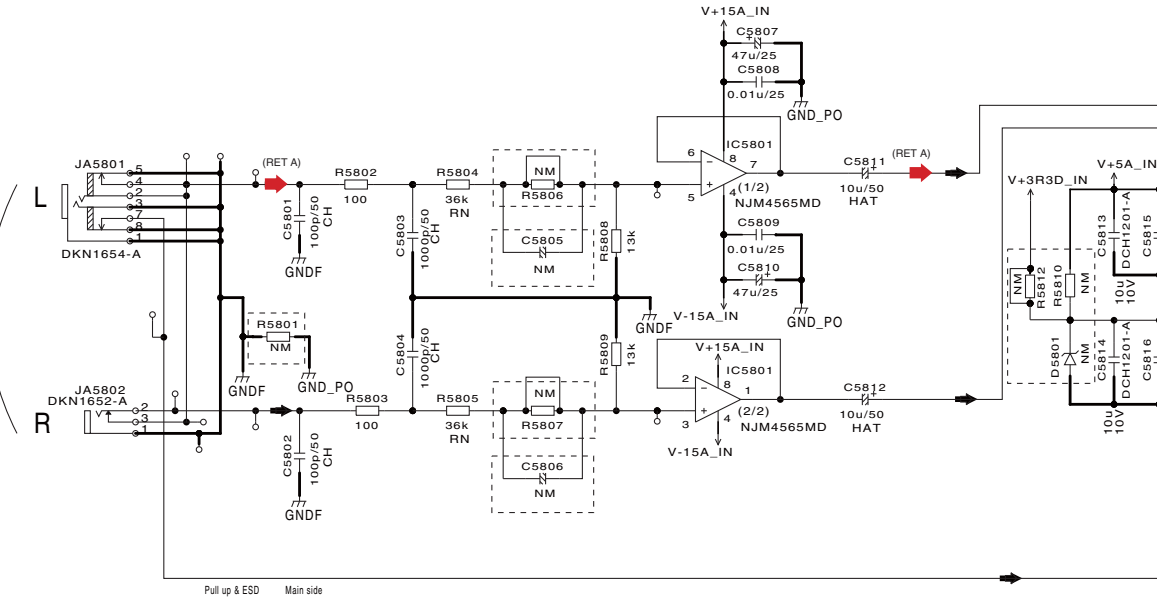
AUDIO	DWM2512
INPUT	DWX3504
PCB	DNP2727

10.10 INPUT ASSY(10/11)

RETURN

INPUT Buffer

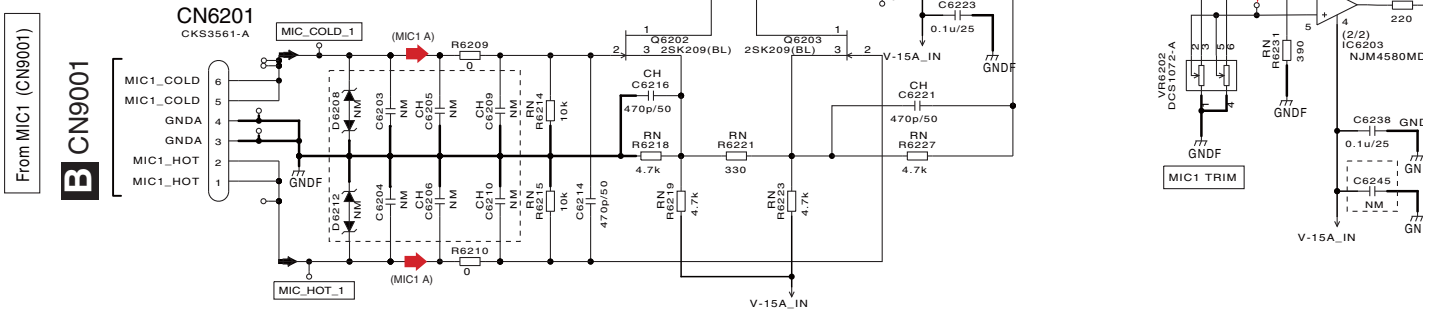
RETURN



10.11 INPUT ASSY(11/11)

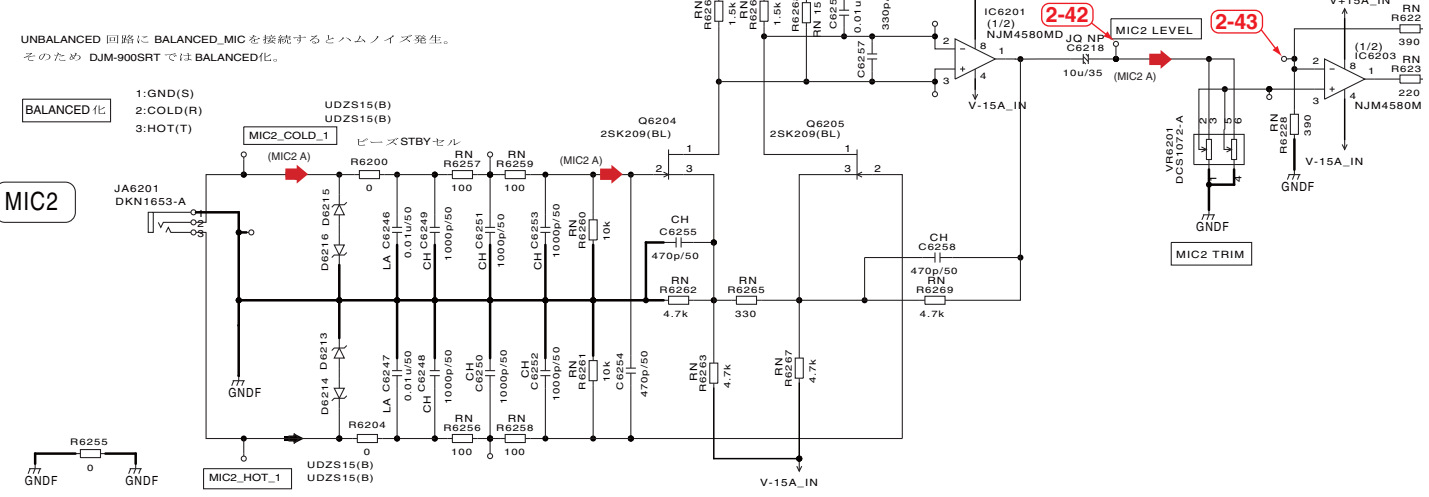
MIC/MIDI

MIC1 AMP

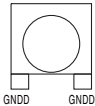
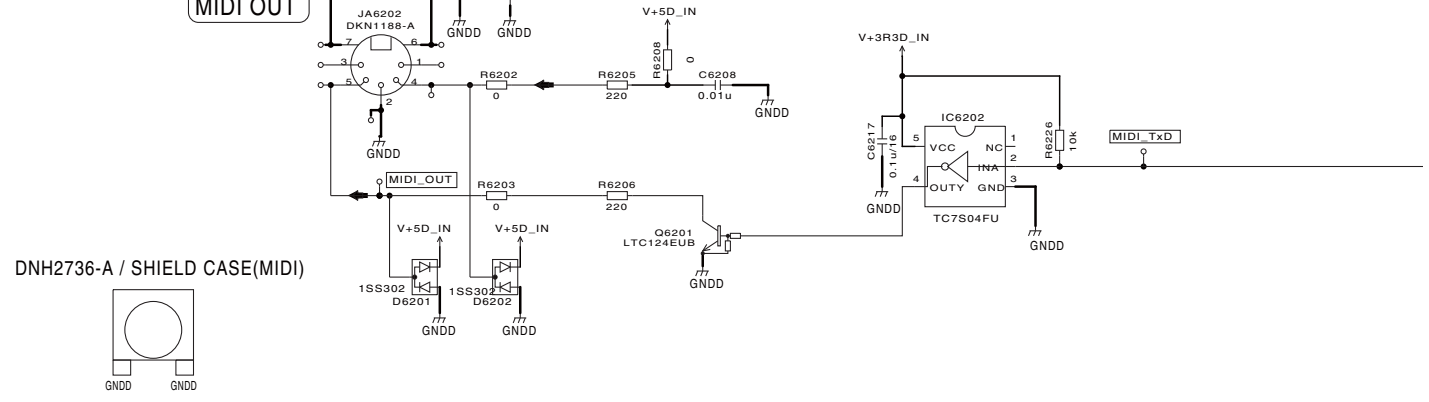


MIC2 AMP

MIC2 ADC

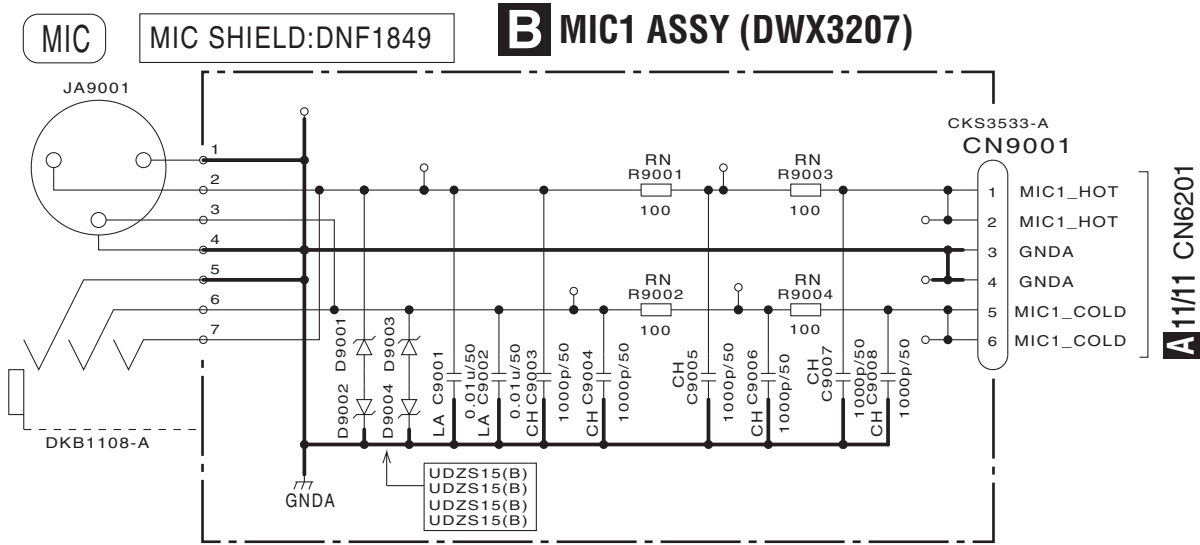


MIDI OUT



10.12 MIC1 and TRM1 to TRM4 ASSYS

A



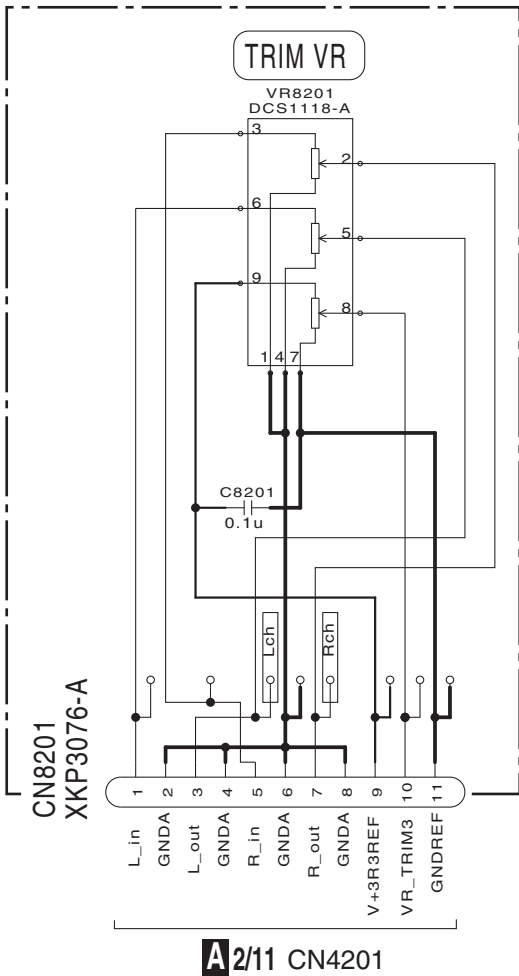
NOTE

C
L
R

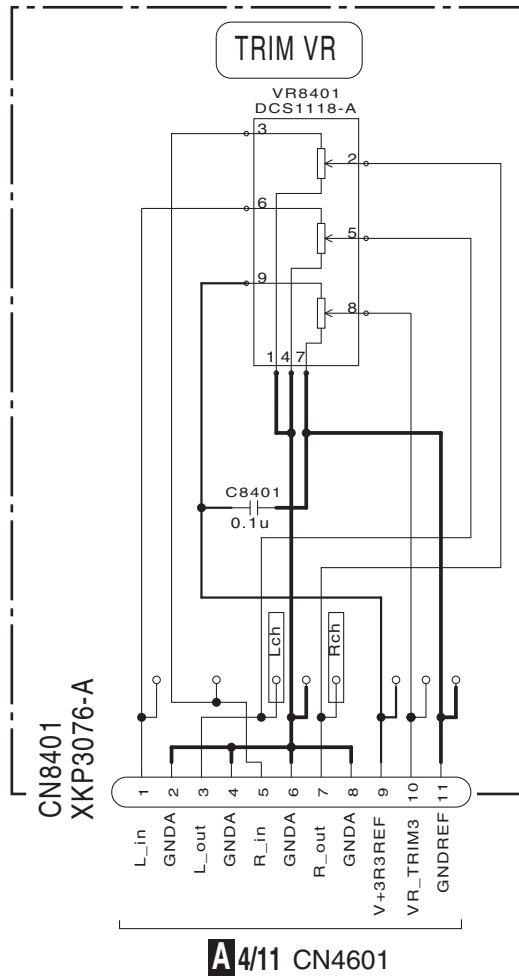
B

C

C TRM1 ASSY (DWX3506)



D TRM2 ASSY (DWX3507)



NOT



F

B **C** **F** **D** **F**

NOTES [NM] is STBY

CH [Symbol] CCSRCH

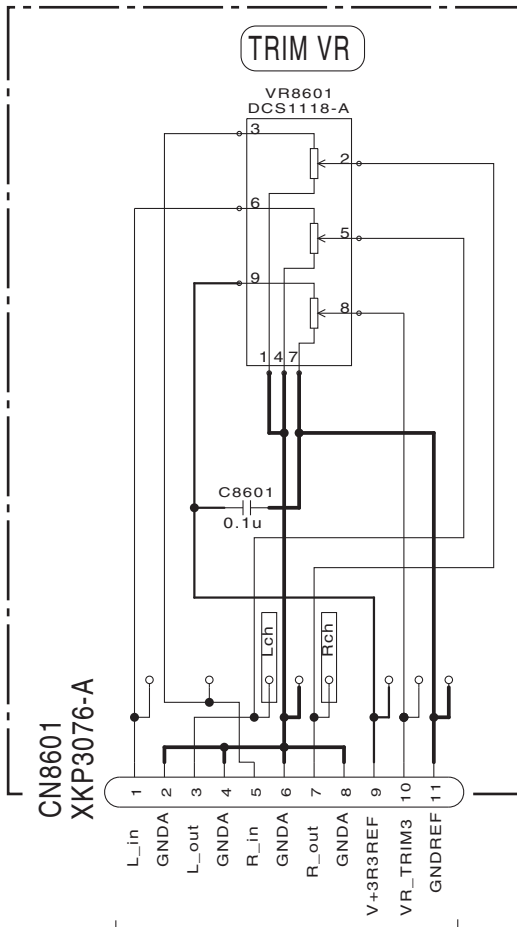
LA [Symbol] CFTLA

RN [Symbol] RN1/16SE****D

NOTES

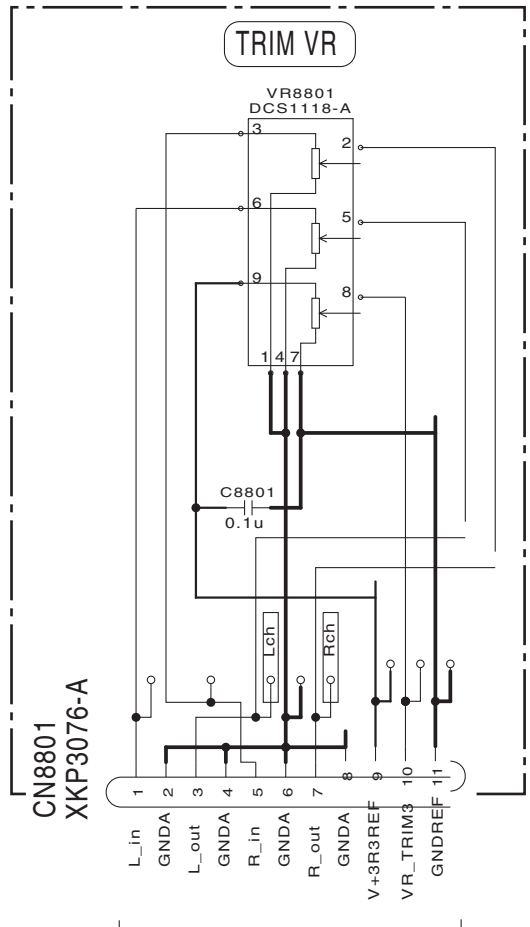
[Symbol] CKSRYB

EF TRM3 ASSY (DWX3508)



A 6/11 CN5001

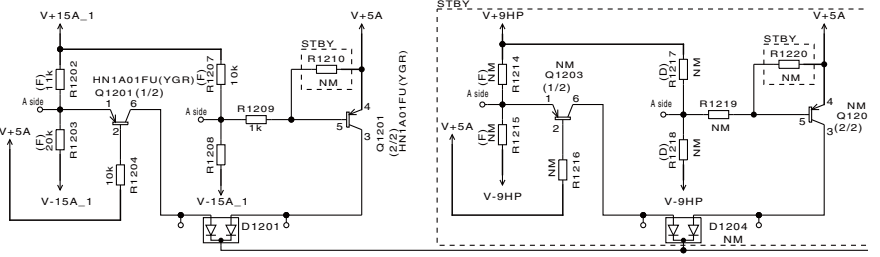
EF TRM4 ASSY (DWX3509)



A 8/11 CN5401

10.14 MAIN ASSY (2/21)

Analog voltage abnormality detect circuit

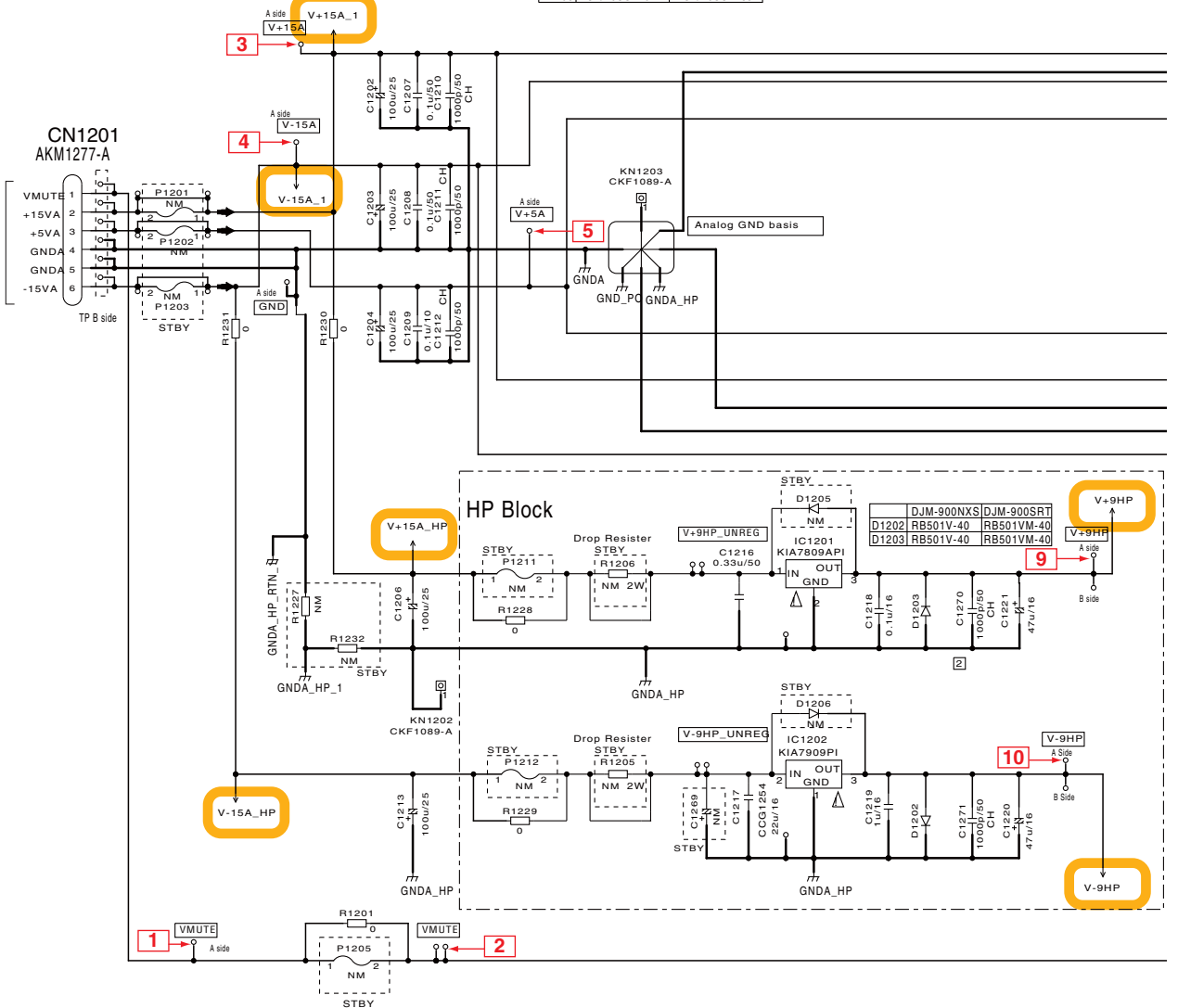


Analog Block

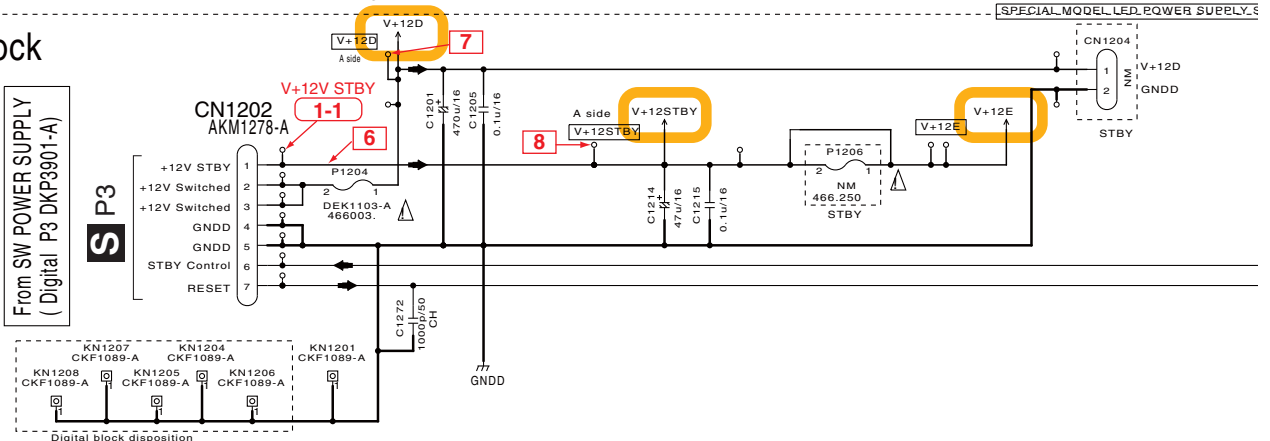
	DJM-900NXS	DJM-900SRT
D1201	1SS301	DAN202UM
R1208	RS1/16SS2202F	RS1/16SS273J

From SW POWER SUPPLY
(Analog P2 PF06EP-S22)

S P2

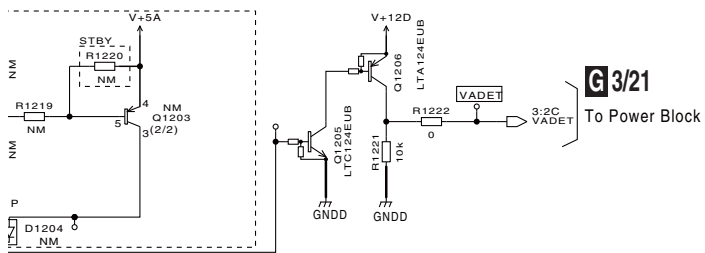


Digital Block

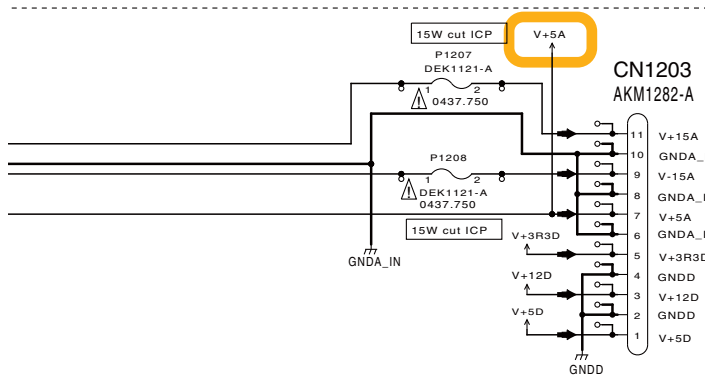


G2/21F

G2/21F MAIN ASSY (DWX3502)



	Low value	Hi value
V+5A	4.28V	5.61V
V+9HP	8.23V	9.94V
V-9HP	-5.96V	-11.82V
V+15A	13.38V	16.74V
V-15A	-11.83V	-18.57V



To ANIN Assy (SMT)
(CN4003 PF11PP-D07)

CAUTION
FOR CONTINUED PROTECTION
AGAINST RISK OF FIRE.
REPLACE ONLY SAME TYPE NO.

466.250 (DEK1093-A-T 250mA)
MFD. BY LITTELFUSE INC. FOR P1206

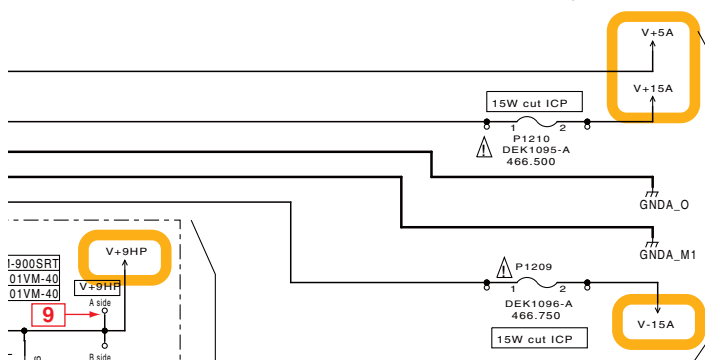
0437.750 (DEK1121-A-T 750mA)
MFD. BY LITTELFUSE INC. FOR P1207,P1208

466.750 (DEK1096-A-T 750mA)
MFD. BY LITTELFUSE INC. FOR P1209

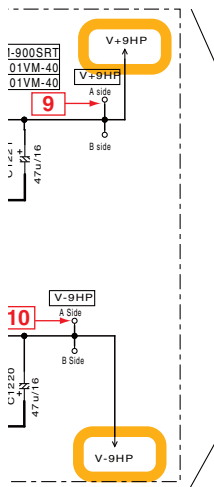
466.500 (DEK1095-A-T 500mA)
MFD. BY LITTELFUSE INC. FOR P1210

466003. (DEK1103-A-T 3A)
MFD. BY LITTELFUSE INC. FOR P1204

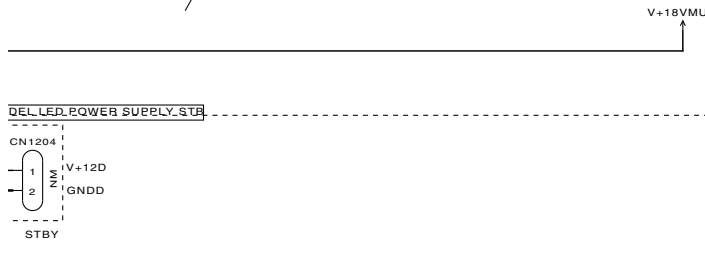
△印の部品は、指定部品(安全規格適合部品)を必ず使用すること。
The △ mark found on some component parts should be replaced
with same parts (safety regulation authorized) of identical
designation.



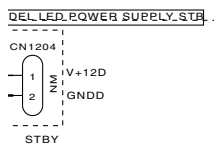
To OUTPUT IF Block



To HP AMP Block



To OUTPUT IF Block

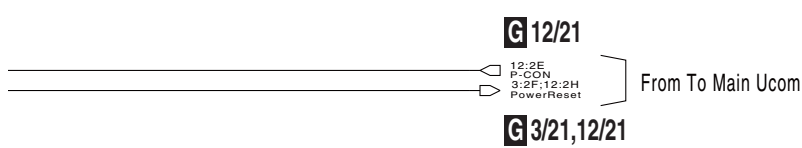


- : Voltage measuring point
- : Waveform measuring point

NOTES

1	is STBY
1	RS1/16S****J
1	RS1/10S****J
1	RS1/16S****J
1	RS1/10S****J
(D)	RS1/16S****D
1	RS1/10S****D
(F)	RS1/16S****F
1	RS1/10S****F
1/8W	RS1/8S****J
1/4W	RS1/4S****J
2W	RS2****J
	RAB4CQ****J
	CKSRYB****
	CKSSYB****
CH	CCSRCH****
	CCSSCH****
	CCSQCH****
	CEVW****
HA	CEHVAW****
NP	CEVWNP****

POWER IF



	DJM-900NXS	DJM-900SRT
MOTHER	DWM2418	DWM2511
MAIN	DWX3190	DWX3502
PCB	DNP2624	

10.15 MAIN ASSY (3/21)

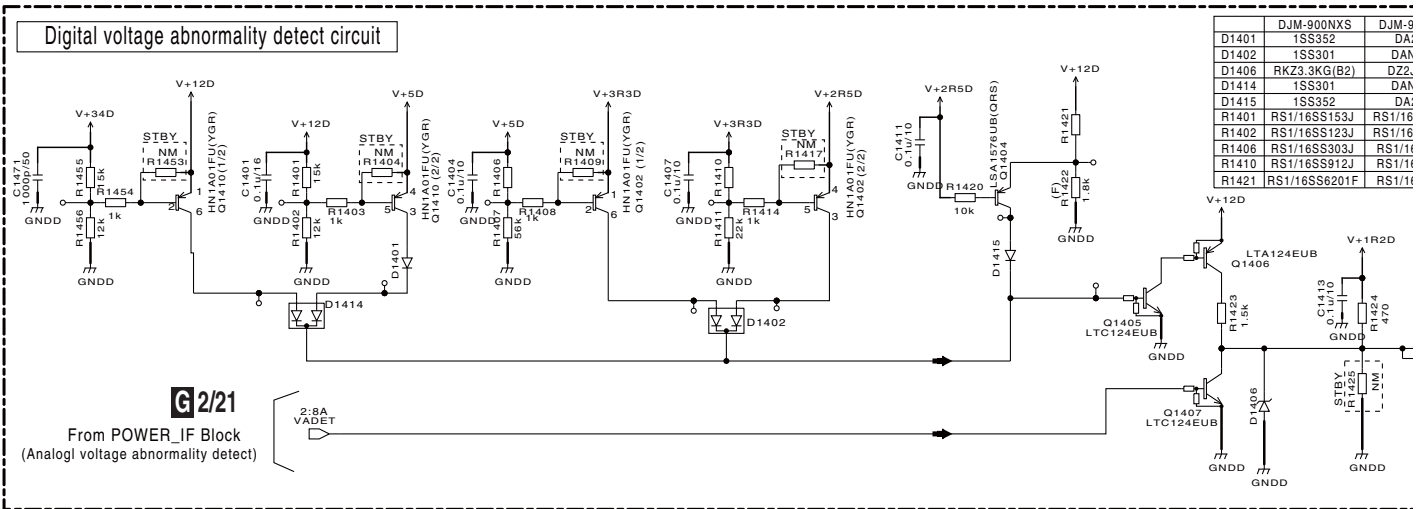
1

2

3

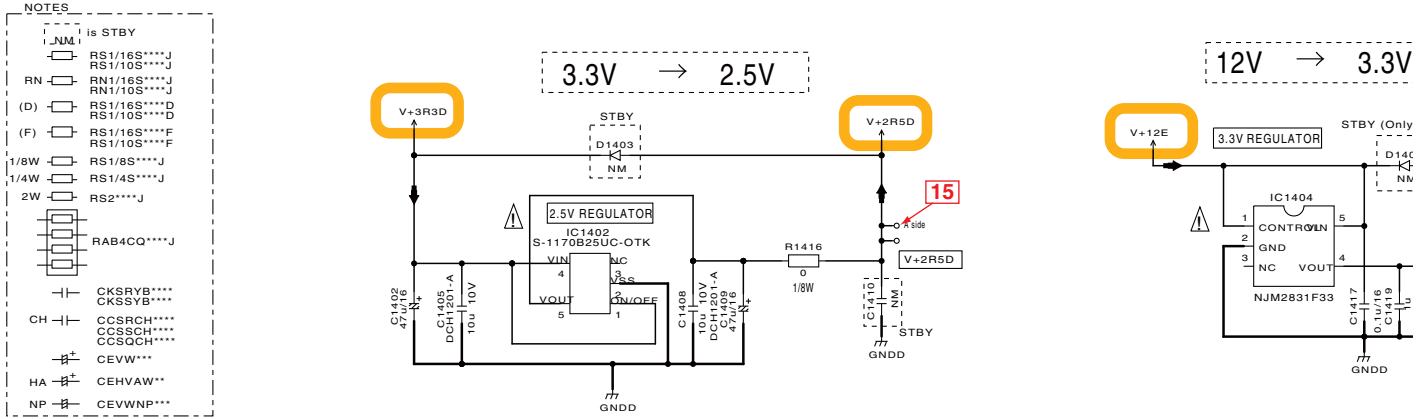
4

A

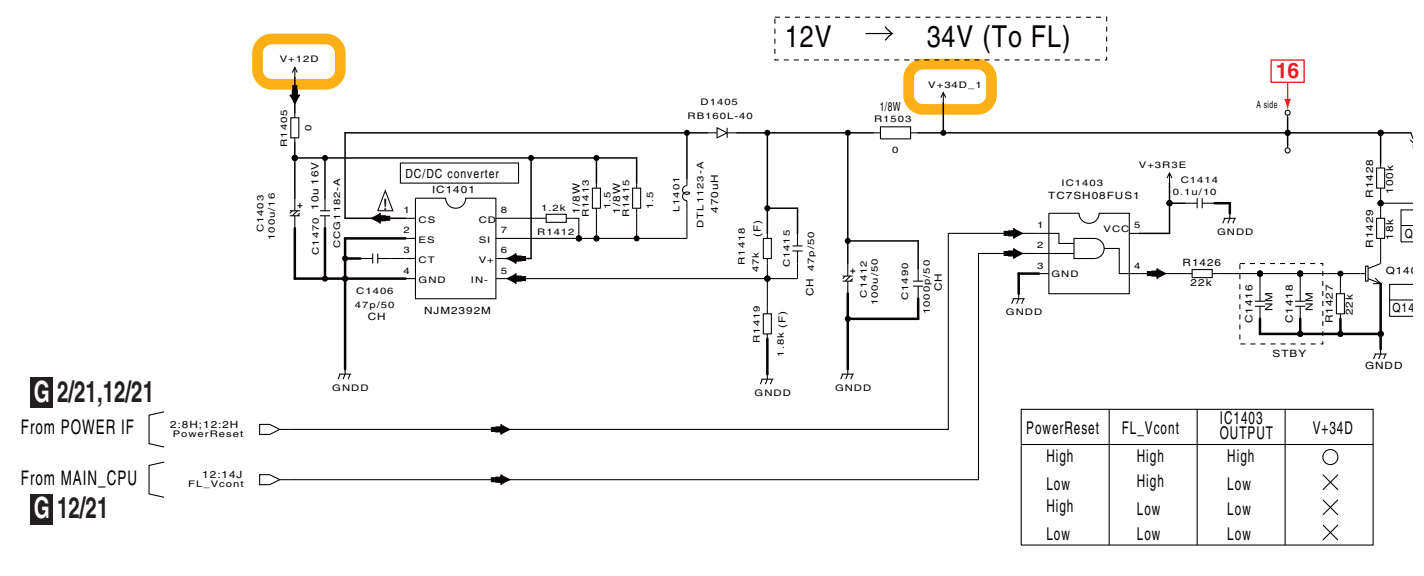


B

C



D



F

△印の部品は、指定部品(安全規格適合部品)を必ず使用すること。
The △ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.

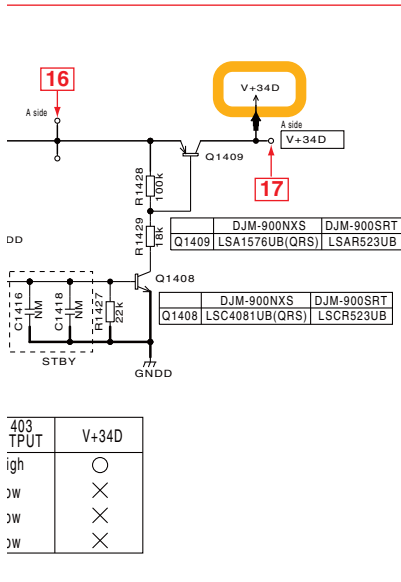
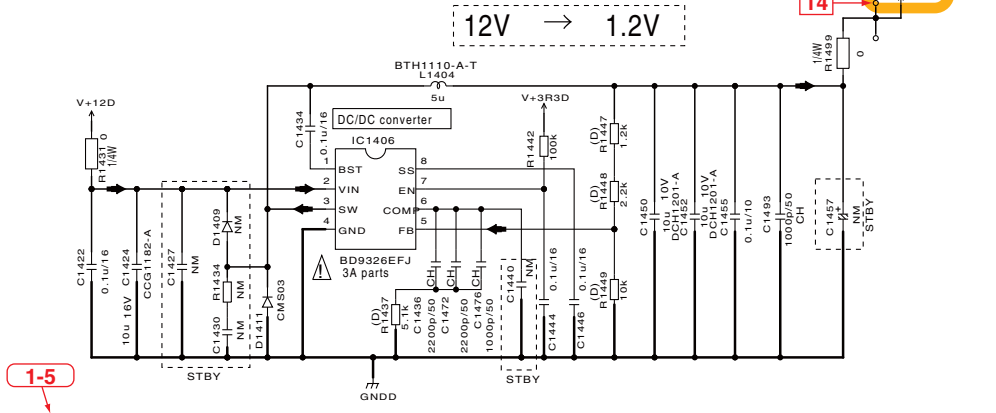
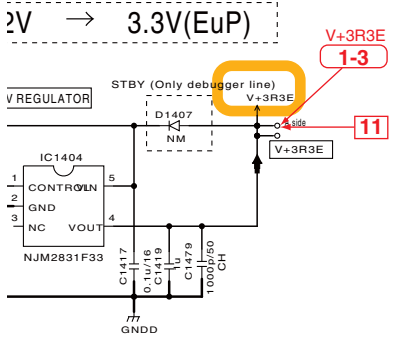
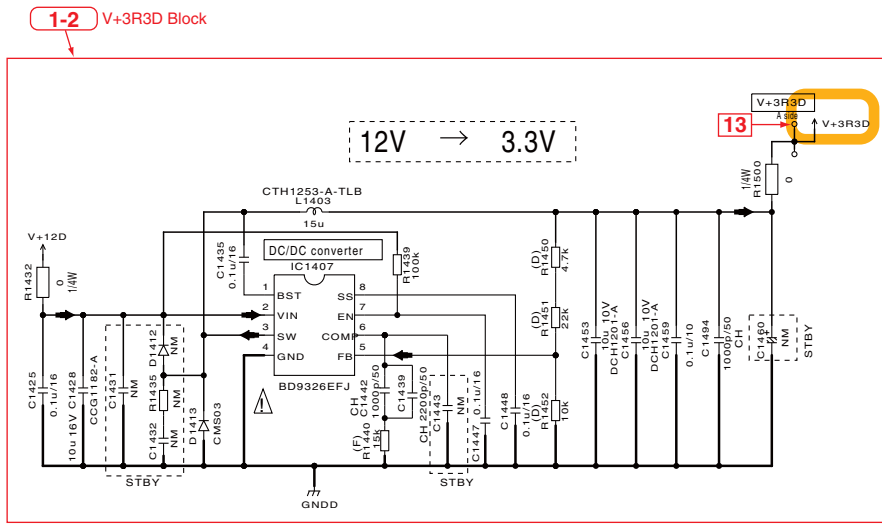
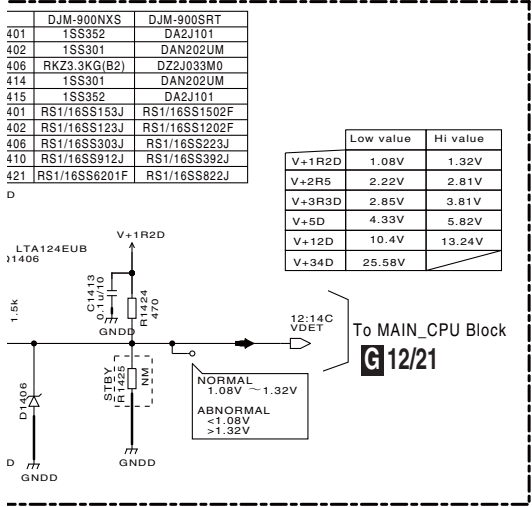
1

2

3

4

G3/21F MAIN ASSY (DWX3502)



DIGITAL POWER SUPPLY CIRCUIT

	DJM-900NXS	DJM-900SRT
MOTHER	DWM2418	DWM2511
MAIN	DWX3190	DWX3502
PCB	DNP2624	

- : Voltage measuring point
- : Waveform measuring point

G18/21F MAIN ASSY (DWX3502)

Balanced Output AMP

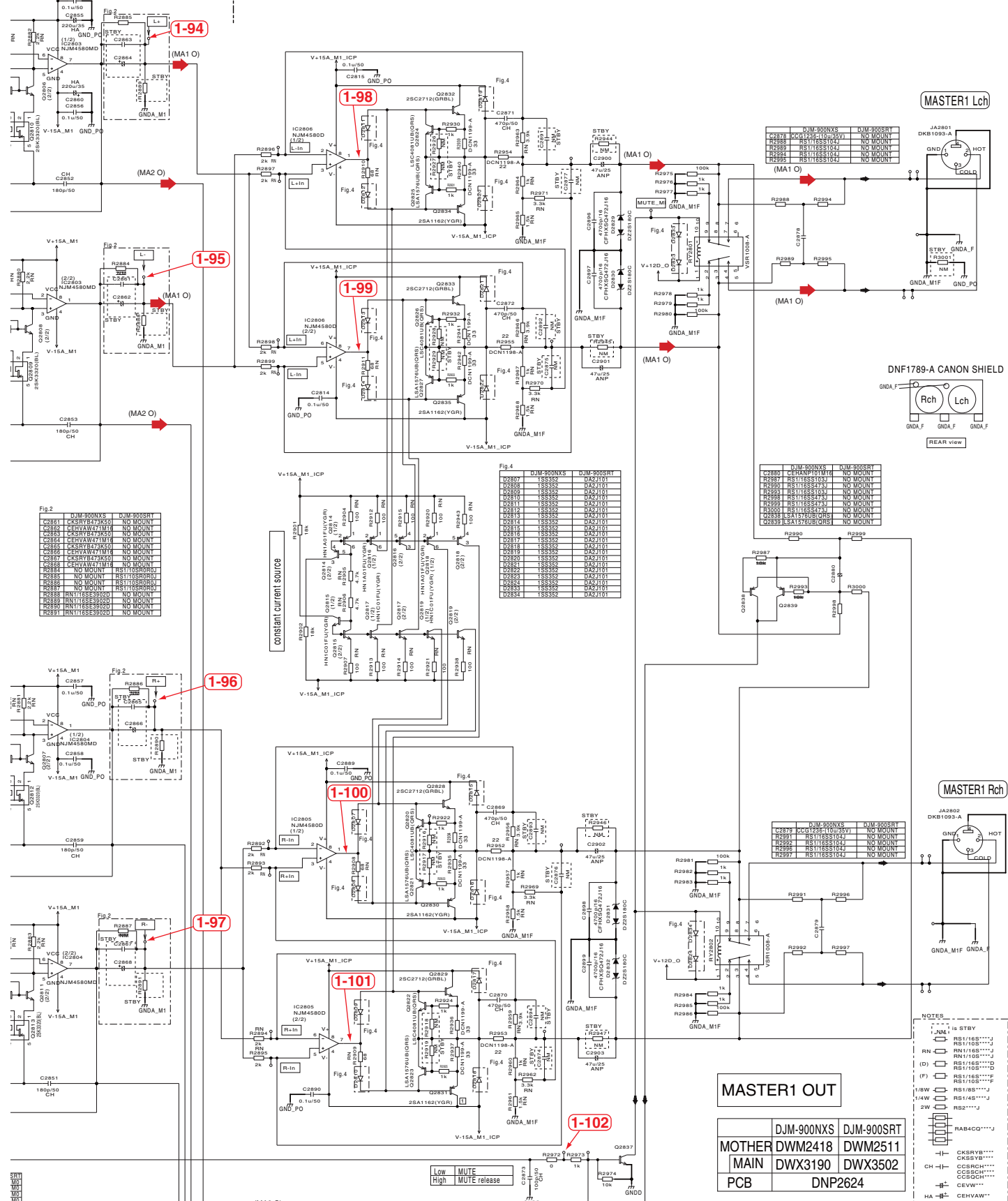


Fig 2

DJM-900NXS	DJM-900SRT	
C2854	CRWV7471M16	NO MOUNT
C2855	CRWV7471M16	NO MOUNT
C2856	CRWV7471M16	NO MOUNT
C2857	CRWV7471M16	NO MOUNT
C2858	CRWV7471M16	NO MOUNT
C2859	CRWV7471M16	NO MOUNT
C2860	CRWV7471M16	NO MOUNT
C2861	CRWV7471M16	NO MOUNT
C2862	CRWV7471M16	NO MOUNT
C2863	CRWV7471M16	NO MOUNT
C2864	CRWV7471M16	NO MOUNT
C2865	CRWV7471M16	NO MOUNT
C2866	CRWV7471M16	NO MOUNT
C2867	CRWV7471M16	NO MOUNT
C2868	CRWV7471M16	NO MOUNT
C2869	CRWV7471M16	NO MOUNT
C2870	CRWV7471M16	NO MOUNT
C2871	CRWV7471M16	NO MOUNT
C2872	CRWV7471M16	NO MOUNT
C2873	CRWV7471M16	NO MOUNT
C2874	CRWV7471M16	NO MOUNT
C2875	CRWV7471M16	NO MOUNT
C2876	CRWV7471M16	NO MOUNT
C2877	CRWV7471M16	NO MOUNT
C2878	CRWV7471M16	NO MOUNT
C2879	CRWV7471M16	NO MOUNT
C2880	CRWV7471M16	NO MOUNT
C2881	CRWV7471M16	NO MOUNT
C2882	CRWV7471M16	NO MOUNT
C2883	CRWV7471M16	NO MOUNT
C2884	CRWV7471M16	NO MOUNT
C2885	CRWV7471M16	NO MOUNT
C2886	CRWV7471M16	NO MOUNT
C2887	CRWV7471M16	NO MOUNT
C2888	CRWV7471M16	NO MOUNT
C2889	CRWV7471M16	NO MOUNT
C2890	CRWV7471M16	NO MOUNT
C2891	CRWV7471M16	NO MOUNT
C2892	CRWV7471M16	NO MOUNT
C2893	CRWV7471M16	NO MOUNT
C2894	CRWV7471M16	NO MOUNT
C2895	CRWV7471M16	NO MOUNT
C2896	CRWV7471M16	NO MOUNT
C2897	CRWV7471M16	NO MOUNT
C2898	CRWV7471M16	NO MOUNT
C2899	CRWV7471M16	NO MOUNT
C2900	CRWV7471M16	NO MOUNT
C2901	CRWV7471M16	NO MOUNT
C2902	CRWV7471M16	NO MOUNT
C2903	CRWV7471M16	NO MOUNT

Fig 4

DJM-900NXS	DJM-900SRT	
D2800	1SS352	Q282101
D2801	1SS352	Q282101
D2802	1SS352	Q282101
D2803	1SS352	Q282101
D2804	1SS352	Q282101
D2805	1SS352	Q282101
D2806	1SS352	Q282101
D2807	1SS352	Q282101
D2808	1SS352	Q282101
D2809	1SS352	Q282101
D2810	1SS352	Q282101
D2811	1SS352	Q282101
D2812	1SS352	Q282101
D2813	1SS352	Q282101
D2814	1SS352	Q282101
D2815	1SS352	Q282101
D2816	1SS352	Q282101
D2817	1SS352	Q282101
D2818	1SS352	Q282101
D2819	1SS352	Q282101
D2820	1SS352	Q282101
D2821	1SS352	Q282101
D2822	1SS352	Q282101
D2823	1SS352	Q282101
D2824	1SS352	Q282101
D2825	1SS352	Q282101
D2826	1SS352	Q282101
D2827	1SS352	Q282101
D2828	1SS352	Q282101
D2829	1SS352	Q282101
D2830	1SS352	Q282101
D2831	1SS352	Q282101

MASTER1 OUT

DJM-900NXS	DJM-900SRT
MOTHER DWM2418	DWM2511
MAIN DWX3190	DWX3502
PCB	DNP2624

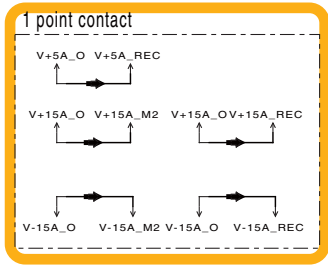
G19/21F To MASTER2/REC Block

DJM-900SRT

G18/21F

10.31 MAIN ASSY (19/21)

1 2 3 4



MASTER 2



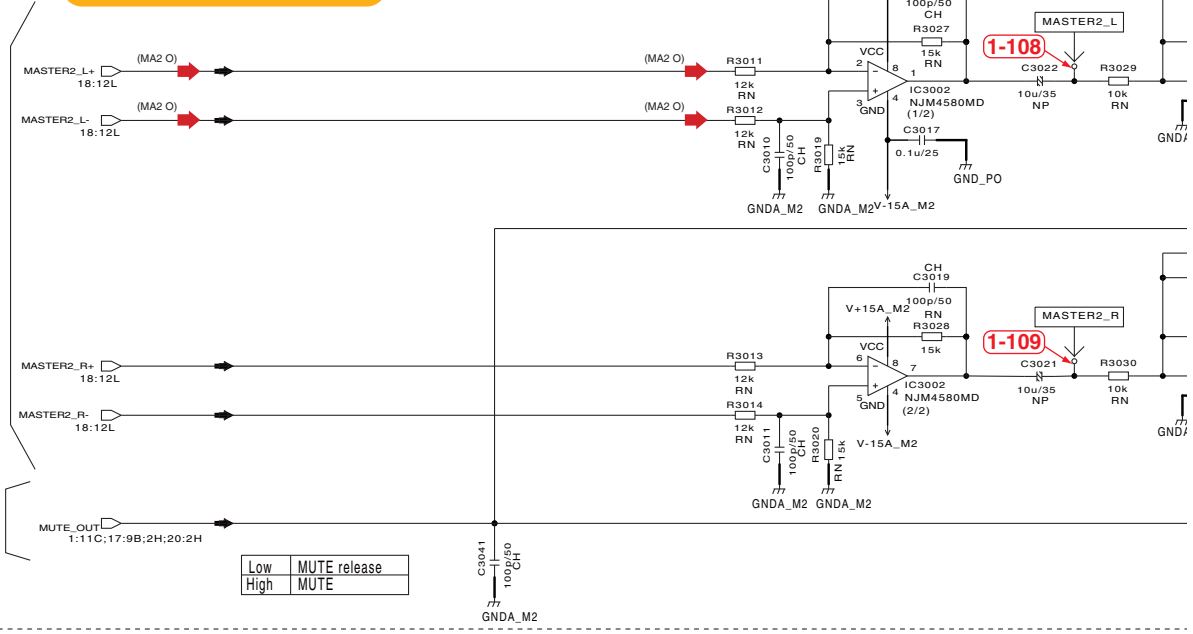
A

B

C

From MASTER1 Block
G 18/21F

From OUTPUT IF Block
G 1/21,17/21,20/21



Low	MUTE release
High	MUTE

REC OUT

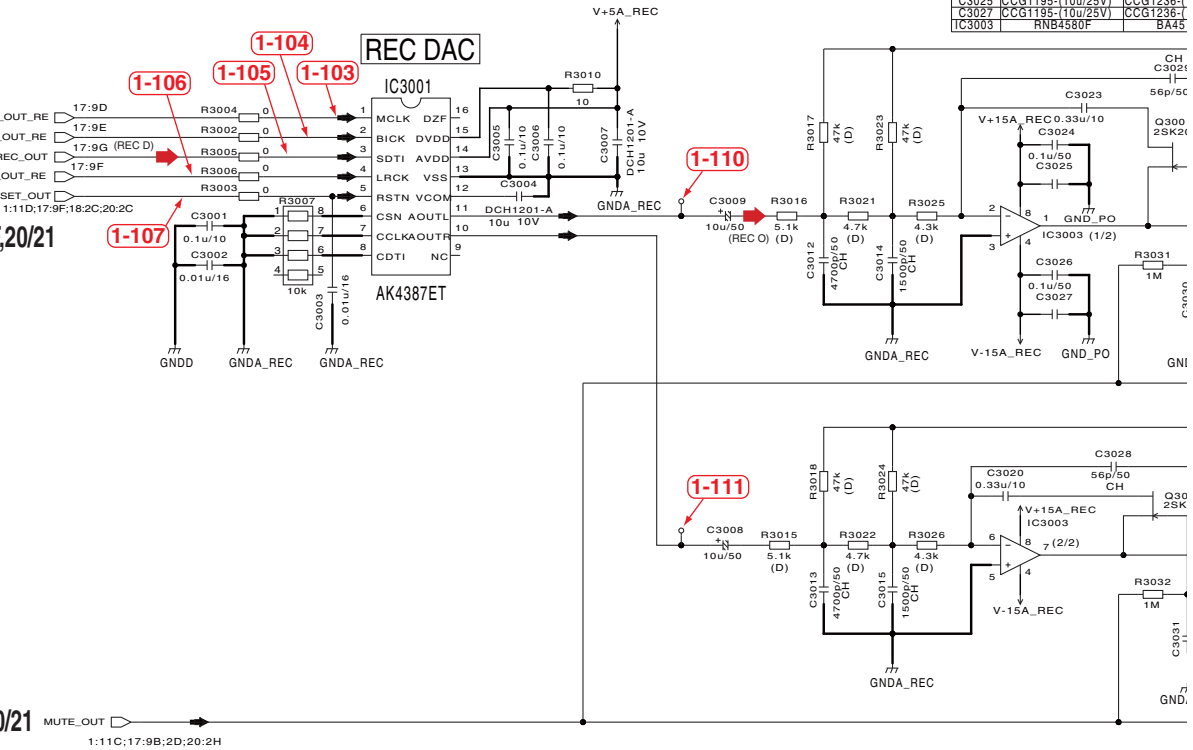
	DJM-900XS	DJM-9
C3025	CCG1135 (10u/25V)	CCG1236
C3027	CCG1135 (10u/25V)	CCG1236
IC3003	RN84580F	BA45

D

E

From OUTPUT IF Block
G 1/21,17/21,18/21F,20/21

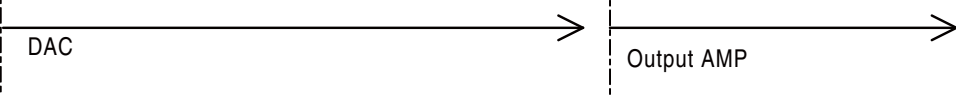
From OUTPUT IF Block
G 1/21,17/21,20/21



Low	MUTE release
High	MUTE

F

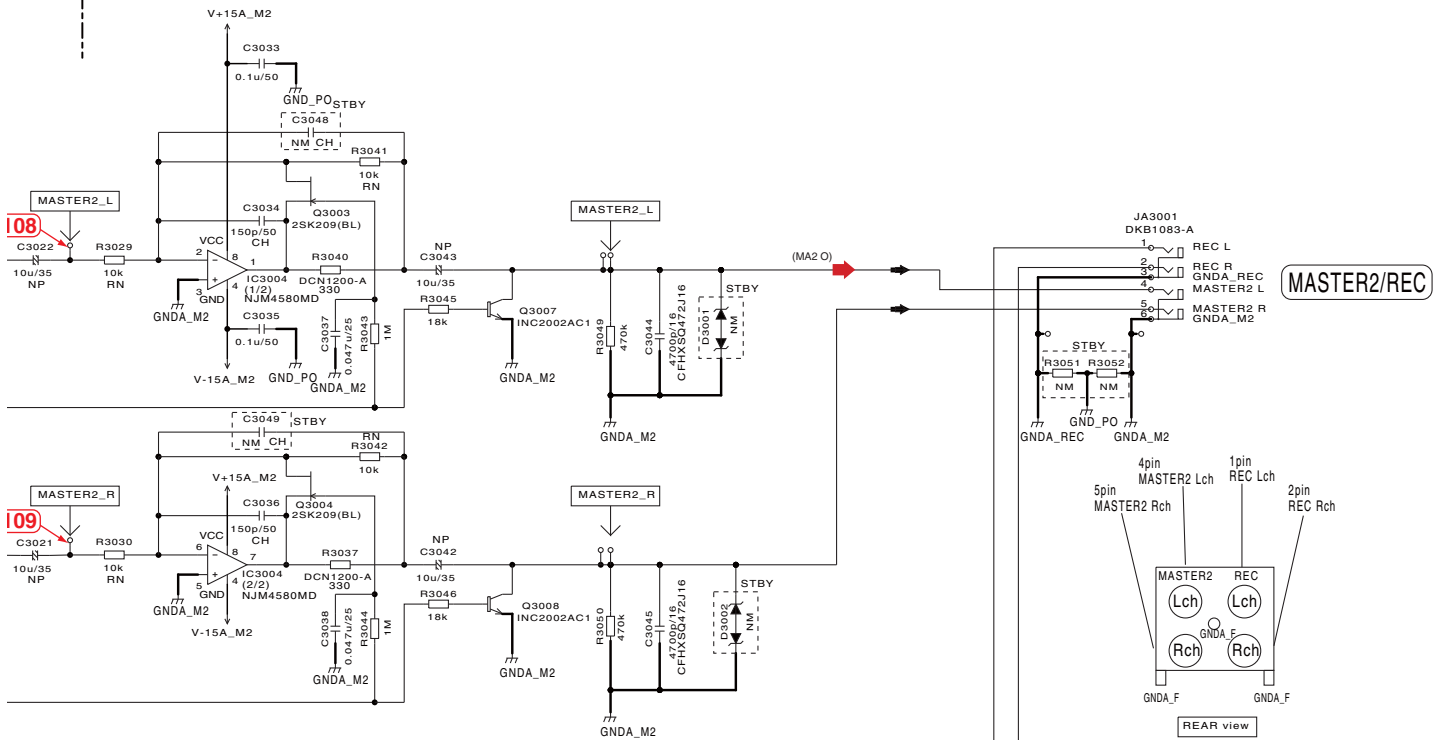
G 19/21F



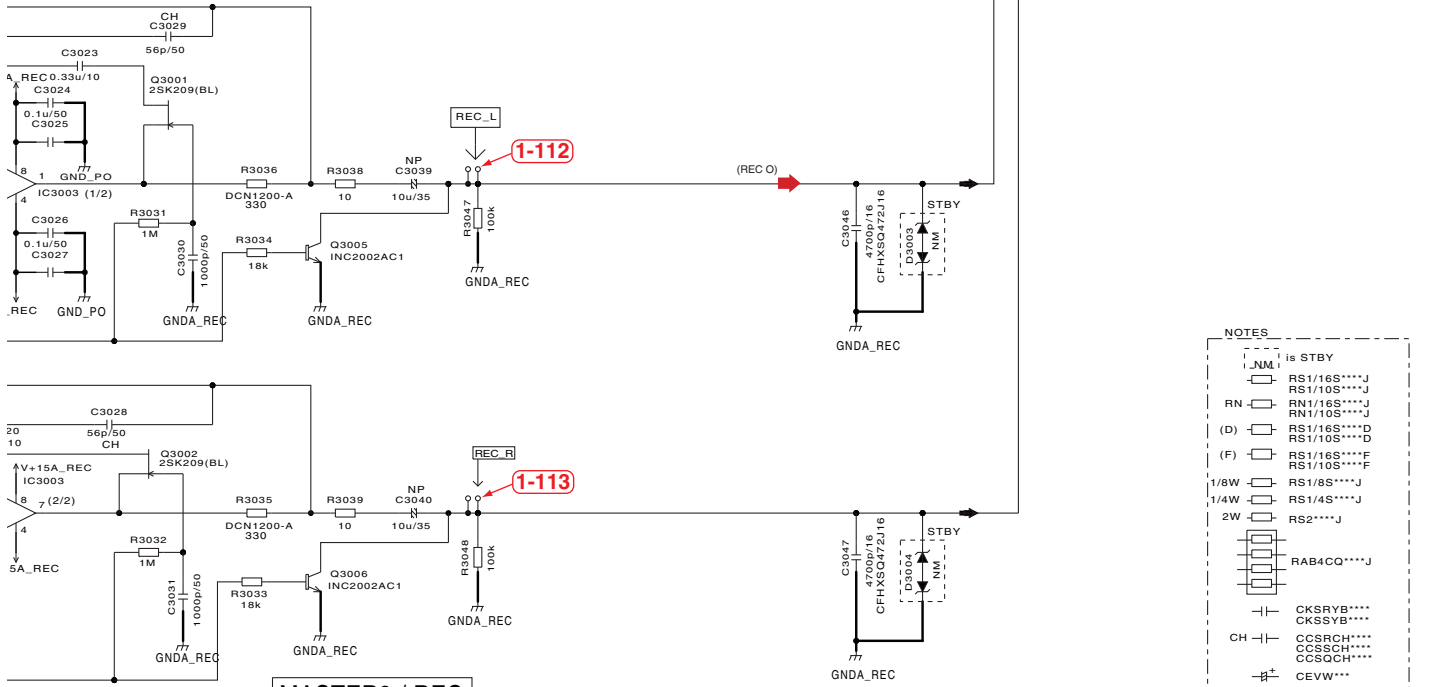
1 2 3 4

G19/21F MAIN ASSY (DWX3502)

Output AMP



DJM-900NXS	DJM-900SRT
CG1195-1(10u/25V)	CCG1236-1(10u/35V)
CG1195-1(10u/25V)	CCG1236-1(10u/35V)
RNB4580F	BA4580RF



MASTER2 / REC

	DJM-900NXS	DJM-900SRT
MOTHER	DWM2418	DWM2511
MAIN	DWX3190	DWX3502
PCB	DNP2624	

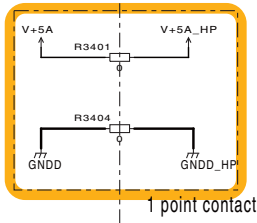
- : Waveform measuring point
- (MA2 O) ➔ : MASTER2 OUT L ch
- (REC D) ➔ : REC OUT Digital Signal
- (REC O) ➔ : REC OUT L ch

NOTES

- 1. NM is STBY
- RS1/16S****J
- RS1/10S****J
- RN RS1/16S****J
- RS1/10S****J
- (D) RS1/16S****D
- RS1/10S****D
- (F) RS1/16S****F
- RS1/10S****F
- 1/8W RS1/8S****J
- 1/4W RS1/4S****J
- 2W RS2****J
- RAB4CQ****J
- CKSRYB****
- CKSSYB****
- CH CCSRCH****
- CCSSCH****
- CCSOCH****
- CEVW****
- HA CEHVAW****
- NP CEVWNP****

10.33 MAIN ASSY (21/21)

Digital ← → HP Block

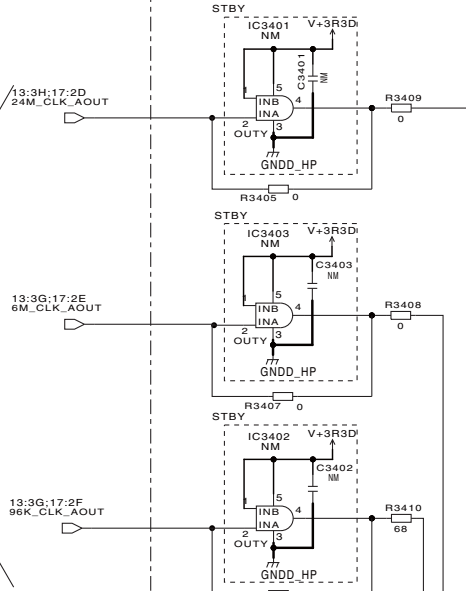


DAC

Noise Filter

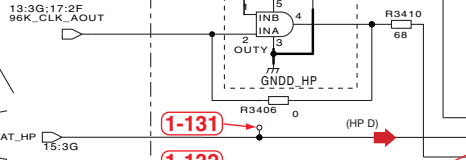
G 13/21,17/21

From FPGA Block



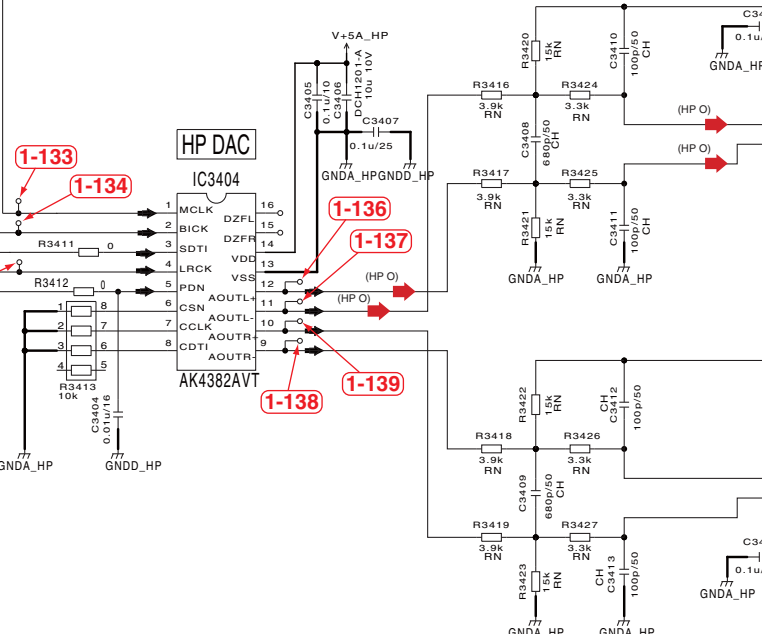
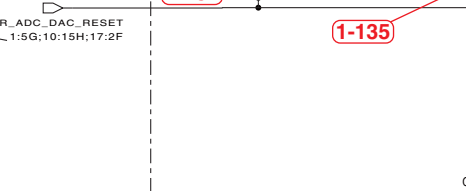
G 15/21

From DSP_2 Block



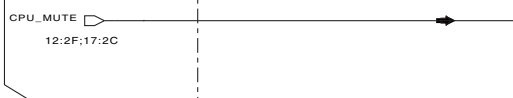
G 1/21,10/21,17/21

From LAN UCOM_2 Block



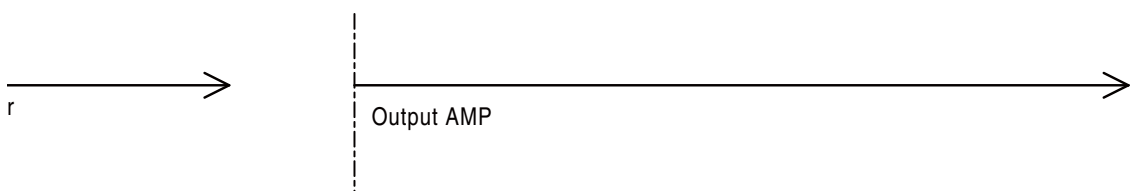
G 12/21,17/21

From MAIN CPU Block



G 21/21F

G21/21F MAIN ASSY (DWX3502)



○ : Waveform measuring point

(HP D) : HP OUT Digital Signal
 (HP O) : HP OUT L ch

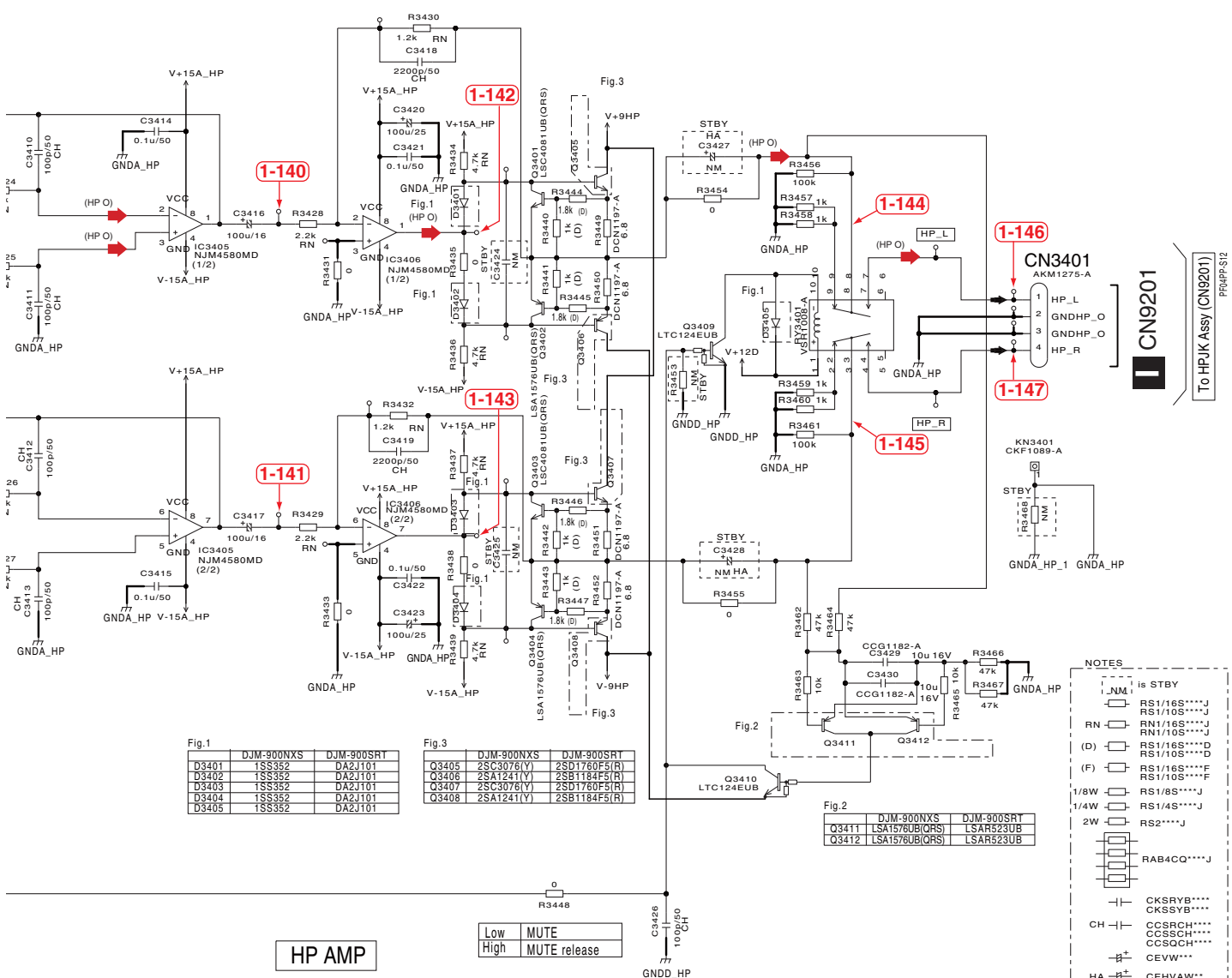


Fig.1

	DJM-900NXS	DJM-900SRT
D3401	1SS352	DA2J101
D3402	1SS352	DA2J101
D3403	1SS352	DA2J101
D3404	1SS352	DA2J101
D3405	1SS352	DA2J101

Fig.3

	DJM-900NXS	DJM-900SRT
Q3405	2SC3076(Y)	2SD1760F5(R)
Q3406	2SA1241(Y)	2SB1184F5(R)
Q3407	2SC3076(Y)	2SD1760F5(R)
Q3408	2SA1241(Y)	2SB1184F5(R)

Fig.2

	DJM-900NXS	DJM-900SRT
Q3411	LSA1576U(ORS)	LSAR523UB
Q3412	LSA1576U(ORS)	LSAR523UB

HP AMP		
	DJM-900NXS	DJM-900SRT
MOTHER	DWM2418	DWM2511
MAIN	DWX3190	DWX3502
PCB	DNP2624	

Low	MUTE
High	MUTE release

- NOTES
- is STBY
 - RS1/16S****J
 - RS1/10S****J
 - RN □ RN1/16S****J
 - RN1/10S****J
 - (D) □ RS1/16S****D
 - RS1/10S****D
 - (F) □ RS1/16S****F
 - RS1/10S****F
 - 1/8W □ RS1/8S****J
 - 1/4W □ RS1/4S****J
 - 2W □ RS2****J
 - RAB4CO****J
 - CKSRFB****
 - CKSSYB****
 - CH □ CC5RCH****
 - CCSSCH****
 - CC5SCH****
 - CEVW****
 - CEHVAW****
 - NP □ CEVWNP****

DJM-900SRT

10.35 SEND ASSY

1 2 3 4

A

B

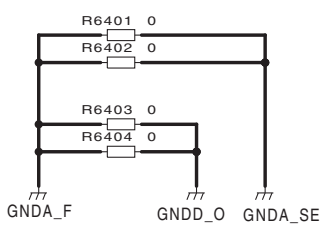
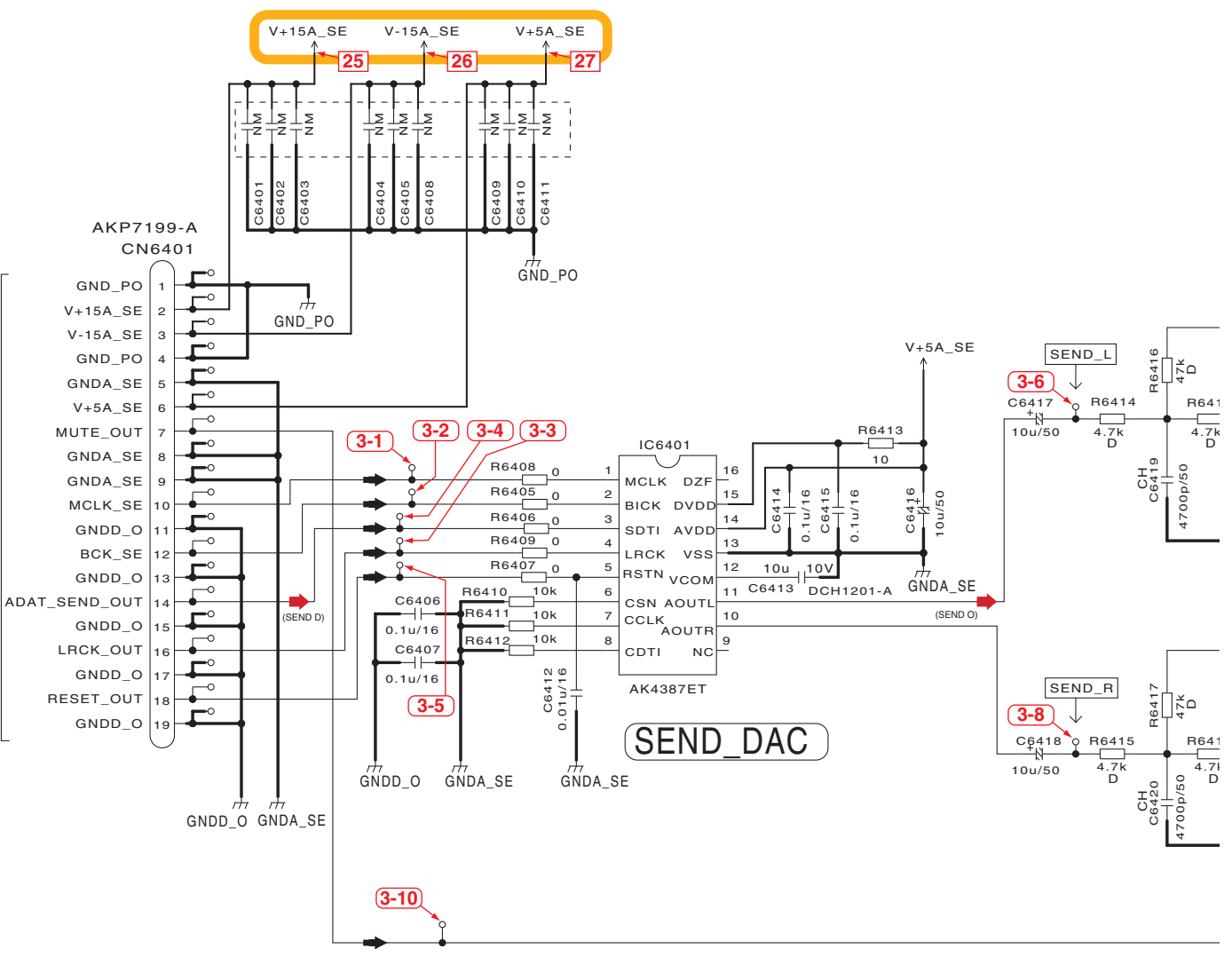
C

D

E

F

G1/21 CN1003

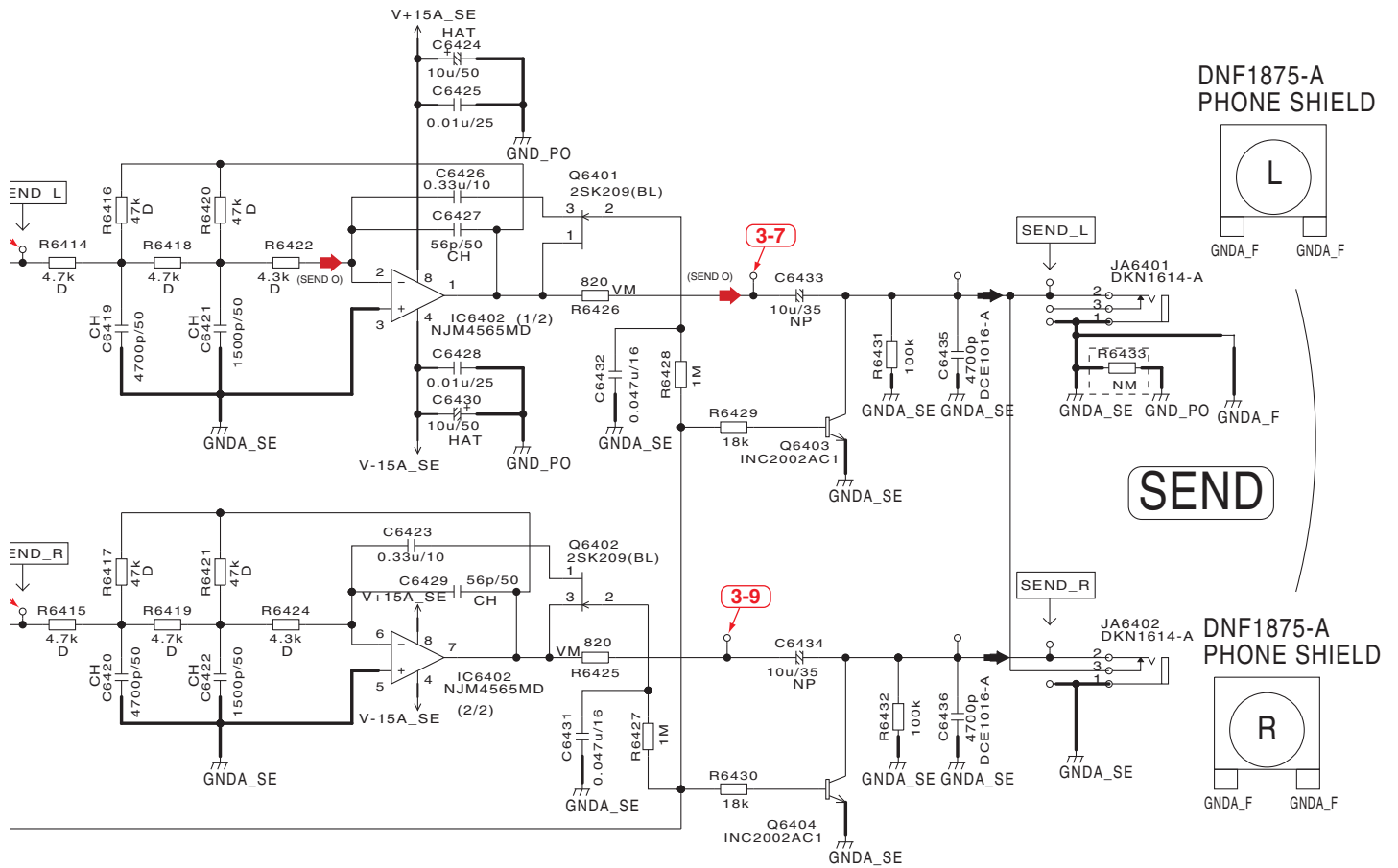


NOTES	
	RD1/2VM***
	RS1/10SS***D
	RS1/10SR***J
	RS1/16SS***J
	CKSRYB
	CKSSYB
	CCSRCH
	CCSSCH
	CCSQCH
	CEANP
	CEAT
	CEHAT
	CEHAZL



1 2 3 4

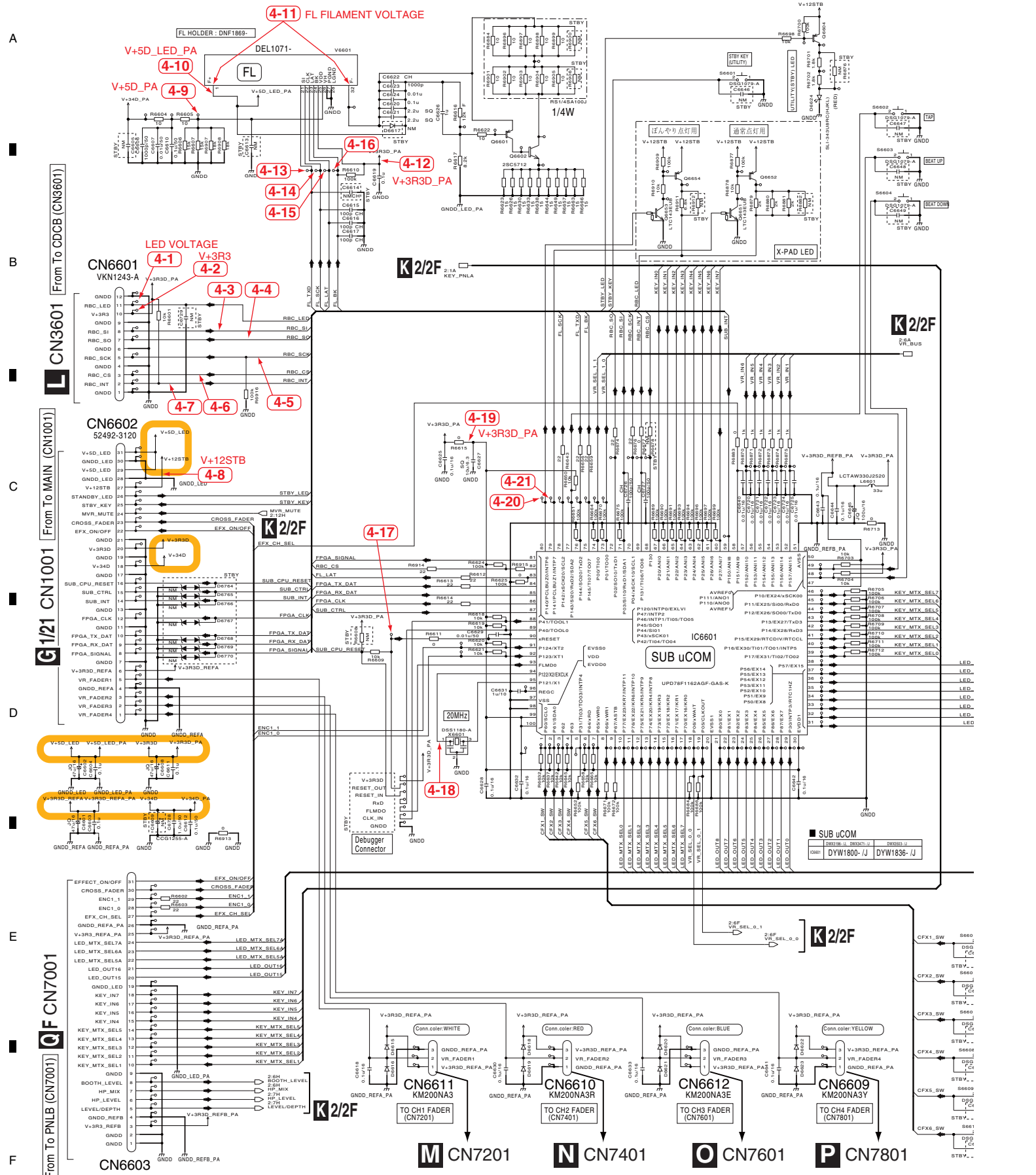
Shaping noise filter



- : Voltage measuring point
- : Waveform measuring point

- ➔ (SEND O) : SEND Output Audio Signal (L CH)
- ➔ (SEND D) : SEND DIGITAL Signal

10.36 PNLA ASSY(1/2)

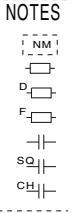
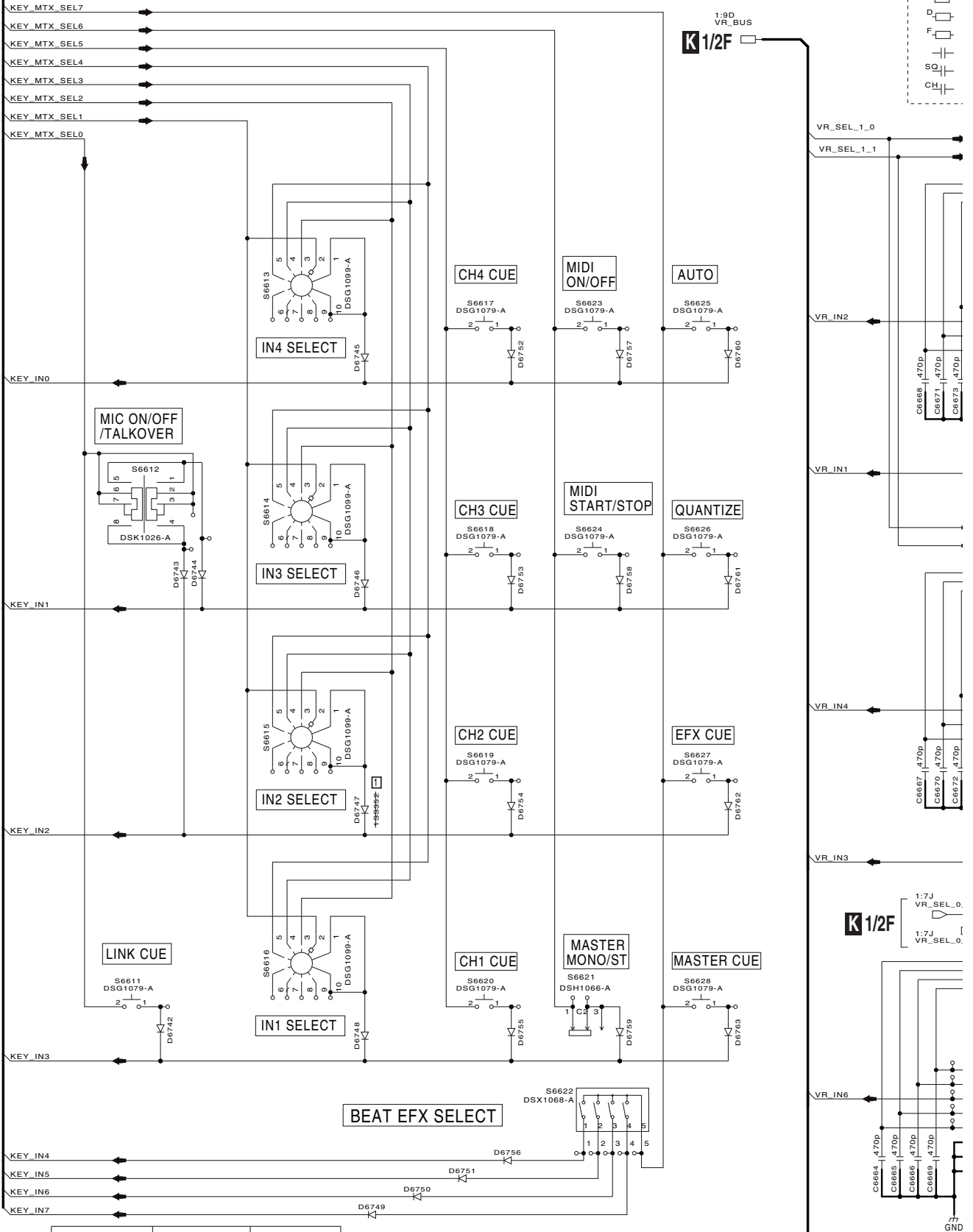


DIODE

DWX3196-1/J, DWX3471-1/J	DWX3503-1/J
D6615, D6616 D6618 ~ D6623	1S8352-TRB NM

DJM-900SRT

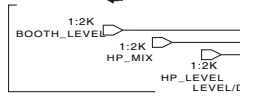
10.37 PNLA ASSY(2/2)



	DJM-900NXS	DJM-900NXS-M	DJM-900SRT
PNLA	DWX3196- /J	DWX3471- /J	DWX3503- /J
PCB	DNP2626-		

DIODE

D6742 ~ D6763	DWX3196- /J, DWX3471- /J	DWX3503- /J
	1SS352-TRB	DA2J101-TRB



K2/2F

K1/2F

10.39 PNLB ASSY

1

2

3

4

A

B

C

D

E

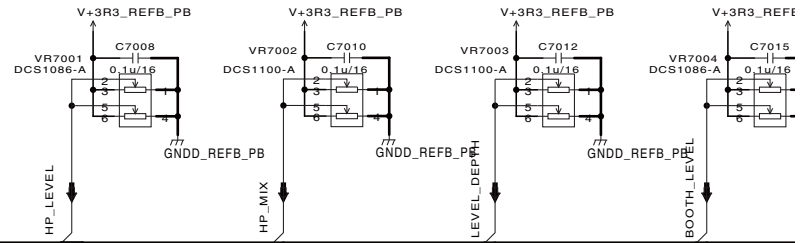
F

HP_LEVEL

HP_MIX

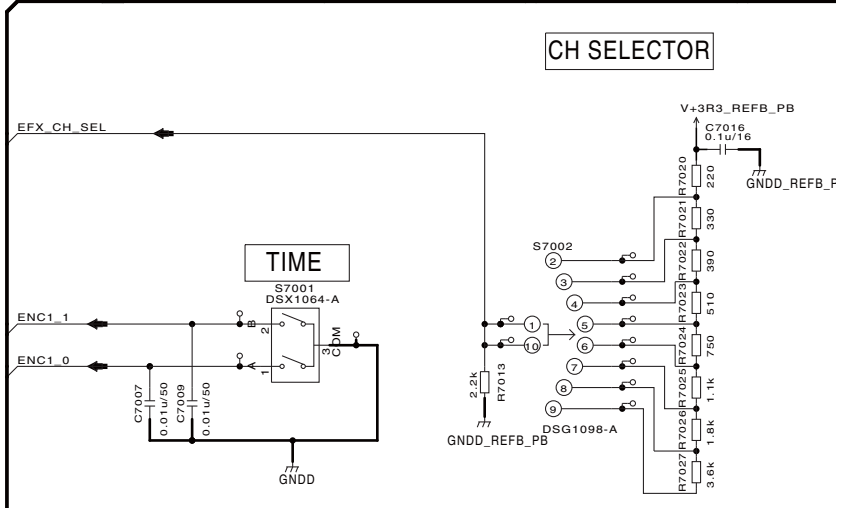
LEVEL DEPTH

BOOTH I

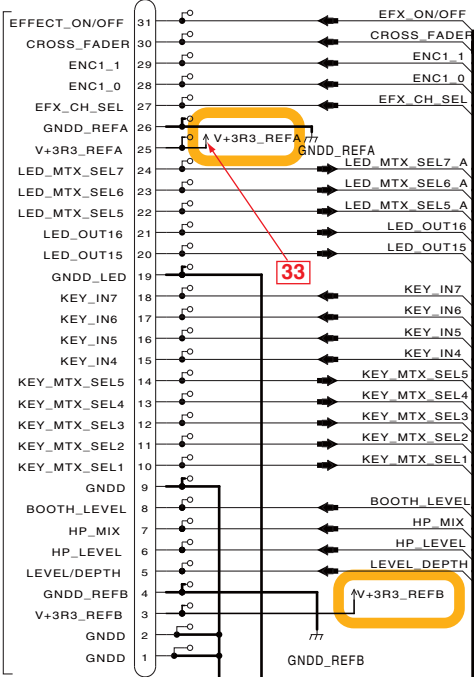


□ : Voltage measuring point

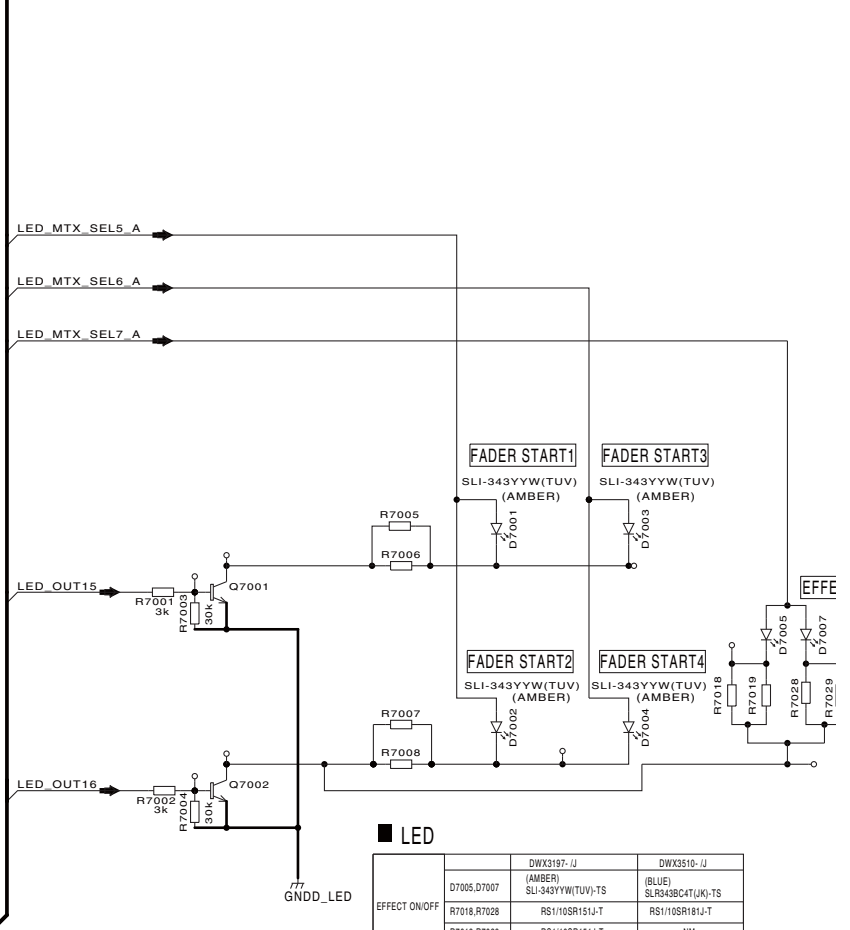
CH SELECTOR



9604S-31C



From To PNL A (CN6603) K 1/2F CN6603



LED

	DWX319T-IJ (AMBER)	DWX3510-IJ (BLUE)
EFFECT ON/OFF	D7005, D7007 R7018, R7028	SLI-343YYW(TUV)-TS RS110SR151J-T
FADER START	R7019, R7029 R7005, R7007 R7006, R7008	NM RS110SR151J-T RS110SR161J-T

NOTES
 - NM is STBY
 - RS110SR****J
 - CKSRYB
 - CEJQ***



100

DJM-900SRT

1

2

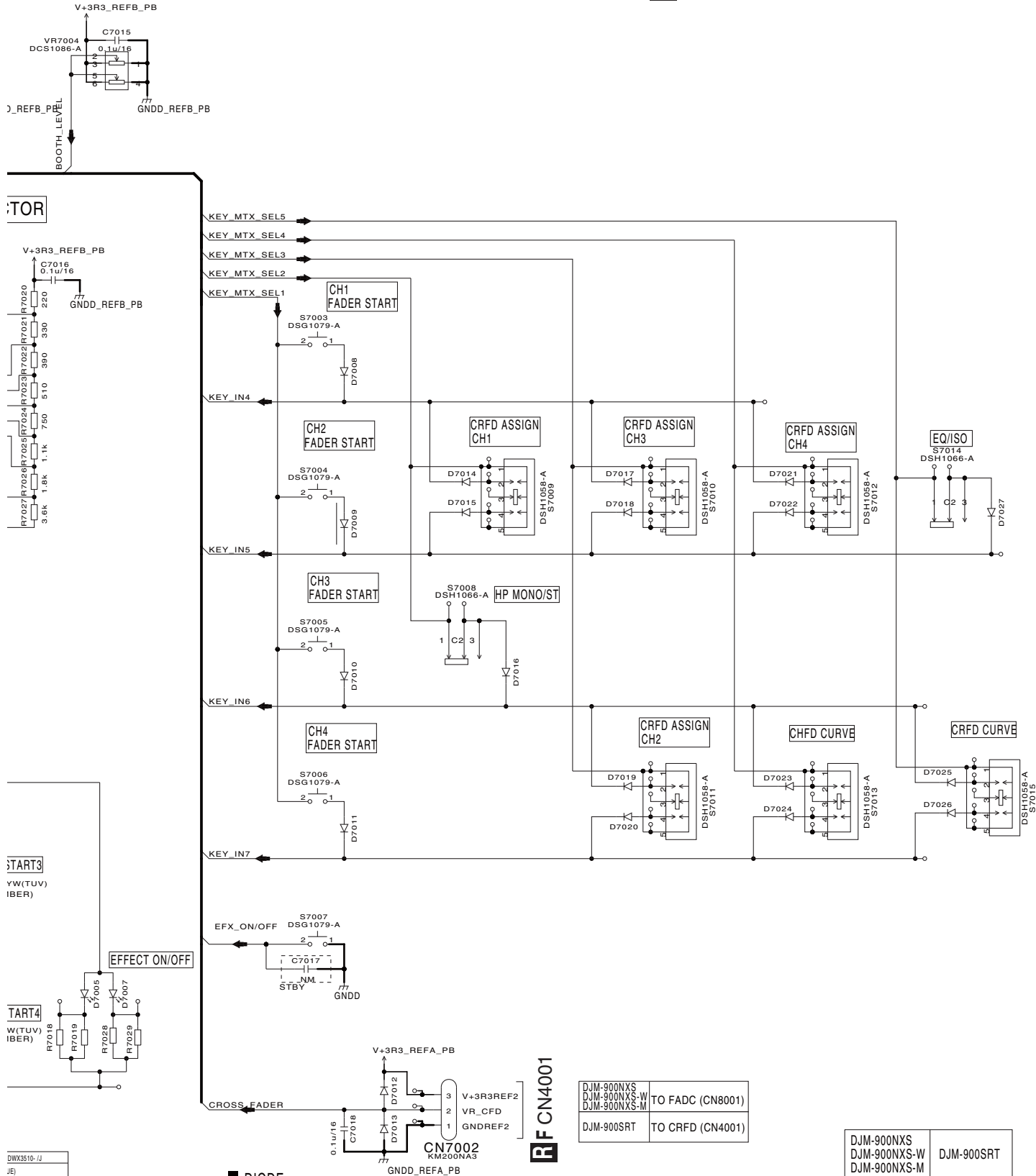
3

4

H BOOTH LEVEL

QF PNLB ASSY (DWX3510)

A
B
C
D
E
F



DWX3510-I/J
J3
349BC4T(JK)-TS
1110SR181J-T
NM
1110SR151J-T
1110SR181J-T

DIODE

DWX3197-I/J	DWX3510-I/J
D7012, D7013	1SS352-TRB
D7009 ~ D7011	NM
D7014 ~ D7027	1SS352-TRB
	DA2J101-TRB

TRANSISTOR

DWX3197-I/J	DWX3510-I/J
Q7001, Q7002	LSC4081UB(GRS)-TLB
	LSC4923UB-TLB

DJM-900NXS	TO FADC (CN8001)
DJM-900NXS-W	
DJM-900NXS-M	
DJM-900SRT	TO CRFD (CN4001)

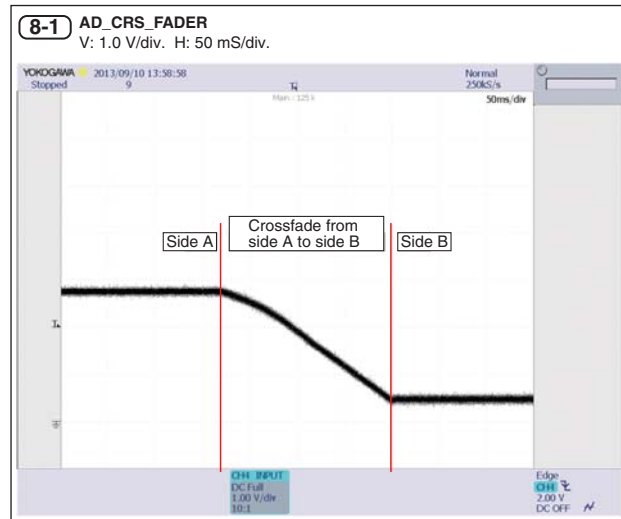
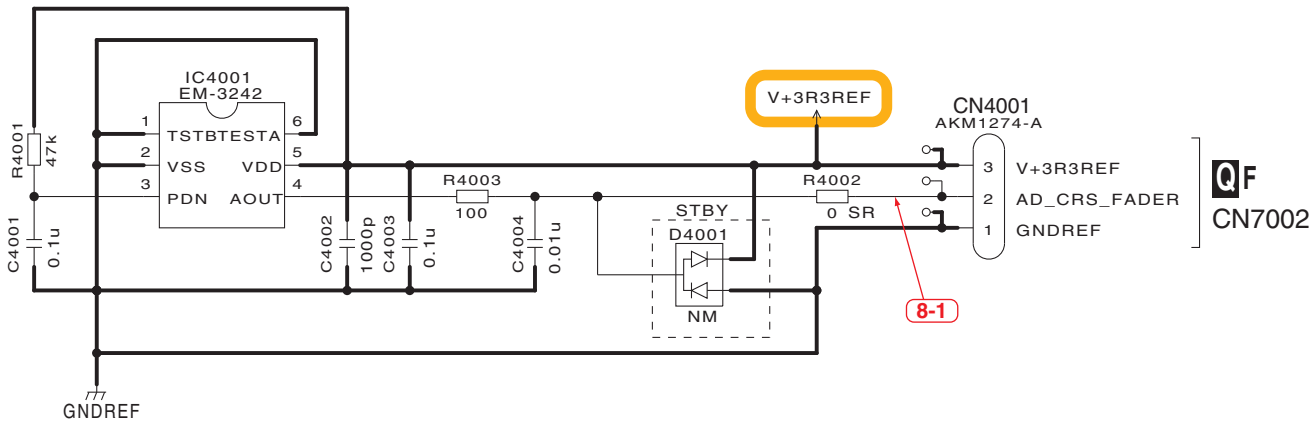
DJM-900NXS	DJM-900SRT
DJM-900NXS-W	
DJM-900NXS-M	
SUB	DWM2420-I/J
PNLB	DWX3197-I/J
PCB	DWX3510-I/J
	DNP2627-

DJM-900SRT



10.42 CRFD ASSY

RF CRFD ASSY (DWX3258)



Notes			
		NM1 is STBY	
		RS1/16SS***J	Ω
SR		RS1/10SR***J	Ω
		CKSSYB***K	F



5



6



7



8



A



B



C



D



E



F



5



6

DJM-900SRT



7

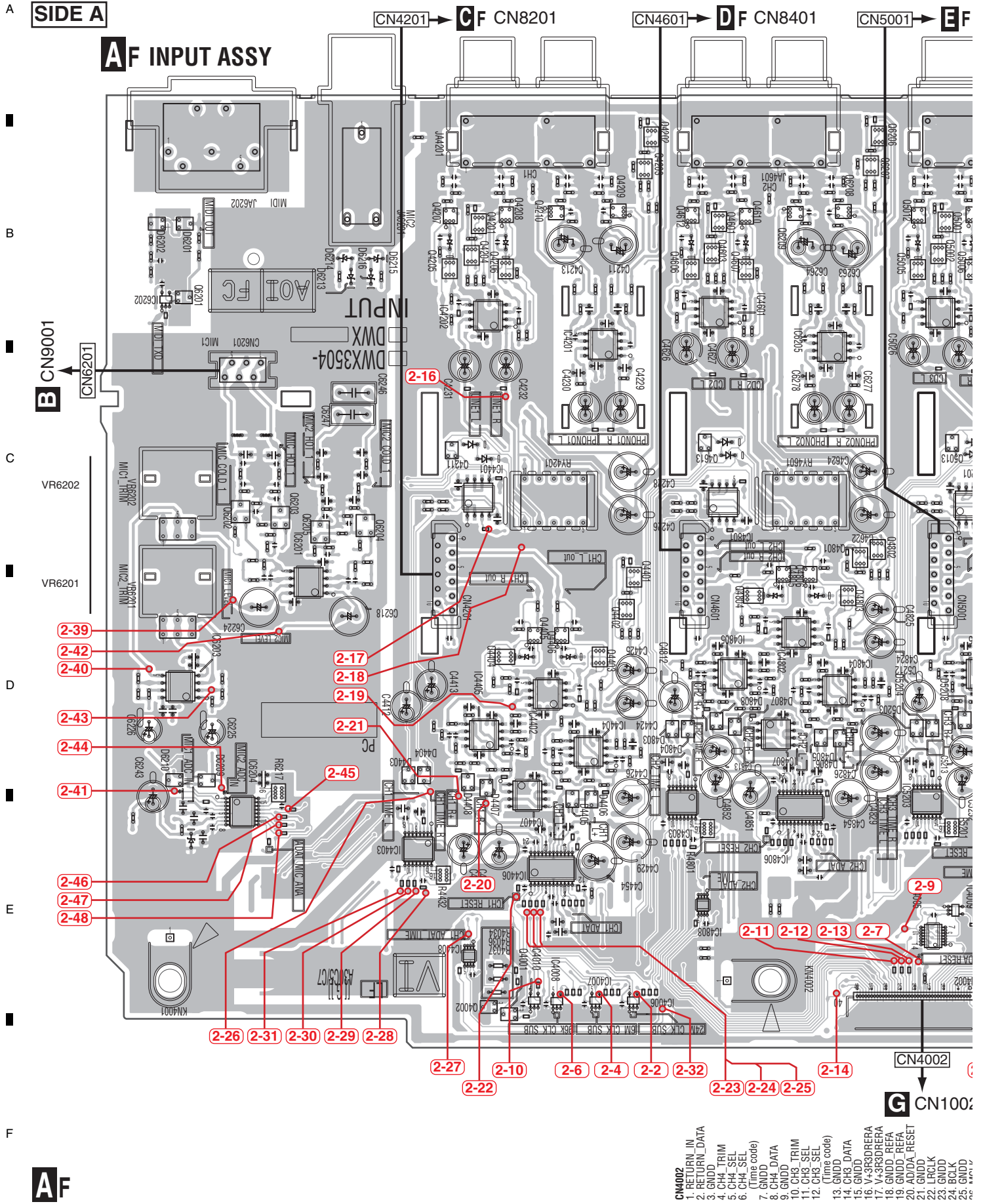


8



11. PCB CONNECTION DIAGRAM

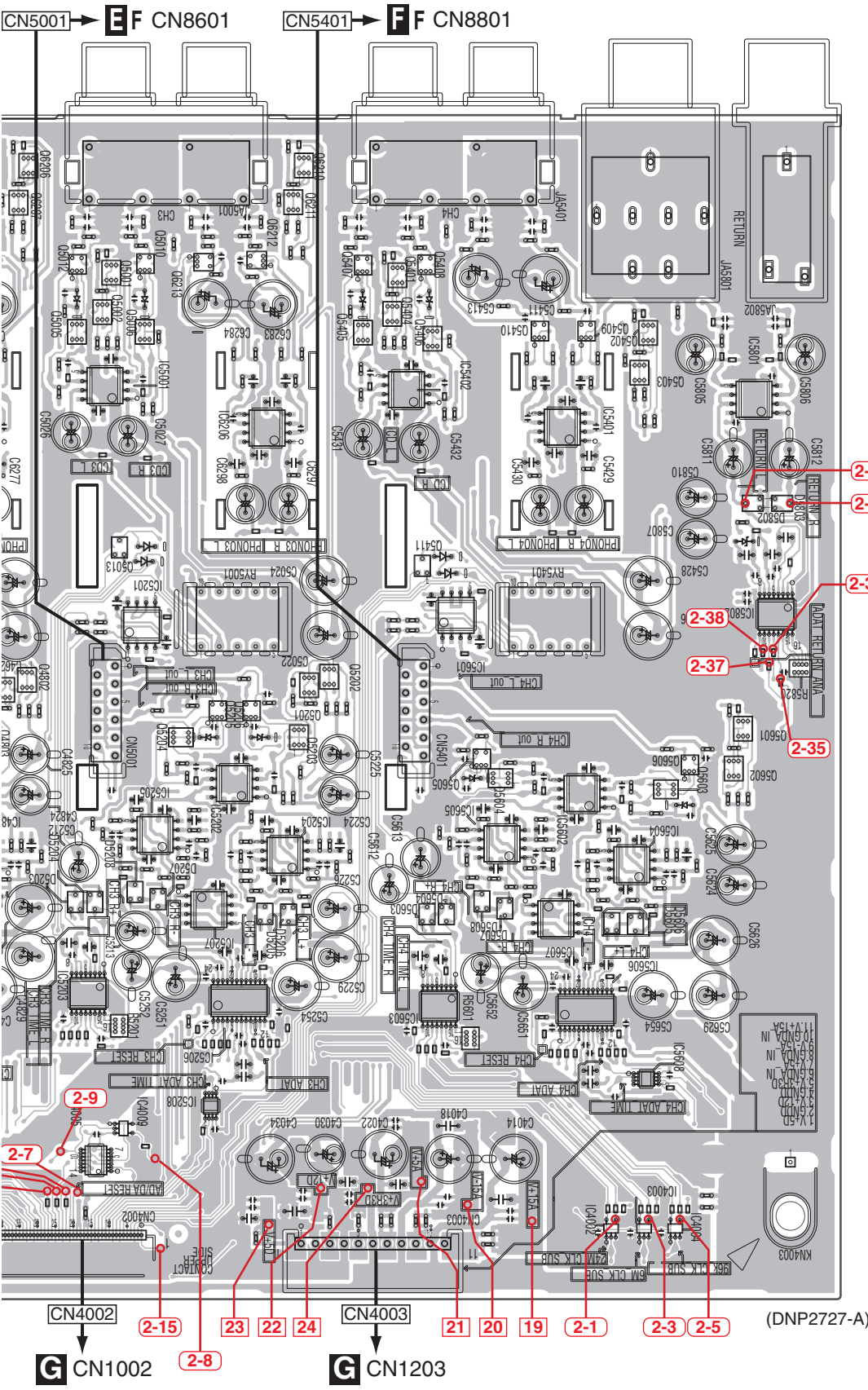
11.1 INPUT ASSY



AF

SIDE A

A



□ : Voltage measuring point
 ○ : Waveform measuring point

- Q4201
- Q4614
- Q4601
- Q5001
- Q5401
- Q4210
- Q4612
- Q5012
- Q5410
- Q6201
- IC6202
- IC4202
- IC4201
- IC4601
- IC4602
- IC5001
- IC5002
- IC5410
- IC5402
- IC5801
- IC5401
- Q4613
- Q5013
- IC4801
- IC5201
- Q4211
- Q5411
- IC4401
- Q6202
- Q6203
- Q4801
- Q5201
- IC5601
- IC5802
- Q6204
- IC6201
- Q6205
- Q4401
- Q4806
- Q5206
- Q5601
- IC4802
- IC5202
- Q5606
- IC5602
- Q4406
- IC4805
- IC5205
- IC5605
- IC6203
- IC4402
- IC4804
- IC5204
- IC5604
- IC4405
- IC4807
- IC5207
- IC5607
- IC4404
- IC4407
- IC4803
- IC4806
- IC5206
- IC5603
- IC5606
- IC4403
- IC4406
- IC5608
- IC5206
- IC4808
- IC4005
- IC4408
- IC4002
- IC4003
- IC4004
- IC4006-IC4010

B

C

D

E

F

- 16. V+3R3DRERA
- 17. V+3R3DRERA
- 18. GND0_REFA
- 19. GND0_REFA
- 20. AD/DA_RESET
- 21. GND0
- 22. LRCLK
- 23. GND0
- 24. BCLK
- 25. GND0
- 26. MCLK
- 27. GND0
- 28. CH2_DATA
- 29. CH2_SEL
- 30. CH2_SEL
- 31. GND0
- 32. CH2_DATA
- 33. GND0
- 34. CH1_TRIM
- 35. CH1_SEL
- 36. CH1_SEL
- 37. GND0
- 38. CH1_DATA
- 39. MIC_DATA
- 40. MIDI_TXD

DJM-900SRT

AF

SIDE B

A

AF INPUT ASSY

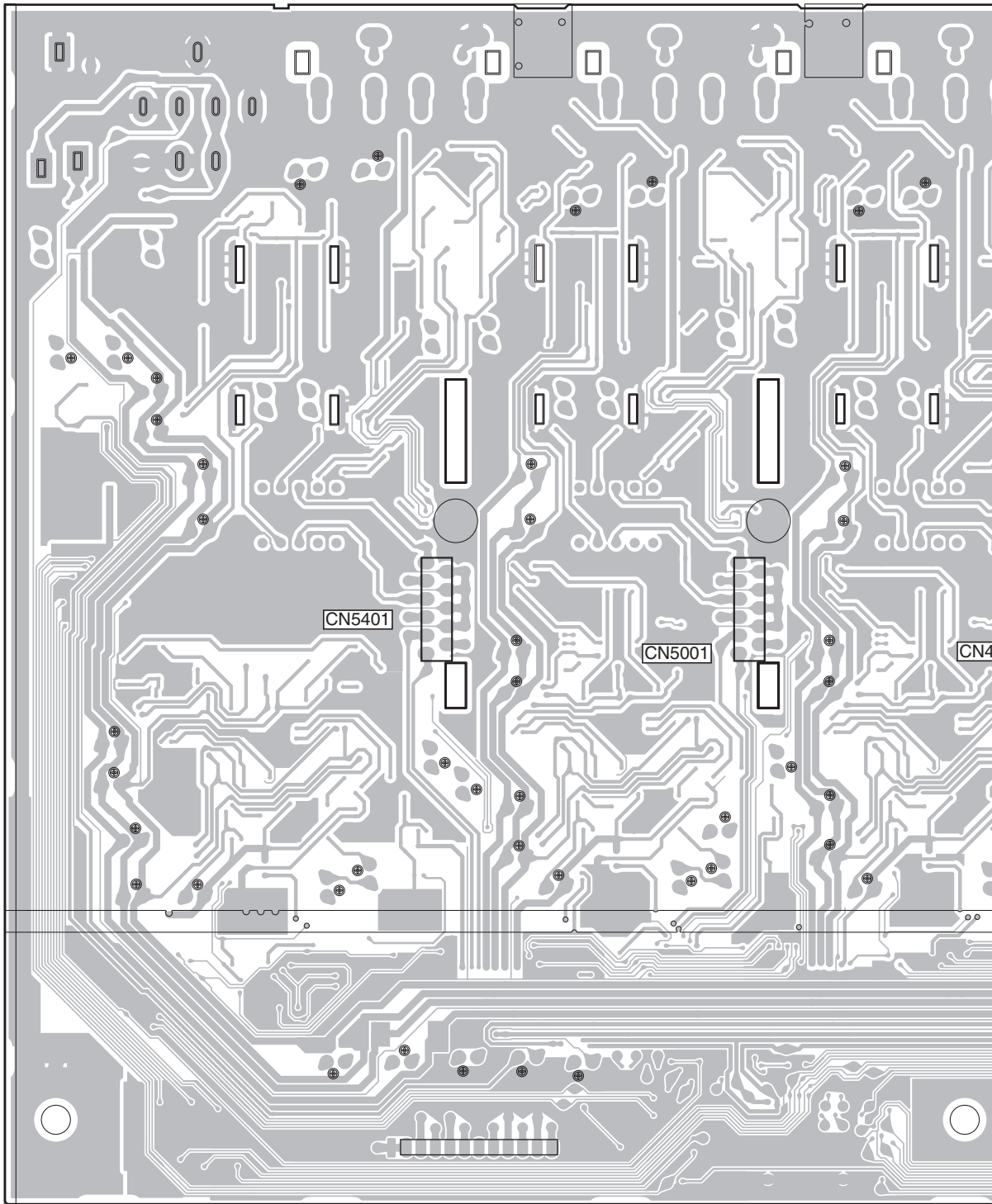
B

C

D

E

F



CN5401

CN5001

CN4

CN4003

AF

SIDE B

A

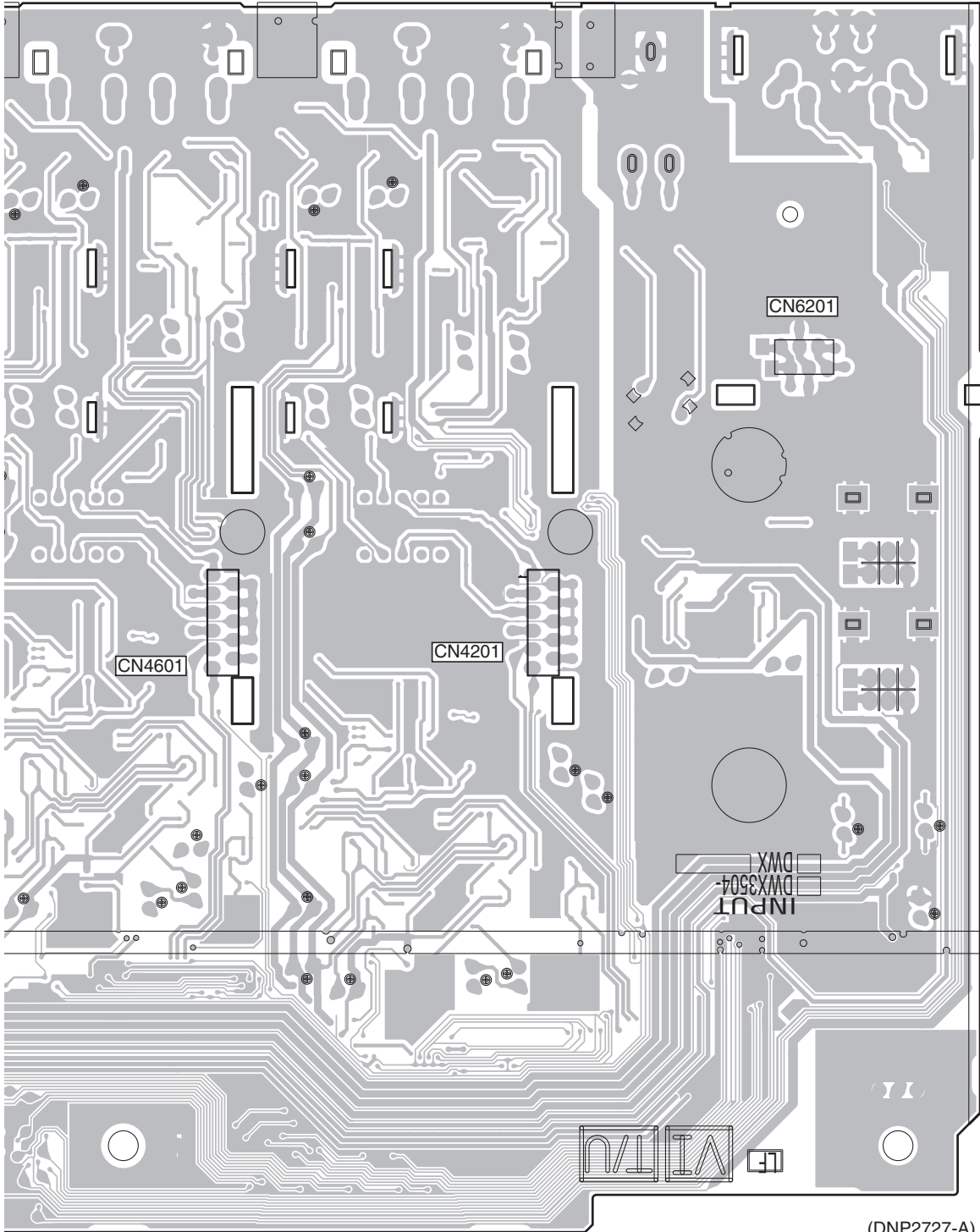
B

C

D

E

F



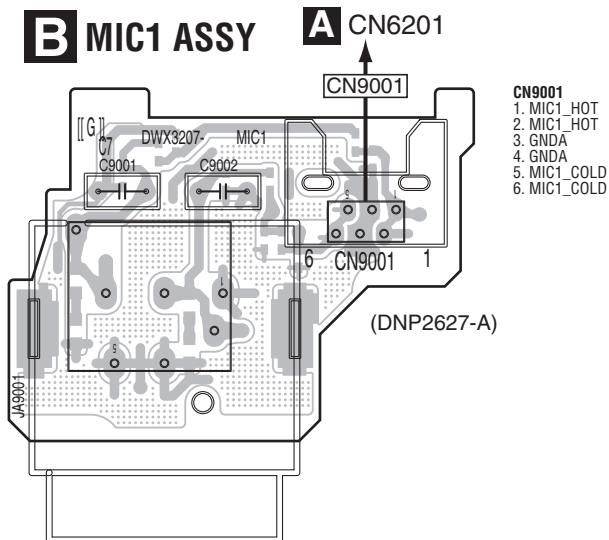
(DNP2727-A)

11.2 MIC1 and TRM1 to TRM4 ASSYS

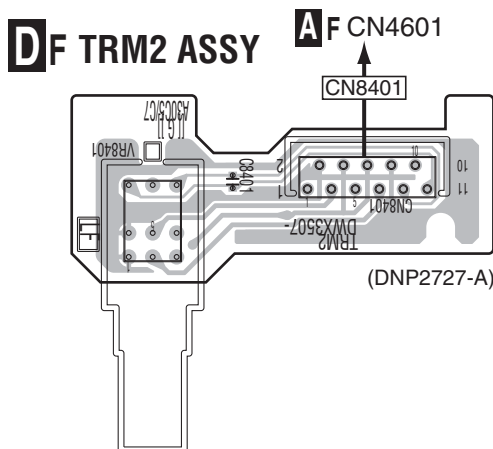
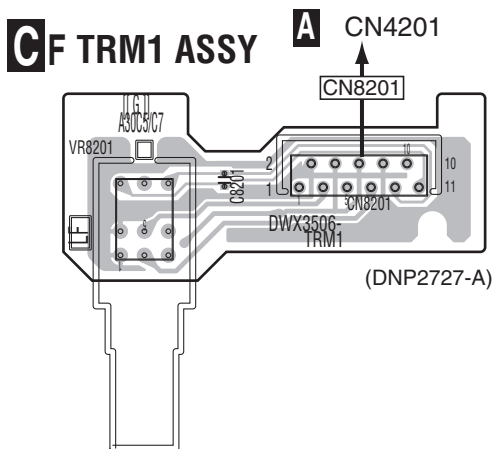
SIDE A

SIDE A

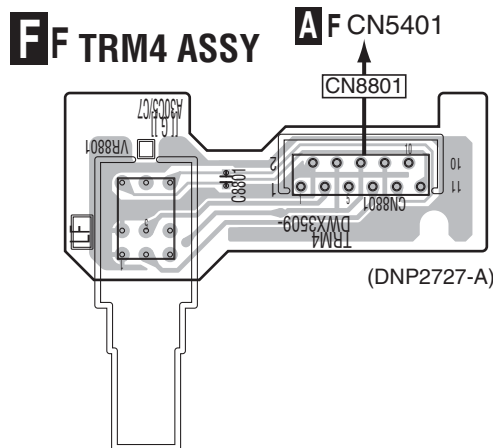
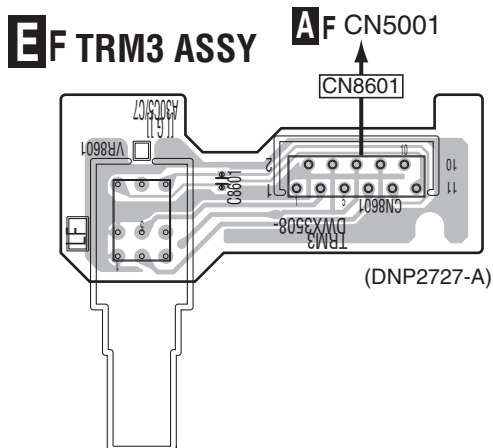
A



C



D



E

F

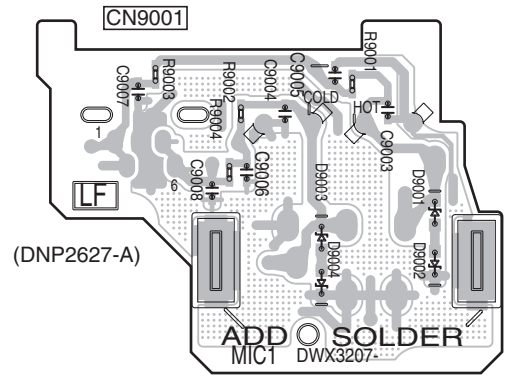
B C F D F E F F F

SIDE B

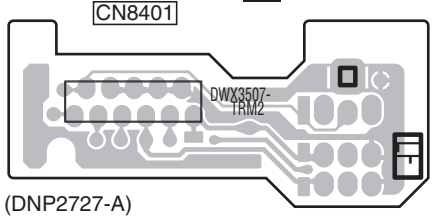
SIDE B

A
B
C
D
E
F

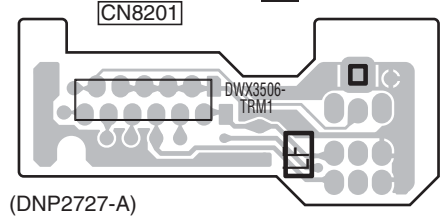
B MIC1 ASSY



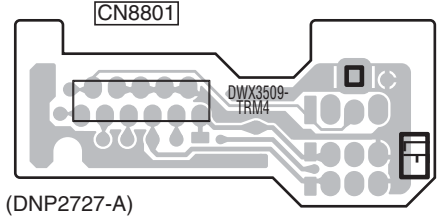
D F TRM2 ASSY



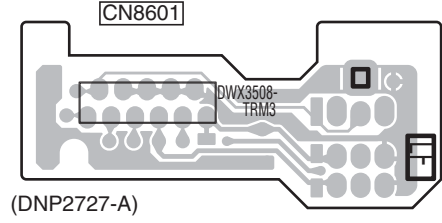
C F TRM1 ASSY



F F TRM4 ASSY



E F TRM3 ASSY



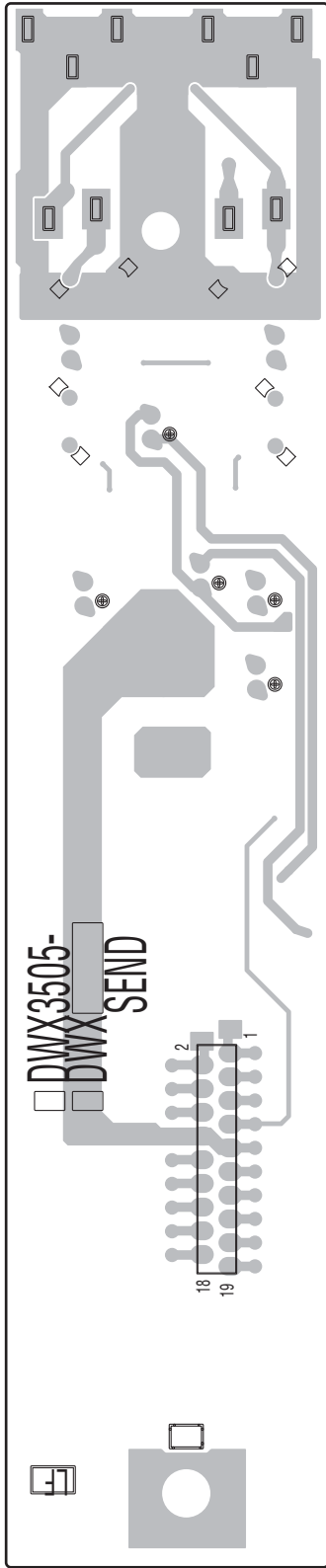
B C F D F E F F F

SIDE B

SIDE B

A

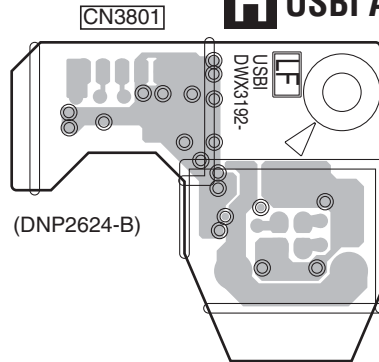
J F SEND ASSY



(DNP2727-A)

CN6401

H USBI ASSY



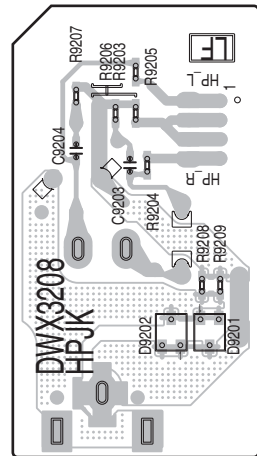
(DNP2624-B)

CN3801

B

C

I HPJK ASSY



(DNP2627-A)

CN9201

D

E

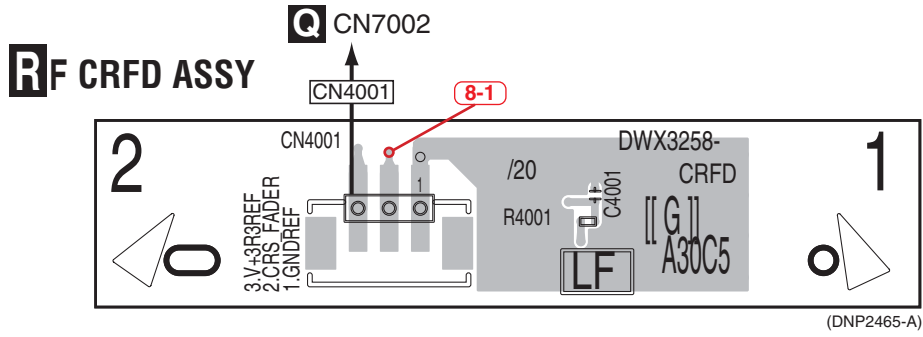
F

11.9 CRFD ASSY

SIDE A

SIDE A

A



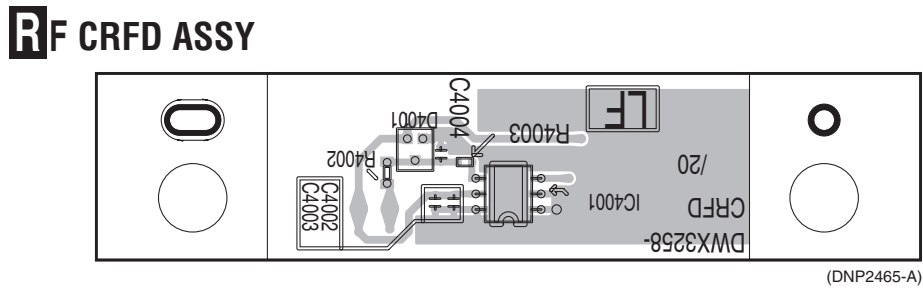
B

C

SIDE B

SIDE B

D



E

F

RF

RF

12. PCB PARTS LIST

NOTES: ● Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.

● The \triangle mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.

● When ordering resistors, first convert resistance values into code form as shown in the following examples.

Ex.1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47 k ohm (tolerance is shown by J = 5%, and K = 10%).

560 Ω \rightarrow 56 $\times 10^1$ \rightarrow 561 RD1/APU $\begin{matrix} 5 & 6 & 1 \\ \hline \end{matrix}$ J
 47 k Ω \rightarrow 47 $\times 10^3$ \rightarrow 473 RD1/APU $\begin{matrix} 4 & 7 & 3 \\ \hline \end{matrix}$ J
 0.5 Ω \rightarrow R50 RN2H $\begin{matrix} R & 5 & 0 \\ \hline \end{matrix}$ K
 1 Ω \rightarrow 1R0 RS1P $\begin{matrix} 1 & R & 0 \\ \hline \end{matrix}$ K

Ex.2 When there are 3 effective digits (such as in high precision metal film resistors).

5.62 k Ω \rightarrow 562 $\times 10^1$ \rightarrow 5621 RN1/APC $\begin{matrix} 5 & 6 & 2 & 1 \\ \hline \end{matrix}$ F

● Meaning of the figures and others in the parentheses in the parts list.

Example) IC 301 is on the point (face A, 91 of x-axis, and 111 of y-axis) of the corresponding PC board.

IC 301 (A, 91, 111) IC NJM2068V

Mark	No.	Description	Part No.	Mark	No.	Description	Part No.
LIST OF ASSEMBLIES							
NSP	1..	MOTHER ASSY	DWM2511		2..	FAD1 ASSY	DWX3198
	2..	MAIN ASSY	DWX3502		2..	FAD2 ASSY	DWX3199
	2..	GDCB ASSY	DWX3191		2..	FAD3 ASSY	DWX3200
	2..	USB1 ASSY	DWX3192		2..	FAD4 ASSY	DWX3201
					2..	MIC1 ASSY	DWX3207
					2..	HPJK ASSY	DWX3208
NSP	1..	AUDIO ASSY	DWM2512		1..	PNLA ASSY	DWX3503
	2..	INPUT ASSY	DWX3504				
	2..	SEND ASSY	DWX3505				
	2..	TRM1 ASSY	DWX3506	\triangle	1..	POWER SUPPLY ASSY	DWR1492
	2..	TRM2 ASSY	DWX3507				
	2..	TRM3 ASSY	DWX3508	NSP	1..	CROSS FADER ASSY	DXA2257
	2..	TRM4 ASSY	DWX3509		2..	CRFD ASSY	DWX3258
NSP	1..	SUB ASSY	DWM2420				
	2..	ACSW ASSY	DWR1490				
	2..	PNLB ASSY	DWX3510				

DJM-900NXS/SYXJ8, DJM-900SRT/LSYXJ8, DJM-900SRT/UXJCB and DJM-900SRT/XJCN5 are constructed the same except for the following:

Mark	Symbol and Description	DJM-900NXS /SYXJ8	DJM-900SRT /LSYXJ8	DJM-900SRT /UXJCB	DJM-900SRT /XJCN5	Remarks
NSP	1..AUDIO ASSY	DWM2419	DWM2512	DWM2512	DWM2512	
	2..INPUT Assy	DWX3193	DWX3504	DWX3504	DWX3504	
	2..TRM1 Assy	DWX3203	DWX3506	DWX3506	DWX3506	
	2..TRM2 Assy	DWX3204	DWX3507	DWX3507	DWX3507	
	2..TRM3 Assy	DWX3205	DWX3508	DWX3508	DWX3508	
	2..TRM4 Assy	DWX3206	DWX3509	DWX3509	DWX3509	
	2..SEND Assy	DWX3195	DWX3505	DWX3505	DWX3505	
NSP	P.C.BOARD(AUDIO)	DNP2625	Not used	Not used	Not used	
NSP	COMPOSITE PCB	Not used	DNP2727	DNP2727	DNP2727	
NSP	1..MOTHER ASSY	DWM2418	DWM2511	DWM2511	DWM2511	
	2..MAIN Assy	DWX3190	DWX3502	DWX3502	DWX3502	
NSP	1..SUB ASSY	DWM2420	DWM2513	DWM2513	DWM2513	
	2..FADC Assy	DWX3202	Not used	Not used	Not used	
	2..PNLB Assy	DWX3197	DWX3510	DWX3510	DWX3510	
NSP	1..CROSS FADER ASSY	Not used	DXA2257	DXA2257	DXA2257	
	2..CRFD ASSY	Not used	DWX3258	DWX3258	DWX3258	
	1..PNLA ASSY	DWX3196	DWX3503	DWX3503	DWX3503	

CONTRAST OF PCB ASSEMBLIES**C F TRM1 ASSY**

DWX3203 and DWX3506 are constructed the same except for the following:

Mark	Symbol and Description	DWX3203	DWX3506	Remarks
	VR8201 Rotary VR	DCS1119	DCS1118	

D F TRM2 ASSY

DWX3204 and DWX3507 are constructed the same except for the following:

Mark	Symbol and Description	DWX3204	DWX3507	Remarks
	VR8401 Rotary VR	DCS1119	DCS1118	

E F TRM3 ASSY

DWX3205 and DWX3508 are constructed the same except for the following:

Mark	Symbol and Description	DWX3205	DWX3508	Remarks
	VR8601 Rotary VR	DCS1119	DCS1118	

F F TRM4 ASSY

DWX3206 and DWX3509 are constructed the same except for the following:

Mark	Symbol and Description	DWX3206	DWX3509	Remarks
	VR8801 Rotary VR	DCS1119	DCS1118	

G F MAIN ASSY

DWX3190 and DWX3502 are constructed the same except for the following:

Mark	Symbol and Description	DWX3190	DWX3502	Remarks
NSP	IC502	DYW1798	DYW1834	
	IC702	MD56V82160-6TAZ	M12L2561616A-5TG2A	
	IC703	DYW1797	DYW1833	
	IC2011	DYW1799	DYW1835	
	IC2801, IC3003	RNB4580F	BA4580RF	
	Q1408, Q2602	LSC4081UB	LSCR523UB	
	Q1409, Q2603, Q3411, Q3412	LSA1576UB	LSAR523UB	
	Q2838, Q2839	LSA1576UB	Not used	
	Q2836	LTC124EUB	Not used	
	Q2837	2SC3326	INC2002AC1	
	Q3405, Q3407	2SC3076	2SD1760F5	
	Q3406, Q3408	2SA1241	2SB1184F5	
	D1201, D1402, D1414	1SS301	DAN202UM	
	D1202, D1203	RB501V-40	RB501VM-40	
	D1401, D1415, D2807-D2824	1SS352	DA2J101	
	D1406	RKZ3.3KG(B2)	DZ2J033M0	
	D2801	HZU5R1(B1)	DZ2J051M0	
	D2803-D2806	HZU3R0(B1)	DZ2J030M0	
	D2833, D2834, D3401-D3405	1SS352	DA2J101	
	R1208	RS1/16SS2202F	RS1/16SS273J	
	R1410	RS1/16SS912J	RS1/16SS392J	
	R1421	RS1/16SS6201F	RS1/16SS822J	
	R1401	RS1/16SS153J	RS1/16SS1502F	
	R1402	RS1/16SS123J	RS1/16SS1202F	
	R1406	RS1/16SS303J	RS1/16SS223J	
	R2884-R2887	Not used	RS1/10SR0R0J	
	R2888-R2891	RN1/16SE3902D	Not used	

Mark	Symbol and Description	DWX3190	DWX3502	Remarks
	R2987, R2993 R2988, R2989, R2991, R2992 R2994-R2997	RS1/16SS103J RS1/16SS104J RS1/16SS104J	Not used Not used Not used	
	R2990, R2998-R3000 C2808, C2809 C2827, C2828 C2861, C2863, C2865, C2867 C2862, C2864, C2866, C2868	RS1/16SS473J CEHVAW101M16 CEVW101M16 CKSRYB473K50 CEHVAW471M16	Not used CEHVAW221M6R3 CEHVAW221M6R3 Not used Not used	
	C2878, C2879 C2880 C2885 C3025, C3027	CCG1236 CEHANP101M16 Not used CCG1195	Not used Not used CCSSCH102J50 CCG1236	

JF SEND ASSY

DWX3195 and DWX3505 are constructed the same except for the following:

Mark	Symbol and Description	DWX3195	DWX3505	Remarks
	IC6402	RNB4580F	NJM4565MD	

KF PNL A ASSY

DWX3196 and DWX3503 are constructed the same except for the following:

Mark	Symbol and Description	DWX3196	DWX3503	Remarks
	IC6601 Q6601, Q6604, Q6638-Q6649 Q6652, Q6654 Q6605-Q6621 D6615, D6616, D6618-D6623	DYW1800 LSA1576UB LSA1576UB LSC4081UB 1SS352	DYW1836 LSAR523UB LSAR523UB LSCR523UB Not used	
	D6742-D6763 D6632-D6640, D6649-D6657 D6665-D6673, D6684-D6692 D6702-D6710, D6717-D6725 D6626, D6627, D6643, D6644	1SS352 SLR-343MC(NPQ) SLR-343MC(NPQ) SLR-343MC(NPQ) SLR-343VC(NPQ)	DA2J101 SLI-343M8G(GHJ) SLI-343M8G(GHJ) SLI-343M8G(GHJ) SLI-343U8R(HJK)	
	D6659, D6660, D6678, D6679 D6696, D6697, D6711, D6712 D6625, D6641, D6642, D6658 D6674-D6677, D6693-D6695 D6734	SLR-343VC(NPQ) SLR-343VC(NPQ) SLI-343YYW(TUV) SLI-343YYW(TUV) SLI-343YYW(TUV)	SLI-343U8R(HJK) SLI-343U8R(HJK) SLR343BC4T(JK) SLR343BC4T(JK) SLR343BC4T(JK)	
	R6748, R6749 R6750, R6751 R6755-R6763 R6769, R6770, R6776, R6779 R6784, R6785, R6788, R6789	RS1/10SR102J RS1/10SR750J RS1/10SR680J RS1/10SR820J RS1/10SR820J	Not used RS1/10SR102J RS1/10SR391J RS1/10SR151J RS1/10SR151J	
	R6796, R6798, R6799 R6808 R6809 R6815 R6818	RS1/10SR820J Not used RS1/10SR470J RS1/10SR680J Not used	RS1/10SR151J RS1/10SR680J RS1/10SR151J RS1/10SR121J RS1/10SR151J	
	R6828	RS1/10SR680J	RS1/10SR151J	

QF PNL B ASSY

DWX3197 and DWX3510 are constructed the same except for the following:

Mark	Symbol and Description	DWX3197	DWX3510	Remarks
	Q7001, Q7002 D7008-D7011, D7014-D7027 D7012, D7013	LSC4081UB 1SS352 1SS352	LSCR523UB DA2J101 Not used	

Mark	Symbol and Description	DWX3197	DWX3510	Remarks
A	D7005, D7007 R7005, R7007	SLI-343YYW(TUV) Not used	SLR343BC4T(JK) RS1/10SR151J	
	R7006, R7008 R7018, R7028 R7019, R7029	RS1/10SR820J RS1/10SR151J RS1/10SR151J	RS1/10SR181J RS1/10SR181J Not used	

PCB PARTS LIST

Mark	No.	Description	Part No.	Mark	No.	Description	Part No.	
A F INPUT ASSY								
SEMICONDUCTORS								
B	IC	4002-4004,4006-4010	TC7SH08FUS1	JA	6201	6.5 DIA JACK	DKN1653	
	IC	4005	TC74VHC74FK	JA	6202	CONNECTOR	DKN1188	
	IC	4201,4202,4402,4404	NJM4580MD	KN	4001-4003	EARTH TERMINAL	AKF7002	
	IC	4401,4407,4801,4807	NJM4565MD	VR	6201,6202	ROTARY VR	DCS1072	
	IC	4403,4803,5203,5603	AK5358AET	RY	4201,4601,5001,5401	RELAY	VSR1008	
	IC	4405,4601,4802,4804	NJM4580MD	CN	4002	FFC CONNECTOR	VKN1811	
	IC	4406,4806,5206,5606	CS5381-KZ	CN	4003	L-PLUG(11P)	KM200NA11L	
	IC	4408,4808,5208,5608	TC7WH157FK	CN	4201,4601,5001,5401	11P PLUG	XKP3065	
	IC	4805,5001,5202,5204	NJM4580MD	CN	6201	CONNECTOR	CKS3561	
	IC	5201,5207,5601,5607	NJM4565MD	0		SHIELD CASE(MIDI)	DNH2736	
C	IC	5205,5401,5402,5602	NJM4580MD	RESISTORS				
	IC	5604,5605,6201,6203	NJM4580MD	R	4034,4036,4037		RS1/4SA130J	
	IC	5801	NJM4565MD	R	4202,4204,4216,4217		RN1/16SE1000D	
	IC	5802,6204	AK5358AET	R	4206,4406,4607,4806		RN1/16SE4701D	
	IC	6202	TC7S04FU	R	4207,4407,4608,4807		RN1/16SE4301D	
	IC	6205,6206	NJM4580MD	R	4208,4211,4213,4215		RN1/16SE4700D	
	Q	4001,4002	LSC4081UB	R	4209,4210,5409,5410		RN1/16SE2201D	
	Q	4201-4206,4401-4404	KTC801U	R	4212,4214,4220,4221		RN1/16SE4702D	
	Q	4207-4210,4405,4406	2SK3320	R	4218,4219,4408,4411		RN1/16SE4700D	
	Q	4211,4613,5013,5411	LTC124EUB	R	4222,4223,4623,4625		RS1/10SR1803D	
D	Q	4601,4603,4606,4607	KTC801U	R	4224,4225,4424-4431		RN1/16SE1201D	
	Q	4610,4612,4805,4806	2SK3320	R	4228,4229,5428,5429		RS1/10SR470J	
	Q	4801-4804,5001,5002	KTC801U	R	4230,4231,5430,5431		RS1/10SR75R0D	
	Q	5005,5006,5201-5204	KTC801U	R	4232,4233,4236,4239		RN1/16SE1001D	
	Q	5010,5012,5205,5206	2SK3320	R	4234,4235,4237,4238		RN1/16SE2001D	
	Q	5401-5406,5601-5604	KTC801U	R	4240,4241,5440,5441		RS1/10SR7501D	
	Q	5407-5410,5605,5606	2SK3320	R	4248-4251,4600,4601		RS1/10SR0R0J	
	Q	6201	LTC124EUB	R	4401,4433,4832,4878		RN1/16SE1202D	
	Q	6202-6205	2SK209	R	4402,4419-4422,4434		RN1/16SE2001D	
	Q	6206,6207,6210,6211	KTC801U	R	4409,4410,4809,4810		RN1/16SE2002D	
E	Q	6208,6209,6212,6213	2SK3320	R	4412,4414,4812,4814		RN1/16SE1303D	
	D	4201,4202,4401,4402	DZ2J030M0	R	4413,4609,4615,4617		RN1/16SE4700D	
	D	4203,4204,4605,4606	DA2J101	R	4415,4416,4815,4816		RS1/10SR3303D	
	D	4403,4404,4803,4804	RB706F-40	R	4432,4801,5201,5601		RAB4CQ103J	
	D	4405-4408,4805-4808	1SS302	R	4439,4441,4443,4445		RN1/16SE2200D	
	D	4602,4604,4801,4802	DZ2J030M0	R	4440,4442,4444,4446		RN1/16SE4300D	
	D	5002,5004,5201,5202	DZ2J030M0	R	4448-4451,4848-4851		RN1/16SE6800D	
	D	5005,5006,5403,5404	DA2J101	R	4452,4455,4456,4459		RN1/16SE3300D	
	D	5203,5204,5603,5604	RB706F-40	R	4453,4454,4457,4458		RN1/16SE1001D	
	D	5205-5208,5605-5608	1SS302	R	4475,4875,5275,5675		RS1/10SR5R1J	
F	D	5401,5402,5601,5602	DZ2J030M0	R	4612,4613,5012,5013		RN1/16SE1000D	
	D	5802,5803,6209,6210	RB706F-40	R	4620,4621,5020,5021		RN1/16SE4702D	
	D	6201,6202	1SS302	R	4631,4633,4635,4637		RN1/16SE1001D	
	D	6213-6216	UDZS15(B)	R	4808,4811,4813,5009		RN1/16SE4700D	
	MISCELLANEOUS				R	4819-4822,4833,4879		RN1/16SE2001D
	JA	4201,4601,5001,5401 JACK	DKB1083	R	4824-4831,5224-5231		RN1/16SE1201D	
	JA	5801 6.5 DIA JACK	DKN1654	R	4839,4841,4843,4845		RN1/16SE2200D	
	JA	5802 6.5 DIA JACK	DKN1652	R	4840,4842,4844,4846		RN1/16SE4300D	
				R	4852,4855,4856,4859		RN1/16SE3300D	
				R	4853,4854,4857,4858		RN1/16SE1001D	

Mark	No.	Description	Part No.
A	C	5409,5410,5615,5616	CKSSYB474K6R3
	C	5412,5414,5617,5618	CKSSYB472K50
	C	5423,5427,5605,5607	CKSRYP104K25
■	C	5426,5428,5626,5629	CEHAZL101M25
	C	5622,5623,5630-5633	CKSRYP104K25
	C	5634-5637,5803,5804	CCSSCH102J50
	C	5646,5653,5658,5815	CKSSYB104K16
	C	5647,5648,5655,5666	CKSSYB103K16
	■	C	5656,6222,6223,6238
C		5801,5802	CCSSCH101J50
C		5807,5810	CEAT470M25
C		5808,5809,6208,6219	CKSSYB103K25
C		5811,5812	CEHAT100M50
B	C	5813,5814,5817,6227	DCH1201
	C	5816,5818,6217,6228	CKSSYB104K16
	C	5819	CKSSYB102K50
	C	5820,5821	CKSSYB103K16
	C	6214,6254,6267,6268	CKSSYB471K50
	■	C	6216,6221,6255,6258
C		6218,6224	CEJQNP100M35
C		6220,6257	CKSSYB331K50
C		6225,6226	CEJQ100M35
C		6229,6231	DCH1201
C	C	6230,6232	CKSSYB104K16
	C	6233,6256	CKSSYB103K25
	C	6240	CKSRYP104K25
	C	6242	CKSRYP103K25
	C	6246,6247	CFTLA103J50
■	C	6248-6253	CCSRCH102J50
	C	6259,6260,6279,6280	CCSSCH221J50
	C	6261,6262,6269,6270	CCSSCH331J50
	C	6263,6264,6283,6284	CEHAT471M6R3
	C	6265,6266,6285,6286	CKSRYP223K50
D	C	6271,6272,6291,6292	CFHXSQ103J16
	C	6277,6278,6297,6298	CEANP100M35
	C	6281,6282,6289,6290	CCSSCH331J50
	C	6287,6288	CKSSYB471K50
	C	6299	CKSRYP102K50

RF CRFD ASSY

* When replacement of the CRFD Assy is required, be sure to order the CROSS FADER Assy (DXA2257) which is a parent Assy.