

- Note on Schematic Diagram:**
- All capacitors are in μF unless otherwise noted. pF ; μF 50 WV or less are not indicated except for electrolytics and tantalums.
 - All resistors are in Ω and $1/4$ W or less unless otherwise specified.
 - Δ : internal component.
 - \square : panel designation.
 - $\text{B}+$: B+ Line.
 - \square : adjustment for repair.
 - Power voltage is dc 14.4 V and fed with regulated dc power supply from battery and accessory terminals.
 - Voltagess and waveforms are dc with respect to ground under no-signal (detuned) conditions.
 - no mark : FM
 - () : MW (LW)
 - [] : TAPE PLAYBACK
 - * : Impossible to measure
- Voltagess are taken with a VOM (Input impedance 10 M Ω). Voltage variations may be noted due to normal production tolerances.
 - Waveforms are taken with an oscilloscope. Voltage variations may be noted due to normal production tolerances.
 - Circled numbers refer to waveforms.
 - Signal path.
 - \rightarrow : FM
 - \rightarrow : MW (LW)
 - \rightarrow : TAPE PLAYBACK
 - \rightarrow : BUS AUDIO IN

6-5. SCHEMATIC DIAGRAM - MAIN Section - See page 41 for IC Block Diagrams, See page 43 for Waveforms.

