



PRIMARE



CD21 MKII Service Manual

1. Schematics and Pcb.

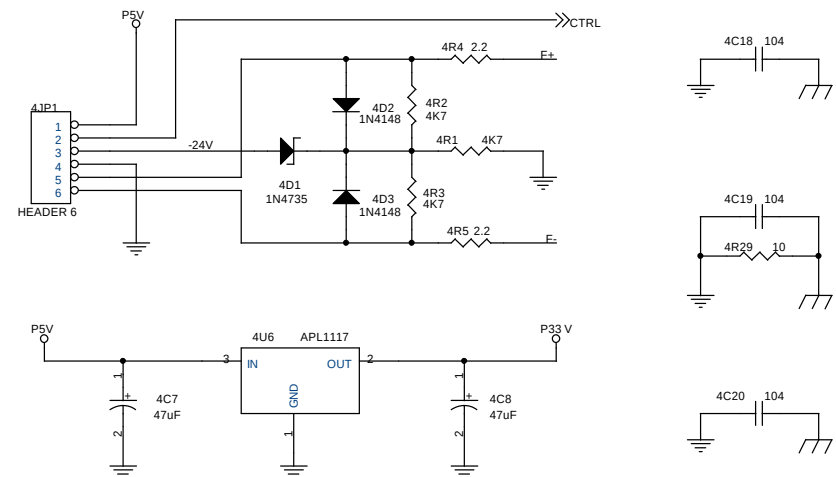
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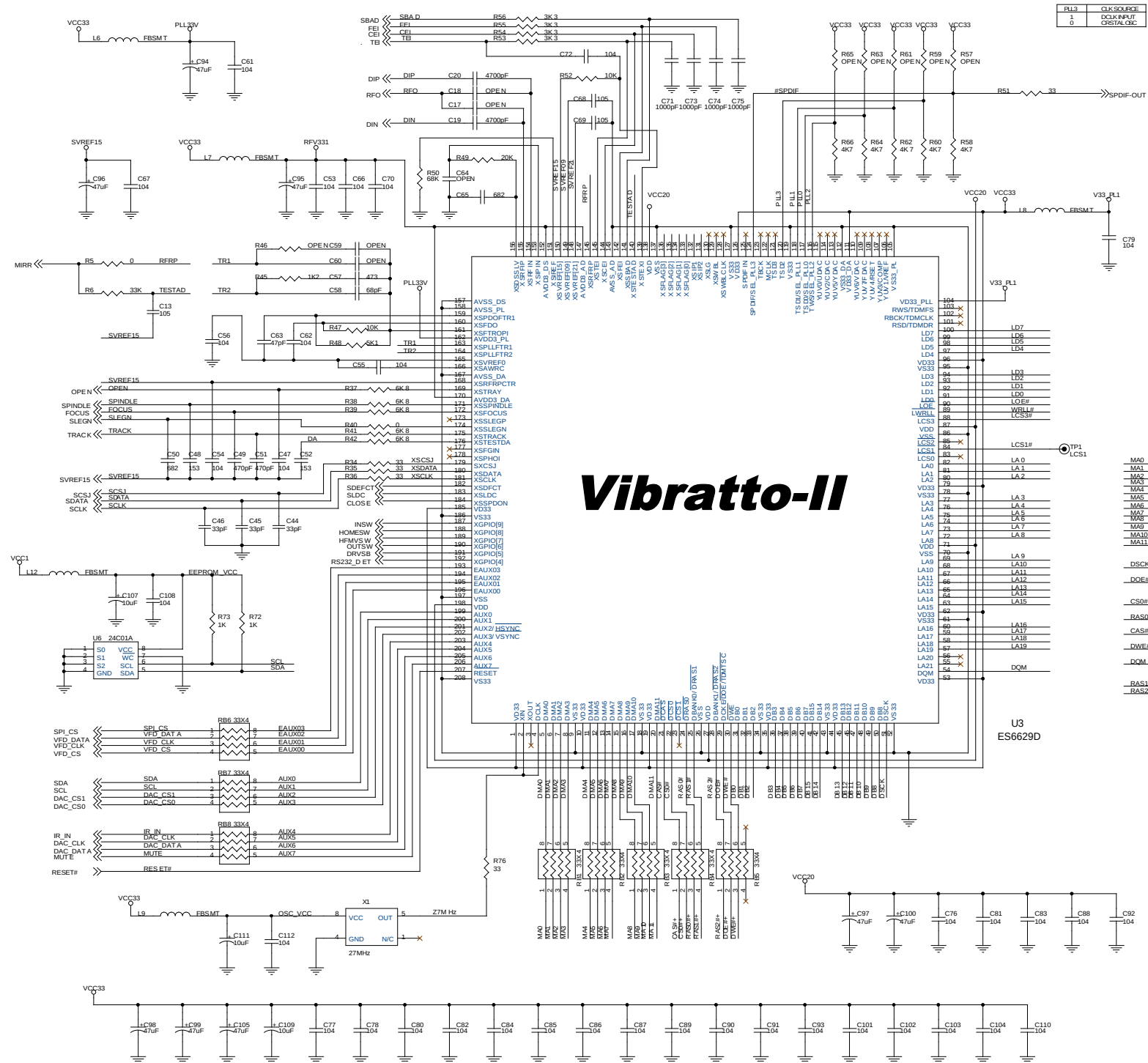
Author:

Bjorn Holmqvist
Primare AB



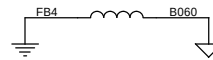
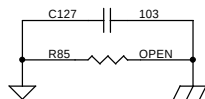
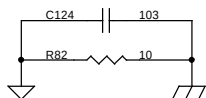
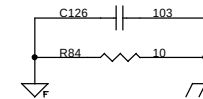
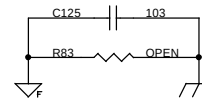
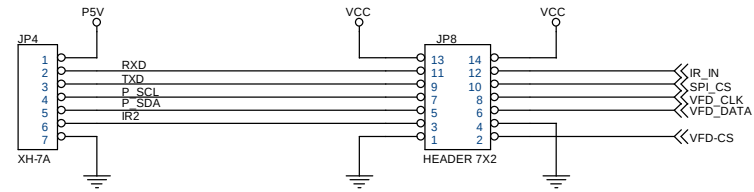
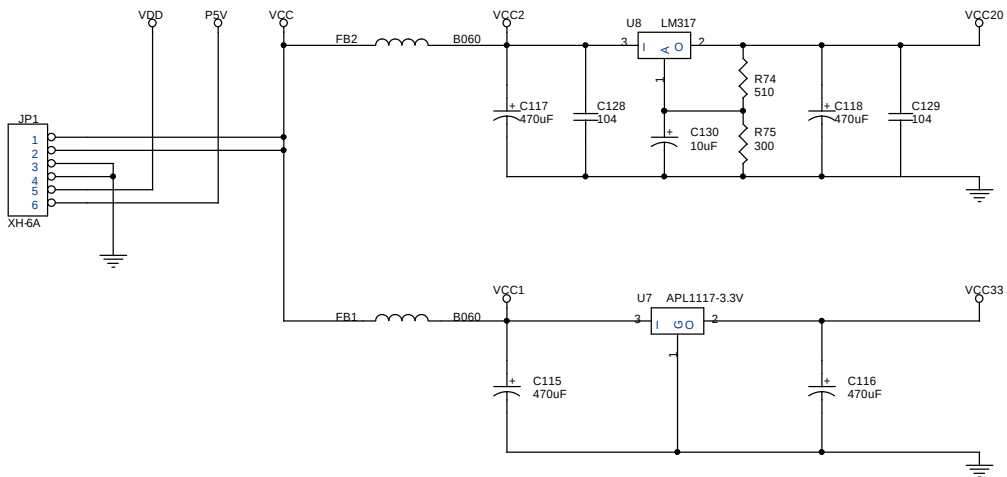
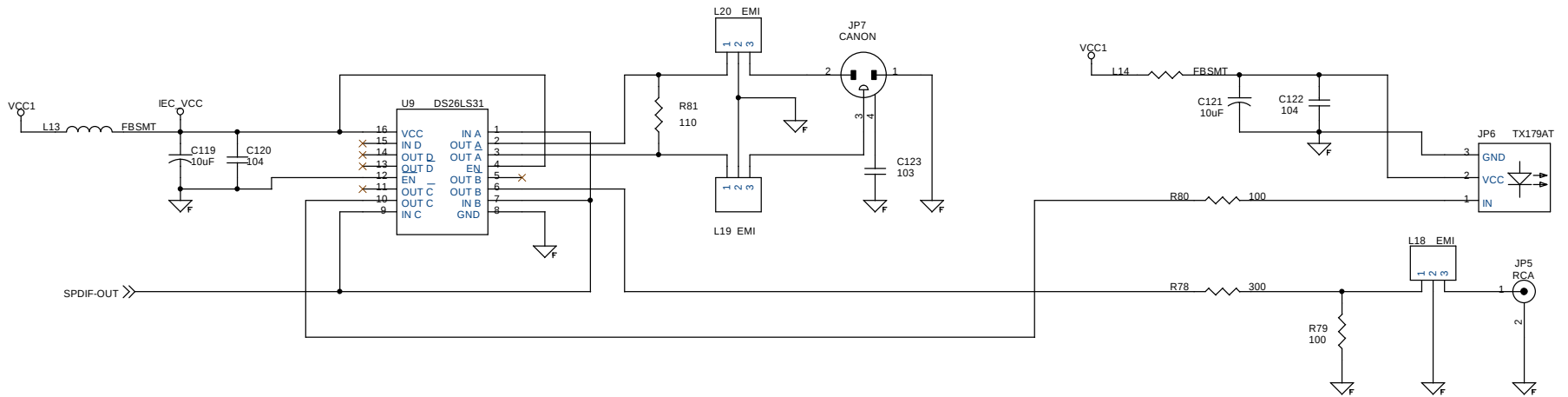




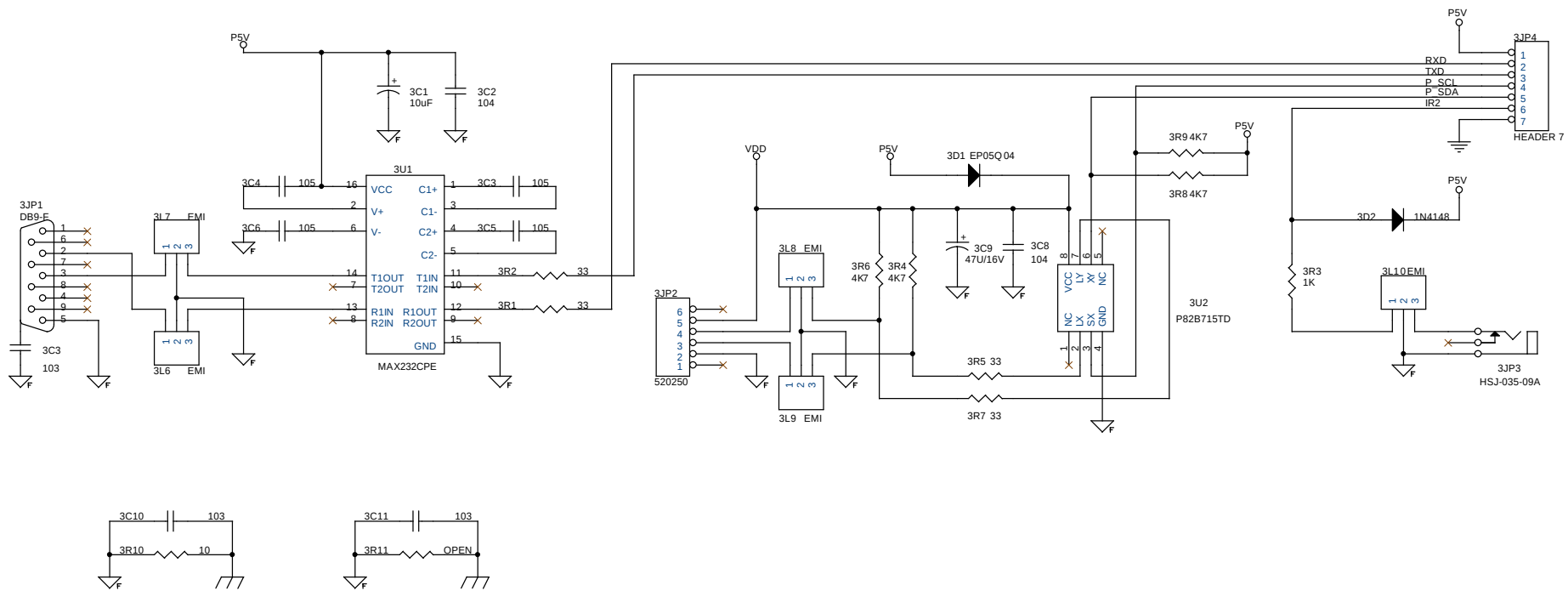


PL3	CLK SOURCE
1	0
0	1

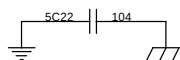
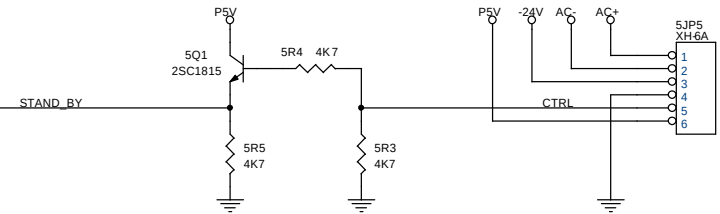
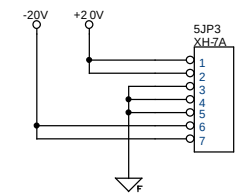
			MULTI		Frequency	
PL2	PL1	PL0	DEFAULT	S.O.P	DEFAULT	S.O.P
0	0	0	425	45	114.75	121.5
0	0	1	reserved		NA	135
0	1	0	bypass	bypass	27	NA
0	1	1	3.75	4	101.25	108
1	0	0	45	425	121.5	114.75
1	0	1	reserved	4.75	NA	128.25
1	1	0	35	55	945	148.5
1	1	1	4	6	108	162



文件编号:	
拟制:	审核:
主机型号: CD21 MKII	硬件版本: 2006-06
文件名称: 主板原理图	
日期:	页次: 3/5



文件编号:	
拟制:	审核:
主机型号: CD21 MKII	硬件版本: 2006-06
文件名称: RS232板原理图	
日期:	页次: 1/1



文件编号:	
拟制:	审核:
主机型号: CD21 MKII	硬件版本: 2006-06
文件名称: 电源板原理图	
日期:	页次: 1/1

