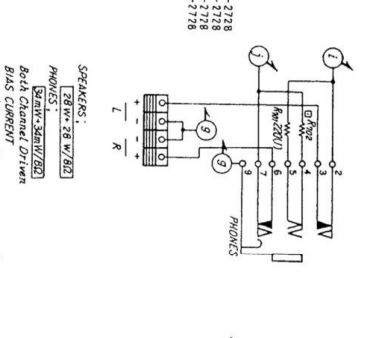
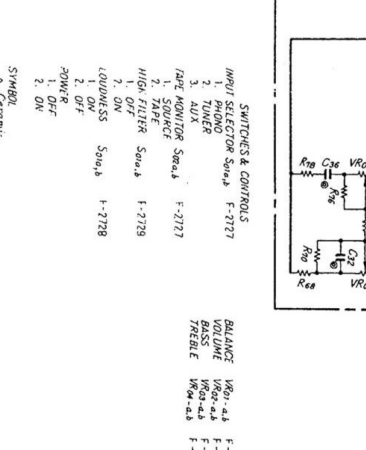
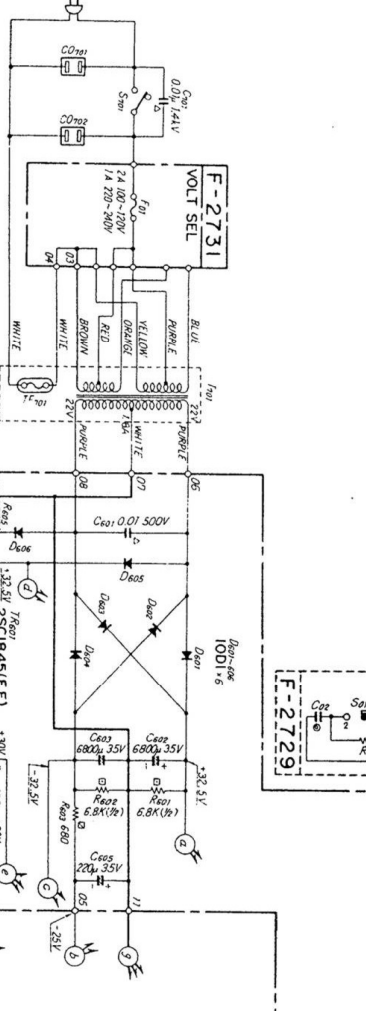
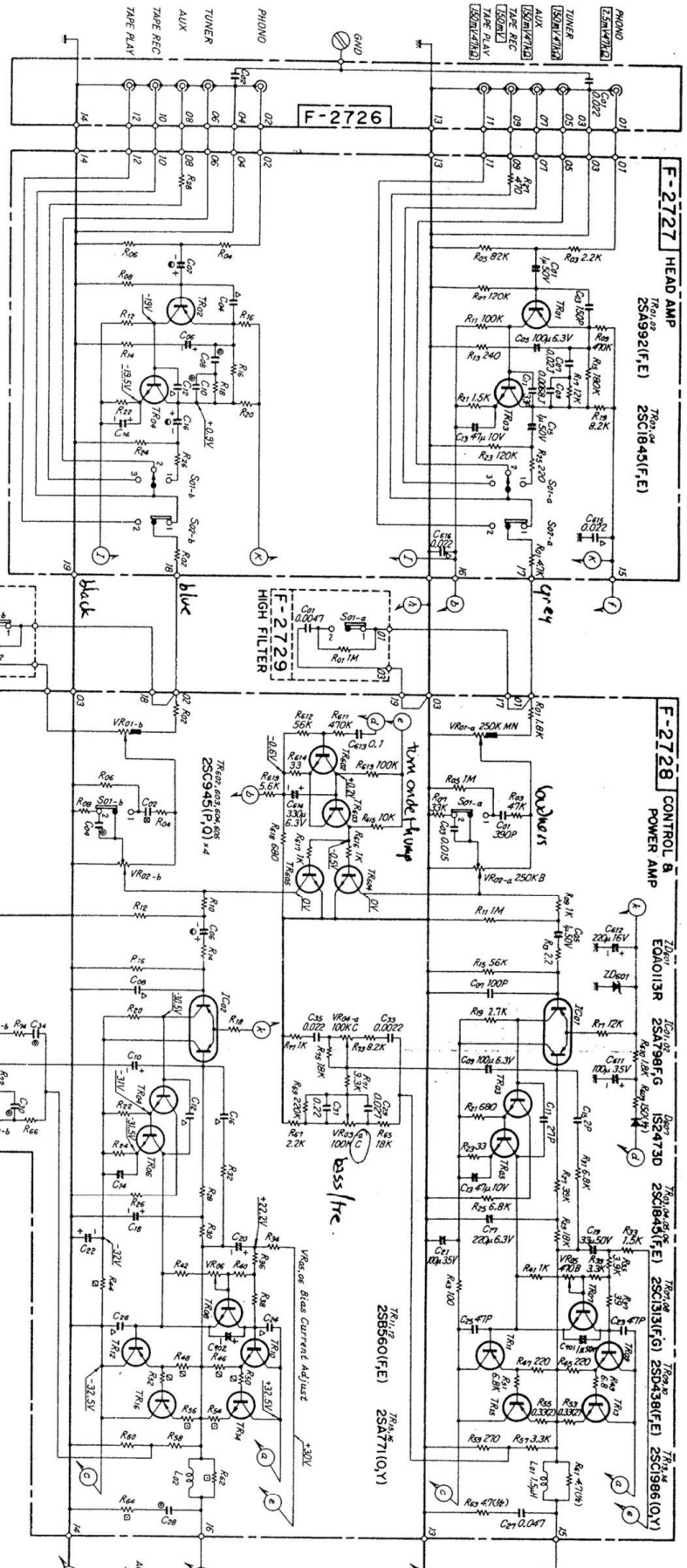


# JSUI AU-117II SCHEMATIC DIAGRAM

Design and specifications subject to change without notice for improvement.  
 La présentation et les spécifications sont susceptibles d'être modifiées sans préavis par suite d'améliorations éventuelles.  
 Änderungen, die dem Technischen Fortschritt dienen, bleiben vorbehalten.



- SYMBOL:**
- Ceramic
  - Mylar
  - Low-leak Electrolytic
  - Fusing Resistor
  - Non-ferromagnetic Resistor
- RESISTORS:** Are in ohms, k=Kilohms, M=Megohms. Indicated tolerance: X=1%, K=10%, W=1%.
- CAPACITORS:** Are in µF (unless otherwise noted), p=pF.

- SWITCHES & CONTROLS:**
- INPUT SELECTOR Swt-a F-2727
  - 1. PHONO
  - 2. AUX
  - 3. TUNER
  - 4. THER REC
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