

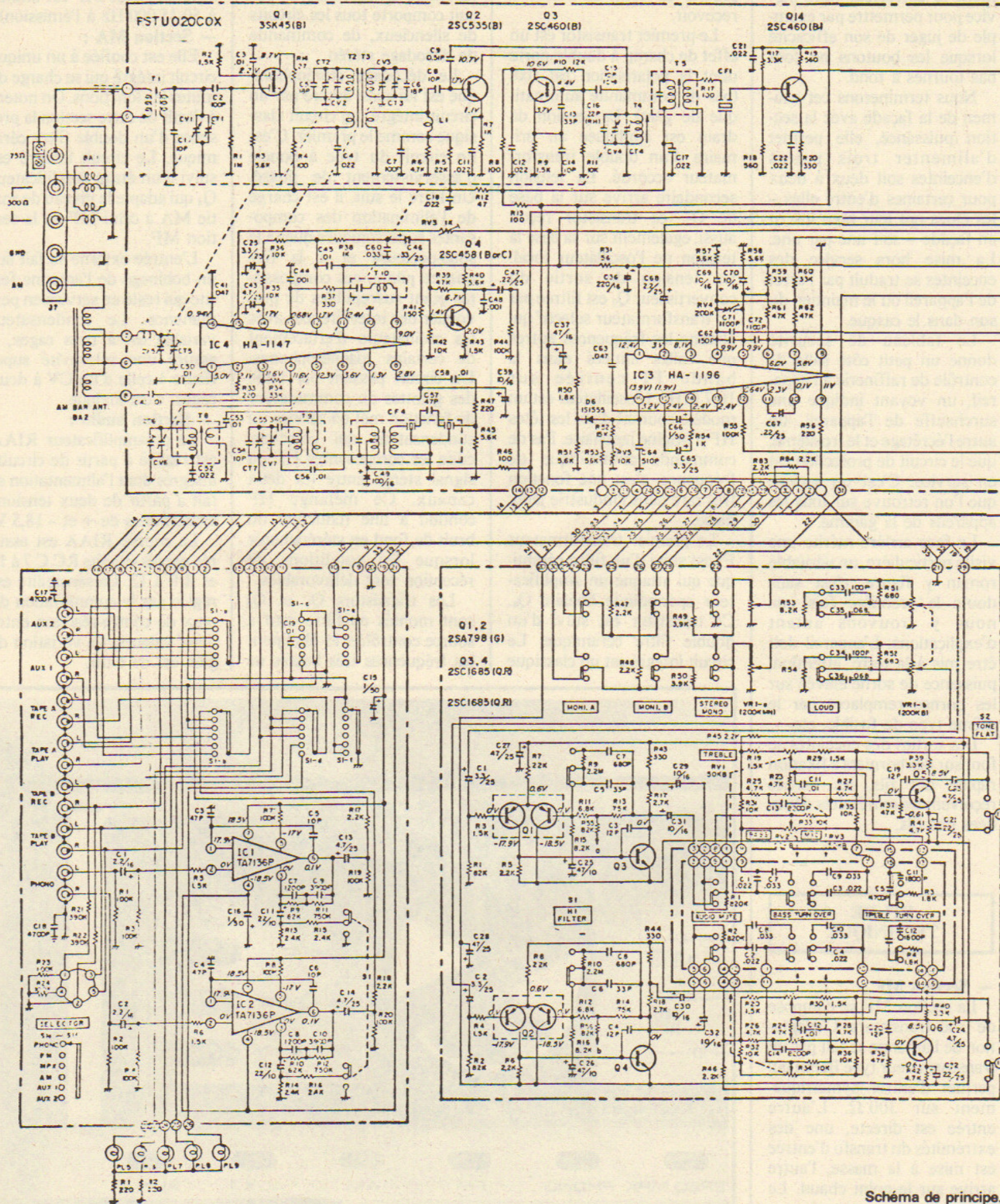
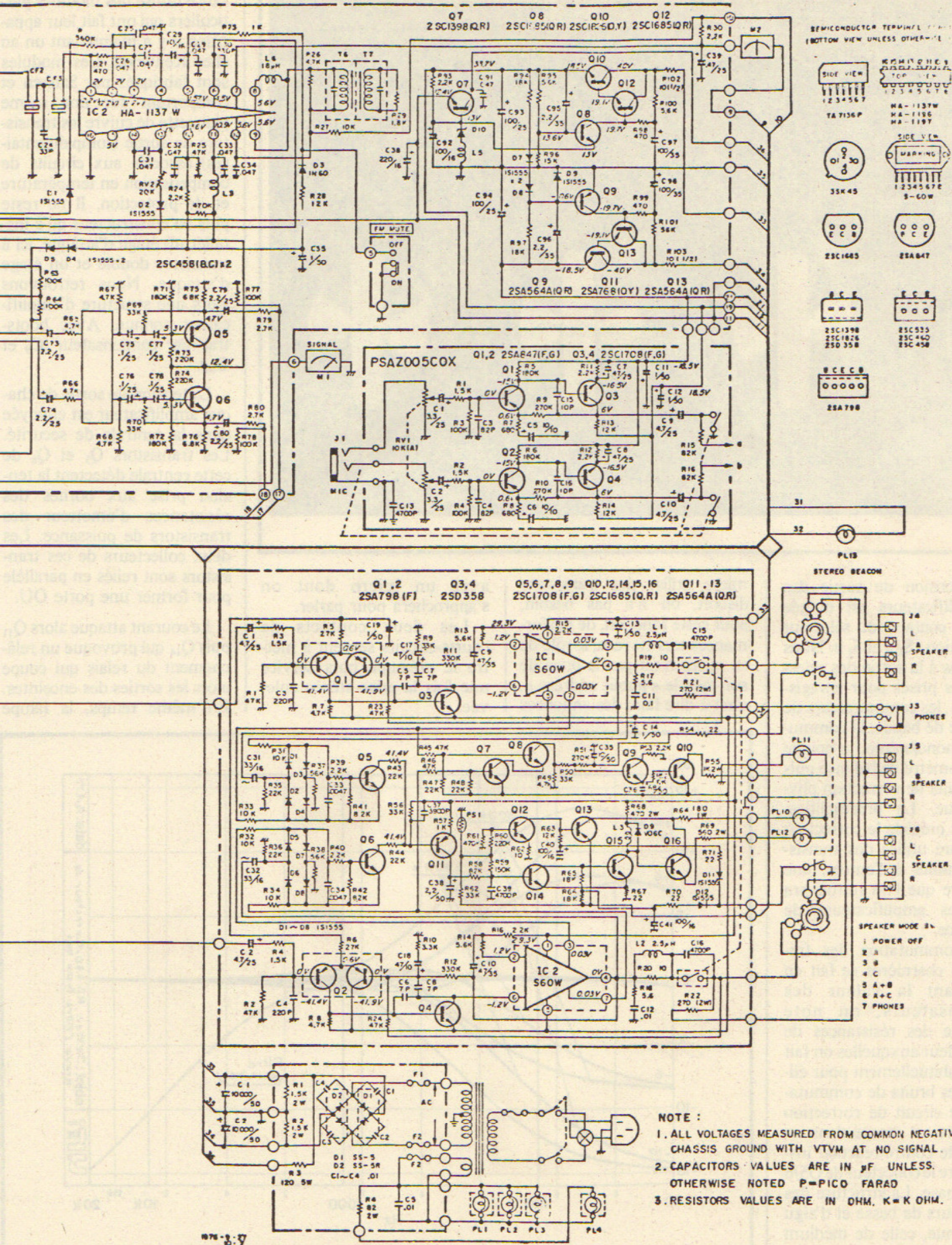


Schéma de principe.



SEMICONDUCTOR TERMINAL
BOTTOM VIEW UNLESS OTHERWISE

