CDP-S7

SERVICE MANUAL

US Model
Canadian Model
AEP Model
UK Model

Model Name Using Similar Mechanism	CDP-791/ X111ES
CD Mechanism Type	CDM14-5BD1
Optical Pick-Up Block Type	BU-5BD1

SPECIFICATIONS

Compact disc player

Frequency response	2 Hz – 20 kHz ± 0.3 dB
Signal-to-noise ratio	More than 113 dB
Dynamic range	More than 100 dB
Harmonic distortion	Less than 0.0025%
Channel separation	More than 105 dB

Outputs

LINE OUT (phono jacks)	Output level 2 V (at 50 kilohms) Load impedance over 10 kilohms

General

Power requirements	120 V AC, 60 Hz (US, Canadian model) 220 – 230 V AC, 50/60 Hz (AEP model) 240 V AC, 50/60 Hz (UK model)
Power consumption	12 W
Dimensions (approx., including projections)	430×95×355 mm (w/h/d) (17×3 3/4×14 inches)
Weight (approx.)	4.5 kg (10 lbs)

Supplied accessories

Audio cord	1 (2 phono plugs – 2 phono plugs)

Design and specifications subject to change without notice.

COMPACT DISC PLAYER
SONY®

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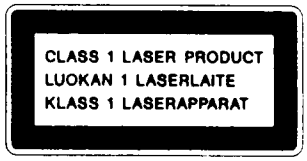
NOTES ON HANDLING THE OPTICAL PICK-UP BLOCK OR BASE UNIT

The laser diode in the optical pick-up block may suffer electrostatic breakdown because of the potential diference generated by the charged electrostatic load, etc. on clothing and the human body.
During repair, pay attention to electrostatic breakdown and also use the procedure in the printed matter which is included in the repair parts.
The flexible board is easily damaged and should be handled with care.

NOTES ON LASER DIODE EMISSION CHECK

The laser beam on this model is concentrated so as to be focused on the disc reflective surface by the objective lens in the optical pick-up block. Therefore, when checking the laser diode emission, observe from more than 30cm away from the objective lens.



This Compact Disc player is classified as a CLASS 1 LASER product.
The CLASS 1 LASER PRODUCT label is located on the rear exterior.

SAFETY CHECK-OUT

After correcting the original service problem, perform the following safety check before releasing the set to the customer:
Check the antenna terminals, metal trim, "metallized" knobs, screws, and all other exposed metal parts for AC leakage. Check leakage as described below.

LEAKAGE TEST

The AC leakage from any exposed metal part to earth ground and from all exposed metal parts to any exposed metal part having a return to chassis, must not exceed 0.5 mA (500 microampers). Leakage current can be measured by any one of three methods.

1. A commercial leakage tester, such as the Simpson 229 or RCA WT-540A. Follow the manufacturers' instructions to use these instruments.
2. A battery-operated AC milliammeter. The Data Precision 245 digital multimeter is suitable for this job.
3. Measuring the voltage drop across a resistor by means of a VOM or battery-operated AC voltmeter. The "limit" indication is 0.75 V, so analog meters must have an accurate low-voltage scale. The Simpson 250 and Sanwa SH-63Trd are examples of a passive VOM that is suitable. Nearly all battery operated digital multimeters that have a 2 V AC range are suitable. (See Fig. A)

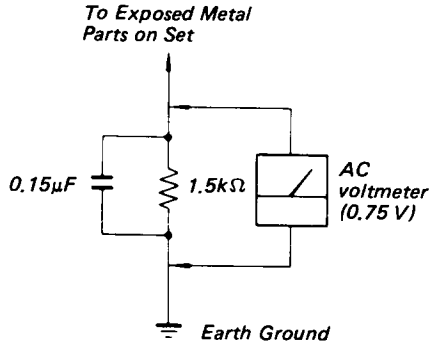
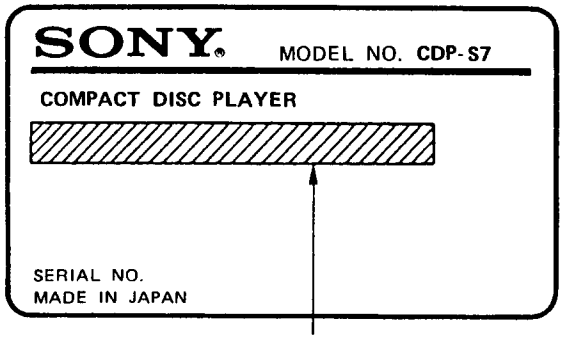


Fig. A. Using an AC voltmeter to check AC leakage.

MODEL IDENTIFICATION

–Model Number Label–



US, Canadian Model: AC: 120V ~ 60Hz
AEP Model: AC: 220V–230V ~50/60Hz
UK Model: AC: 240V ~50/60Hz

SAFETY-RELATED COMPONENT WARNING!!

COMPONENTS IDENTIFIED BY MARK ⚠ OR DOTTED LINE WITH MARK ⚠ ON THE SCHEMATIC DIAGRAMS AND IN THE PARTS LIST ARE CRITICAL TO SAFE OPERATION. REPLACE THESE COMPONENTS WITH SONY PARTS WHOSE PART NUMBERS APPEAR AS SHOWN IN THIS MANUAL OR IN SUPPLEMENTS PUBLISHED BY SONY.

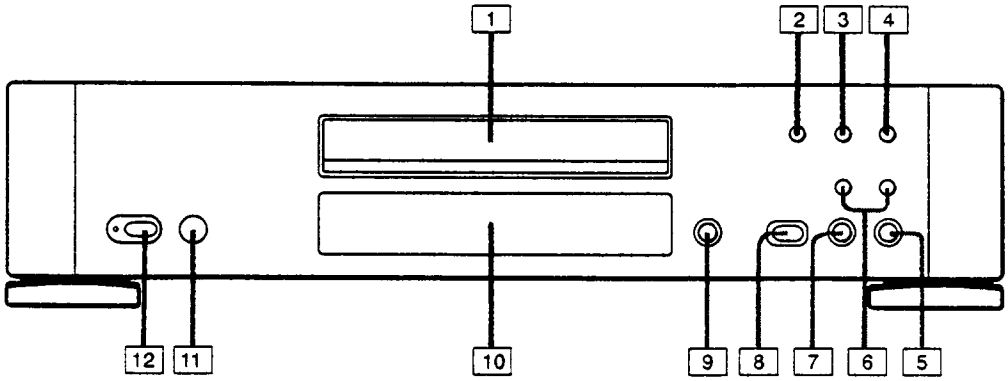
ATTENTION AU COMPOSANT AYANT RAPPORT À LA SÉCURITÉ!

LES COMPOSANTS IDENTIFIÉS PAR UNE MARQUE ⚠ SUR LES DIAGRAMMES SCHÉMATIQUES ET LA LISTE DES PIÈCES SONT CRITIQUES POUR LA SÉCURITÉ DE FONCTIONNEMENT. NE REMPLACER CES COMPOSANTS QUE PAR DES PIÈCES SONY DONT LES NUMÉROS SONT DONNÉS DANS CE MANUEL OU DANS LES SUPPLÉMENTS PUBLIÉS PAR SONY.

SECTION 1
GENERAL

This section is extracted from instruction manual.

1-1. LOCATION OF CONTROLS



Refer to the pages indicated in ● for details.

1 Disc tray ●
2 CONTINUE button ●, ●
3 SHUFFLE button ●
4 PROGRAM button ●
5 ■ (stop) button ●
6 ⏮/⏭ (AMS*) buttons ●, ●
7 ⏸ (pause) button ●
8 ▶ (play) button ●
9 ⏏ OPEN/CLOSE button ●
10 Display window
11 Remote sensor
Can be used to remotely control this unit with:
- A remote commander with a R mark which is provided with a Sony amplifier and that can operate a CD Player.
- An optional Sony remote commander with a R mark that can operate a CD Player.

12 POWER switch and indicator ●

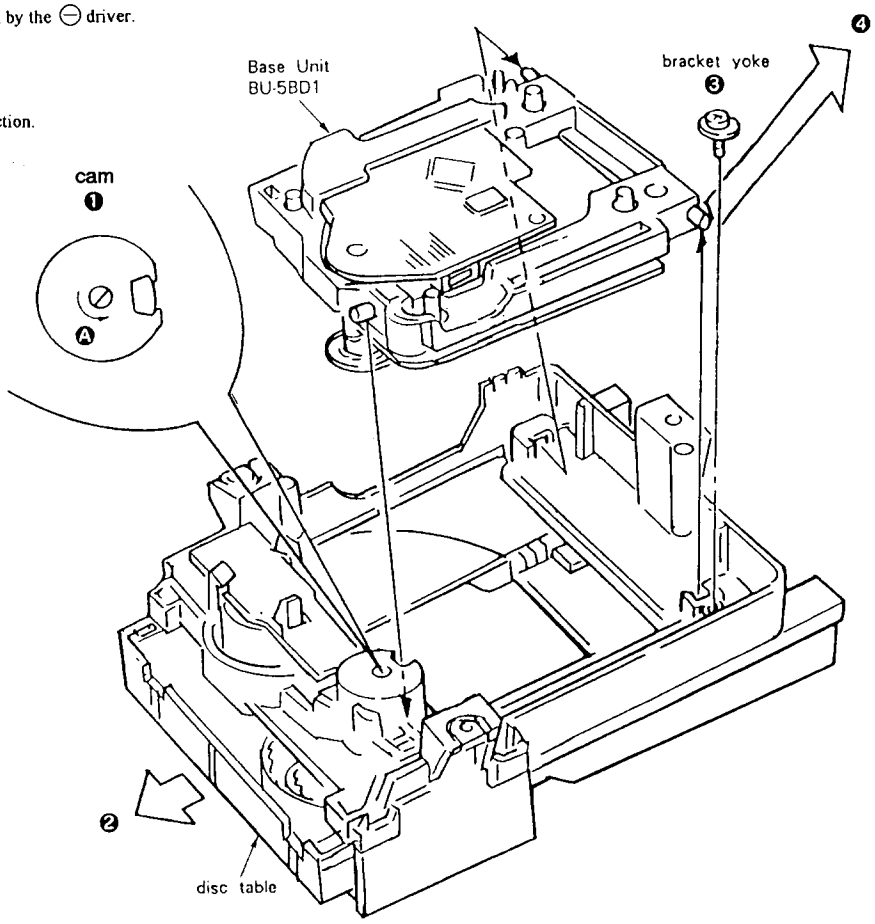
* AMS is the abbreviation of Automatic Music Sensor.

SECTION 2
DISASSEMBLY

BASE UNIT REMOVAL

Note: Follow the disassembly procedure in the numerical order given.

1. Remove CD mechanism from the set and turn over.
2. Turn the cam ❶ in the Arrow Ⓐ direction by the ⊖ driver.
3. Take out disc table ❷.
4. Remove bracket yoke ❸.
5. Remove BU-5BD1 ❹ in the Arrow ❹ direction.

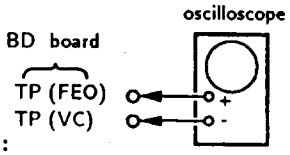


SECTION 3
ELECTRICAL BLOCK CHECKING

Note :

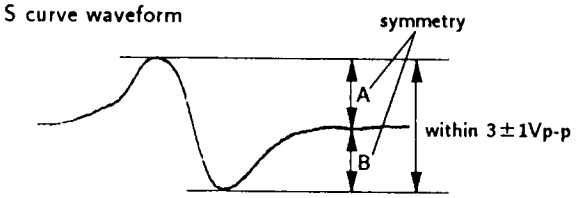
1. CD Block basically constructed to operate without adjustment. Therefore, check each item in order given.
2. Use YEDS-18 disc (3-702-101-01) unless otherwise indicated.
3. Use the oscilloscope with more than 10MΩ impedance.
4. Clean an object lens by an applicator with neutral detergent when the signal level is low than specified value with the following checks.

S Curve Check



Procedure :

1. Connect oscilloscope to test point TP (FEO) on BD board.
2. Connect between test point TP (FES) and TP (VC) by lead wire.
3. Turned Power switch on and actuate the focus serch. (actuate the focus serch when disc table is moving in and out.)
4. Check the oscilloscope waveform (S curve) is symmetrical between A and B. And confirm peak to peak level within 3±1Vp-p.

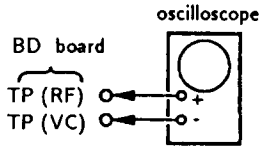


5. After check, remove the lead wire connected in step 2.

Note : • Try to mesure several times to make sure that the ratio of A : B or B : A is more than 10 : 7.

• Take sweep time as long as possible and light up the brightness to obtain best waveform.

RF Level Check

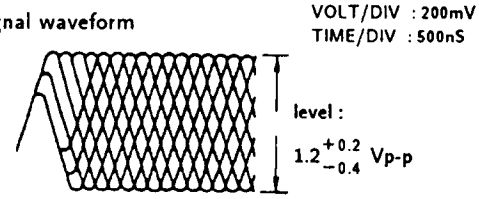


Procedure :

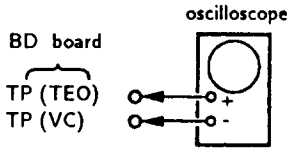
1. Connect oscilloscope to test point TP (RF) on BD board.
2. Turn Power switch on.
3. Put disc (YEDS-18) in and playback.
4. Confirm that oscilloscope waveform is clear and check RF signal level is correct or not.

Note :

Clear RF signal waveform means that the shape "◇" can be clearly distinguished at the center of the waveform.

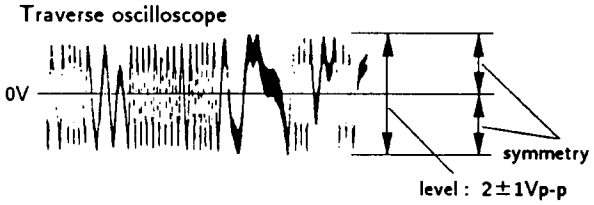


E-F Balance Check



Procedure :

1. Connect test point TP (ADJ) to ground and TP (TES) to TP (VC) with lead wire.
2. Connect oscilloscope to test point TP (TEO) on BD board.
3. Turn Power switch on.
4. Put disc (YEDS-18) in and playback.
5. Confirm that the osilloscope waveform is symmetrical on the top and bottom in relation to 0V, and check this level.

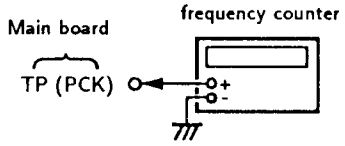


6. Remove the lead wire connected in step 1.

RF PLL Free-run Frequency Check

Procedure :

1. Connect frequency counter to test point (PCK) with lead wire.



2. Turn Power switch on.
3. Confirm that reading on frequency counter is 4. 3218MHz.

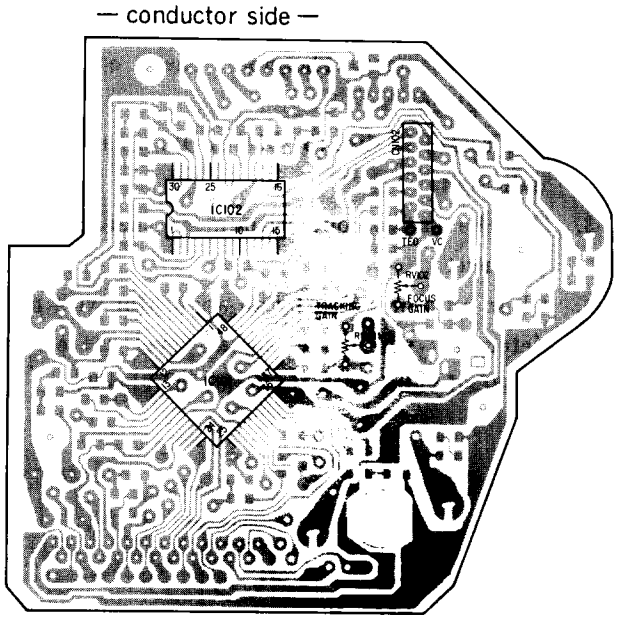
Focus/Tracking Gain

This gain has a margin, so even if it is slightly off. There is no problem.

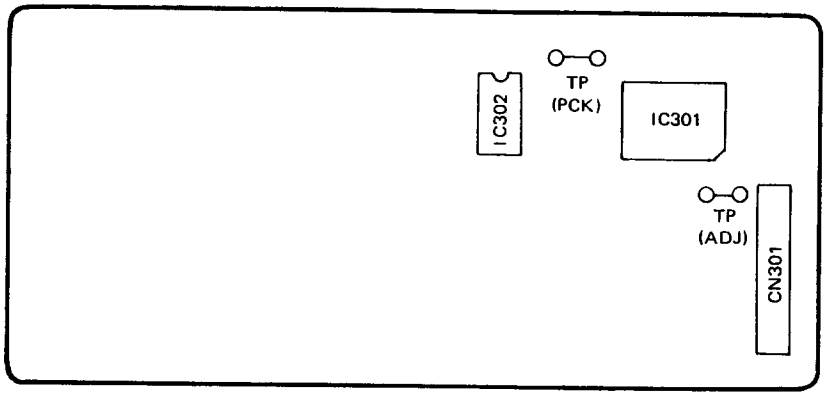
Therefore, do not perform, this adjustment.

Please note that it should be fixed to mechanical center position when you moved and do not know original position.

Adjustment Locations :
[BD board]



[Main board] —Component Side—



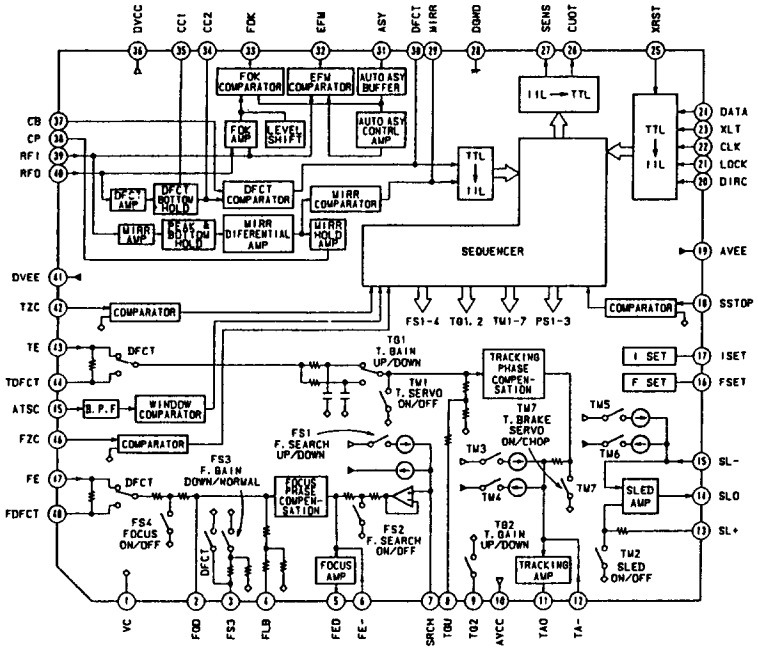
SECTION 4
DIAGRAMS

IC101 (CXA1372Q) PIN DESCRIPTIONS

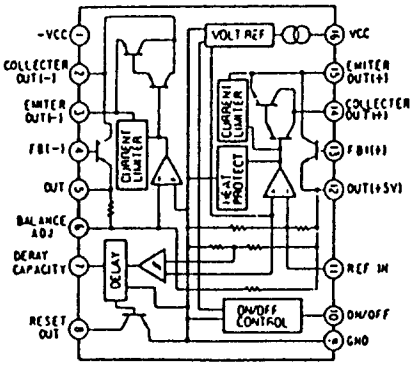
PIN NO.	PIN NAME	I/O	FUNCTION
1	VC		2.5 Volts power supply.
2	FGD	I	Focus gain adjusting capacitor connected between ② pin and ③ pin.
3	FS3	I	Focus gain adjusting capacitor connected between ② pin and ③ pin.
4	FLB	I	Focus Servo low frequency boost-up capacitor connected.
5	FEO	O	Focus drive output.
6	FE−	I	Focus error amp inverted input.
7	SRCH	I	Connected capacitor to making the focus serch waveform.
8	TGU	I	Tracking gain adjusting capacitor connected between ⑧ pin and ⑨ pin.
9	TG2	I	Tracking gain adjusting capacitor connected between ⑧ pin and ⑨ pin.
10	AV CC		+5 Volts power supply.
11	TAO	O	Tracking drive output.
12	TA−	I	Tracking amp inverted input.
13	SL+	I	Sled amp non-inverted input.
14	SLO	O	Sled drive output.
15	SL−	I	Sled amp non-inverted input.
16	FSET	I	Phase stabilizer setting resistor connected.
17	ISET	I	Current setting resistor connected.
18	SSTOP	I	Limit switch connection port.
19	AV EE		Ground (0V).
20	DIRC	I	Direct control port. Non-connected.
21	LOCK	I	Sled free-run protection is operate at "L".
22	CLK	I	Serial data transmission clock input form digital signal processor.
23	XLT	I	Latch input from digital signal processor.
24	DATA	I	Serial input from digital signal processor.
25	SENS	O	Outputs internal state corresponding to address.
26	XRST	I	System reset input. Reset at "L".
27	C. OUT	O	Tracking counter output.
28	D GND		Digitel ground. Grounded
29	MIRR	O	Mirror output digital signal processor.
30	DFCT	O	Deffect output. Deffect at "H".
31	ASY	I	Auto symmetry control input.
32	EFM	O	EFM Comparator output.
33	FOK	O	Focus OK.
34	CC2	I	Deffect bottom hold input.
35	CC1	O	Deffect bottom hold output.
36	DV CC		+5 Volts power supply.
37	CB	I	Deffect bottom hold capacitor connected.
38	CP	I	Mirror hold capacitor connected.
39	RFI	I	RF Signal input (Capacitance coupled).
40	RFO	I	RF Signal input (Direct Coupled).
41	DV EE		Grounded (0V).
42	TZC	I	Tracking Zero-cross comparator input.
43	TE	I	Tracking error amp input.
44	TDFCT	I	Deffect correction hold capacitor connected.
45	ATSC	I	Anti-shock input.
46	FZC	I	Focus Zero-cross comparator input.
47	FE	I	Focus error input.
48	FDFCT	I	Deffect correction hold capacitor connected.

4-6. IC BLOCK DIAGRAMS

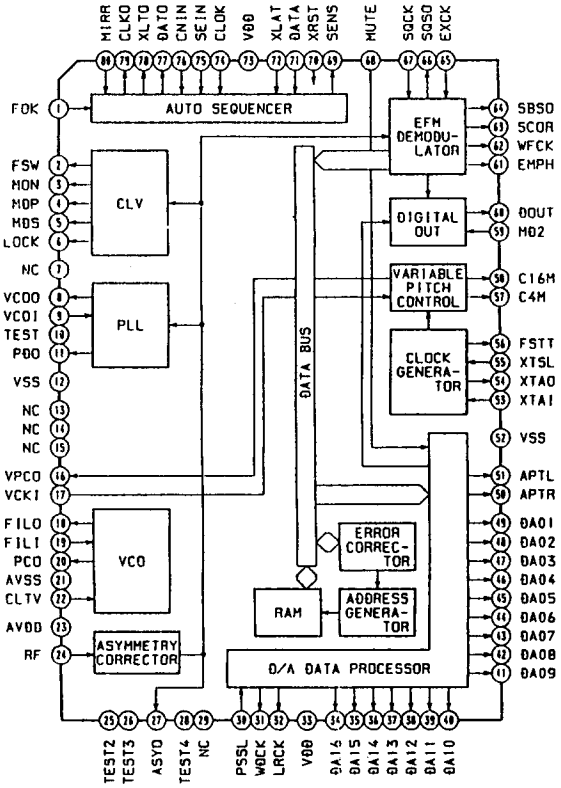
IC101 CXA1372Q



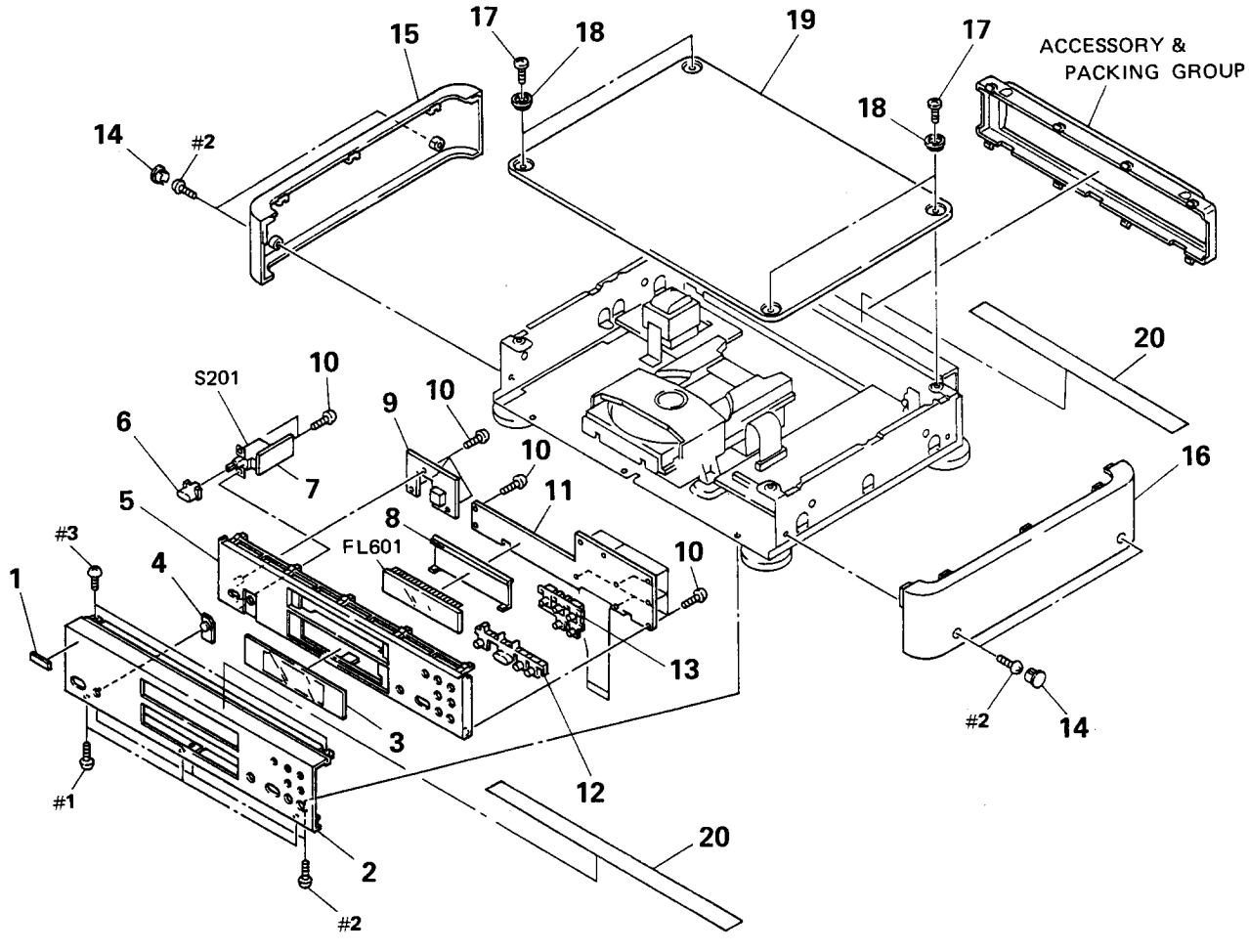
IC202 M5290P-16



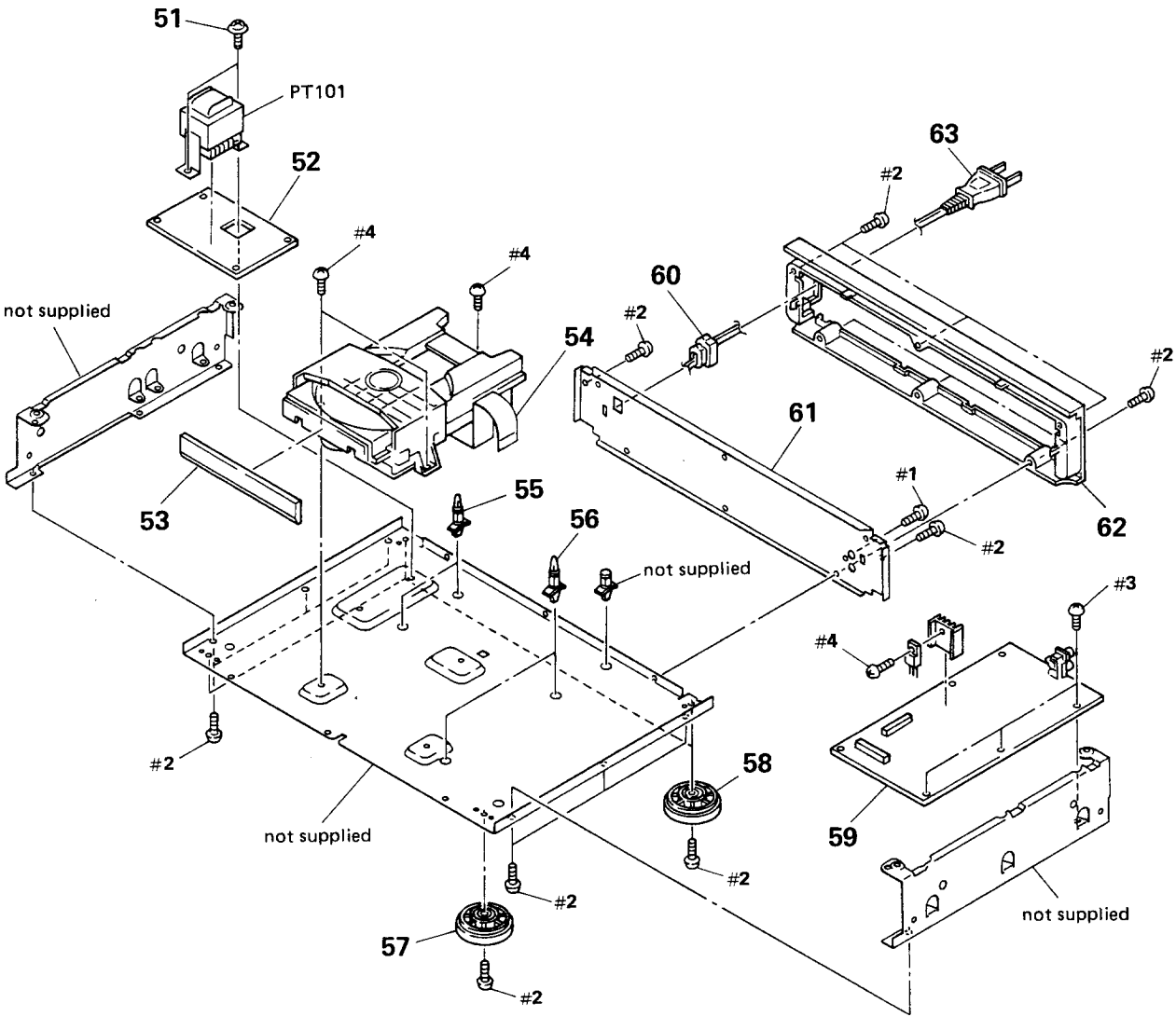
IC301 CDX2500AQ



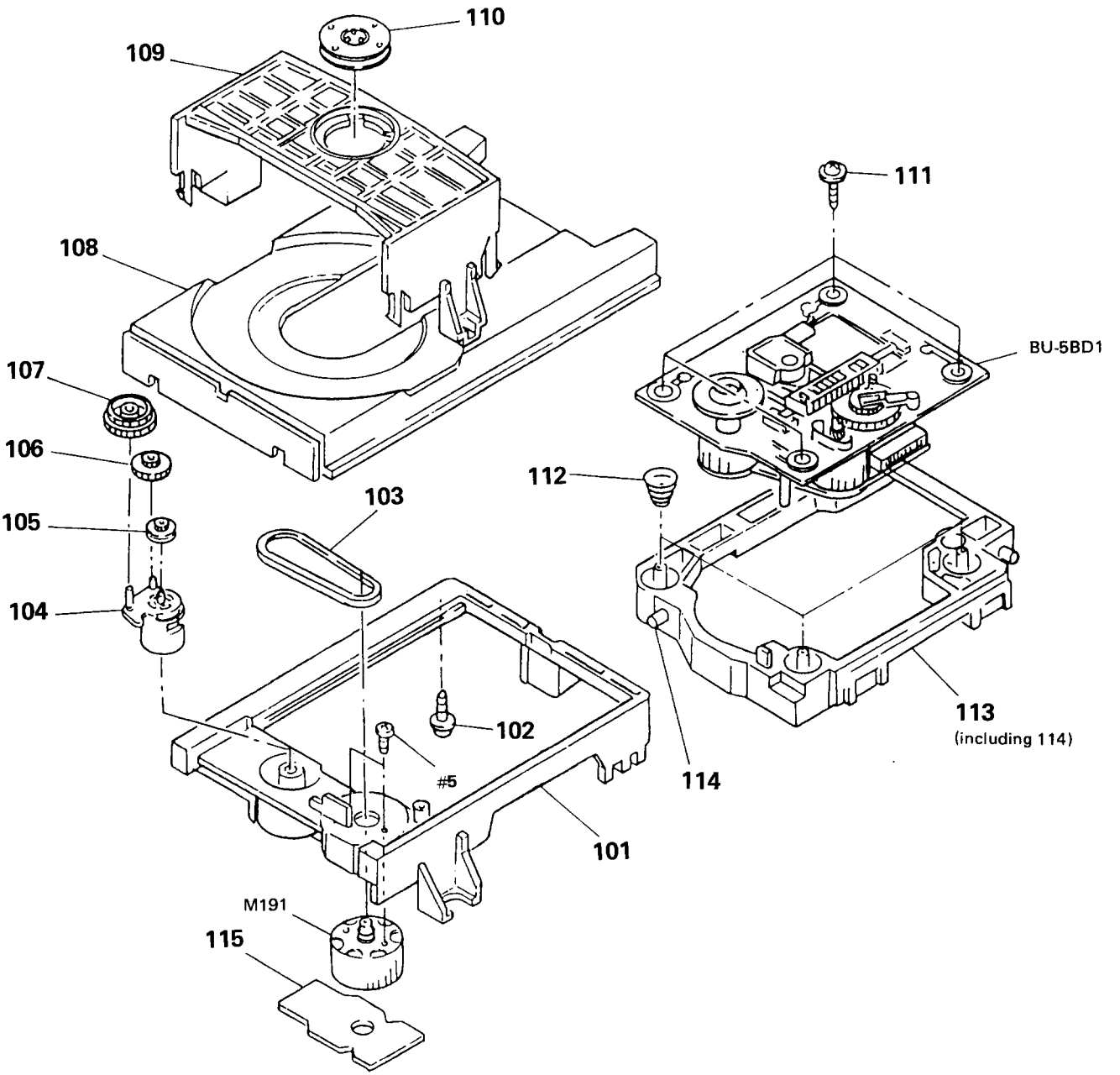
5-1. FRONT PANEL SECTION



5-2. CHASSIS SECTION



5-3. CD MECHANISM SECTION (CDM14-5BD1)



5-4. OPTICAL PICK-UP BLOCK (BU-5BD1)