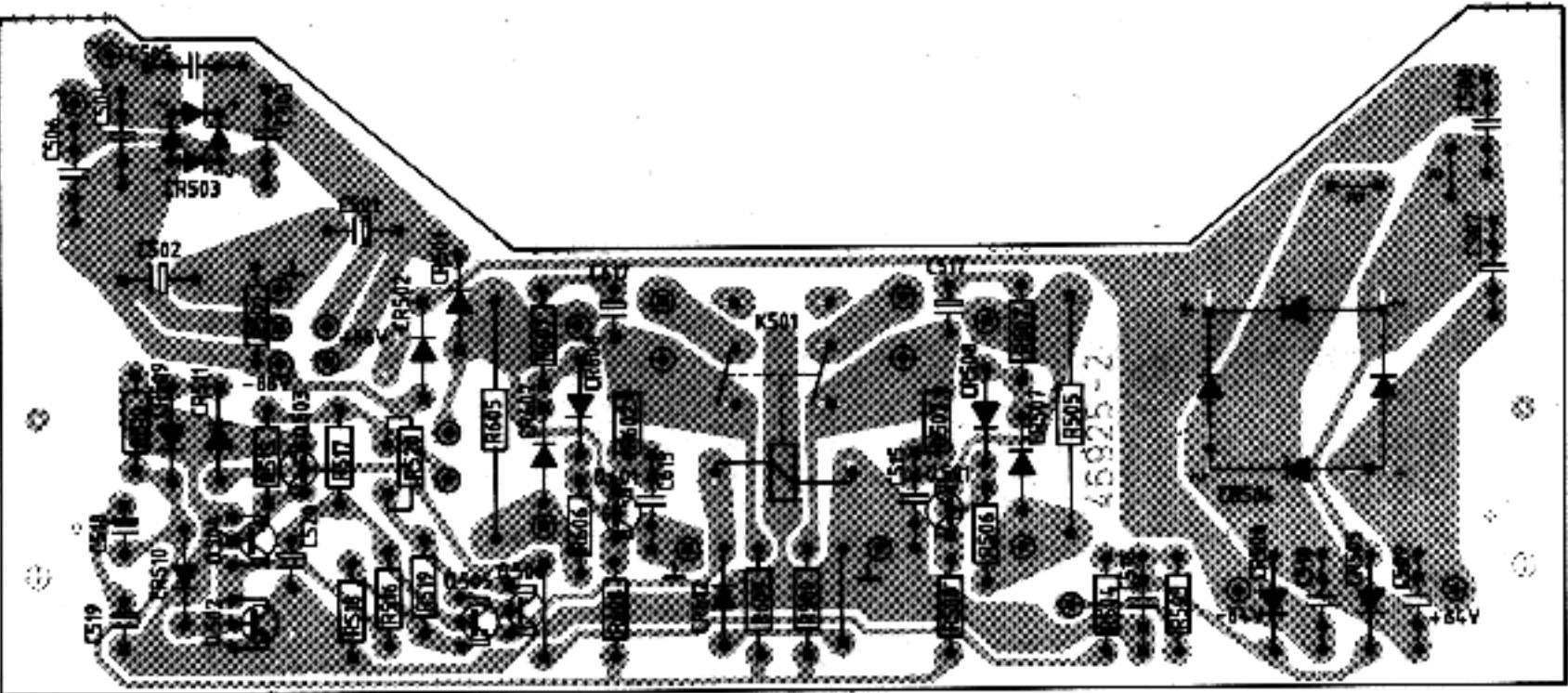
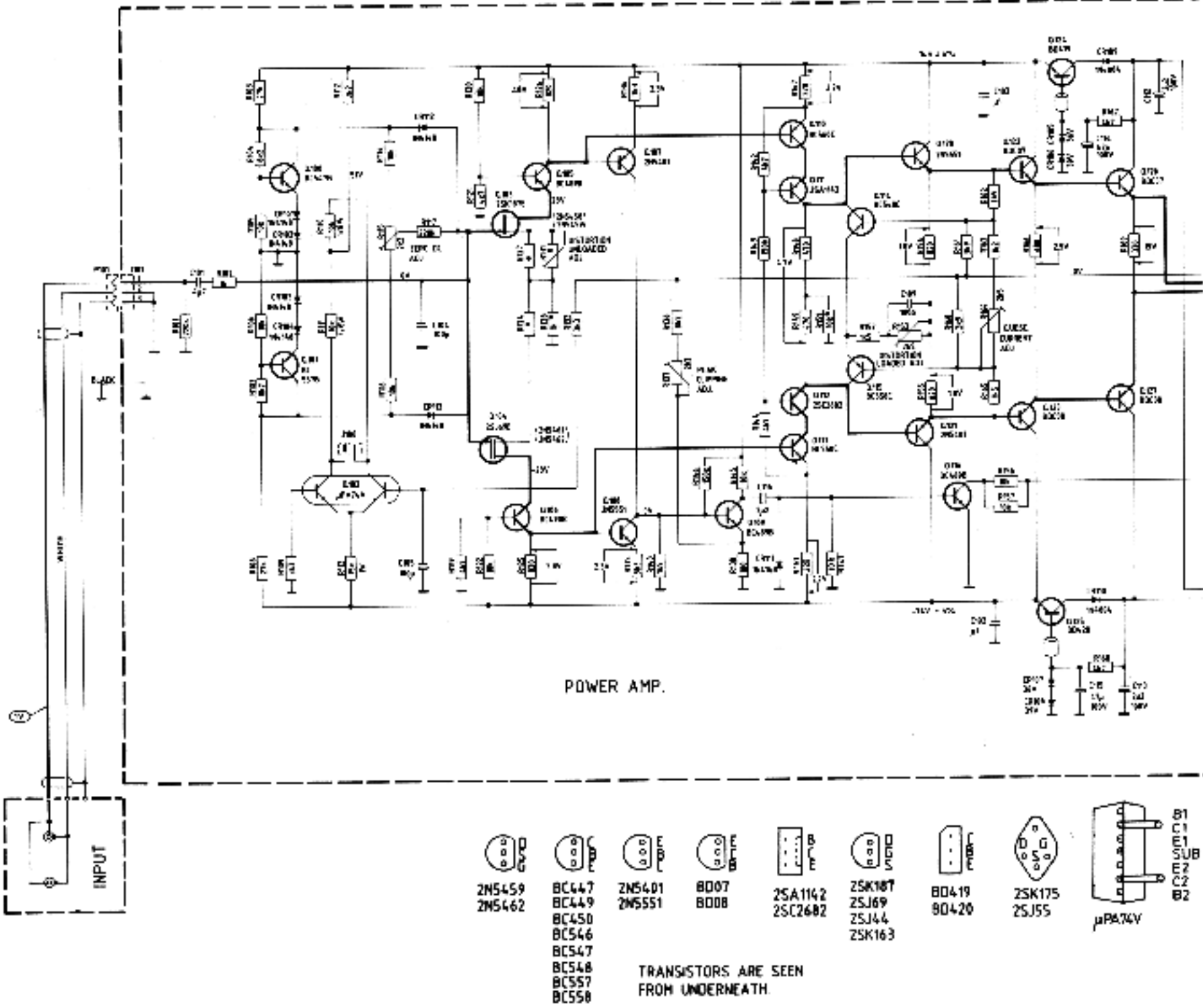
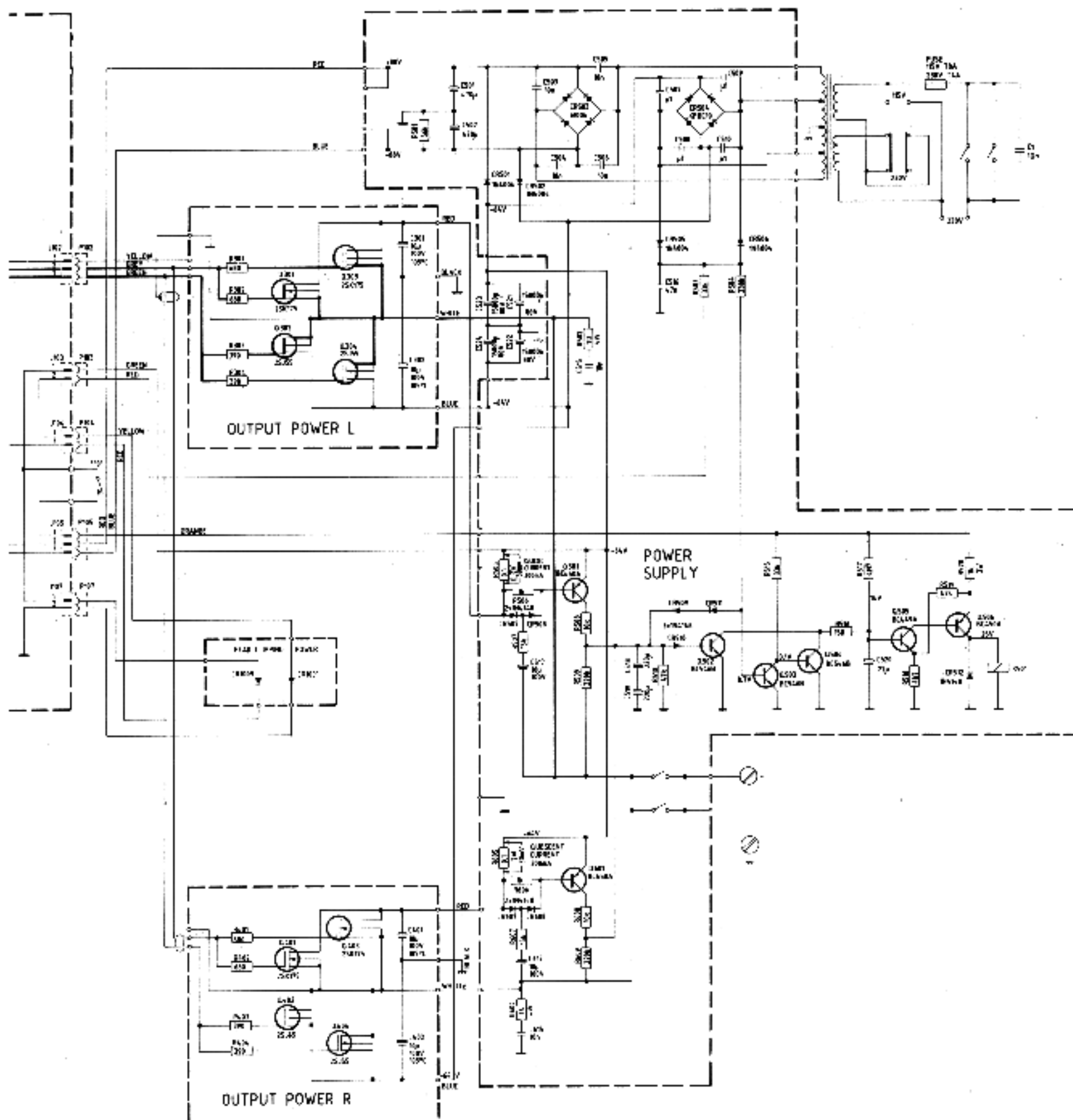


Circuit diagram





ADJUSTMENTS

1. Adj. "Distortion unloaded". R131/231 to min. distortion, 12V output, 1kHz, no load.
0.004% max. distortion.
The voltage across R150/250 should be $2.65V \pm 0.45V$
All measurements must be made with a probe having minimum 5kohms resistors in both terminals
2. Connect two LED's in antiparallel to J206. Adj. "Zero DC" R115/215 until both diodes turn off. DC at speaker output should be within $\pm 50mV$ DC.
3. Adj. "Distortion loaded" R153/253 to min, distortion at 12V across 4 Ω , 1kHz, 0.005% max.
4. Adj. "Quiescent current" R164/264 to 30mV across R505/605.
5. Adj. "Peak clipping" R137/237 to min, light in LED at slight clipping.

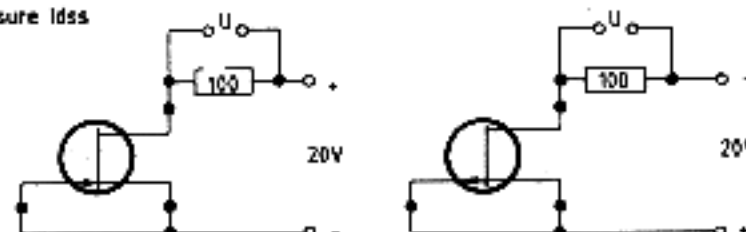
Check the adjustments after 15 min. warm-up time.

* Components with this marking has 1% tolerance.

When Q103/104/203/204 have to be replaced, the following must be done. Use the table below to find the combination of the transistors' I_{dss} groups and source resistors. Resistors marked * is dependent on I_{dss} of the field-effect transistors.

2N5458	2N5459	2N5461	2N5462
$I_{dss}(mA)$	R124	R127	R131
4-5	82 Ω	220 Ω	1k
5-6	120 Ω	220 Ω	1k
6-7	180 Ω	470 Ω	1k
7-8	270 Ω	680 Ω	1k
8-9	330 Ω	1k	1k
9-10	390 Ω	1k2	1k
10-11	470 Ω	1k	2k2

How to measure I_{dss}



$$I_{dss}(mA) = \frac{U(\text{volt})}{0,1(\text{kohm})}$$

Both FET's must be within the same I_{dss} -group