

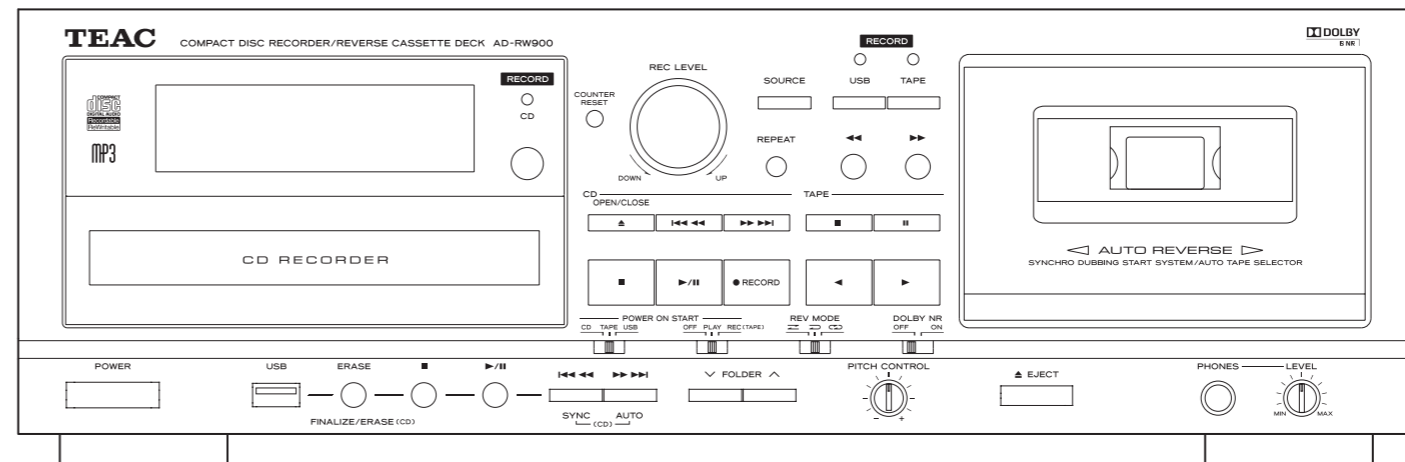
# SCHEMATIC DIAGRAM

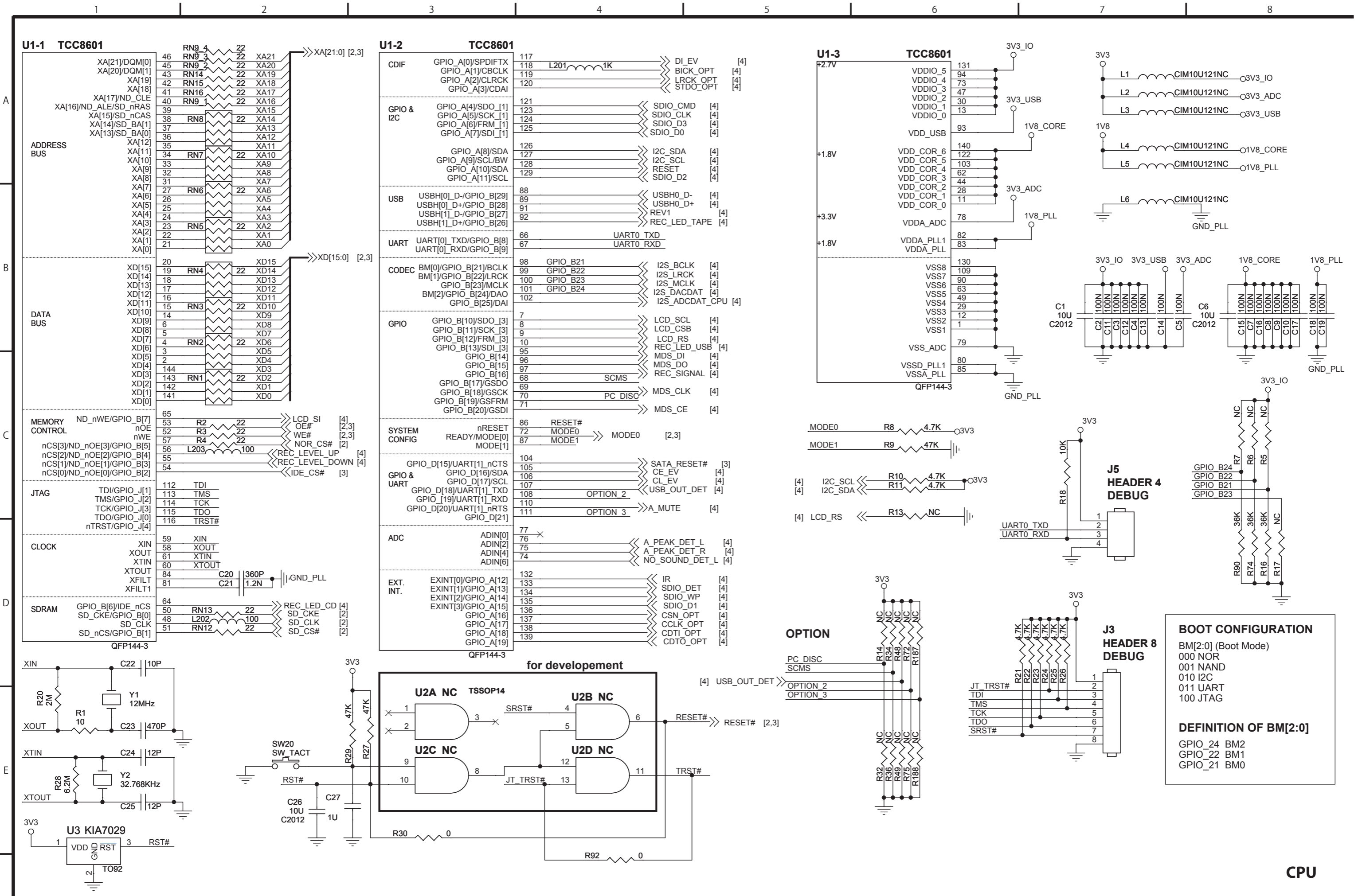
## 回路図

## CD Recorder/Reverse Cassette Deck AD-RW900/900E

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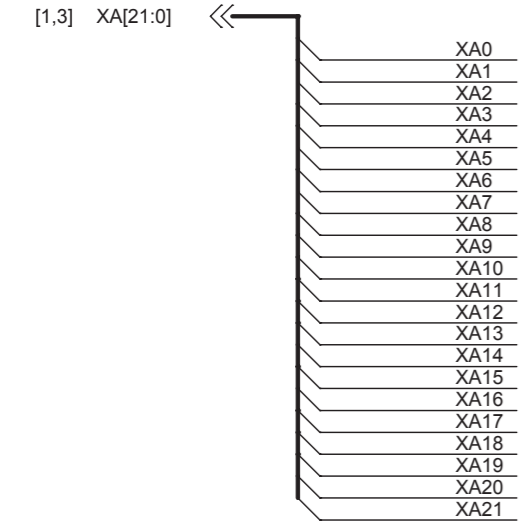
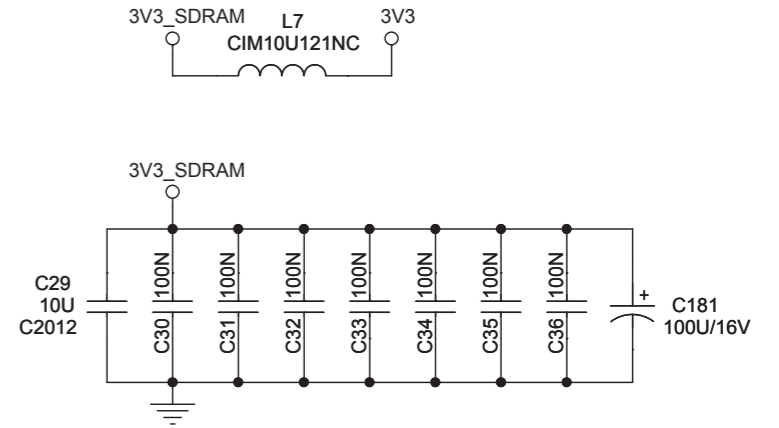
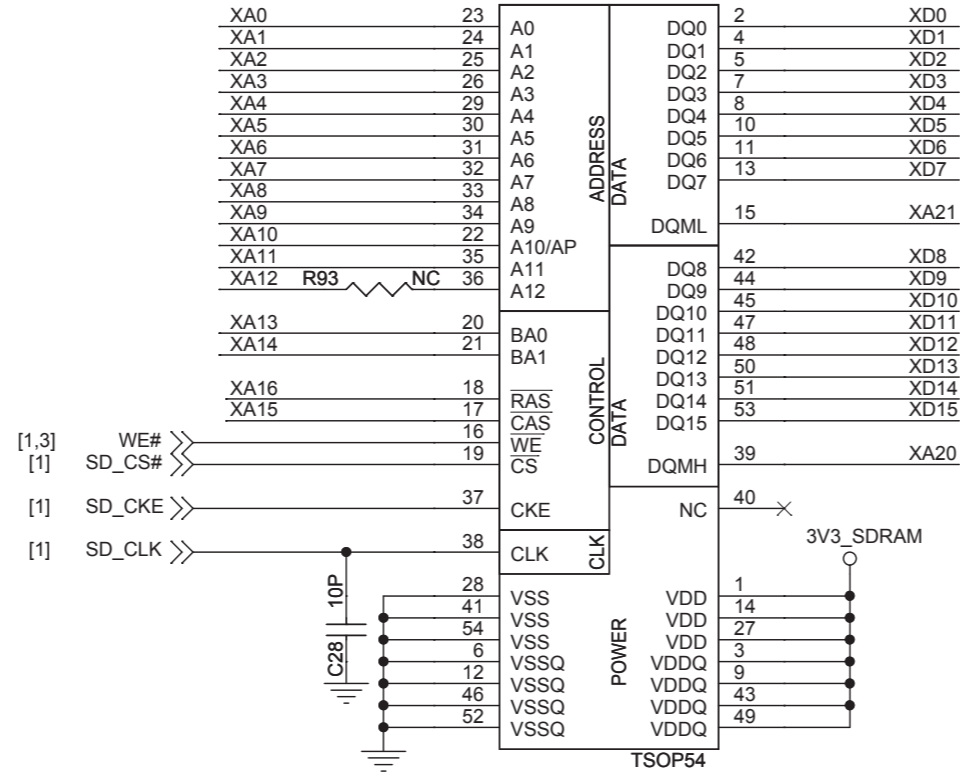


**BOOT CONFIGURATION**  
 BM[2:0] (Boot Mode)  
 000 NOR  
 001 NAND  
 010 I2C  
 011 UART  
 100 JTAG

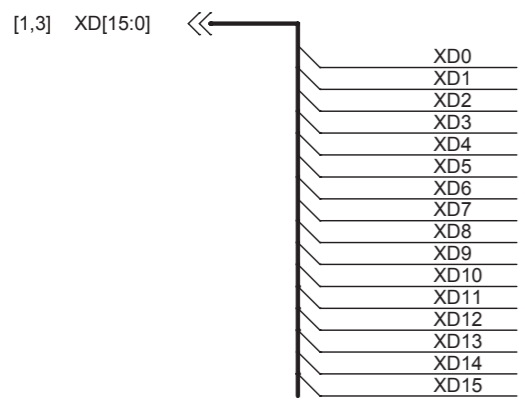
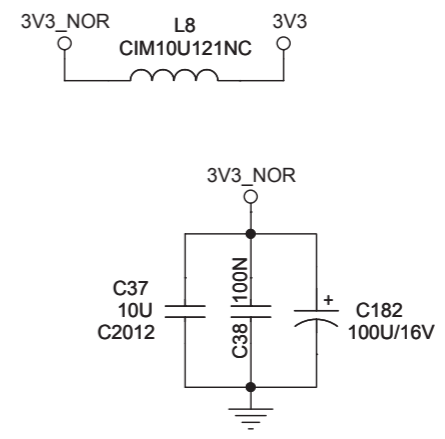
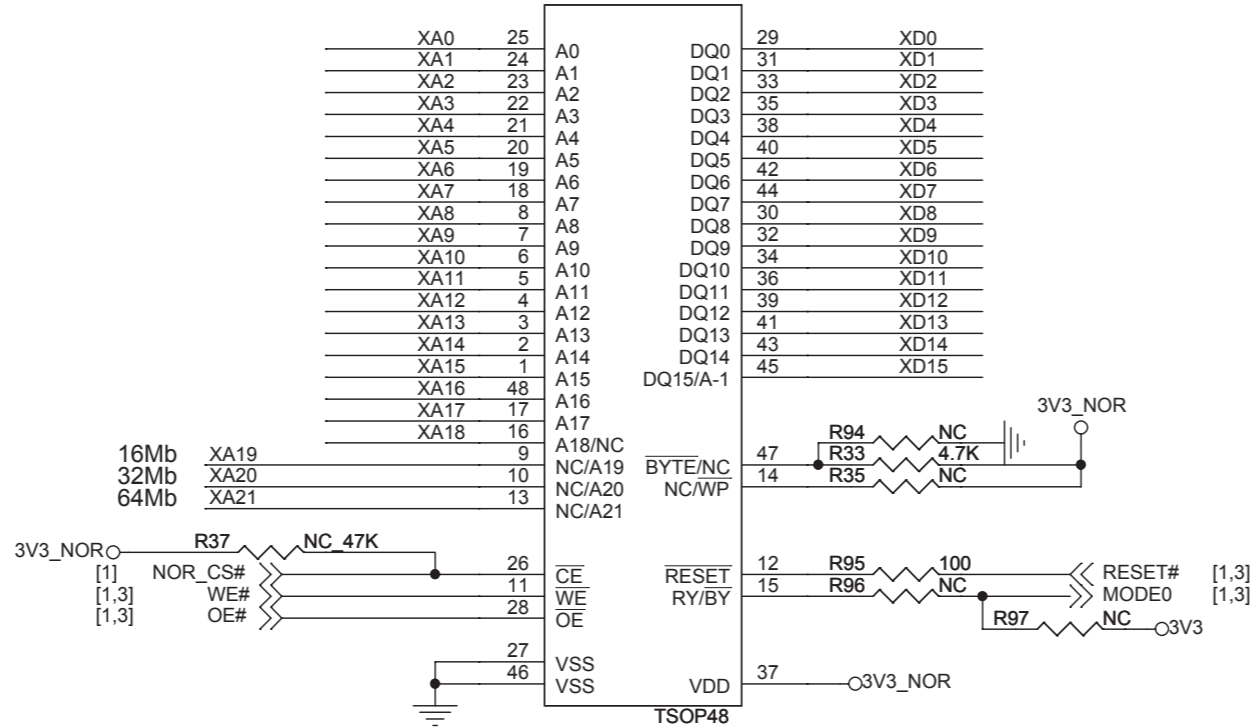
**DEFINITION OF BM[2:0]**  
 GPIO\_24 BM2  
 GPIO\_22 BM1  
 GPIO\_21 BM0

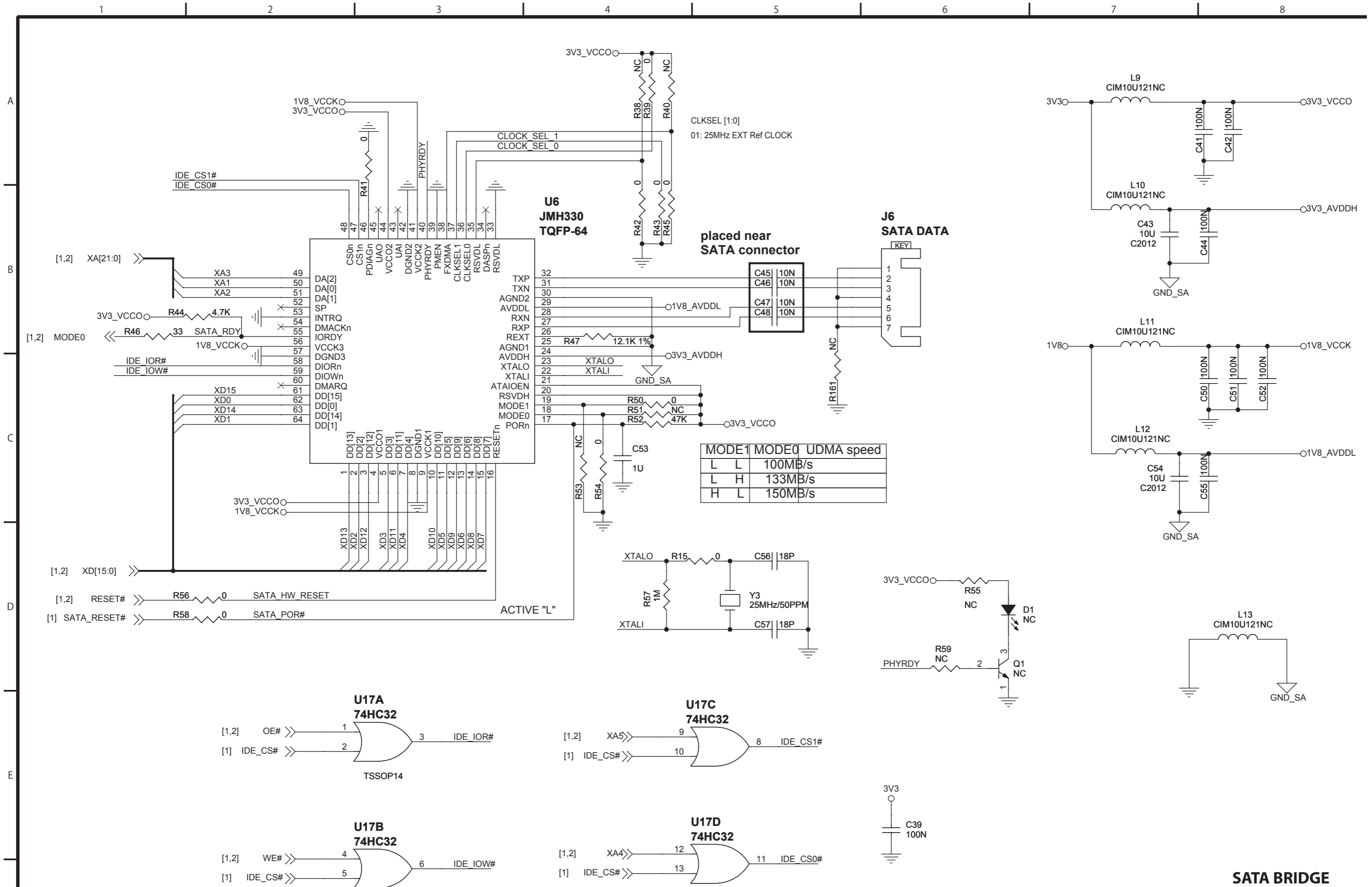
A  
B  
C  
D  
E

**U4**  
**M12L128168A-7T**  
**IC 128Mb SDR SDRAM**

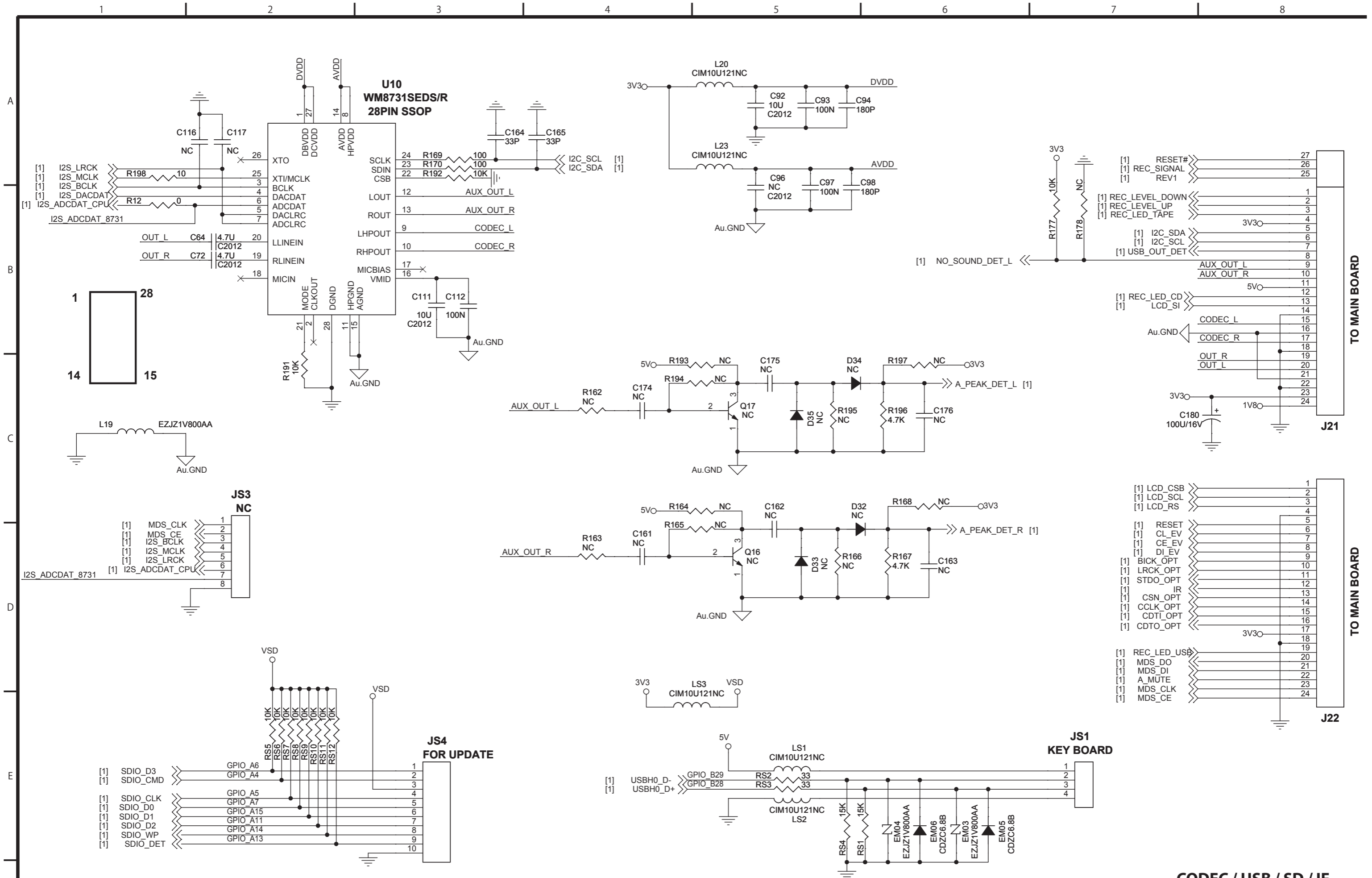


**U5**  
**S29AL008J70TFI02**  
**IC 8Mb (512Kx16) NOR FLASH**



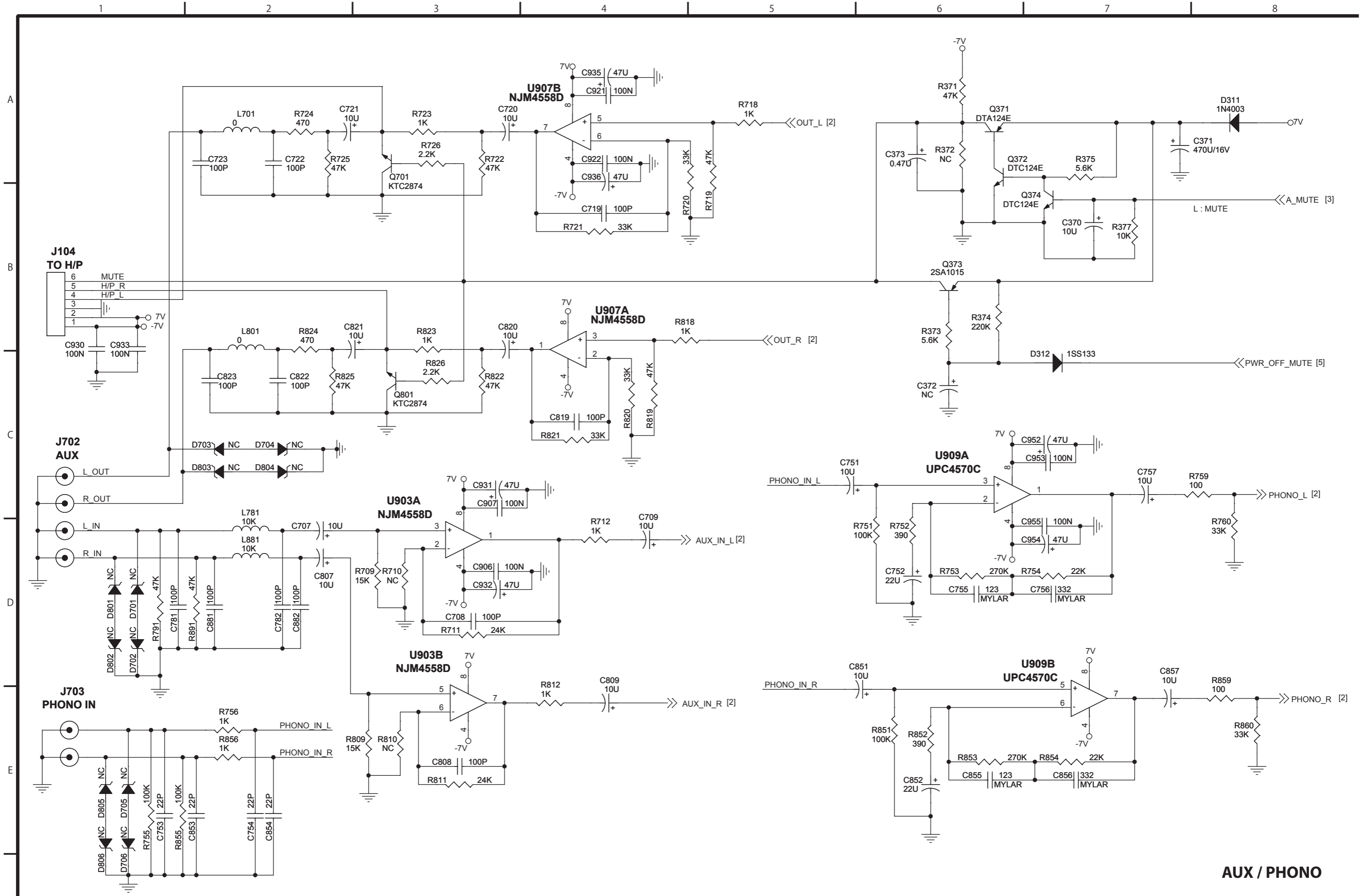


SATA BRIDGE

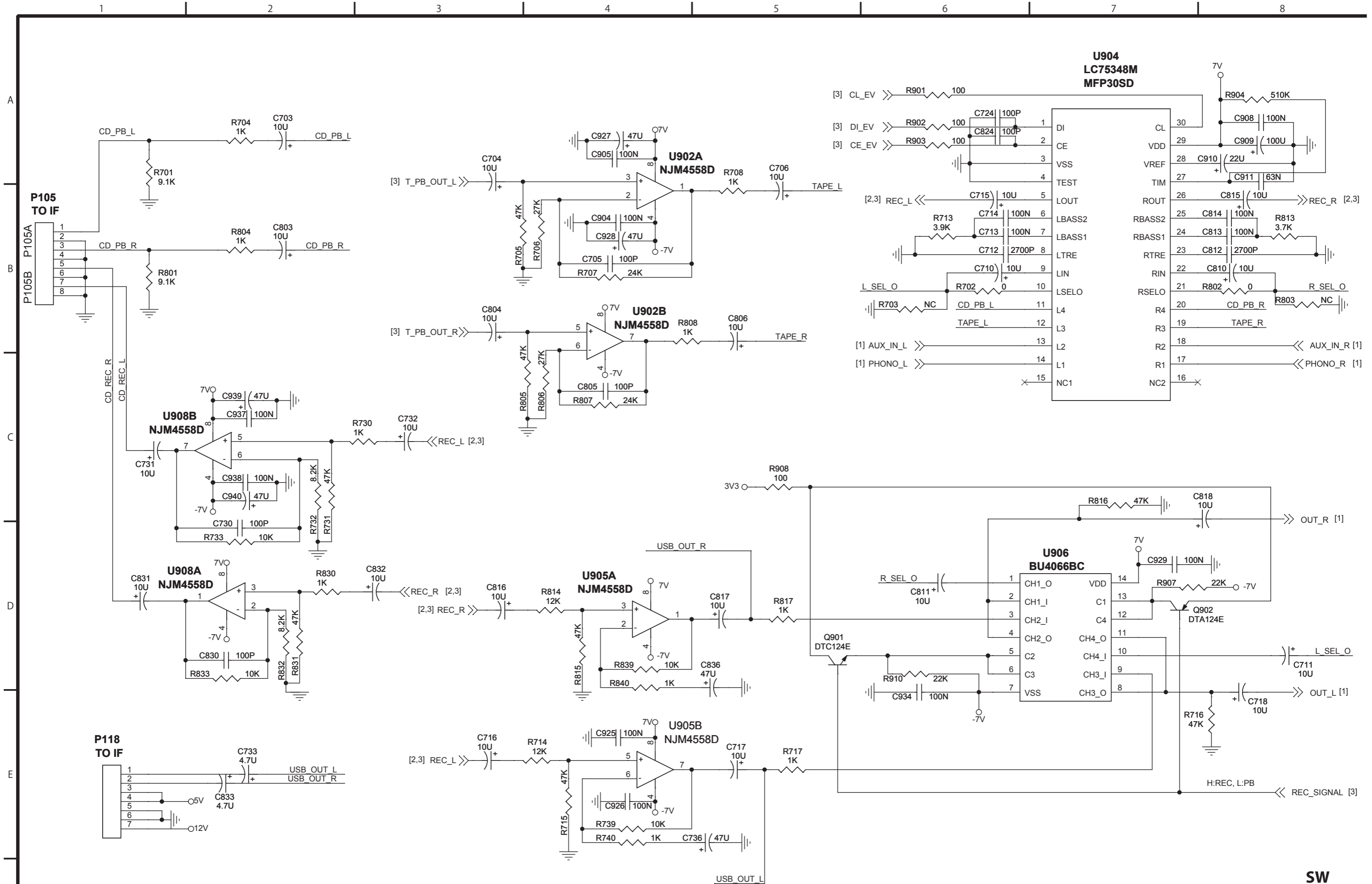


CODEC / USB / SD / IF





**AUX / PHONO**



1 2 3 4 5 6 7 8

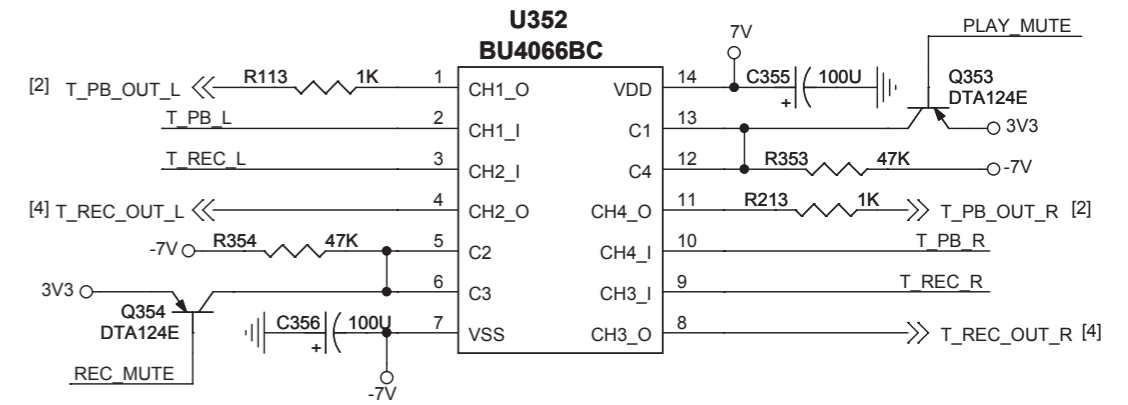
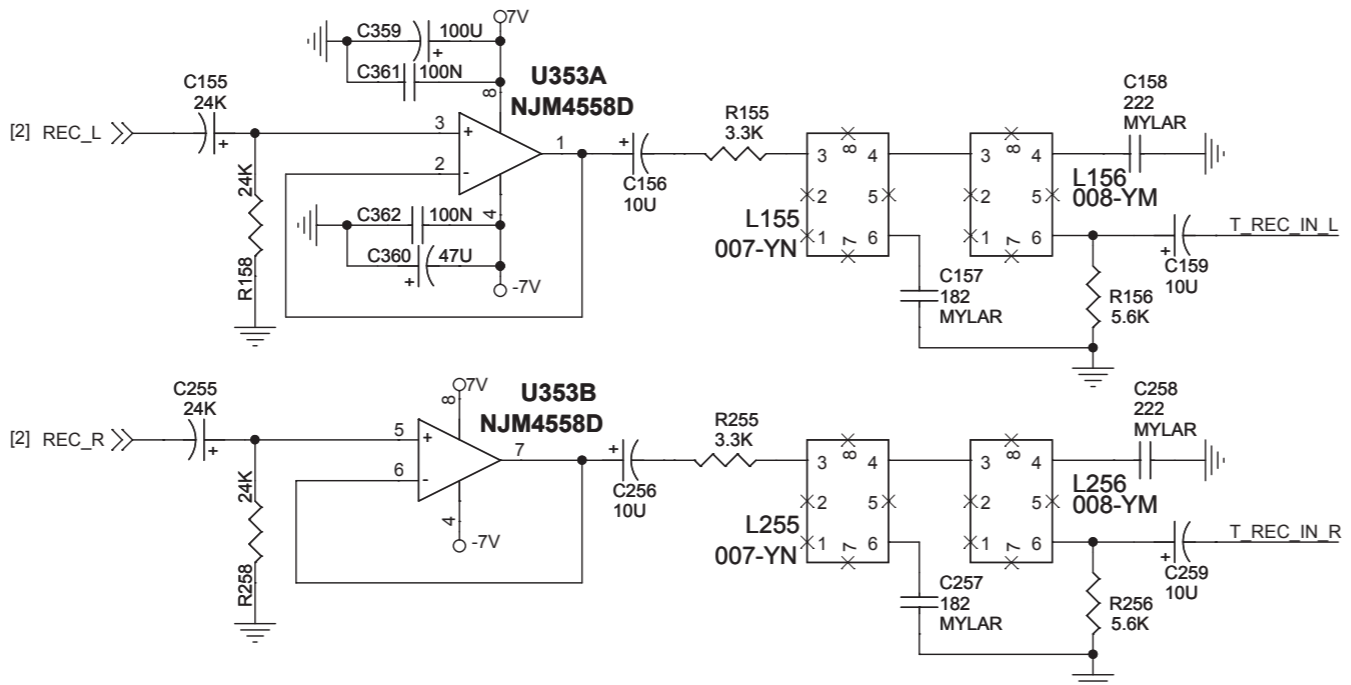
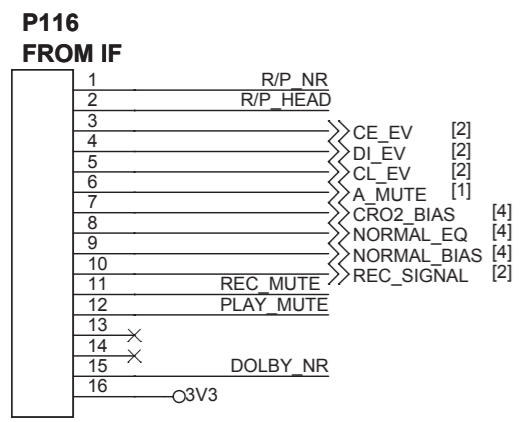
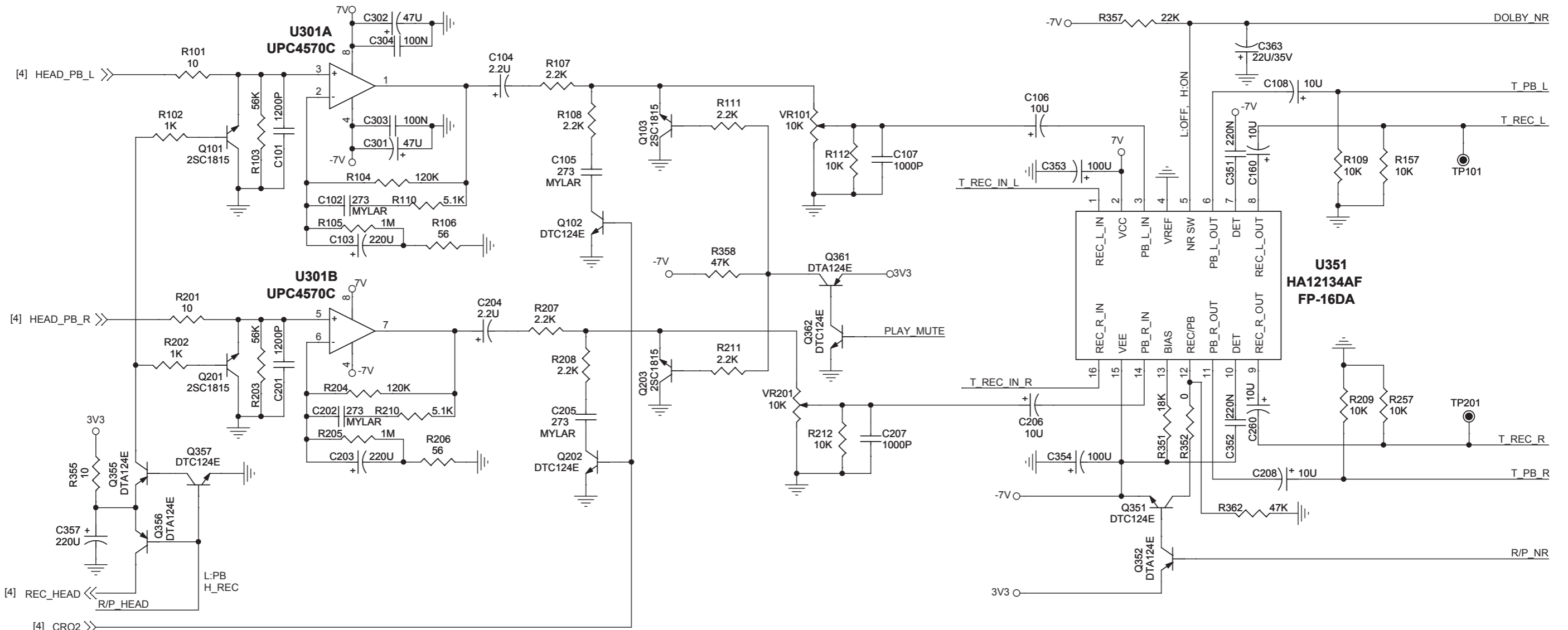
A

B

C

D

E





1 2 3 4 5 6 7 8

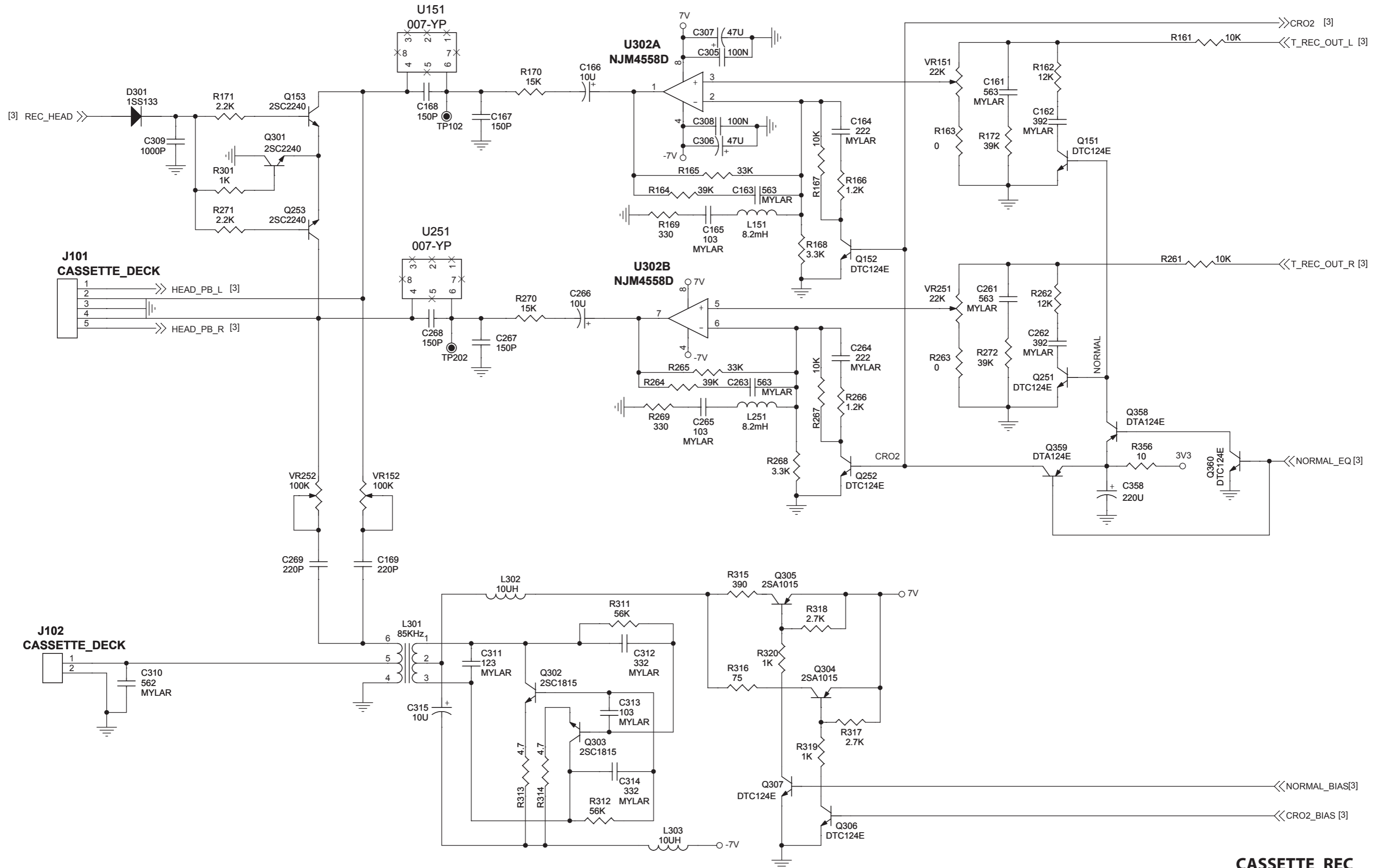
A

B

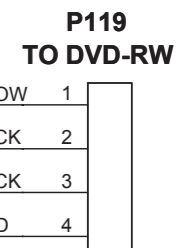
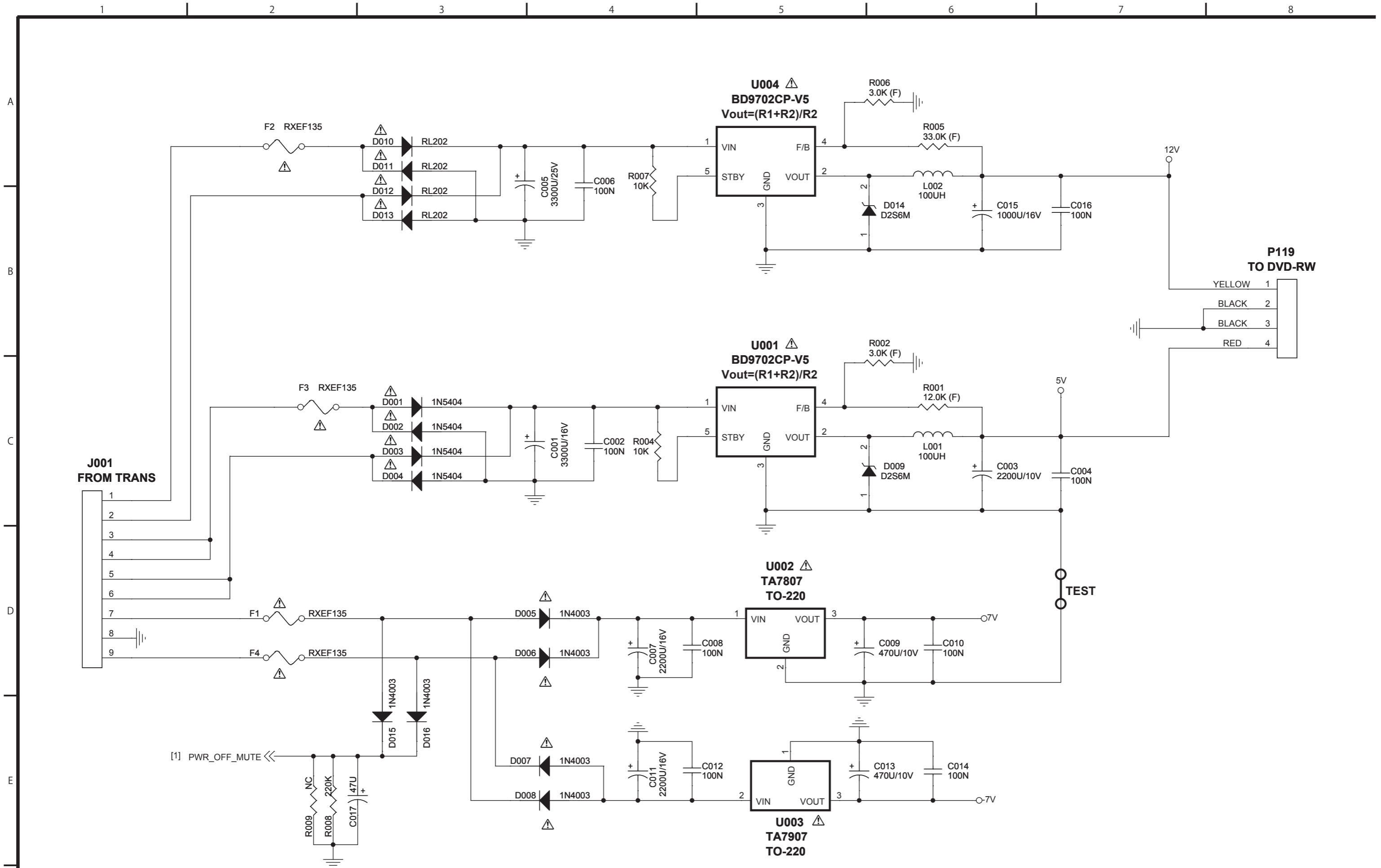
C

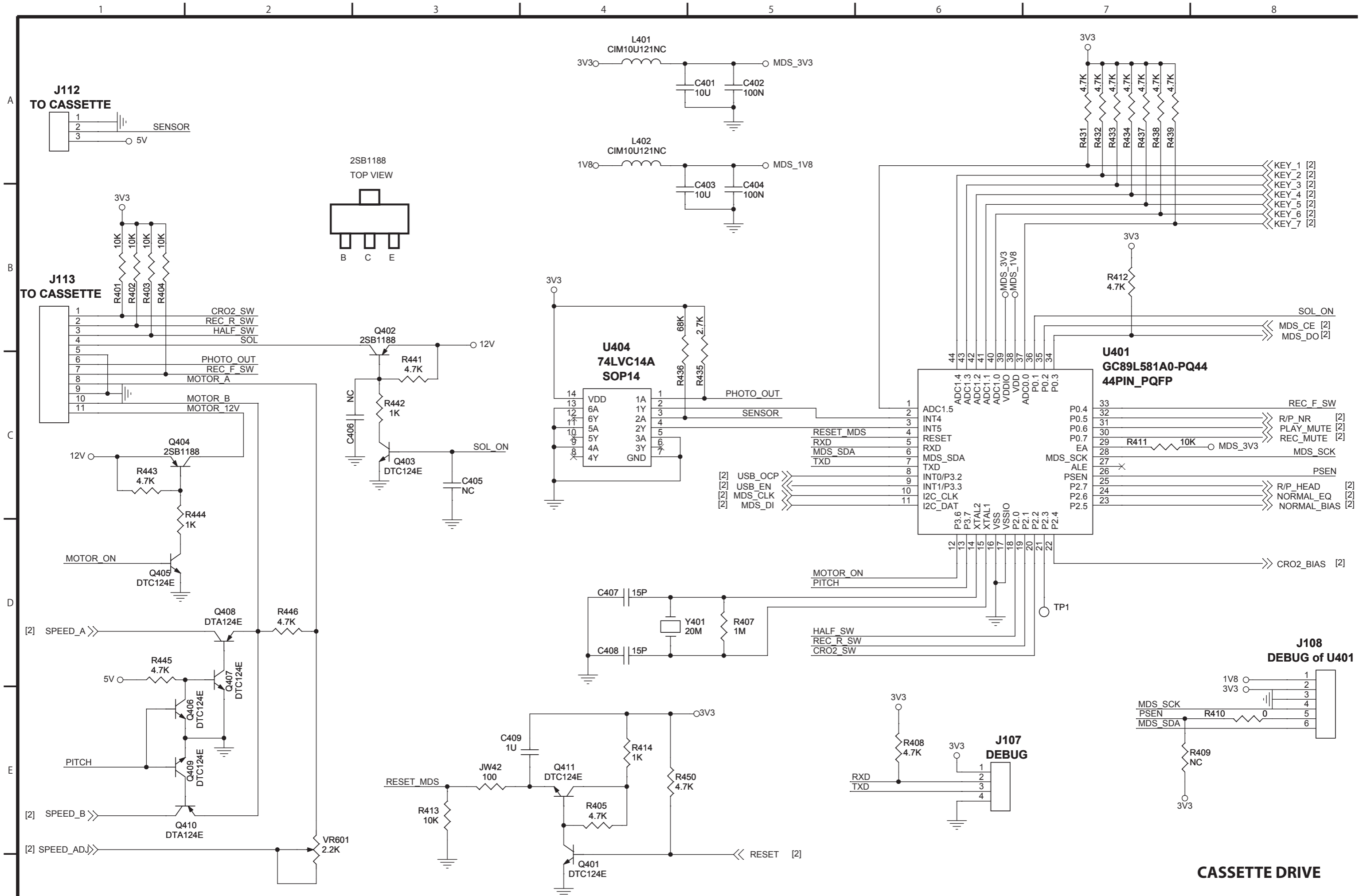
D

E

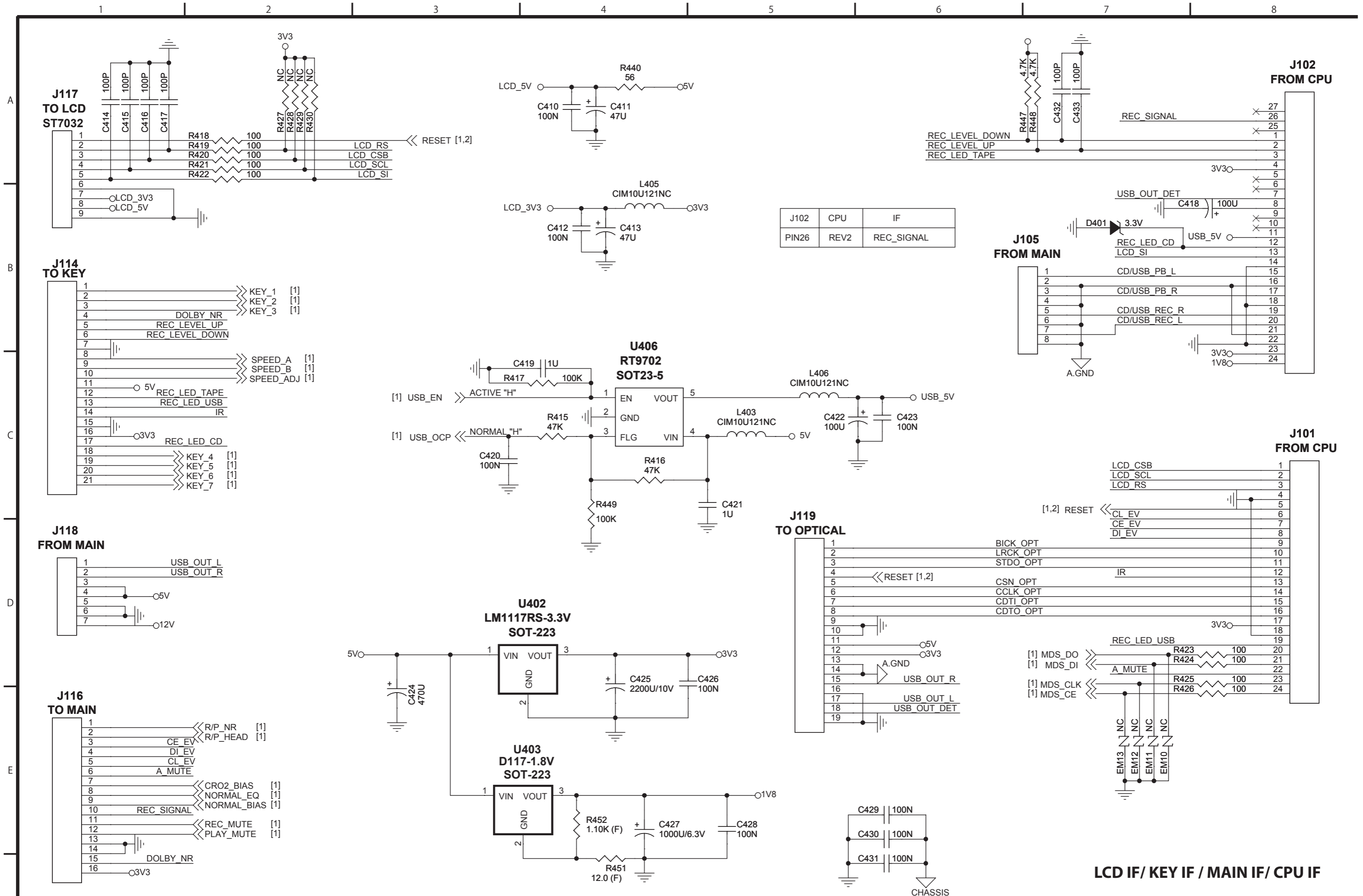


CASSETTE\_REC





**CASSETTE DRIVE**



J102	CPU	IF
PIN26	REV2	REC_SIGNAL

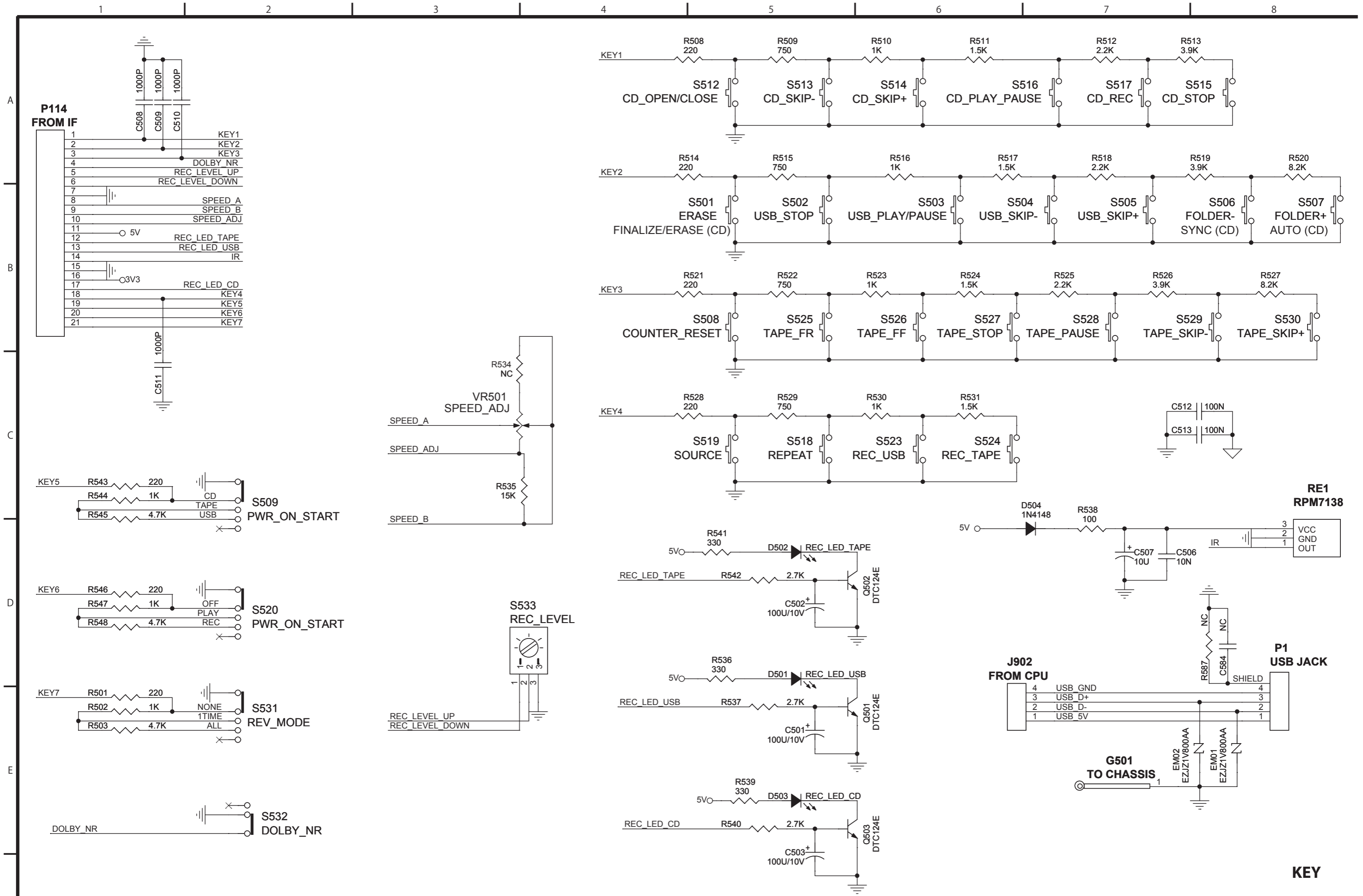
**J105 FROM MAIN**

**J102 FROM CPU**

**J101 FROM CPU**

**J119 TO OPTICAL**

LCD IF / KEY IF / MAIN IF / CPU IF



KEY

1 2 3 4 5 6 7 8

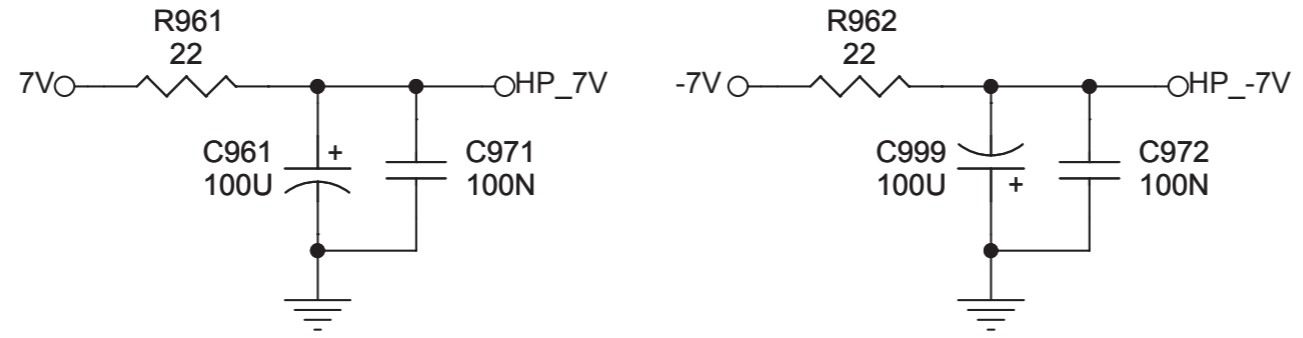
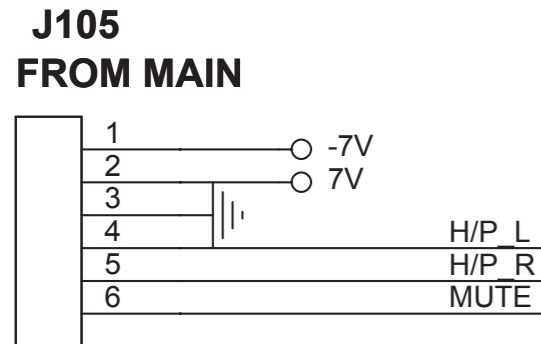
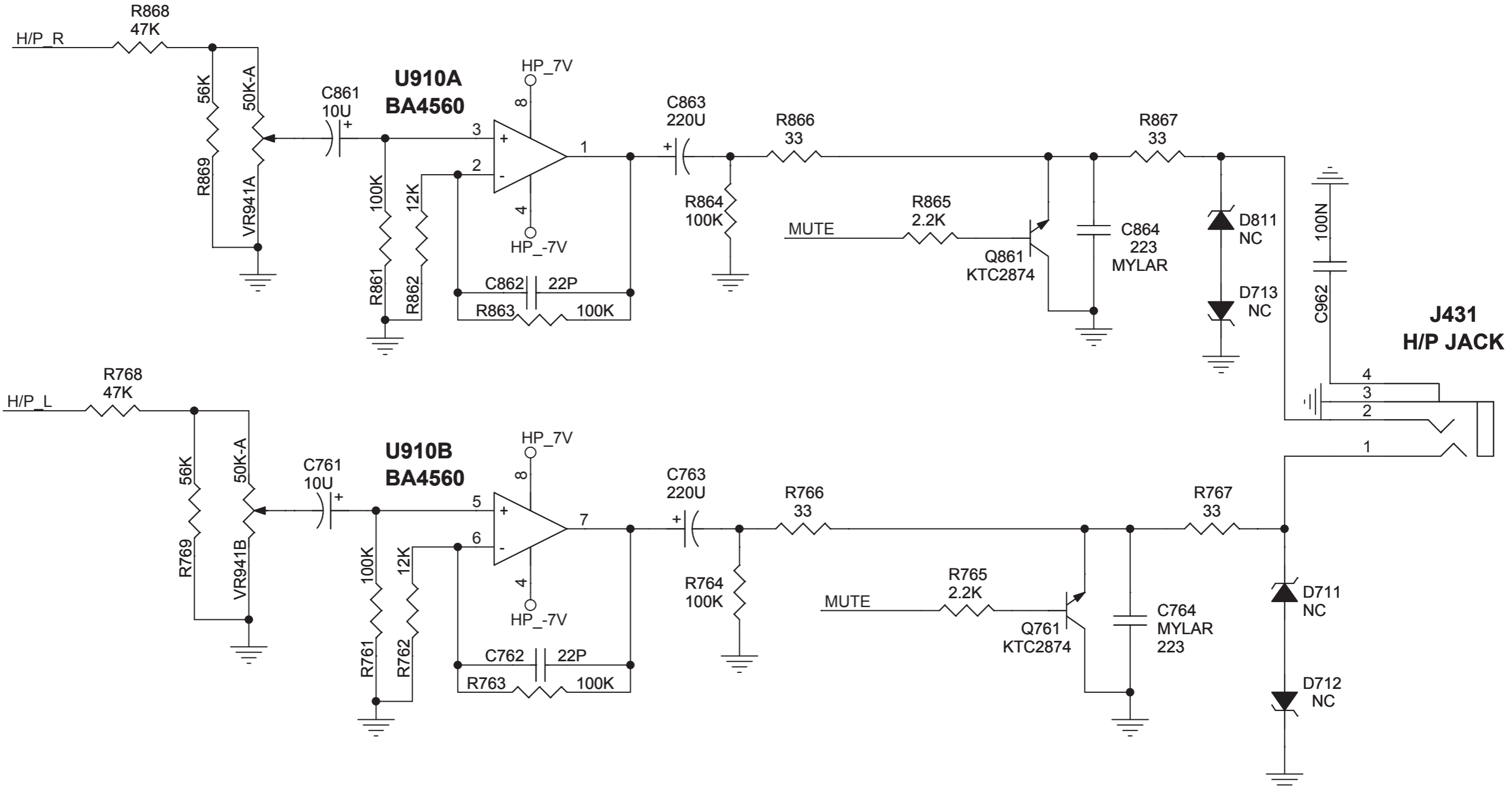
A

B

C

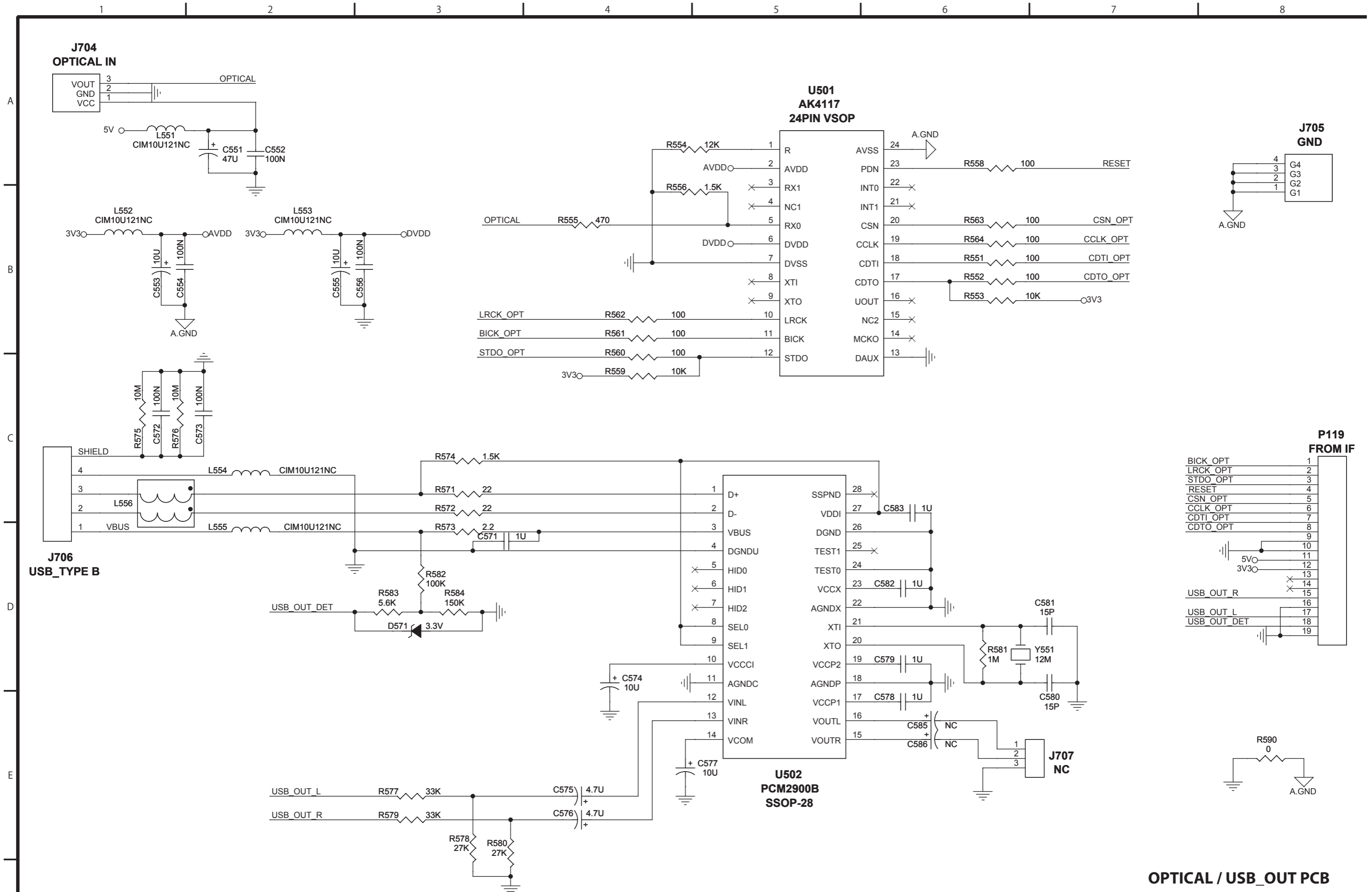
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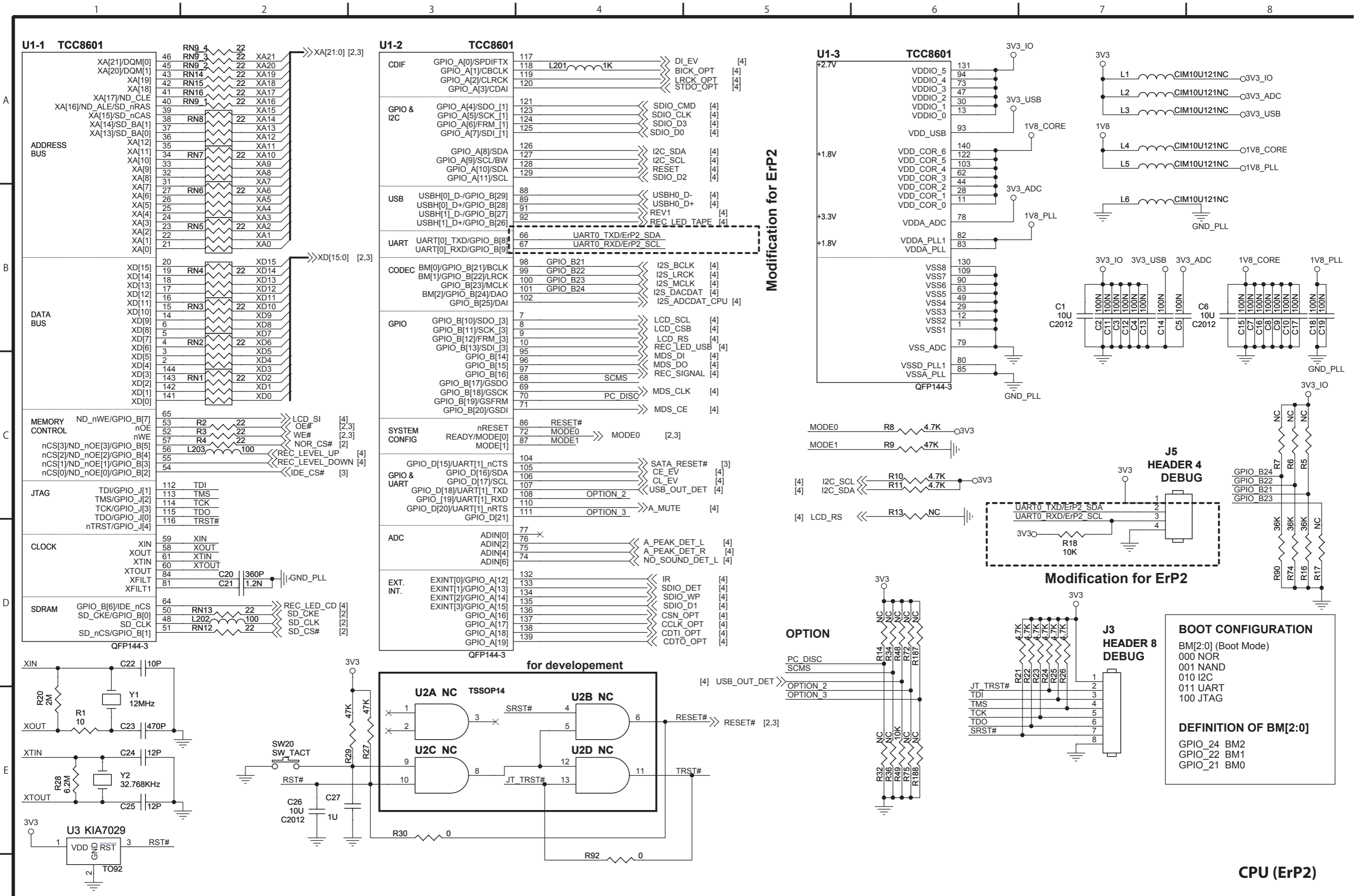
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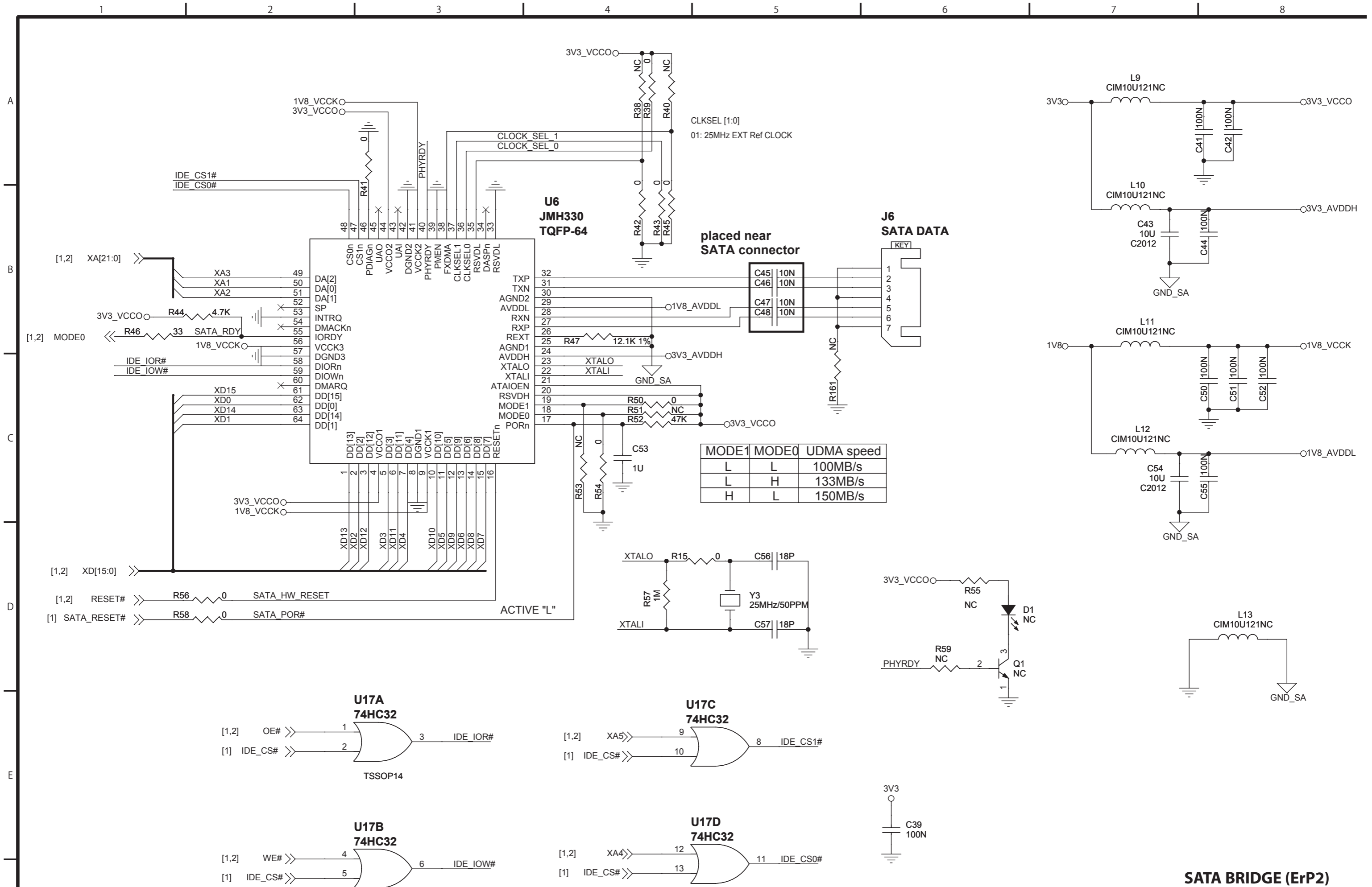
HEAD PHONE



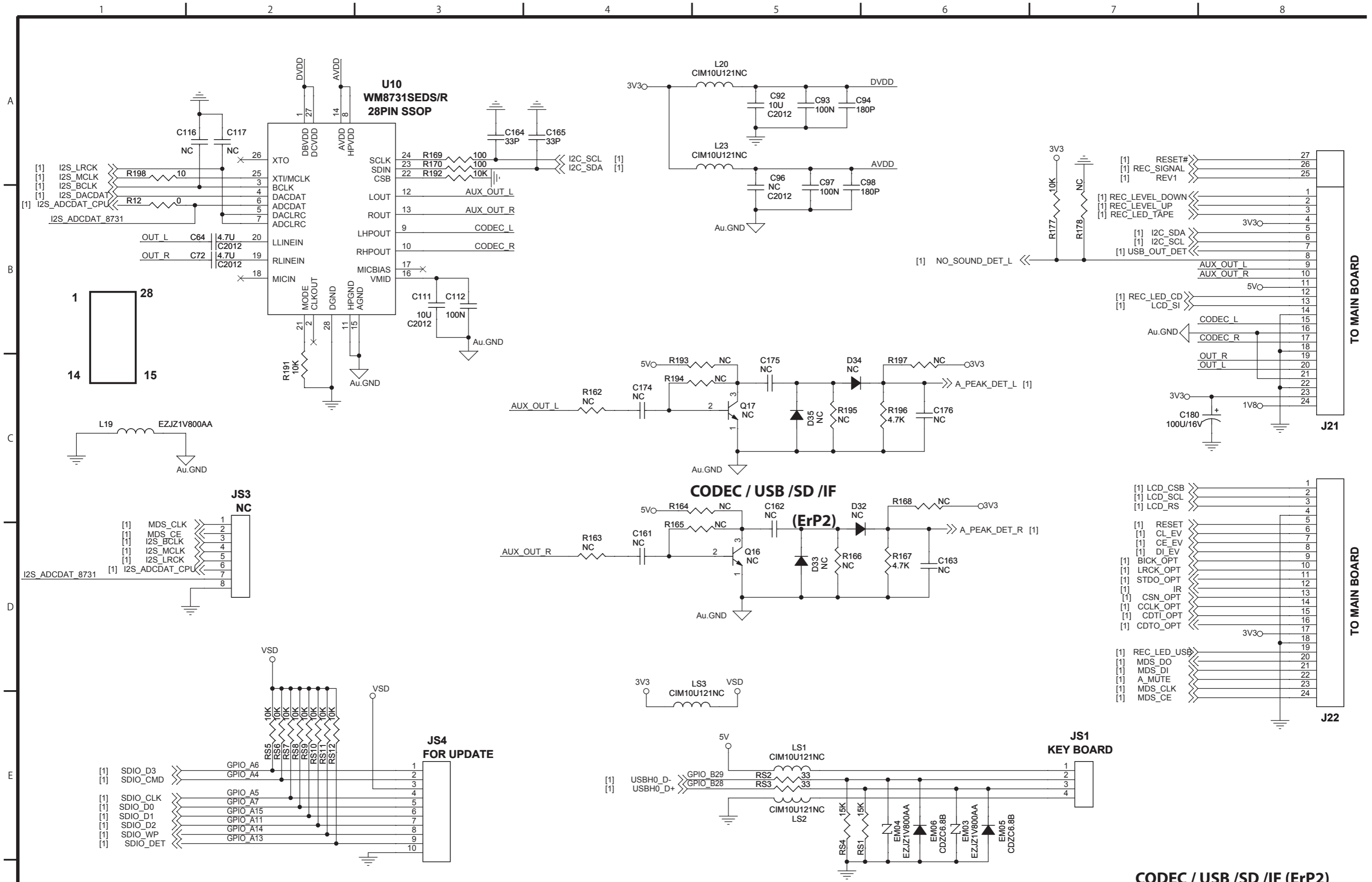






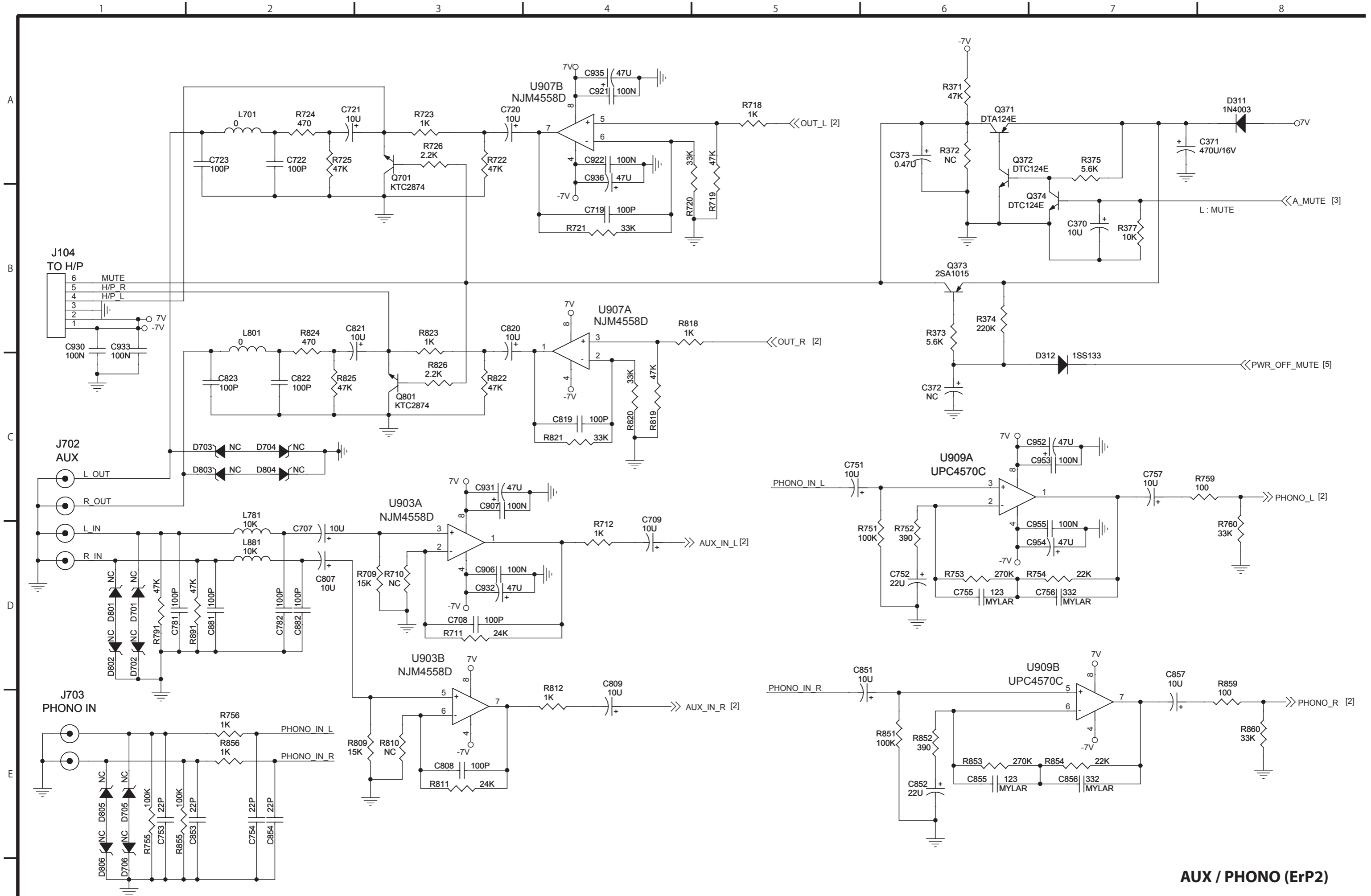


SATA BRIDGE (ErP2)



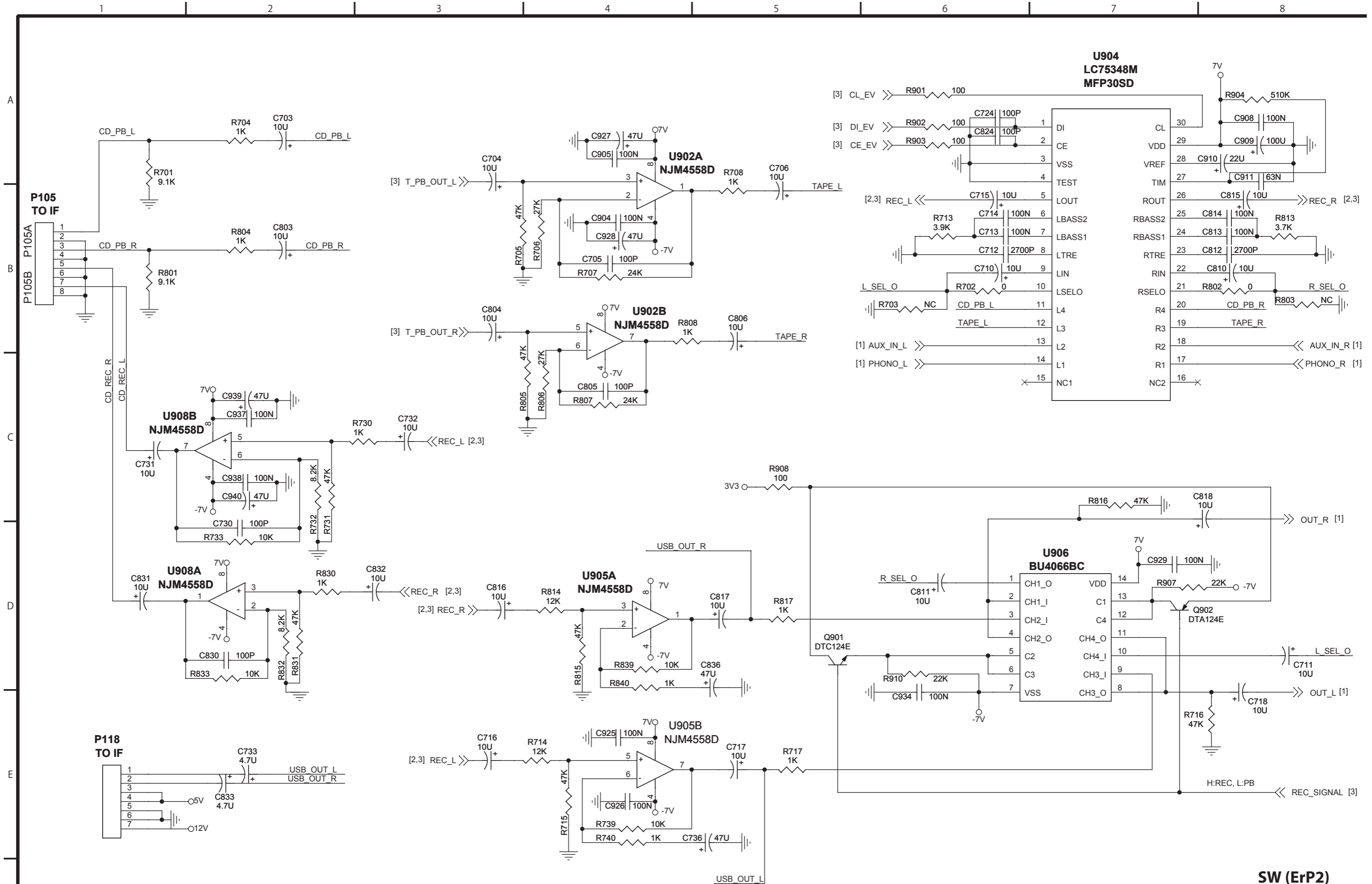
CODEC / USB / SD / IF (ErP2)





**AUX / PHONO (ErP2)**





1 2 3 4 5 6 7 8

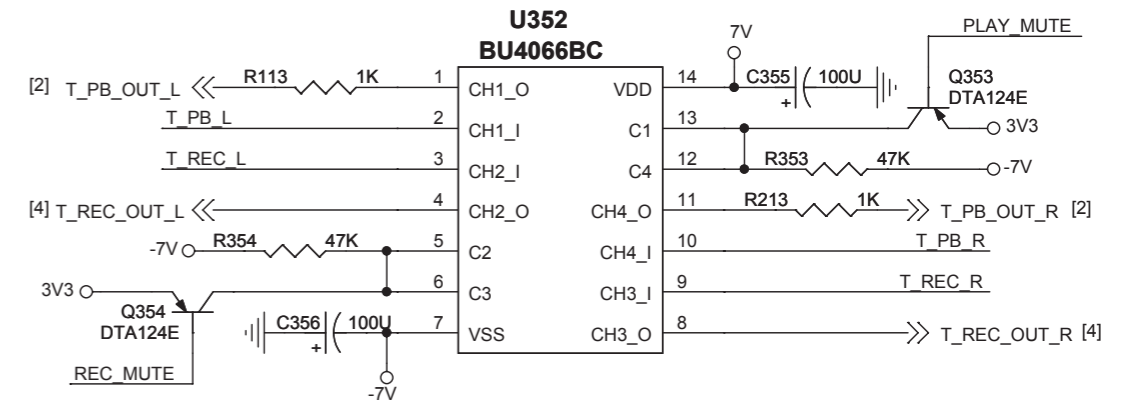
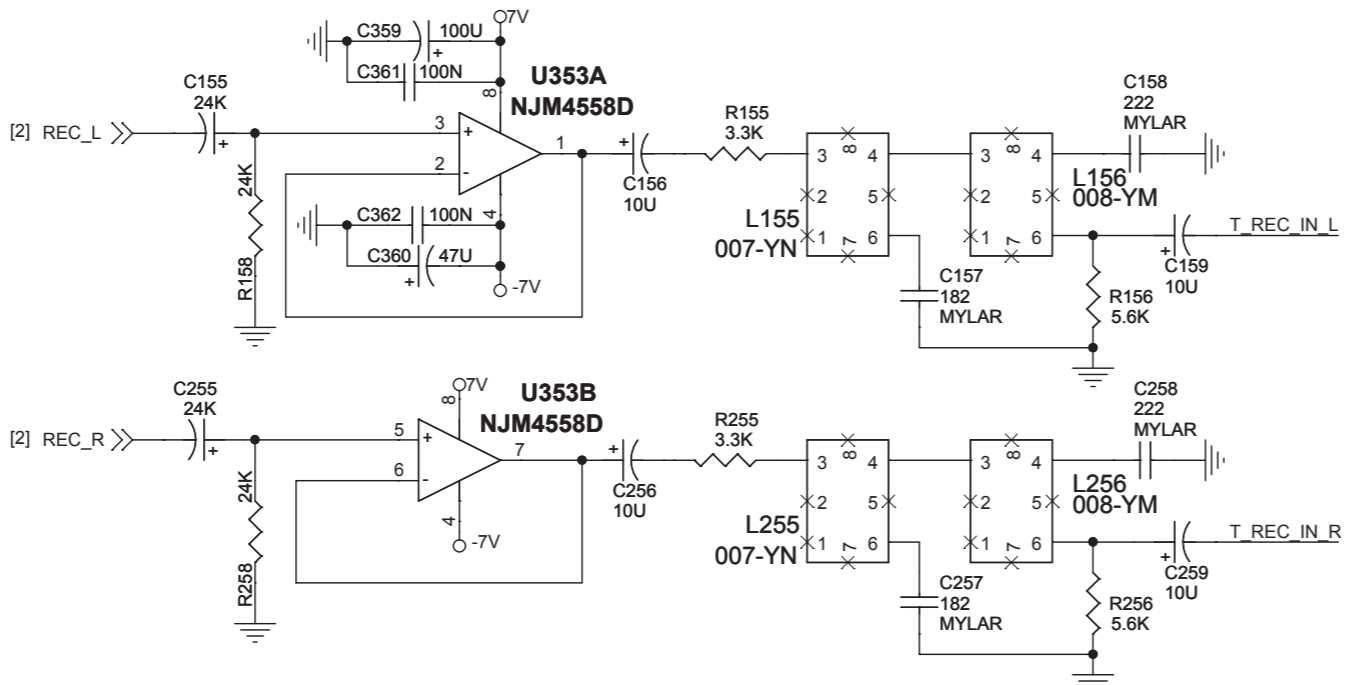
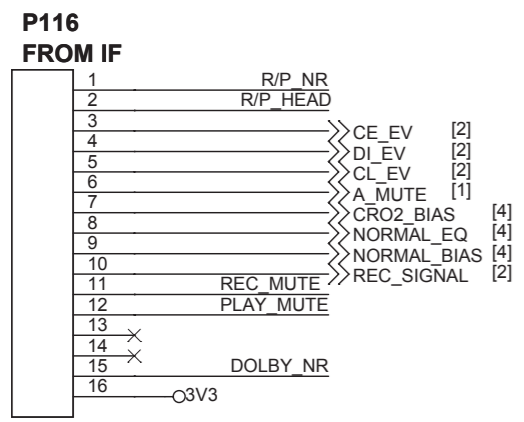
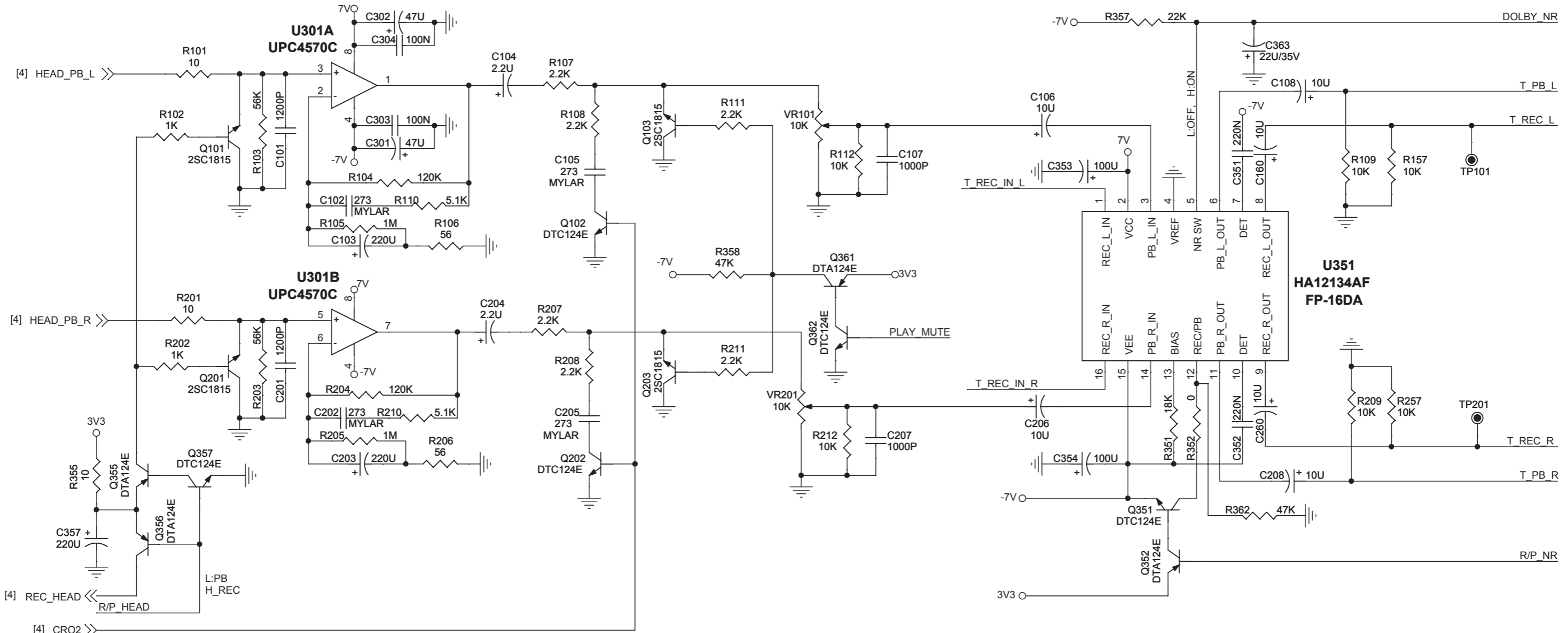
A

B

C

D

E



1 2 3 4 5 6 7 8

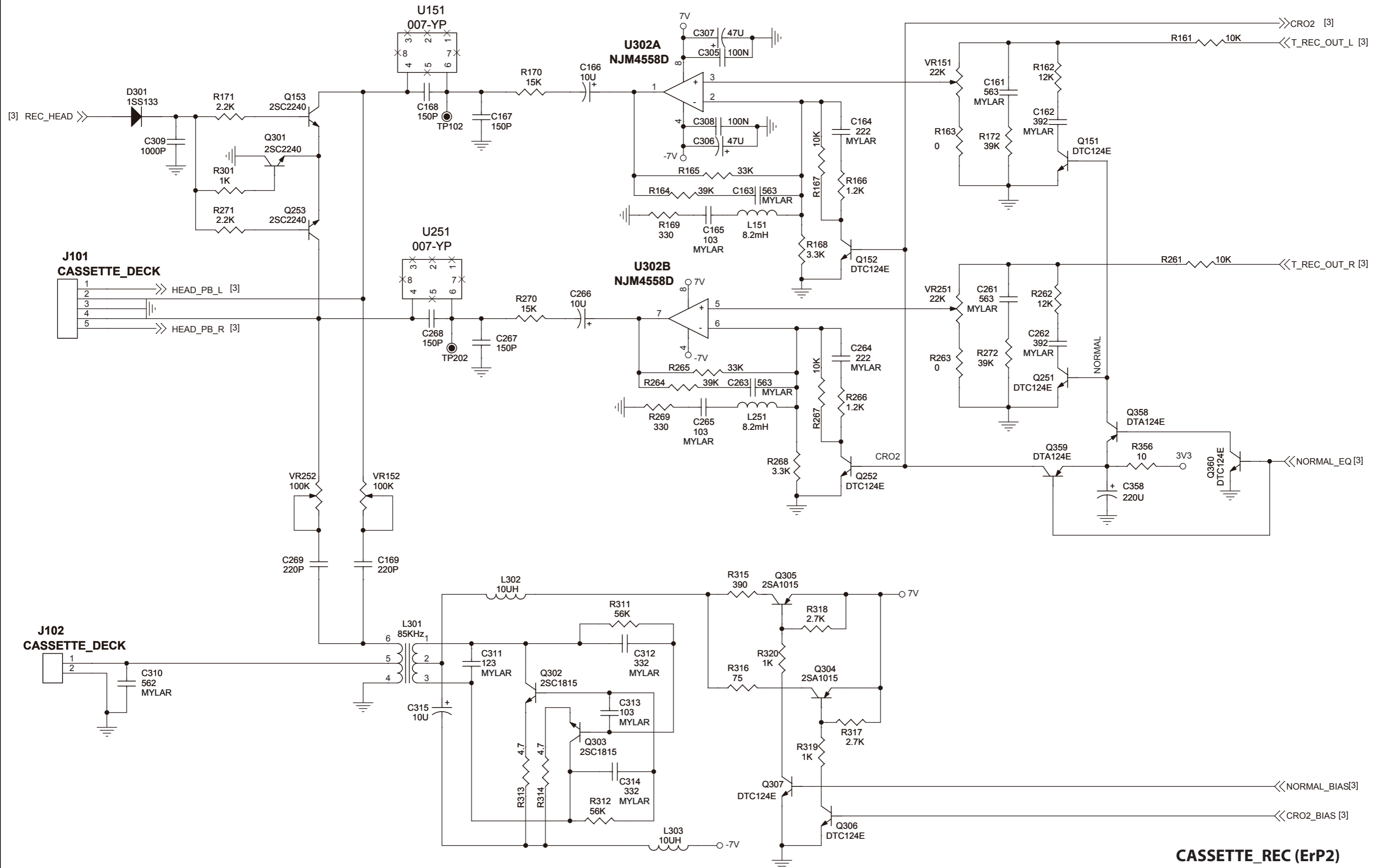
A

B

C

D

E



CASSETTE\_REC (ErP2)

1 2 3 4 5 6 7 8

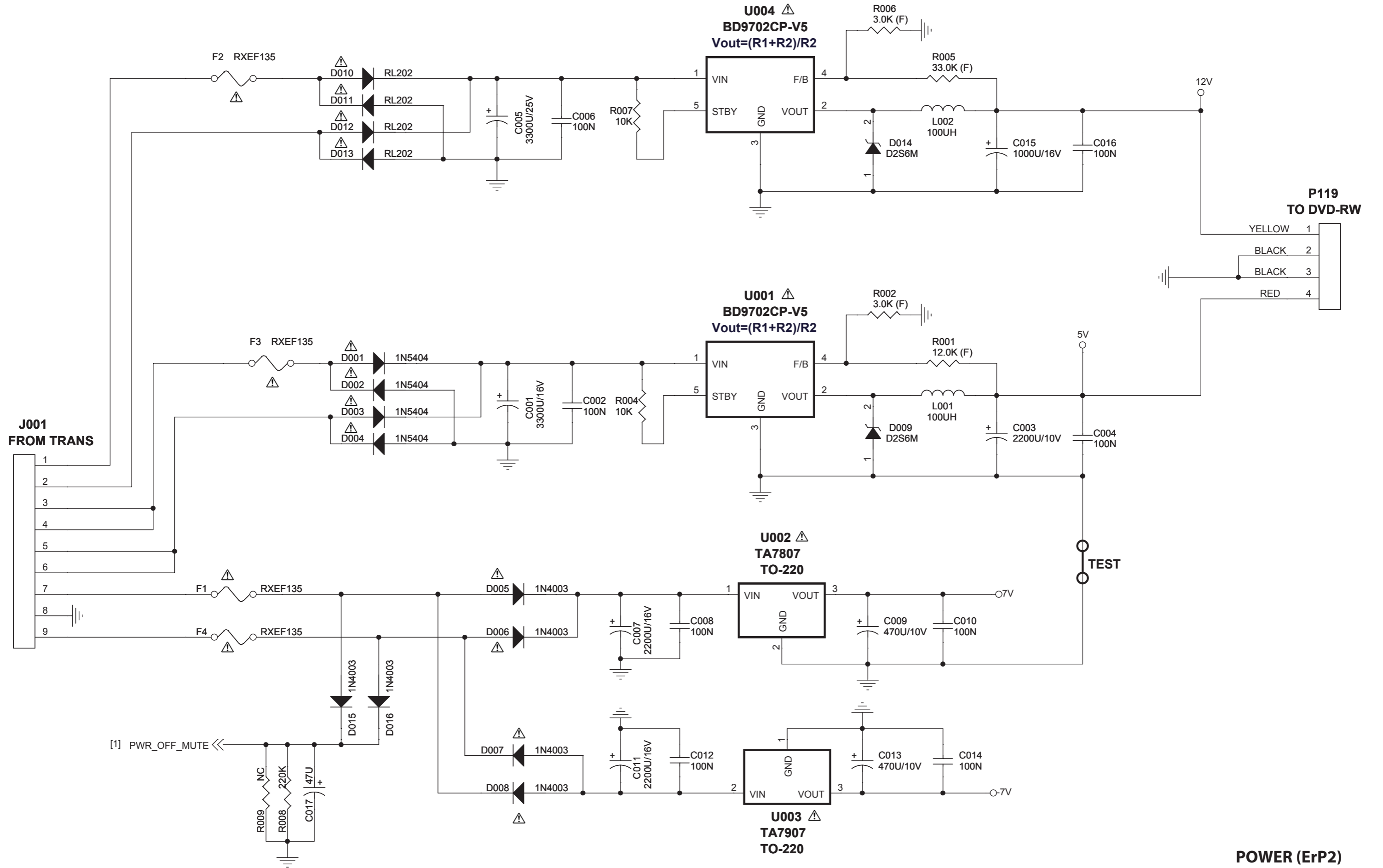
A

B

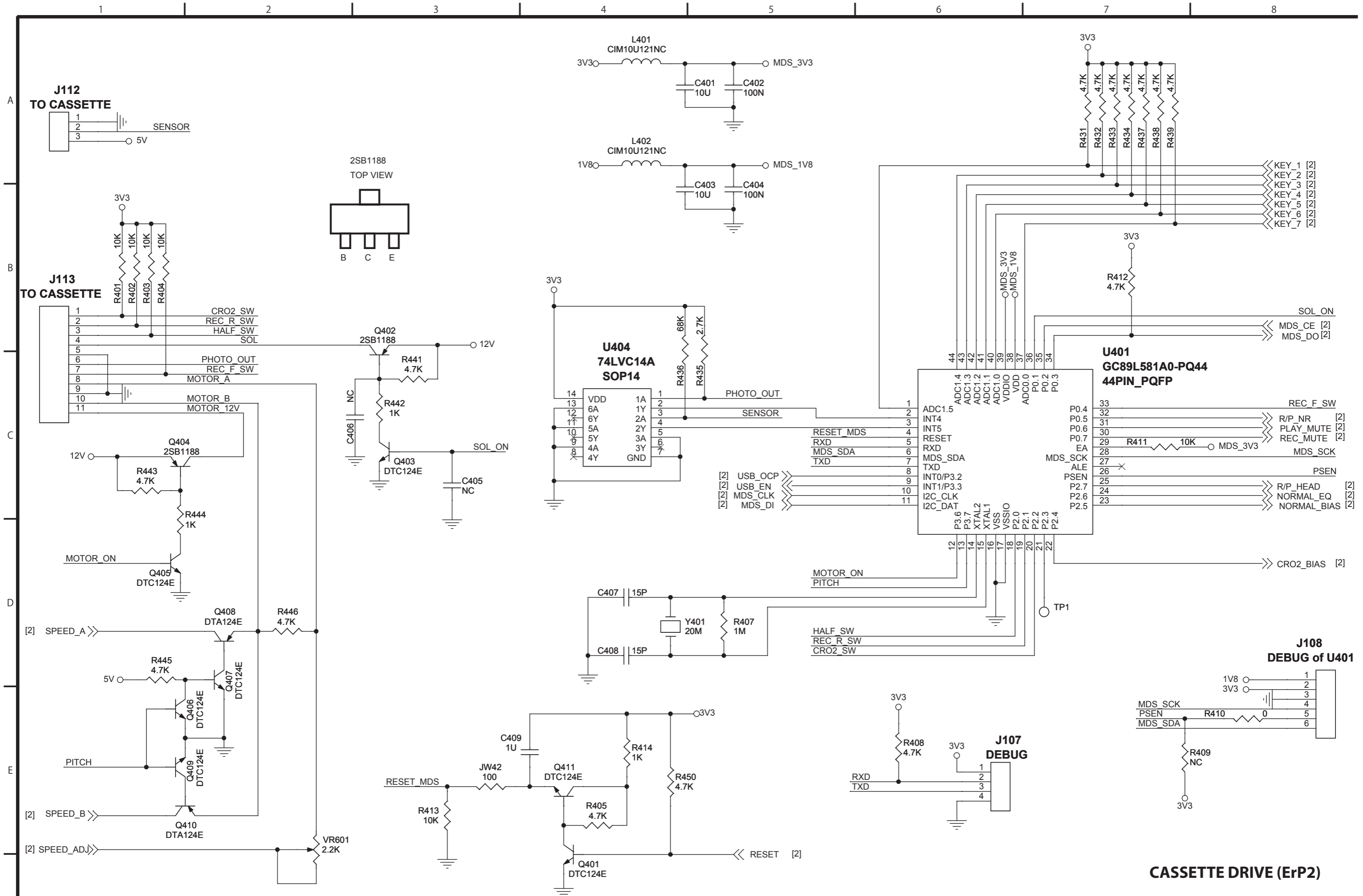
C

D

E



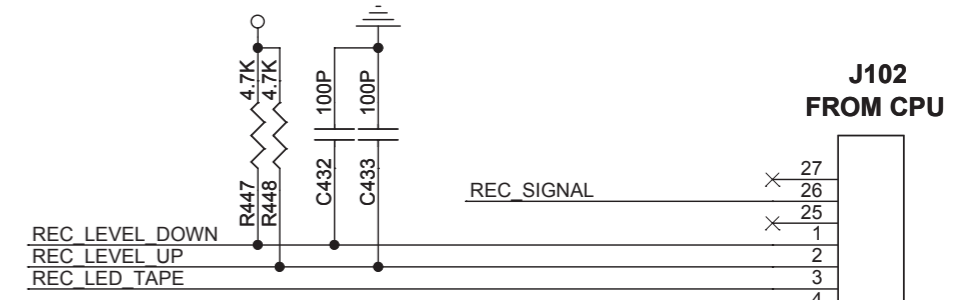
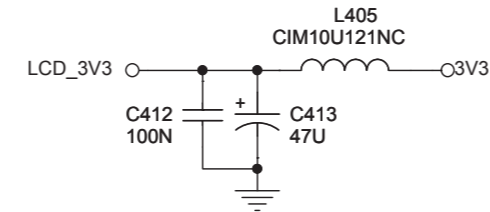
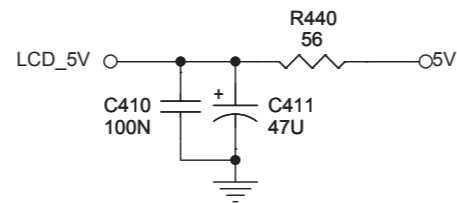
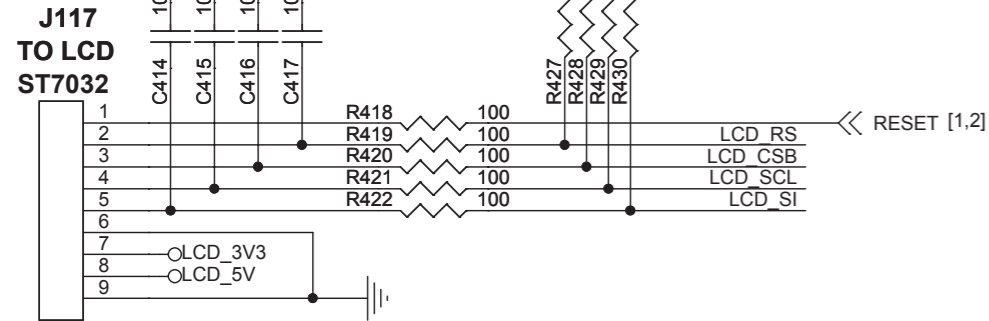
POWER (ErP2)



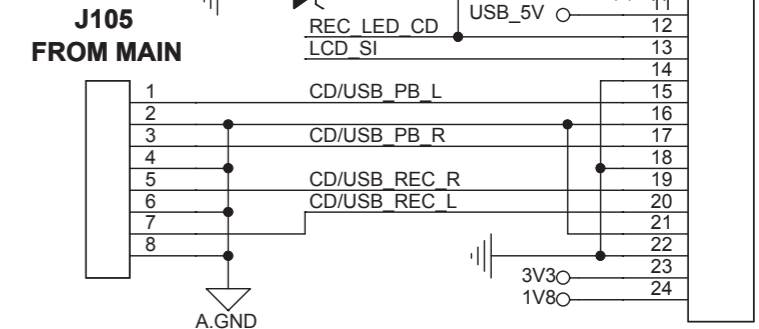
**CASSETTE DRIVE (ErP2)**

1 2 3 4 5 6 7 8

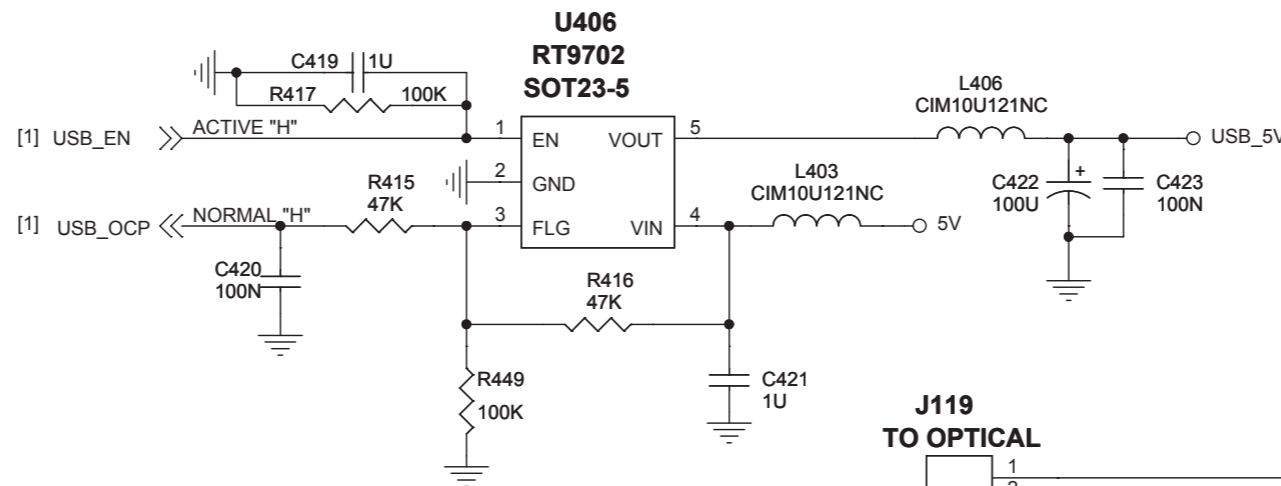
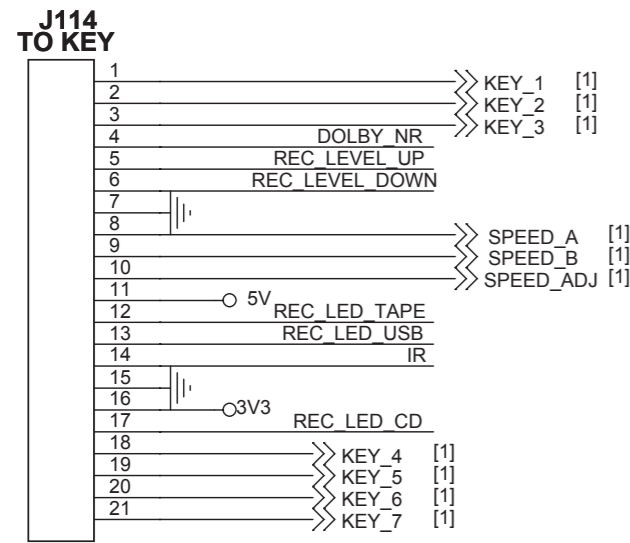
A



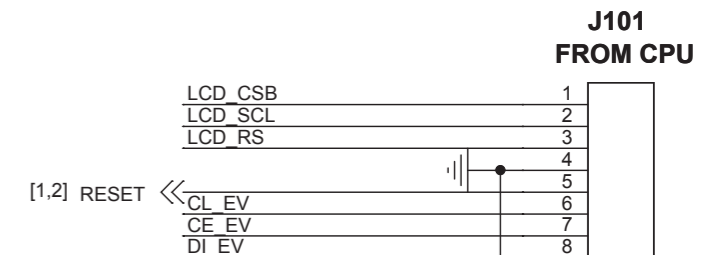
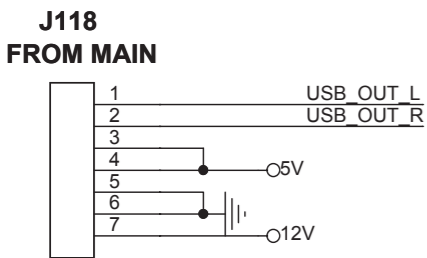
J102	CPU	IF
PIN26	REV2	REC_SIGNAL



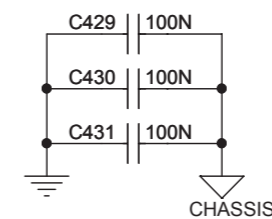
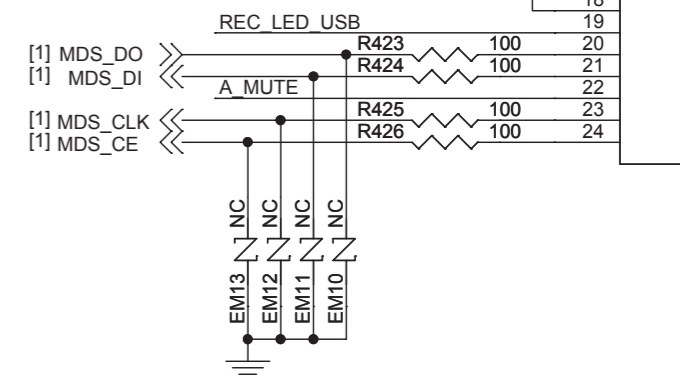
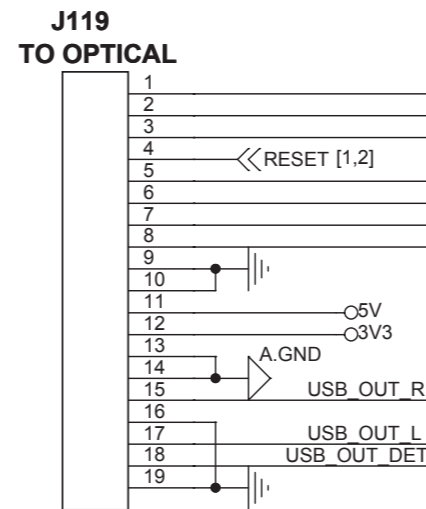
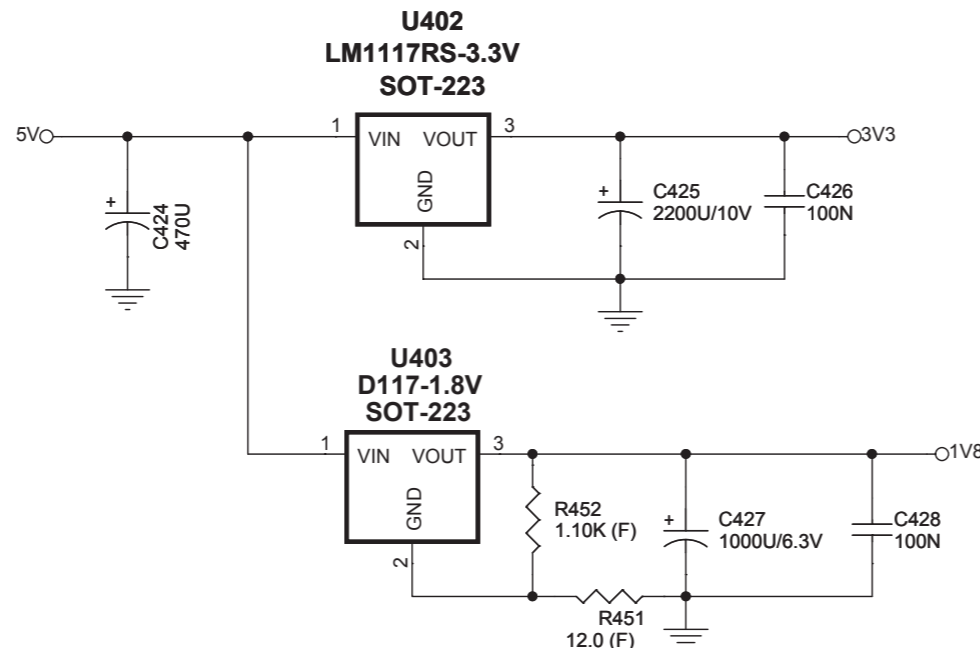
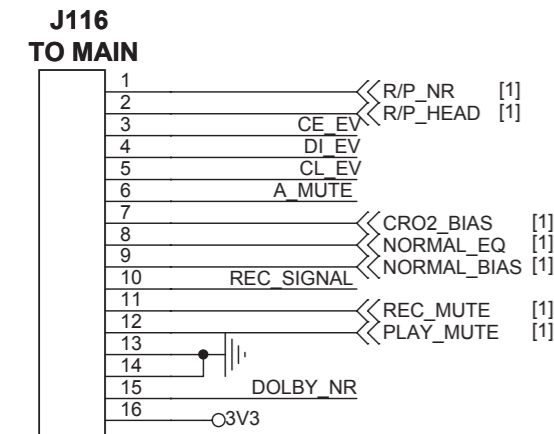
B



C



D





1 2 3 4 5 6 7 8

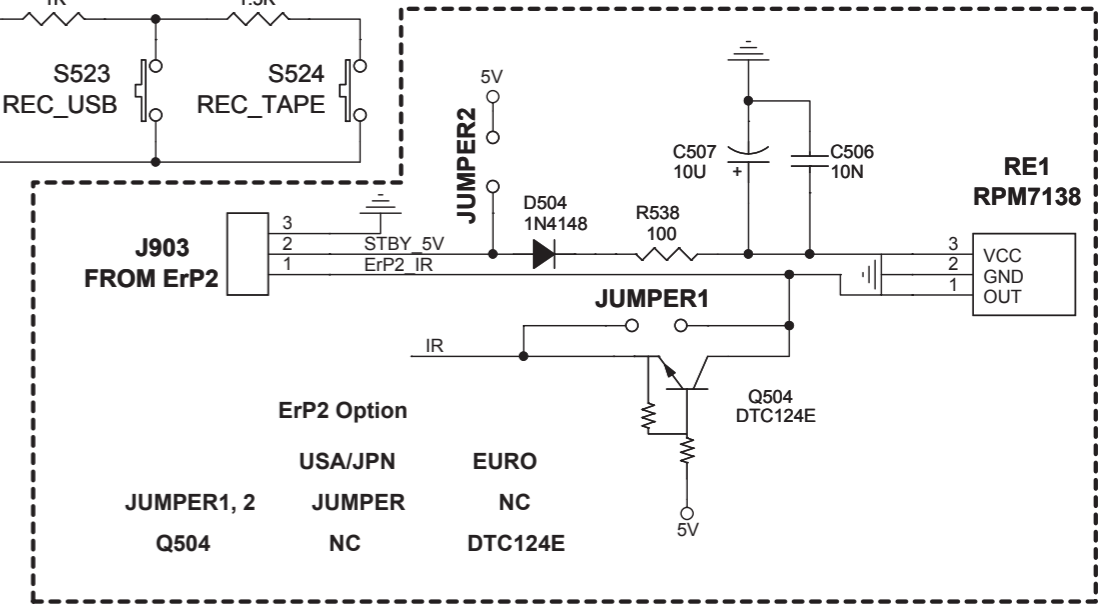
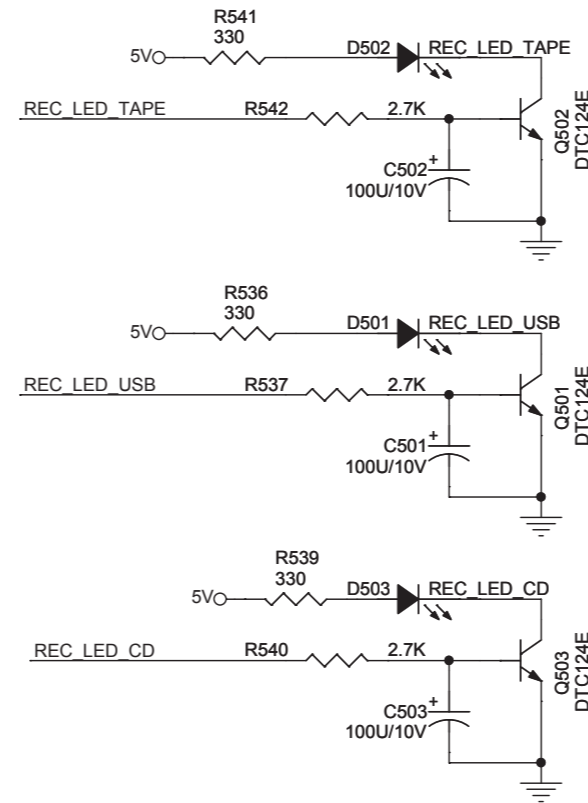
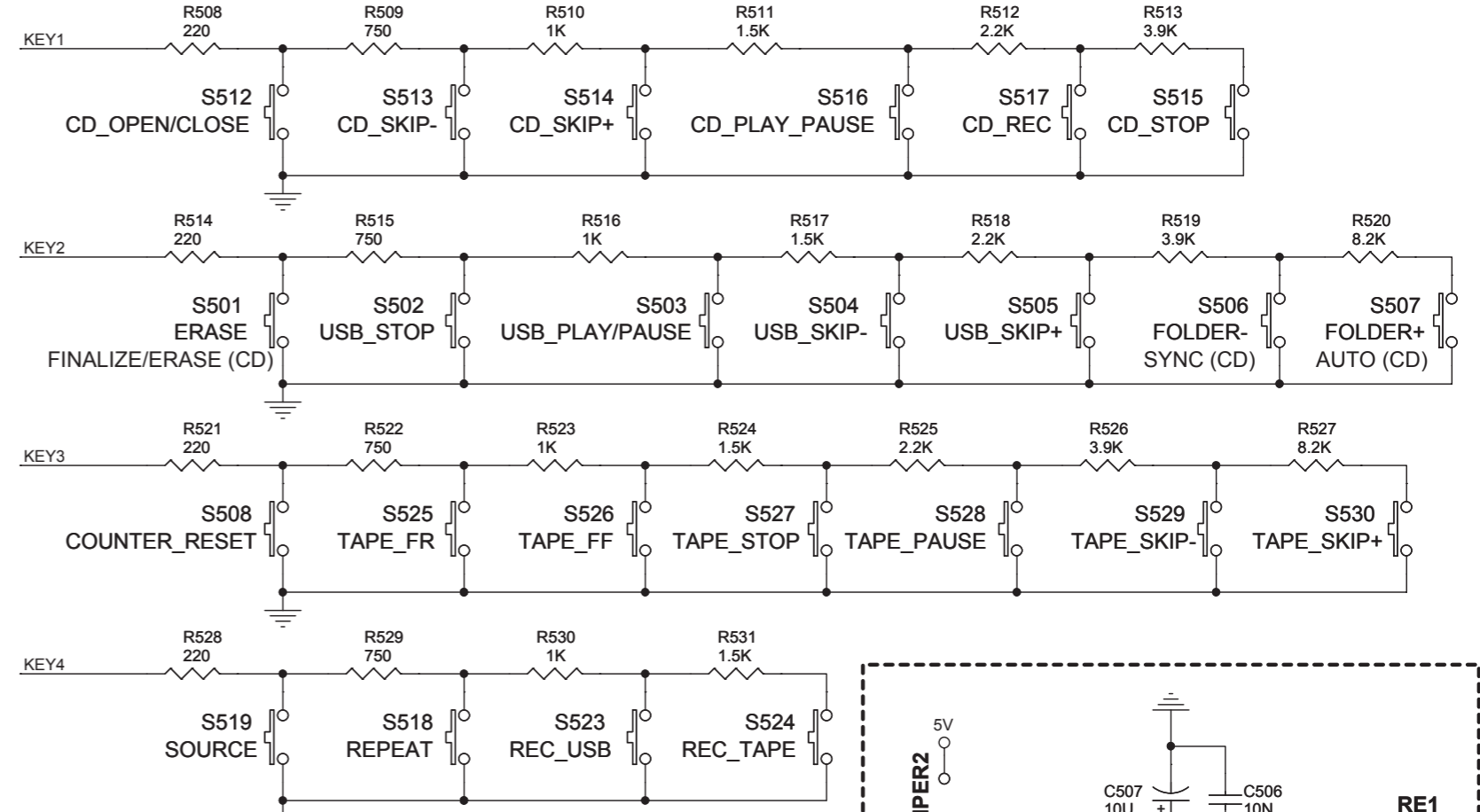
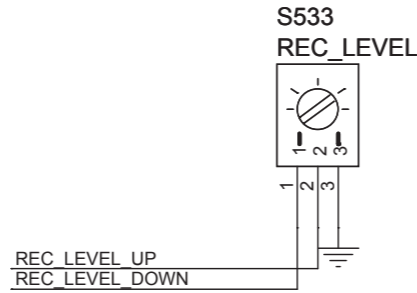
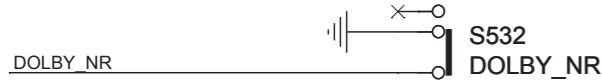
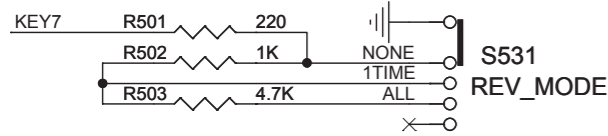
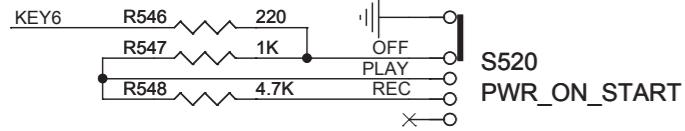
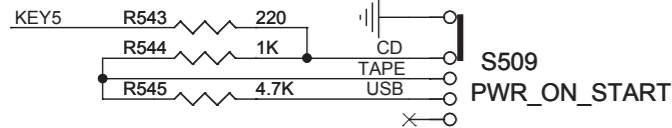
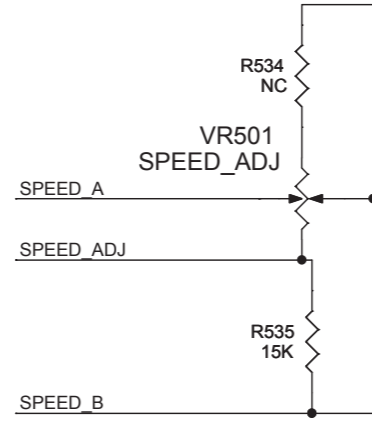
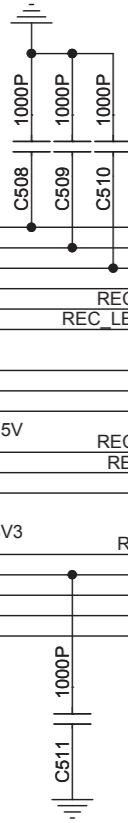
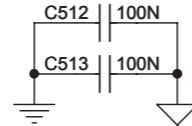
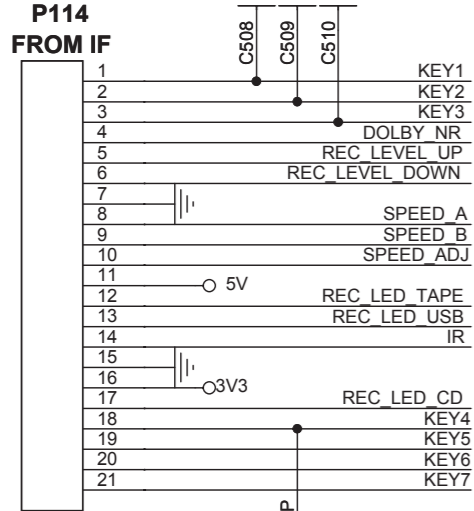
A

B

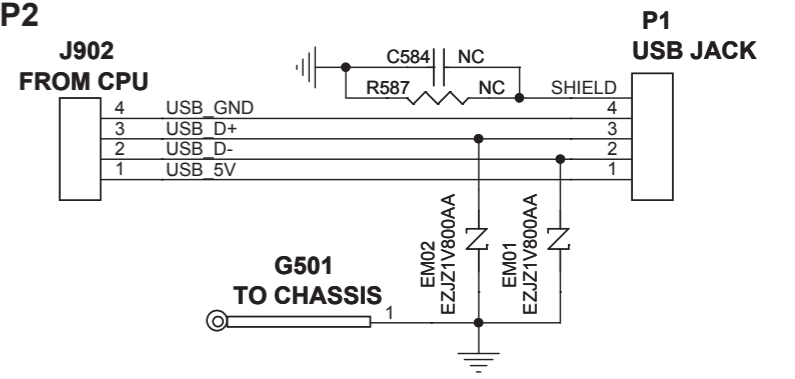
C

D

E



Modification for ErP2



KEY (ErP2)

1 2 3 4 5 6 7 8

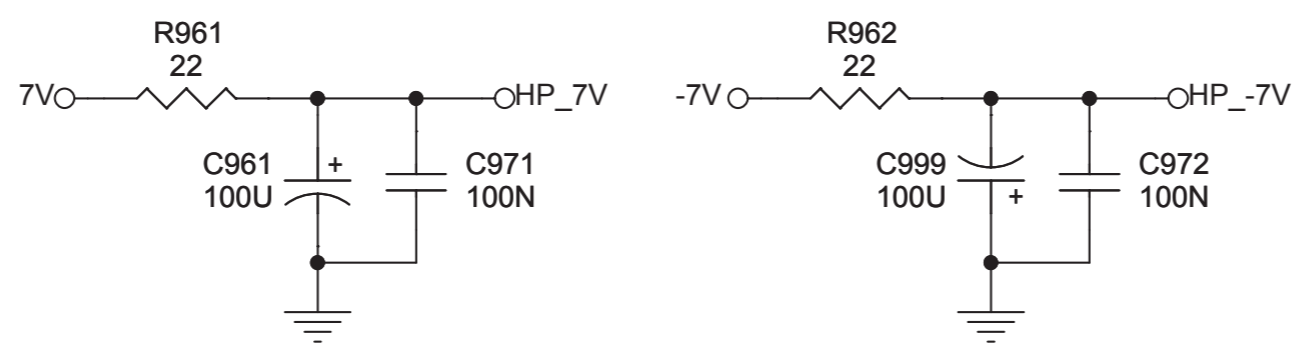
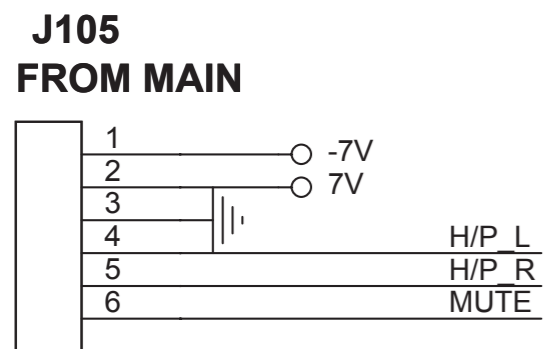
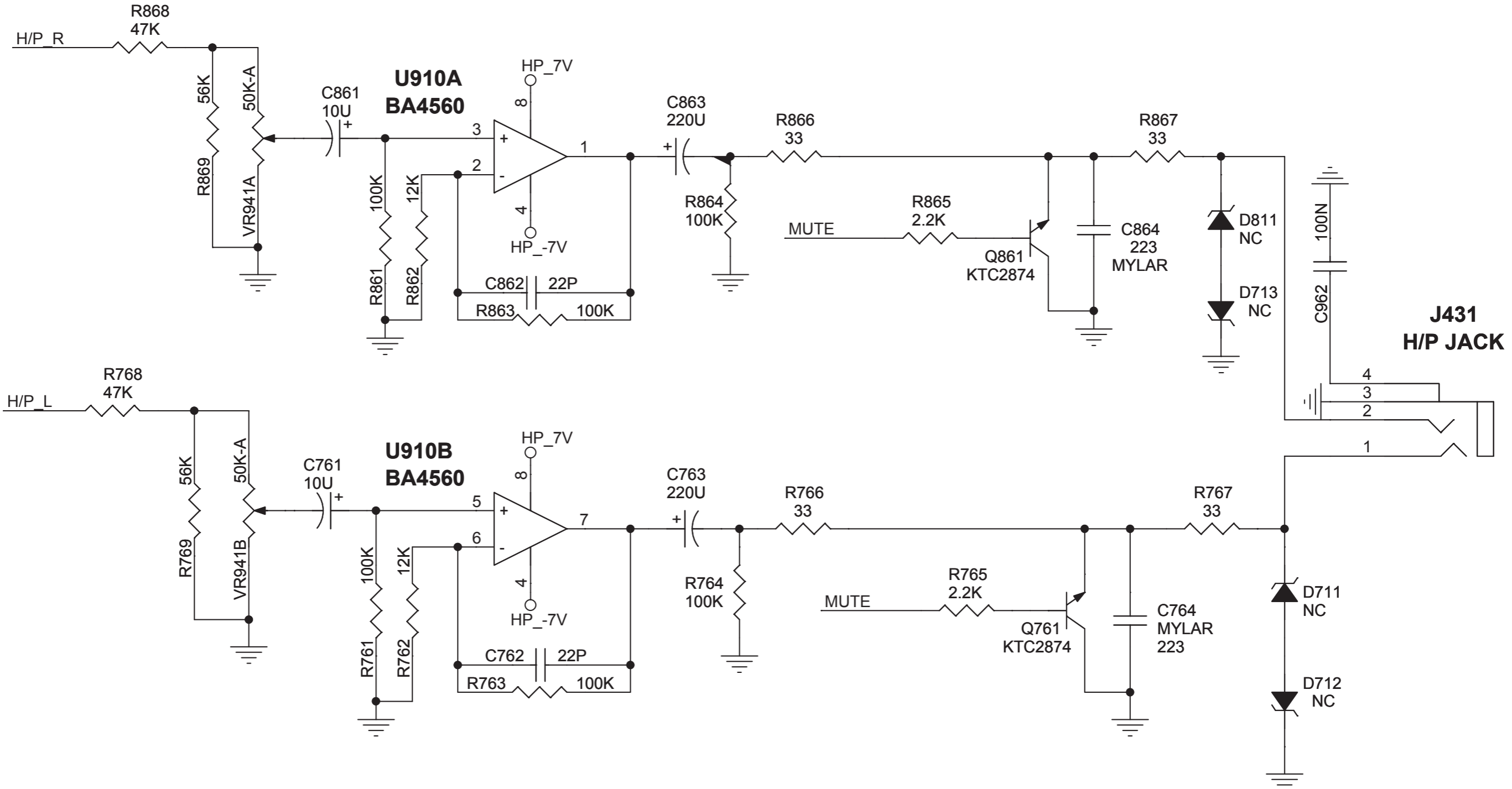
A

B

C

D

E



HEAD PHONE (ErP2)

1 2 3 4 5 6 7 8

A

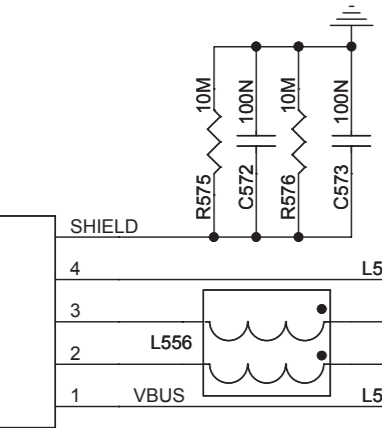
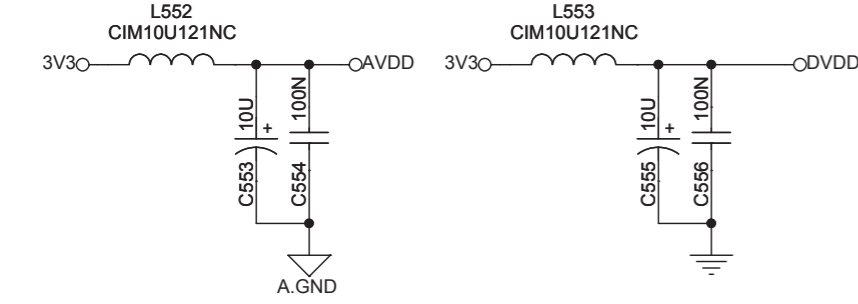
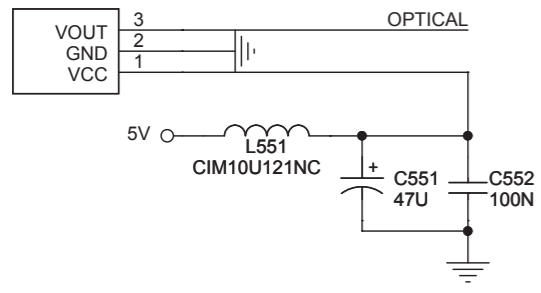
B

C

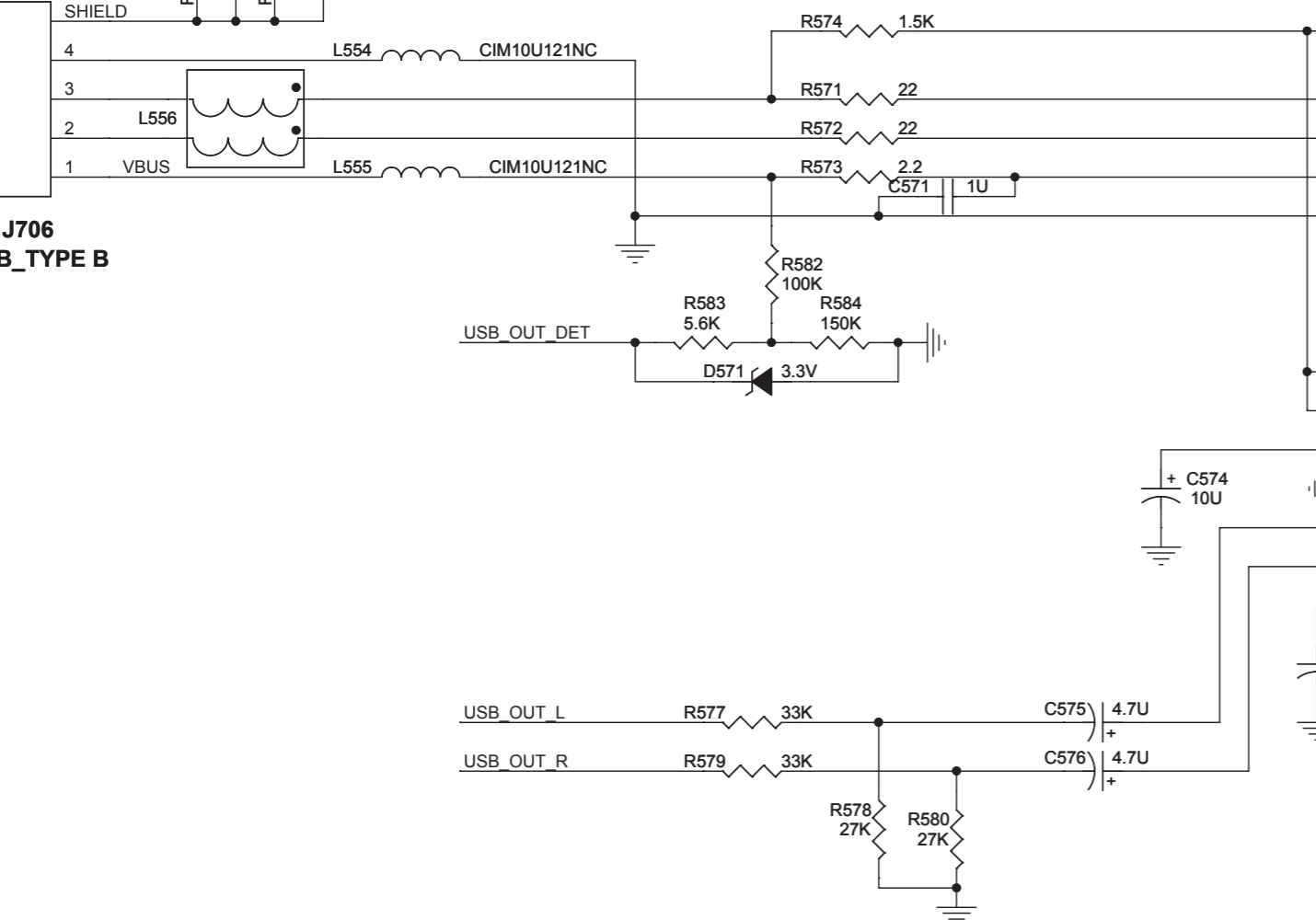
D

E

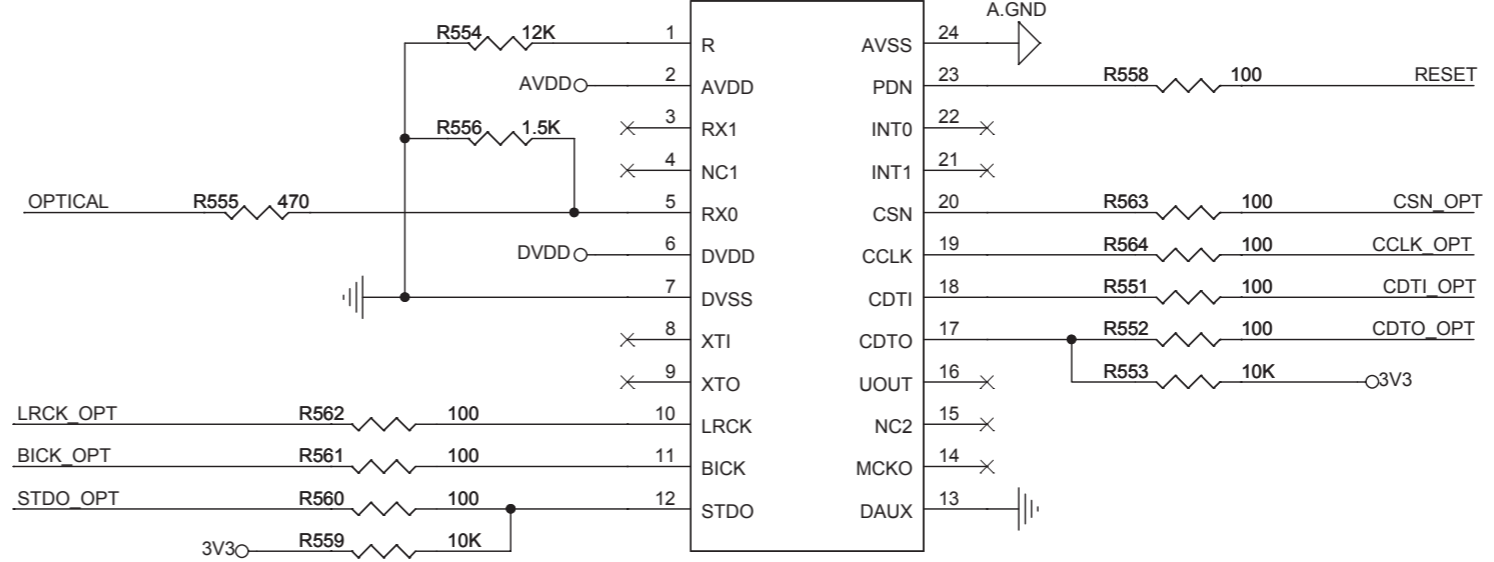
**J704  
OPTICAL IN**



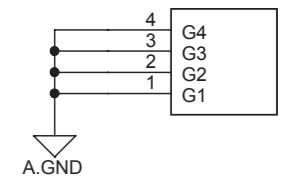
**J706  
USB\_TYPE B**



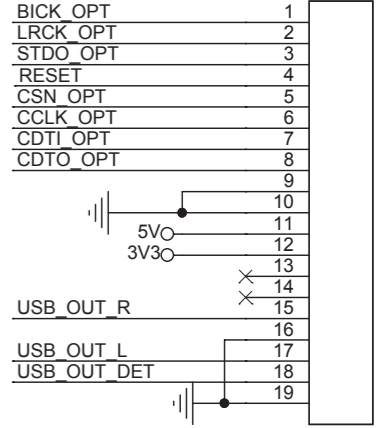
**U501  
AK4117  
24PIN VSOP**



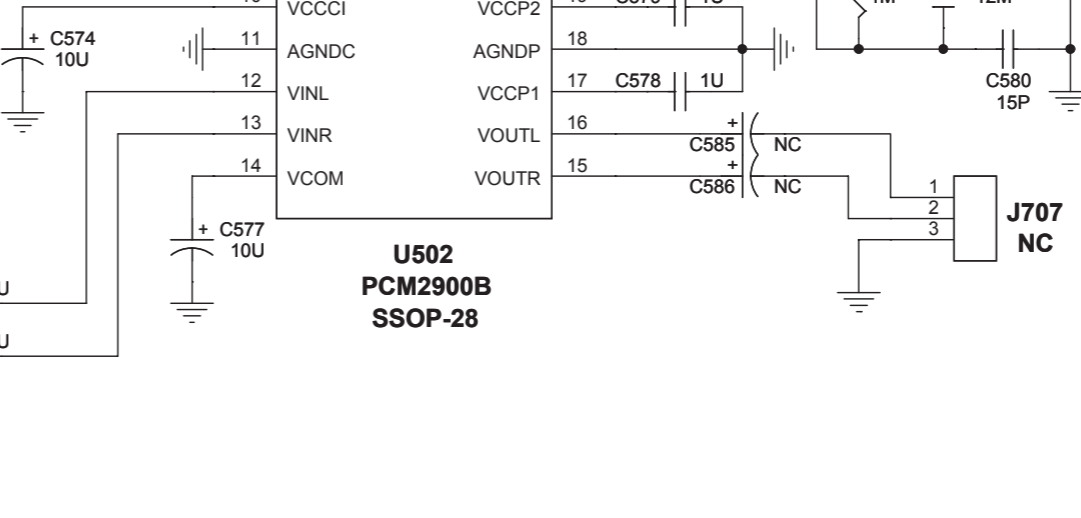
**J705  
GND**



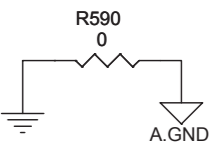
**P119  
FROM IF**



**U502  
PCM2900B  
SSOP-28**



**J707  
NC**



1 2 3 4 5 6 7 8

A

B

C

D

E

**MAIN POWER**

