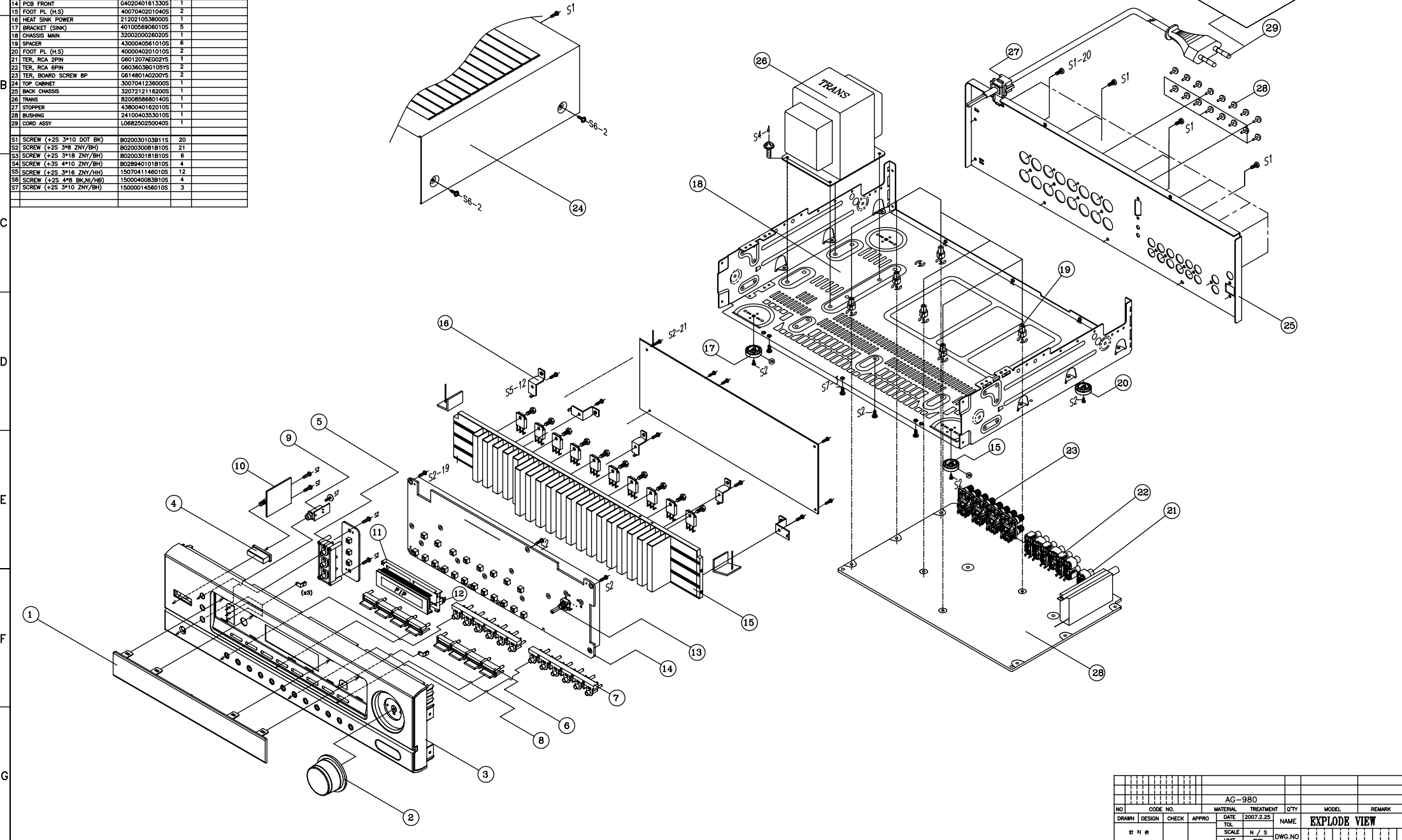


NO	PART NAME	PART NO (EURY)	QTY	REMARKS
1	WINDOW DISPLAY (ACRYL WINE)	5077212083010S	1	
2	KNOB VOLLINE	5087211131010S	1	
3	PANEL FRONT	3067212081420S	1	
4	BTN POWER	5097212681100S	1	
5	BTN 3-KEY	5097212651000S	1	
6	BTN 4-KEY	5097212661000S	1	
7	BTN 8-KEY	5097212671000S	1	
8	INDICATOR	5160040643010S	4	
9	JACK DB.5	G402P619A01YS	1	
10	S/W POWER PUSH	G00312209600S	1	
11	GUIDE FLT	K32004078101AS	1	
12	FL DISPLAY	K530121300010S	1	
13	VOL. ENCODER	G121123070010S	1	
14	PCB FRONT	G402P619A01YS	1	
15	HEAT FL (HLS)	40070402010A0S	2	
16	HEAT SINK POWER	2120210538000S	1	
17	BRACKET (SINK)	401005909010S	5	
18	CHASSIS MAIN	3200200028020S	1	
19	SPACER	4300040561010S	6	
20	FOOT PL (HLS)	40030040201010S	2	
21	TER. RCA 2PIN	0601207AE020YS	1	
22	TER. RCA 6PIN	060360380100YS	2	
23	TER. BOARD SCREW BP	G614801A0200YS	2	
24	TOP CABINET	3007041238000S	1	
25	BACK CHASSIS	32027212118200S	1	
26	TRANS	8200858680140S	1	
27	STOPPER	4380040182010S	1	
28	BUSHING	2410040353010S	1	
29	CORD ASSY	L068250250040S	1	
S1	SCREW (+2S 3*10 DOT BK)	B020030103B11S	20	
S2	SCREW (+2S 3*8 ZNY/BH)	B020030081B10S	21	
S3	SCREW (+2S 3*18 ZNY/BH)	B020030181B10S	6	
S4	SCREW (+3S 4*10 ZNY/BH)	B028940101B10S	4	
S5	SCREW (+2S 3*16 ZNY/H)	1507041146010S	12	
S6	SCREW (+2S 4*8 BK/NI/HB)	1500040083B10S	4	
S7	SCREW (+2S 3*10 ZNY/BH)	1500001456010S	3	

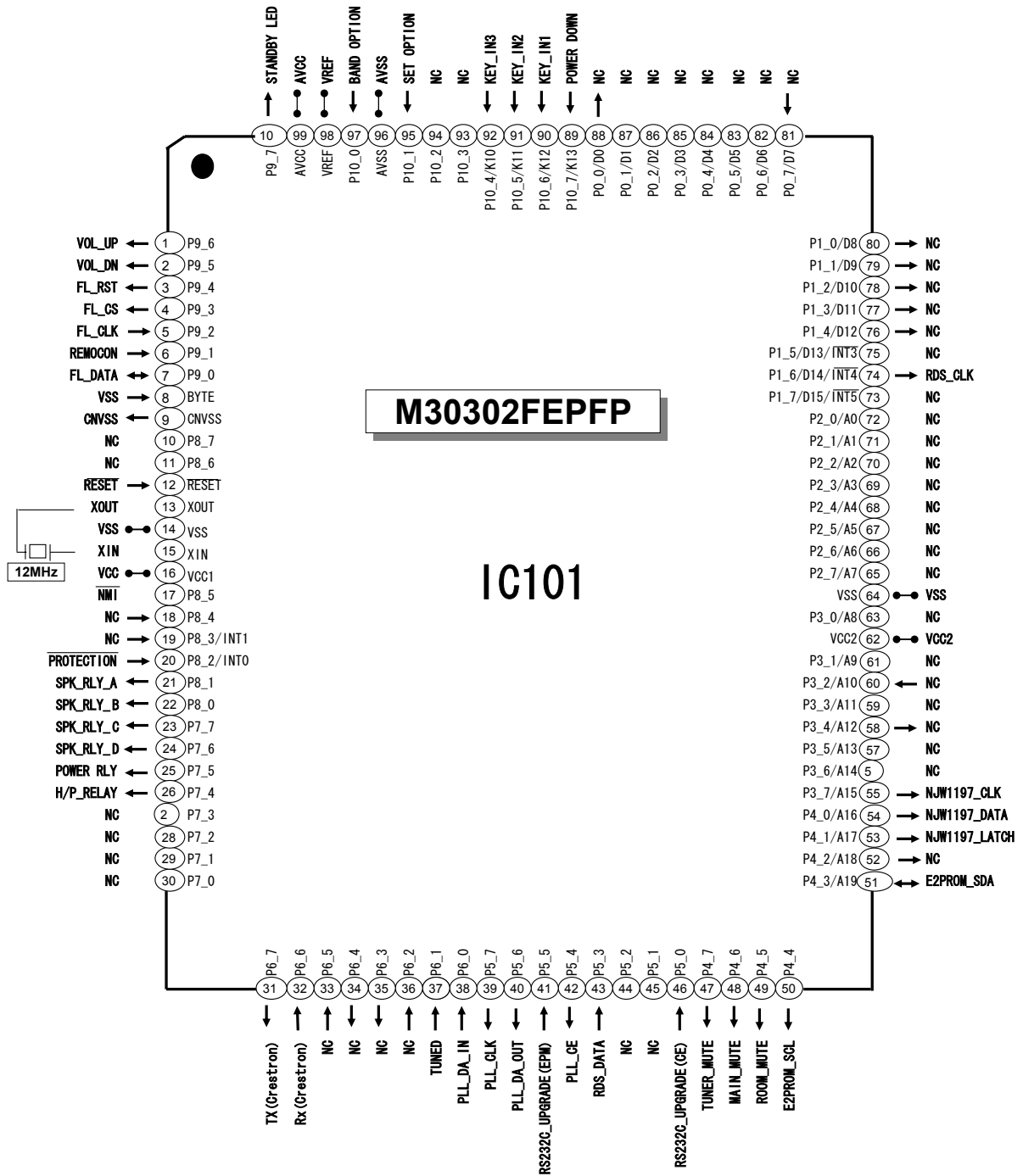
# AG-980 EXPLODE

DATE	ECO NO.	CONTENT	DESIGN	CHECK



NO	CODE NO.	MATERIAL	TREATMENT	QTY	MODEL	REMARK
					AG-980	
DRAWN	DESIGN	CHECK	APPRO	DATE	2007.2.25	NAME
한 지 순						EXPLODE VIEW
		SCALE	N / S	DWG.NO		
		UNIT	mm			

# AG980B MICOM CONTROL PIN DEFINE



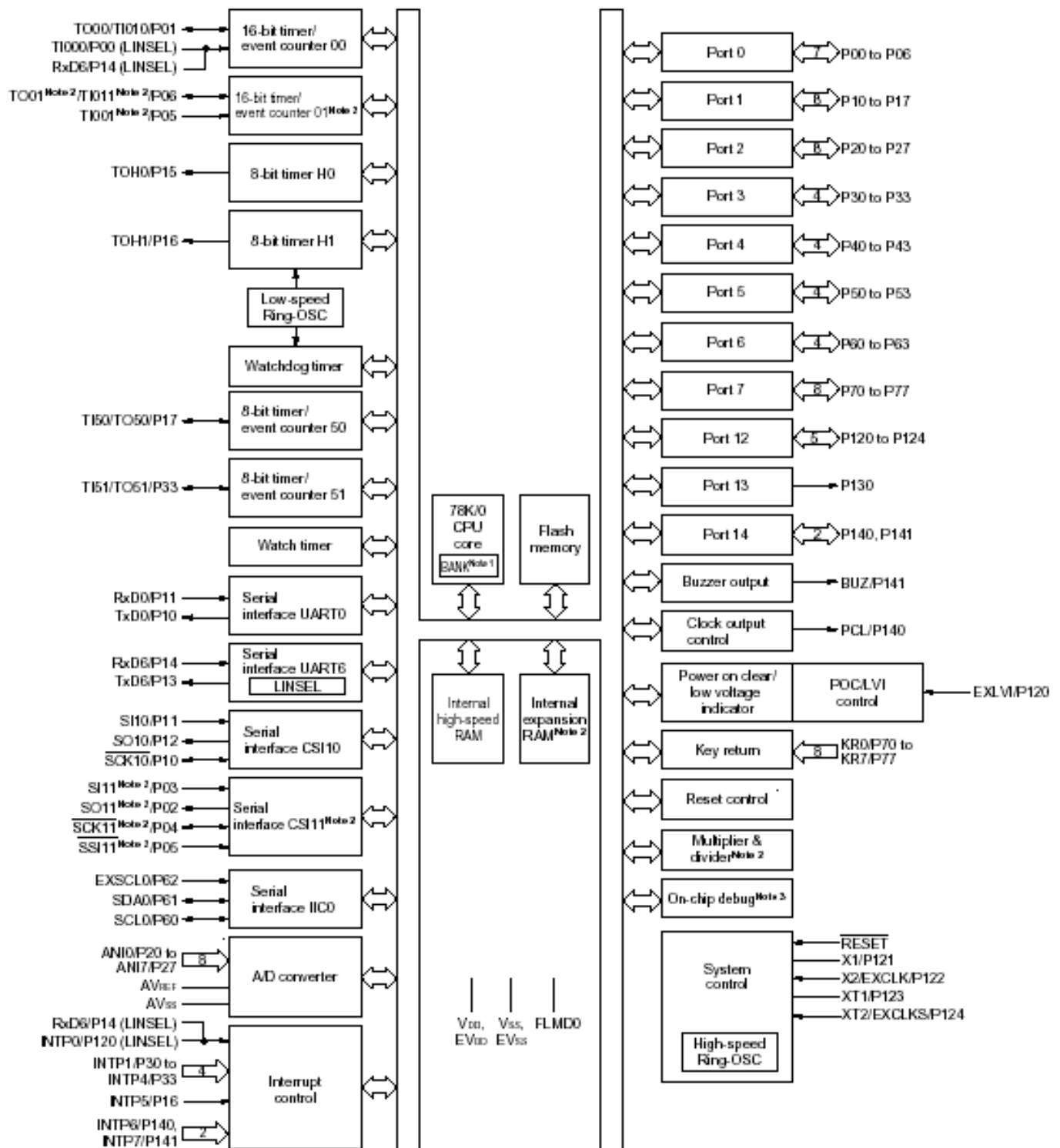
PIN NO.	NAME	I/O	B' D
1	VOL_UP	I	Input for main volume up.
2	VOL_DN	I	Input for main volume down.
3	FL_RST	0	Reset signal output to SC16315
4	FL_CS	0	Chip enable signal output to SC16315
5	FL_CLK	0	Clock signal output to SC16315
6	REMOCON	I	Input for the Remocon data
7	FL_DATA	0	Data signal output to SC16315
8	VSS		Ground
9	CNVSS		Flash memory programming mode setting
10	NC		Not used!
11	NC		Not used!
12	RESET	I	Input for resetting the CPU
13	XOUT		Output for 12MHz crystal oscillator
14	VSS		Ground
15	XIN		input for 12MHz crystal oscillator
16	VCC		+3.3V power supply
17	NMI		CPU internal operating for Port
18	NC		Not used!
19	NC		Not used!
20	PROTECTION	I	Input for protect data
21	SPK_RLY_A	0	Output for Relay control data
22	SPK_RLY_B	0	Output for Relay control data
23	SPK_RLY_C	0	Output for Relay control data
24	SPK_RLY_D	0	Output for Relay control data
25	POWER_RLY	0	Output for Relay control data
26	H/P_RELAY	0	Output for Relay control data
27	NC	0	Not used!
28	NC		Not used!
29	NC		Not used!
30	NC		Not used!
31	TX (Crestron)	0	CPU upgrade data transmit
32	Rx (Crestron)	I	CPU upgrade data receiver
33	NC		Not used!
34	NC		Not used!
35	NC		Not used!
36	NC		Not used!
37	TUNED	I	Input for detecting "TUNED" condition.
38	PLL_DA_IN	I	Input for PLL data receiver
39	PLL_CLK	0	Clock signal output to tuner pack
40	PLL_DA_OUT	0	Output for PLL data receiver
41	RS232C_UPGRADE (EPM)	I	Flash memory programming mode setting
42	PLL_CE	I	Chip enable signal output to tuner pack
43	RDS_DATA	I	Radio data signal input from tuner pack
44	NC		
45	NC		
46	RS232C_UPGRADE (CE)	I	Chip select for CPU upgrade
47	TUNER_MUTE	0	Data signal output to tuner mute
48	MAIN_MUTE	0	Data signal output to main mute
49	ROOM_MUTE	0	Data signal output to room2 mute
50	E2PROM_SCL	0	Clock signal output for E2PROM
51	E2PROM_SDA	I/O	Data signal output for E2PROM
52	NC		Not used!
53	NJW1197_LATCH	0	Chip select data for NJW1197
54	NJW1197_DATA	0	Communication data for NJW1197
55	NJW1197_CLK	0	Clock signal for NJW1197
56	NC		Not used!
57	NC		Not used!
58	NC		Not used!
59	NC		Not used!
60	NC		Not used!
61	NC		Not used!
62	VCC2		+3.3V Power supply
63	NC		Not used!
64	VSS		Not used!
65	NC		Not used!
66	NC		Not used!
67	NC		Not used!

PIN NO.	NAME		B' D
68	NC		Not used!
69	NC		Not used!
70	NC		Not used!
71	NC		Not used!
72	NC		Not used!
73	NC		Not used!
74	RDS_CLK	0	Clock signal for tuner RDS
75	NC		Not used!
76	NC		Not used!
77	NC		Not used!
78	NC		Not used!
79	NC		Not used!
80	NC		Not used!
81	NC		Not used!
82	NC		Not used!
83	NC		Not used!
84	NC		Not used!
85	NC		Not used!
86	NC		Not used!
87	NC		Not used!
88	NC		Not used!
89	POWER DOWN	I	Input for power down
90	KEY_IN1	I	Data input for key scan
91	KEY_IN2	I	Data input for key scan
92	KEY_IN3	I	Data input for key scan
93	NC		Not used!
94	NC		Not used!
95	SET OPTION	I	Input for select set
96	AVSS		Ground
97	BAND OPTION	I	Input for select the frequency ranges steps of FM and AM
98	VREF		+3.3V power supply
99	AVCC		+3.3V power supply
100	STANDBY LED	0	Output signal for LED

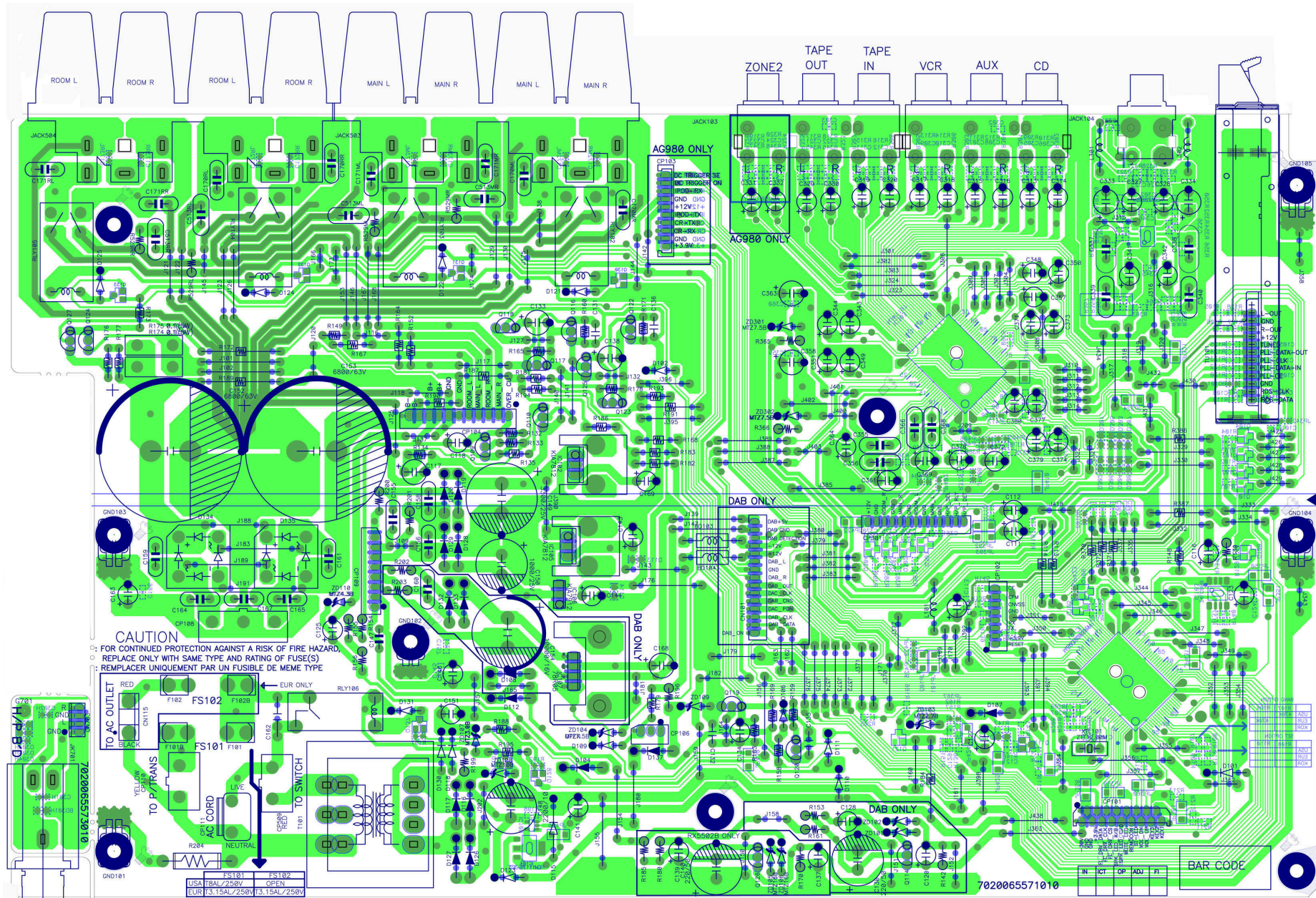
Key Matrix Table

IN OUT	KEY1	KEY2	KEY3
0.1V~0V	ON/STBY SW701	SP-B SW718	ROOM2 SW715
0.2V~0.5V	MEMORY SW703	SP-A SW719	
0.6V~0.8V	TUNING-UP SW705	T/P MODE SW702	
0.9V~1.2V	TUNING-DOWN SW707	TONE DIRECT SW704	
1.3V~1.5V	BAND SW709	DOWN SW706	
1.6V~1.8V	INPUT-LEFT SW711	UP SW708	
1.9V~2.1V	INPUT-RIGHT SW713	TONE SW710	
2.2V~2.4V		BALANCE SW712	

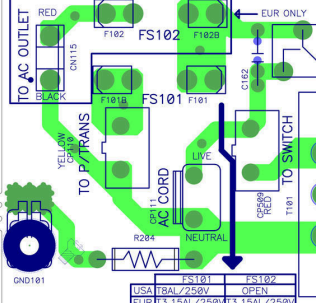
1.6 Block Diagram



- Notes
1. Available only in the  $\mu$ PD78F0536, 78F0537, and 78F0537D.
  2. Available only in the  $\mu$ PD78F0534, 78F0535, 78F0536, 78F0537, and 78F0537D.
  3. Available only in the  $\mu$ PD78F0537D.



**CAUTION**  
 FOR CONTINUED PROTECTION AGAINST A RISK OF FIRE HAZARD,  
 REPLACE ONLY WITH SAME TYPE AND RATING OF FUSE(S)  
 REPLACER UNIQUEMENT PAR UN FUSIBLE DE MEME TYPE

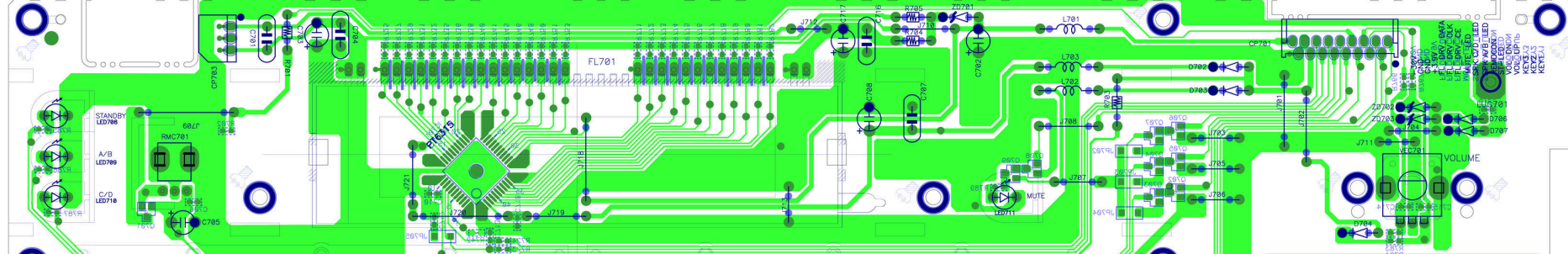
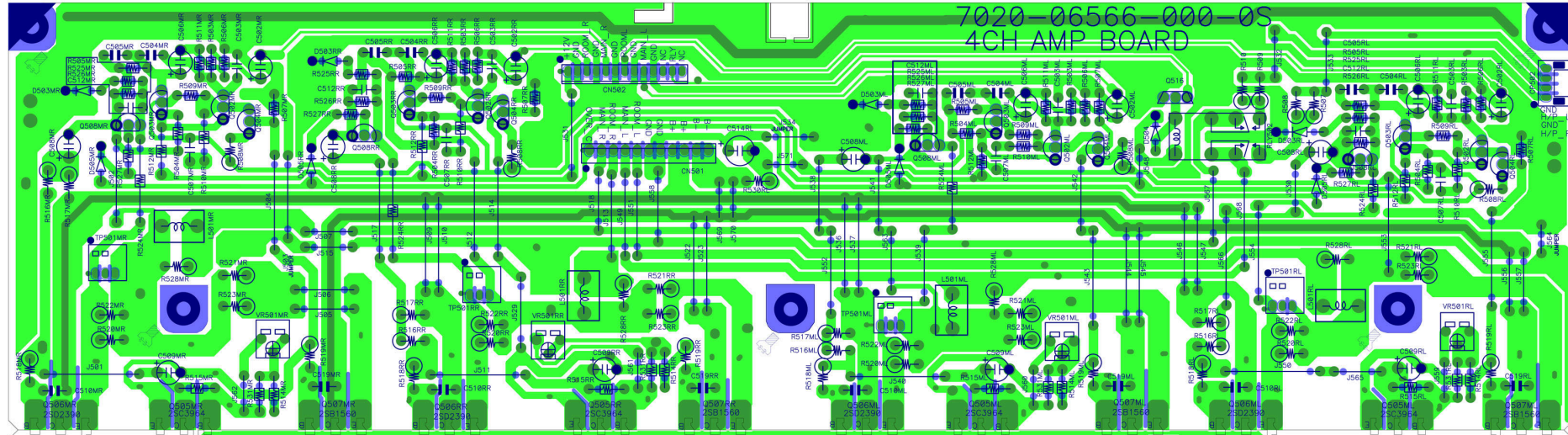


RX5502B MAIN PCB\_MP 7020-06557-000-0S

7020065571010

BAR CODE

7020-06566-000-0S  
4CH AMP BOARD



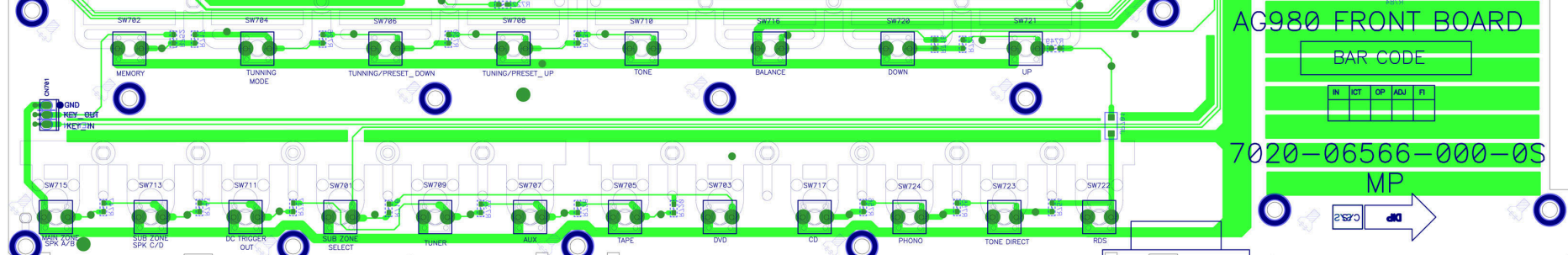
AG980 FRONT BOARD

BAR CODE

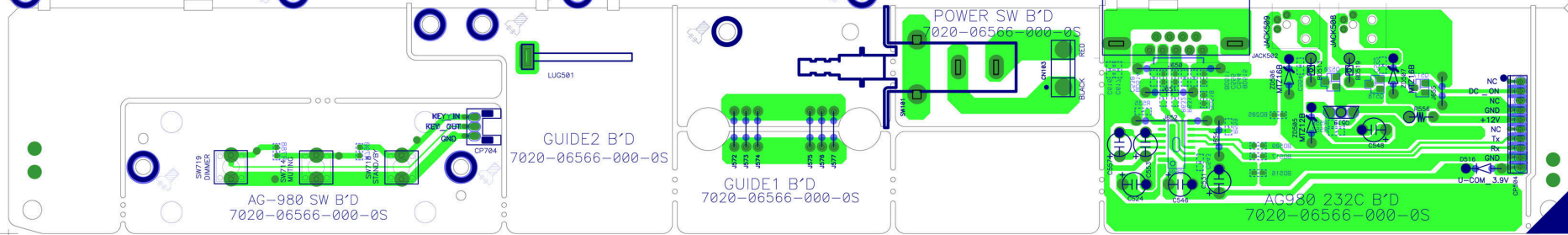
IN	ICT	OP	ADJ	FI
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7020-06566-000-0S

MP



MEMORY TUNNING MODE TUNNING/PRESET\_DOWN TUNNING/PRESET\_UP TONE BALANCE DOWN/PRESET UP



AG-980 SW B'D  
7020-06566-000-0S

GUIDE2 B'D  
7020-06566-000-0S

GUIDE1 B'D  
7020-06566-000-0S

POWER SW B'D  
7020-06566-000-0S

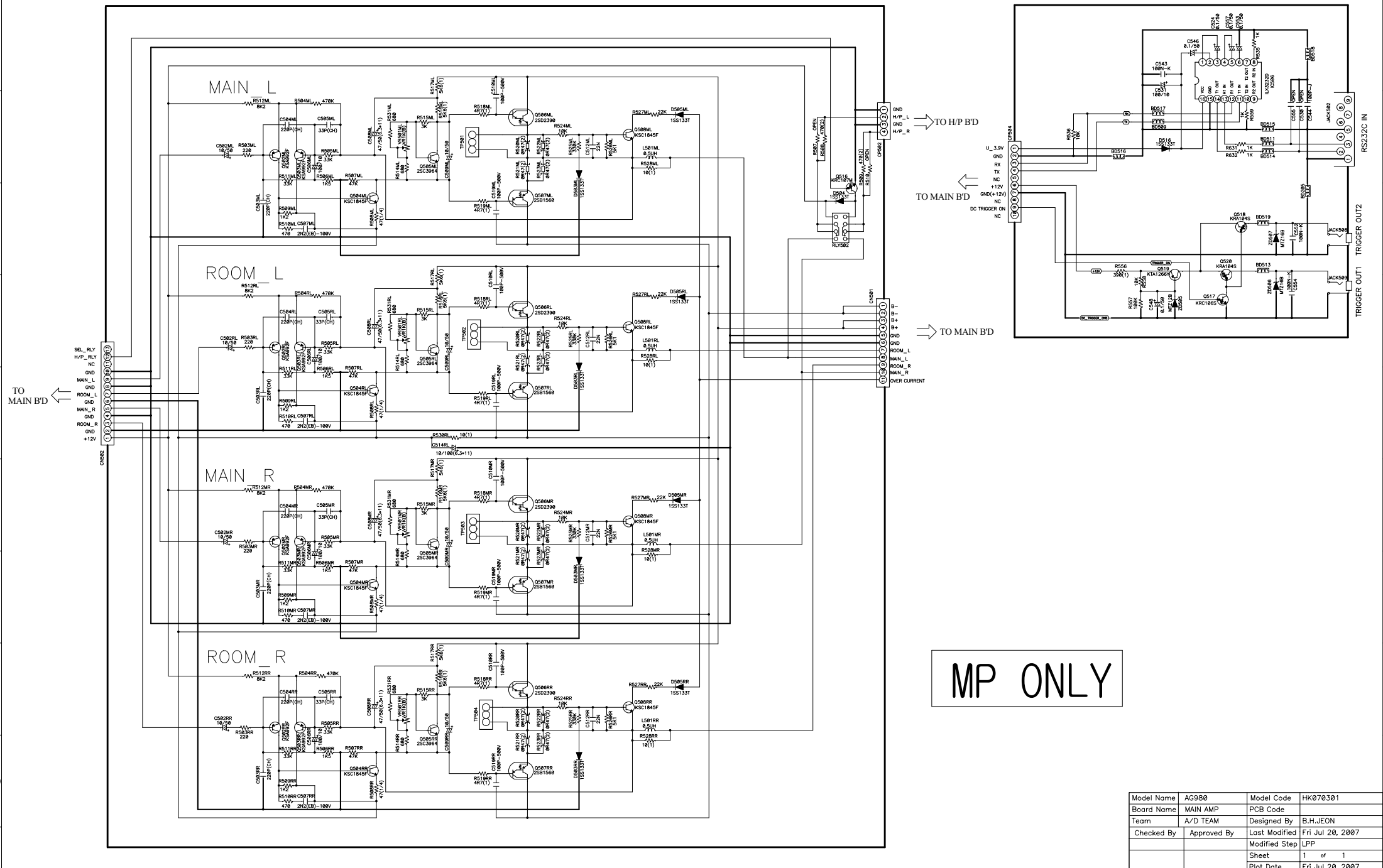
AG980 232C B'D  
7020-06566-000-0S





# AG980 AMP PART

# RS232C PART

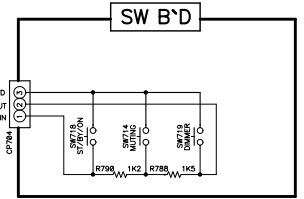
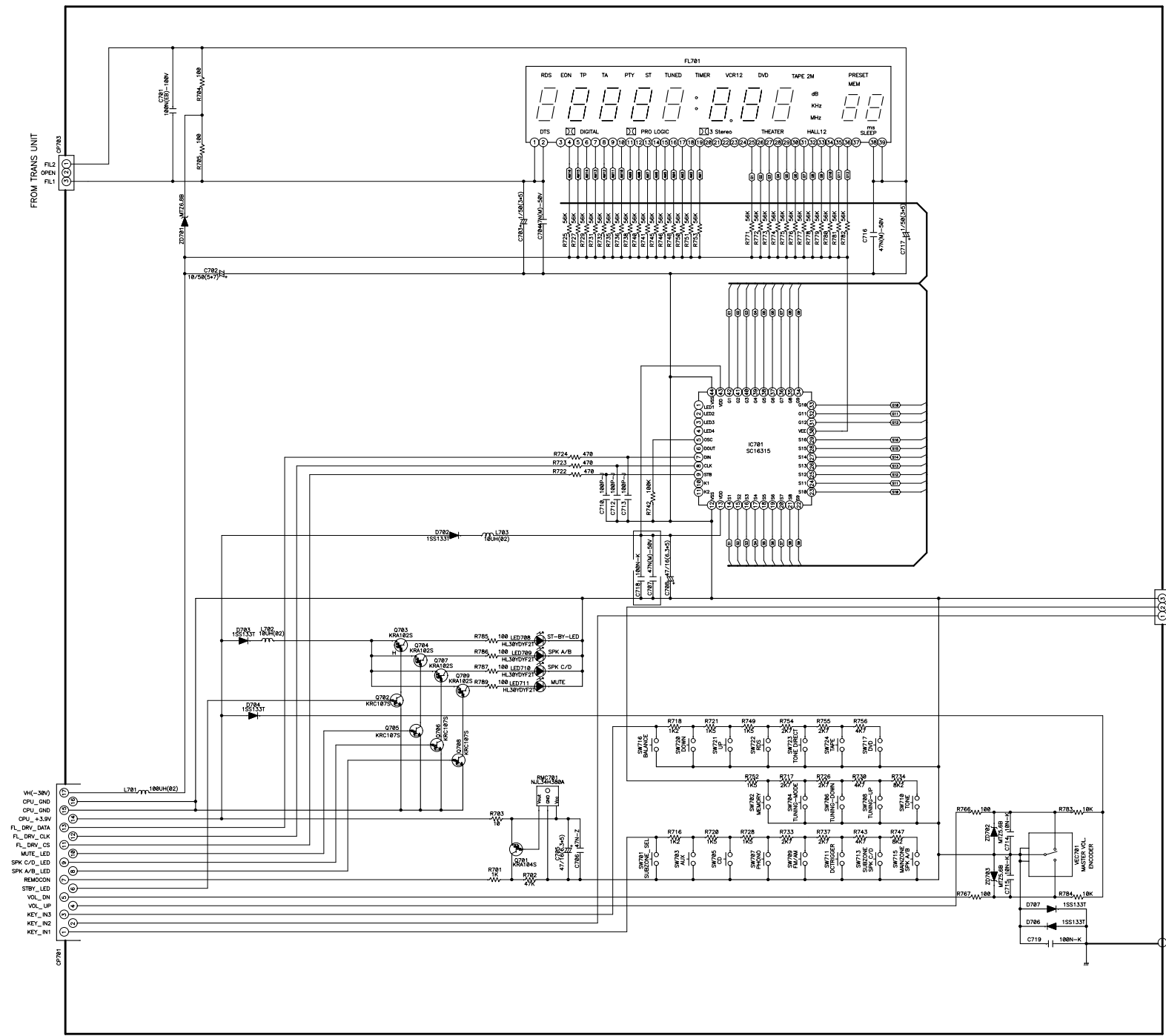


MP ONLY

Model Name	AG980	Model Code	HK070301
Board Name	MAIN AMP	PCB Code	
Team	A/D TEAM	Designed By	B.H.JEON
Checked By	Approved By	Last Modified	Fri Jul 20, 2007
		Modified Step	LPP
		Sheet	1 of 1
		Plot Date	Fri Jul 20, 2007

# AG-980 FRONT Schematic

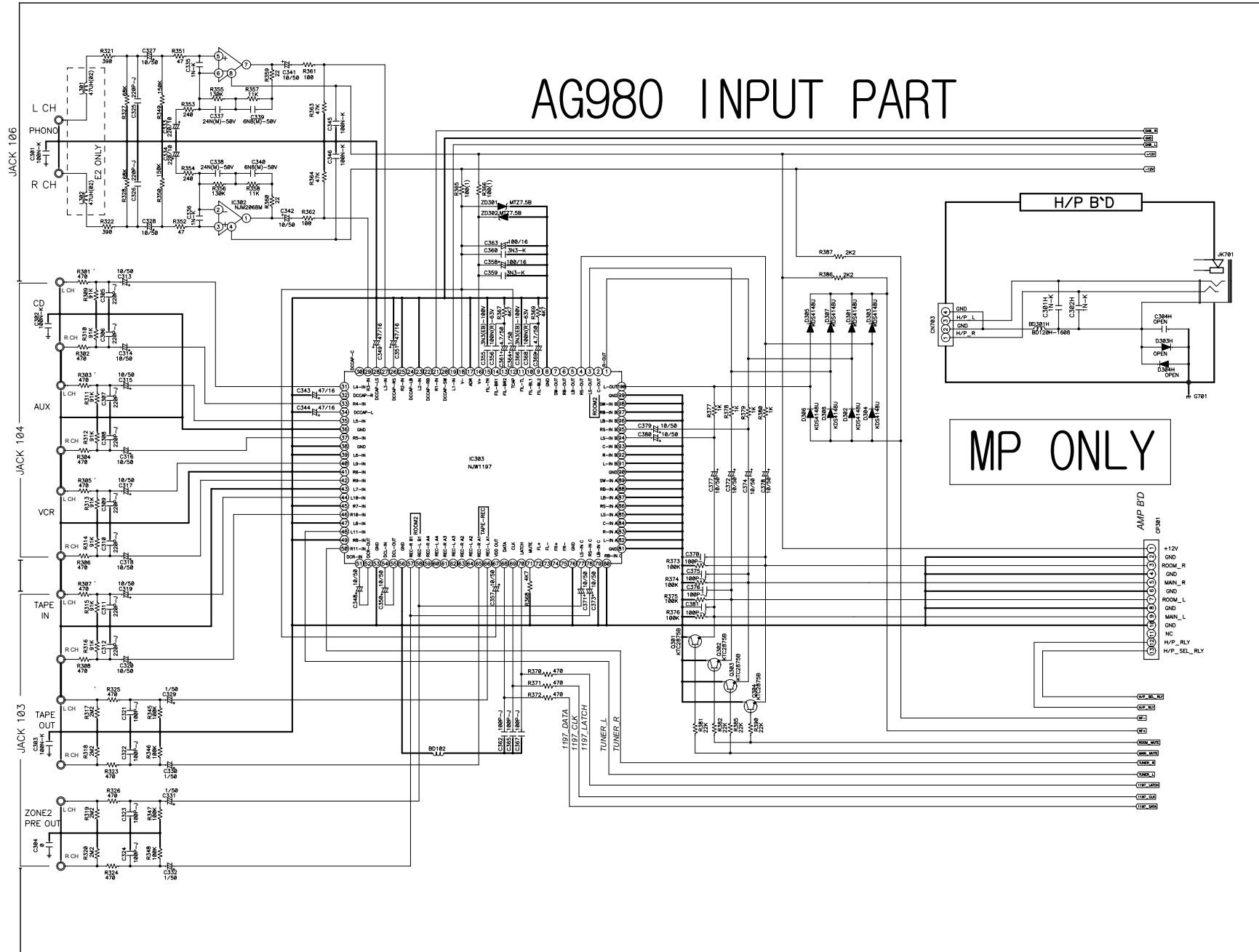
## MP ONLY



- VH(-30V)
- CP1\_GND
- CP2\_GND
- CP3\_+3.9V
- FL\_DRV\_DATA
- FL\_DRV\_CLK
- FL\_CRV\_CS
- FL\_CRV\_DS
- MUTE\_LED
- SPK\_C/D\_LED
- SPK\_A/B\_LED
- REMCON
- STBY\_LED
- VOL\_UP
- KEY\_IN3
- KEY\_IN2
- KEY\_IN1

Model Name	AG980	Model Code	HK070301
Board Name	FRONT	PCB Code	{PCB CODE}
Team	A/D TEAM	Designed By	B.H.JEON
Drawing No.	{Drawing Number}	Last Modified	Wed Jul 18, 2007
Checked By	Approved By	Modified Step	LPP
		Sheet	1 of 1
		Plot Date	Wed Jul 18, 2007

# AG980 INPUT PART



MP ONLY

Model Name	AG980	Model Code	HK070301
Board Name	MAIN INPUT	PCB Code	
Team	A/D TEAM	Designed By	B.H.JEON
Checked By	Approved By	Last Modified	Mon Jul 09, 2007
		Modified Step	LPP
		Sheet	1 of 1
		Plot Date	Wed Jul 18, 2007