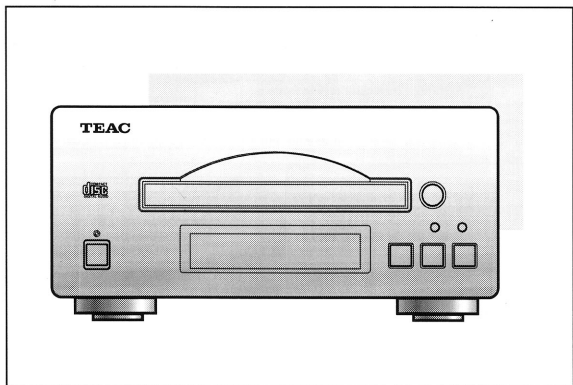




TEAC

D00338000A

SERVICE MANUAL

PD-H500

Compact Disc Player

NOTES

- PC boards shown are viewed from parts side.
- The parts with no reference number or no parts number in the exploded views are not supplied.
- As regards the resistors and capacitors, refer to the circuit diagrams contained in this manual.
- ⚠ Parts marked with this sign are safety critical components.
They must be replaced with identical components- refer to the appropriate parts list and ensure exact replacement.
- Parts of [] mark can be used only with the version designated.
[DM]: JAPAN [E]: EUROPE

CONTENTS

SPECIFICATIONS2

MEASUREMENT AND ADJUSTMENT METHODS3

IC PIN FUNCTION5

WIRING DIAGRAM16

BLOCK DIAGRAM17

SCHEMATIC DIAGRAM19

PRINTED CIRCUIT BOARDS21

EXPLODED VIEW23

ELECTRICAL PARTS LIST26

Specifications

Laser System	: 3-beam laser	Power Consumption	: 9W [EUR]
Digital Filter	: 8-times oversampling		8W [DM]
Frequency Response	: 20-20,000Hz(±2dB)	Dimensions (W×H×D)	: 285×131×292mm
Error Correction Method	: Cross Interleave Reed-Solomon code	Weight	: 3.9Kg
S/N Ratio	: More than 100dB (IHF "A" Filter used)	Standard accessories	
T.H.D	: Less than 0.02% (1KHz)	Remote control cord	1
Output Voltage	: 2V RMS	Signal cord	1
Power requirements	: 230V, 50Hz [EUR] 100V, 50Hz [DM]	* Improvements may result in specification or feature changes without notice.	

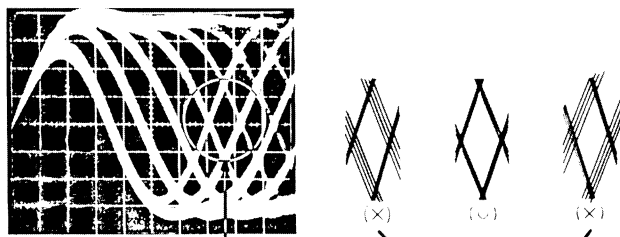
MEASUREMENT AND ADJUSTMENT METHODS

Measuring instruments

- Oscilloscope
- Audio Signal Generator (Oscillator)
- AC Voltmeter
- 1KHz B. P. F

FOCUS OFF SET ADJUSTMENT

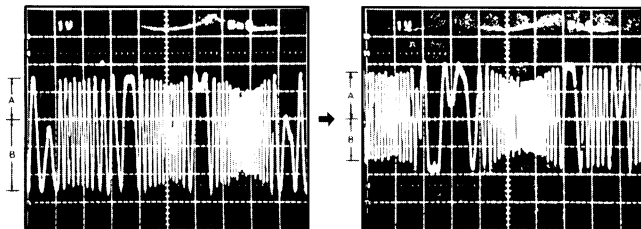
- Set the oscilloscope range.
Oscilloscope setting : Volt/Div 0.2V/Div
Sec/Div 0.2 μ s/Div
Input coupling AC
Probe 10:1
- Put in the disc and play it.
- Connect input terminal of oscilloscope to pin 1 of CN02.
Connect GND terminal to pin 2 of CN02.
- Adjust VR41 to obtain the argest and clearest wave watching the wave form from oscilloscope.(See Fig 1)



(Fig 1)

E/F BALANCE ADJUSTMENT

- Set the oscilloscope range.
Oscilloscope setting : Volt/Div 1V/Div
Sec/Div 2ms/Div
Input coupling DC
Probe 10:1
- Put in the disc and play it.
- Connect input terminal of oscilloscope to pin 1 of CN01.
Connect GND terminal to pin 3 of CN01.
- Make pin 6 and pin 3 of CN01 be short-circuited. (After adjustment, back to open circuit)
- Adjust VR42 to get the exactly symmetric wave from oscilloscope. (See Fig 2)
- At this time, if the disc stops rotating, make pin 6 and pin 3 of CN01 be open-circuited. Then play again and repeat No. 4 and 5 to adjust.



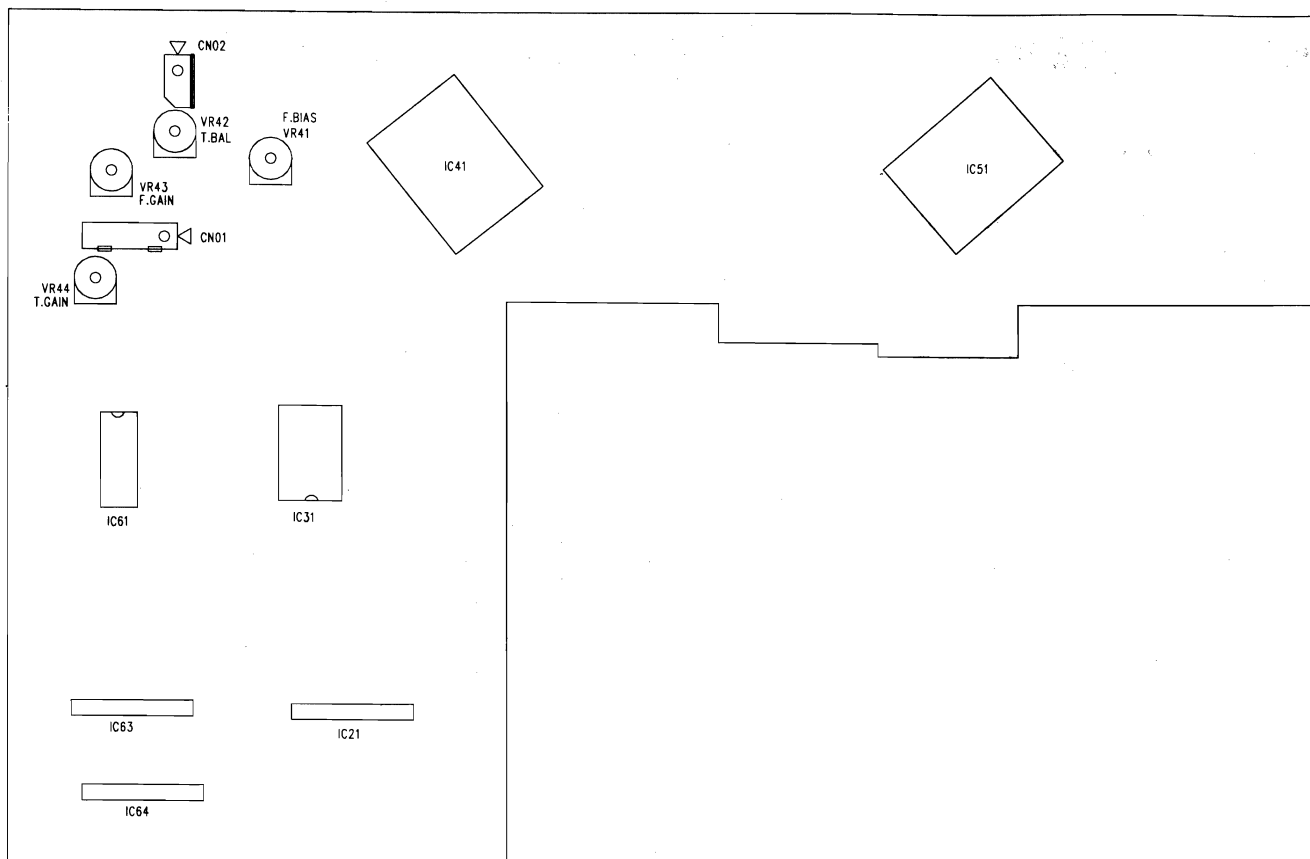
(Fig 2)

FOCUS GAIN ADJUSTMENT

- After setting the output of audio signal generator to 1KHz, 200mV, apply that value to AC voltmeter. Adjust until 0dB.
(Adjust to make 200mV correspond to 0dB)
- Put in the disc and play it.
- Connect input terminal of AC voltmeter to pin 5 of CN01.
Connect GND terminal to pin 3 of CN01. (Use 1KHz B.P.F)
- Connect output terminal of Audio signal generator to pin 4 of CN01. Connect GND terminal to pin 3 of CN01.
- Adjust VR43 until the value of AC voltmeter is in the range of -1dB to -2dB.
- At this time, if the disc stops rotating or rotates faster, make the output terminal of audio signal generator be open-circuited. Then play again and repeat No. 4 and 5 to adjust.

TRACKING GAIN ADJUSTMENT

- After setting the output of audio signal generator to 1KHz, 200mV, apply that value to AC voltmeter. Adjust until 0dB.
(Adjust to make 200mV correspond to 0dB)
- Put in the disc and play it.
- Connect input terminal of AC voltmeter to pin 2 of CN01.
Connect GND terminal to pin 3 of CN01. (Use 1KHz B.P.F)
- Connect output terminal of Audio signal generator to pin 1 of CN01. Connect GND terminal to pin 3 of CN01.
- Adjust VR44 until the value of AC voltmeter is in the range of +1dB to +2dB.
- At this time, if the disc stops rotating or rotates faster, make the output terminal of audio signal generator be open-circuited. Then play again and repeat No. 4 and 5 to adjust.



IC11 BVIANAM1206C(μ -com)

Pin No.	Symbol	I/O	Description
1	SENS	I	SSP · DSP Status input pin
2	RMCI	I	Remocon input pin
3	FOKI	I	Focus ok input pin
4			
5	RMCO	O	Remocon output pin
6	MUTE	O	Mute ON · $\overline{\text{OFF}}$ output pin
7	GFSI	I	Frame sync Status input pin
8	CLOCK	O	CLOCK output pin
9	XLAT	O	Latch output pin
10	DATA	O	Data output pin
11	SQCK	O	Subcode-Q data clock output pin
12	SQSO	I	Subcode-Q data Serial input pin
13			
14	KI-0	I	key scan input pin
15	KI-1	I	key scan input pin
16	KI-2	I	key scan input pin
17	KI-3	I	key scan input pin (No used)
18	KI-4	I	key scan input pin (No used)
19	ISWI	I	Option check input pin
20	LDSW	O	LD $\overline{\text{ON}}$ · OFF output pin
21	FESW	I	Feed Limit switch check input pin
22	CLSW	I	Close switch check input pin
23	OPSW	I	Open switch check input pin
24	CLOSE	O	Mechanism close control output pin
25	OPEN	O	Mechanism open control output pin
26	FLOW	O	Fip Filament POWER ON · $\overline{\text{OFF}}$ output pin
27	PWSW	O	Power ON · $\overline{\text{OFF}}$ output pin
28			
29			
30	/RST		System reset pin. "Low" = Active
31	ETAL	I	System clock oscillation Crystal interface input pin
32	XTAL	O	System clock oscillation Crystal interface output pin
33	VSS		GND
34			
35	KS-1	O	Key scan output pin
36	KS-2	O	Key scan output pin
37	KS-3	O	Key scan output pin (No used)
38	KS-4	O	Key scan output pin
39	KS-5	O	Key scan output pin (No used)
40	KS-6	O	Key scan output pin (No used)

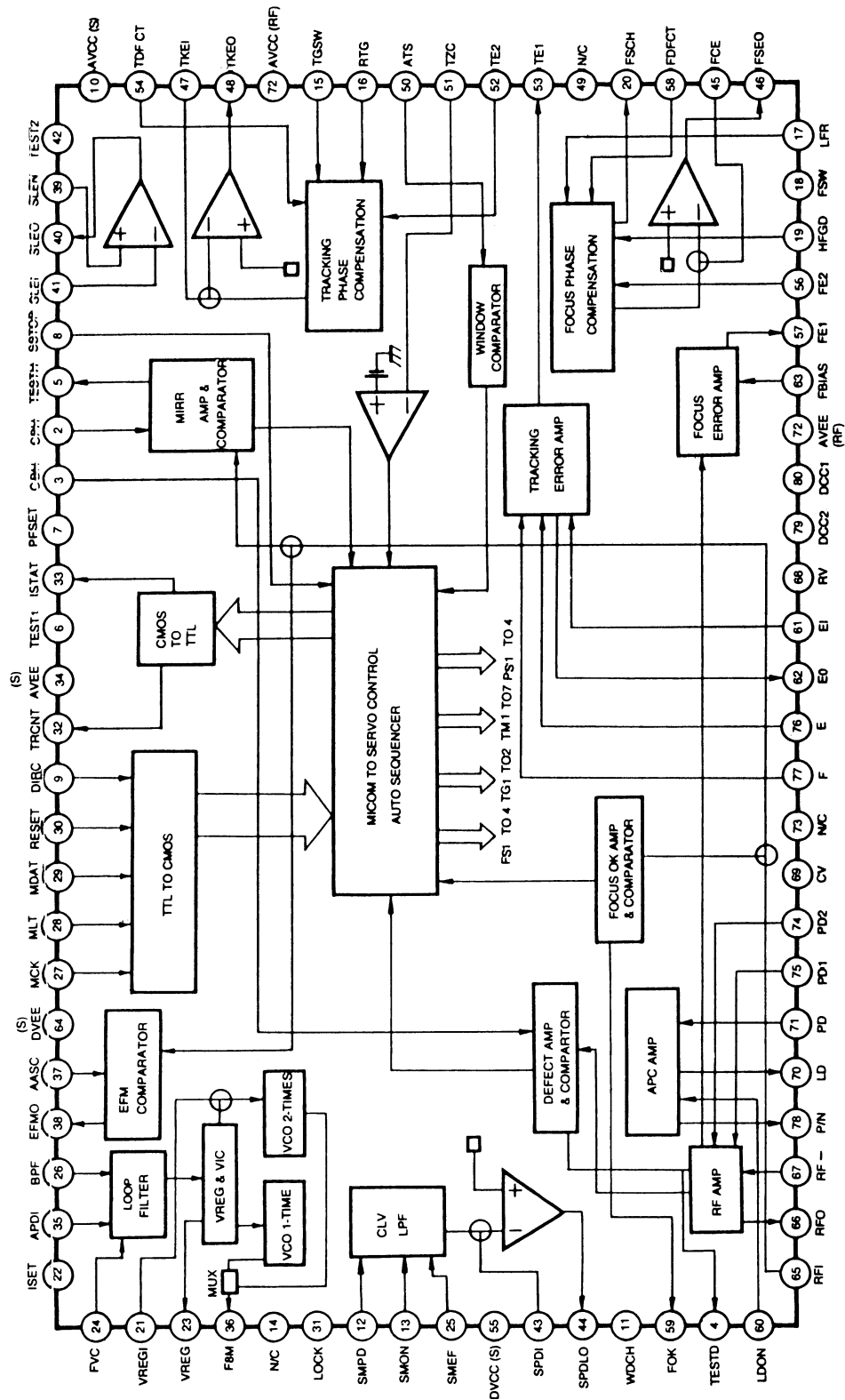
Pin No.	Symbol	I/O	Description
41	KS-7	O	Key scan output pin (No used)
42	PWIO	O	STANBY LED ON · OFF output pin
43	S-01	O	Fip segment signal output pin
44	S-02	O	Fip segment signal output pin
45	S-03	O	Fip segment signal output pin
46	S-04	O	Fip segment signal output pin
47	S-05	O	Fip segment signal output pin
48	S-06	O	Fip segment signal output pin
49	S-07	O	Fip segment signal output pin
50	S-08	O	Fip segment signal output pin
51	S-09	O	Fip segment signal output pin
52	S-10	O	Fip segment signal output pin
53	S-11	O	Fip segment signal output pin
54	S-12	O	Fip segment signal output pin
55	S-13	O	Fip segment signal output pin
56	S-14	O	Fip segment signal output pin
57	S-15	O	Fip segment signal output pin
58	S-16	O	Fip segment signal output pin
59	S-17	O	Fip segment signal output pin
60			
61			
62			
63	G-01	O	Fip timing signal output pin
64	G-02	O	Fip timing signal output pin
65	G-03	O	Fip timing signal output pin
66	G-04	O	Fip timing signal output pin
67	G-05	O	Fip timing signal output pin
68	G-06	O	Fip timing signal output pin
69	G-07	O	Fip timing signal output pin
70	V-08	O	Fip timing signal output pin
71	VFDP		Fip voltage supply pin
72	VDD		+5V power supply pin
73			
74			
75			
76	AMUTE	O	Audio mute output pin (No used)
77			
78			
79	COUT	I	Track count input pin
80	SCOR	I	Subcode sync signal (S0+S1) input pin

KA9220B

Pin No.	System	Description
1	AVEE(R)	Analog negative power supply input pin for RF part
2	CPH	Capacitor connection pin of mirror hold
3	CBH	Capacitor connection pin of defect bottom-hold
4	TESTD	Defect test pin
5	TESTM	Mirror test pin
6	Test 1	Input pin for test
7	PFSET	Peak frequency setting pin for focus, tracking compensation and fc (cut off frequency) of CLV LPF.
8	SSTOP	Check the position pin of pick-up whether inside or not.
9	DIRC	Direct 1 Track Jump Control pin
10	AVCC(S)	Analog positive power supply input pin for SERVO part
11	WDCH	Auto-sequencer clock-input pin (Normal speed=88.2KHz, Double speed=176.4KHz)
12	SMPD	Connection pin of DSP SMPD
13	SMON	Connection pin of DSP SMON, spindle servo ON at "H"
14	N/C	No connection pin
15	TGSW	Providing time constant to change the high frequency tracking gain
16	RTG	Capacitor connection pin to switch the tracking gain of high frequency
17	LFR	Capacitor connection pin to perform rising low bandwidth of focus servo loop
18	FSW	High frequency gain of focus servo loop can be changed by FS3 switch ON or OFF
19	HFGE	Reducing high frequency gain with capacitor connected between pin 18 and pin 19.
20	FSCH	Time constant external pin to generate focus search waveform
21	VREGI	External regulator voltage input pin for VCO
22	ISSET	Determining the peak value of focus search, track jump and SLED kick
23	VREG	3.5V Regulator output pin
24	FVC	Pin connected external resistor to adjust free running frequency of VCO
25	SMEF	Providing an external LPF time constant of CLV SERVO Loop
26	BPF	Providing time constant for Loop filter of VCO
27	MCK	Clock input pin from micom
28	MLT	Latch input pin from micom
29	MDAT	Data input pin from micom
30	RESET	Reset input pin from micom, reset at "L"
31	LOCK	Pin for operation of the sled runaway prevention function at "L"
32	TRCNT	Track count output pin
33	ISTAT	Internal status output pin
34	AVEE(S)	Analog negative power supply input pin for SERVO part
35	APDI	Input pin of DSP phase comparison output (PHAS)
36	F8M	Output pin of analog VCO Normal speed=8.64MHz, Double speed=17.28MHz
37	AASC	Auto-Asymmetry control input pin

Pin No.	System	Description
38	EFMO	EFM comparator output pin
39	SLEN	Non-inverting input pin of SLED SERVO amplifier
40	SLEO	Output pin of SLED SERVO amplifier
41	SLEI	Inverting input pin of SLED SERVO amplifier
42	TEST2	Test input pin to change speed mode Normal speed="H", Double speed="L"
43	SPDI	Inverting input pin of spindle servo amplifier
44	SPDLO	Spindle servo amplifier output pin
45	FCE	Inverting input pin of focus servo amplifier
46	FSEO	Output pin of focus servo amplifier
47	TKEI	Non-inverting input pin of tracking servo amplifier
48	TKEO	Output pin of tracking servo amplifier
49	N/C	Noconnection
50	ATS	Anti-shock input pin
51	TZC	Tracking Zero Crossing input pin
52	TE2	Tracking Error Servo input pin
53	TE1	Output pin of tracking Error Amplifier
54	TDFCT	Capacitor Connection pin for Defect Compensation of tracking servo
55	DVCC(S)	Digital positive power supply input pin for servo part
56	FE2	Focus error servo input pin
57	FE1	Output pin of focus error Amplifier
58	FDFCT	Capacitor connection pin for defect compensation of focus servo
59	FOK	Output pin of Focus ok comparator
60	LDON	Laser diode ON/OFF control pin
61	EI	Feedback input pin of EI-V amplifier
62	EO	Output pin of EI-V Amplifier
63	FBIAS	Bias pin of non-inverting input of focus error amplifier
64	DVEE(S)	Digital negative power supply input pin for servo part
65	RFI	Output Signal of RF summing amplifier is inputted through capacitor
66	RFO	Output pin of RF summing amplifier
67	RF-	Inverting input pin of RF summing amplifier
68	RV	Output pin of (AVCC+AVEE)/2 Voltage
69	CV	Bias input pin of Center Voltage buffer
70	LD	Output pin of APC amplifier
71	PD	Input pin of APC amplifier
72	AVCC(R)	Analog positive power supply input pin for RF part
73	N/C	No connection
74	PD2	Inverting input pin of RF I-V AMP2
75	PD1	Inverting input pin of RF I-V AMP1
76	F	Inverting input pin of F I-V AMP
77	E	Inverting input pin of E I-V AMP
78	P/N	Selecting P-sub/N-sub of Laser diode
79	DCC2	Defect bottom-hold output is inputted through capacitor
80	DCC1	Output pin of defect bottom-hold

BLOCK DIAGRAM

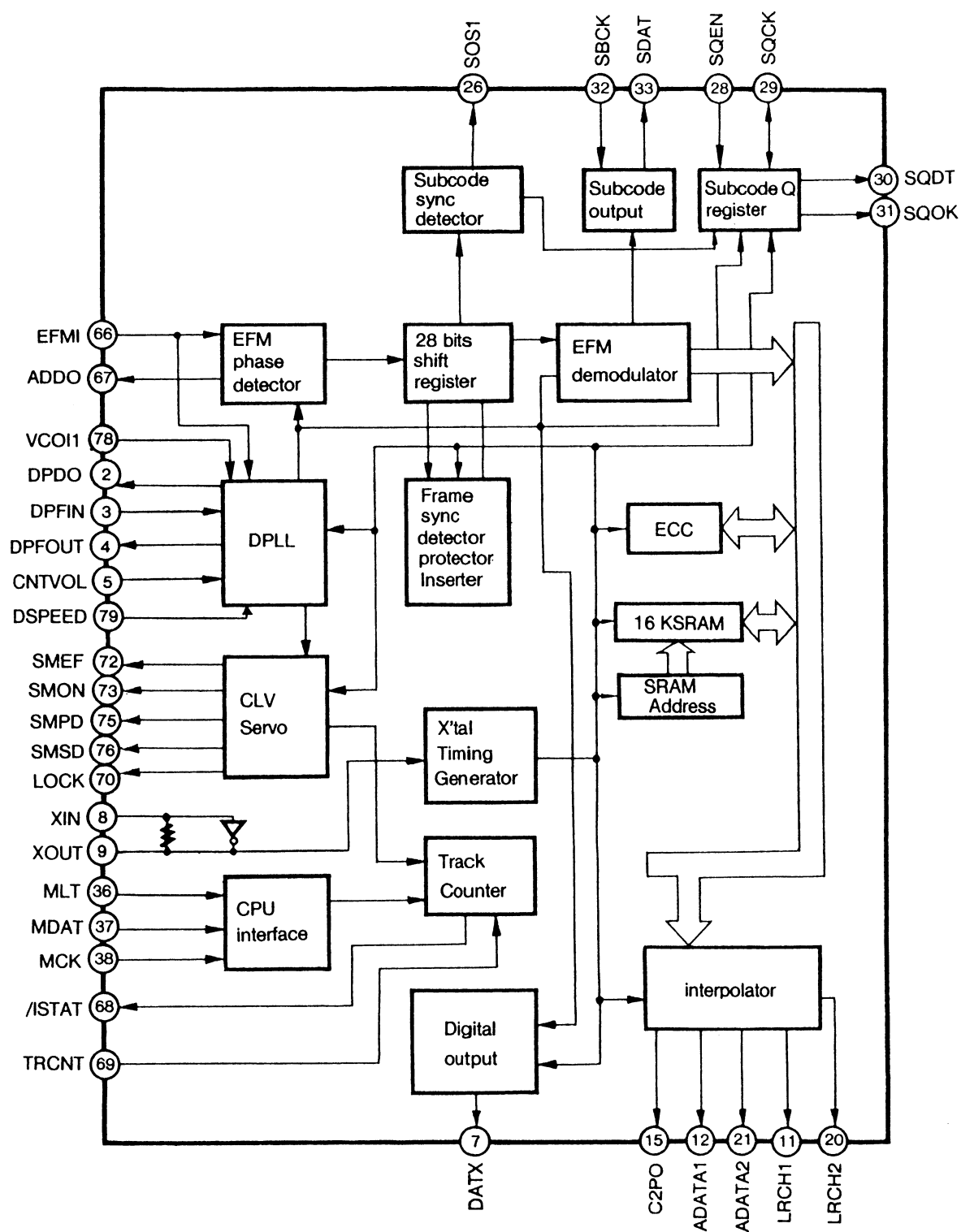


KS9283

Pin No.	Symbol	I/O	Description
1	AV _{DD} 1		Analog Vcc 1
2	DPDO	O	Charge pump output for master PLL
3	DPFIN	I	Filter input for master PLL
4	DPFOUT	O	Filter output for master PLL
5	CNTVOL	I	VCO Control Voltage for master PLL
6	AV _{SS} 1		Analog Ground 1
7	DATX	O	Digital audio output
8	XIN	I	X-tal oscillator input
9	XOUT	O	X-tal oscillator output
10	WDCH1	O	Word clock of 48 bit/SLOT
11	LRCH1	O	Channel clock of 48 bit/SLOT
12	ADATA1	O	Serial audio data output of 48 bit/SLOT(MSB first)
13	DV _{SS} 1		Digital Ground 1
14	BCK1	O	Audio data Bit clock for 48 bit/SLOT
15	C2PO	O	C2 pointer for output audio data
16	TIM2	O	Normal and Double Speed Control pin
17	/BCK1	O	Clock of BCK1
18	/BCK2	O	Clock of BCK2
19	BCK2	O	Audio data Bit clock of 64 Bit/SLOT
20	LRCH2	O	Channel clock of 64 Bit/SLOT
21	ADATA2	O	Serial audio data output of 64 Bit/SLOT(LSB First)
22	N. C		No Connection
23	WDCH2	O	Word clock of 64 Bit/SLOT
24	EMPH	O	Emphasis/Non-Emphasis output("H":Emphasis)
25	LKFS	O	The Lock status output of frame Sync
26	SOS1	O	Output of subcode sync signal (SO+S1)
27	RESET	I	System reset at "L"
28	SQEN	I	SQCK I/O Control ("L":internal CK, "H":external CK)
29	SQCK	I/O	Clock for output subcode-Q data
30	SQDT	O	Serial output of subcode-Q data
31	SQOK	O	The CRC Check result signal output of subcode-Q
32	SBCK	I	Clock for output subcode-Q data
33	SDAT	O	Subcode serial data output
34	DV _{DD}		Digital Vcc 1
35	MUTE	I	Mute control Input("H":Mute ON)
36	MLT	I	Latch signal input from Micom
37	MDAT	I	Serial data input from Micom
38	MCK	I	Serial clock input from Micom
39	DB8	I/O	SRAM data I/o Port 8 (MSB)
40	DB7	I/O	SRAM data I/o Port 7
41	DB6	I/O	SRAM data I/o Port 6

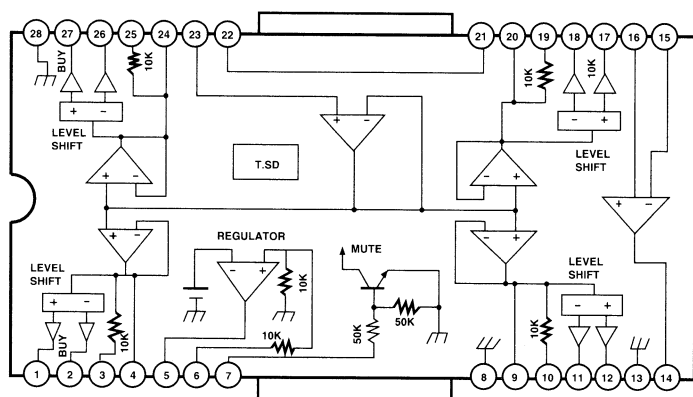
Pin No.	Symbol	I/O	Description
42	DB5	I/O	SRAM data I/o Port 5
43	DB4	I/O	SRAM data I/o Port 4
44	DB3	I/O	SRAM data I/o Port 3
45	DB2	I/O	SRAM data I/o Port 2
46	DB1	I/O	SRAM data I/o Port 1 (LSB)
47	C1F1	I/O	Monitoring output for C1 error Correction (RA1)
48	C1F2	I/O	Monitoring output for C1 error Correction (RA2)
49	C2F1	I/O	Monitoring output for C2 error Correction (RA3)
50	C2F2	I/O	Monitoring output for C2 error Correction (RA4)
51	C2FL	I/O	C2 decoder flag (High : When the processing C2 code is Impossible correction state) (RA5)
52	/PBCK	I/O	Output of $\frac{VCO}{2}$ (Normal speed=4.3218MHz Double speed=8.6436MHz)(RA6)
53	DVss2		Digital Ground 2
54	FSDW	I/O	Unprotected frame Sync (RA7)
55	ULKFS	I/O	Frame sync protection state (RA8)
56	/JIT	I/O	Display of either RAM overflow or under flow for ± 4 frame jitter margin (RA9)
57	C4M	I/O	Only Monitoring signal (Normal playback:4.2336MHz) (RA10)
58	C16M	I/O	16.9344MHz signal output(RA11)
59	/WE	I/O	Terminal for test
60	/CS	I/O	Terminal for test
61	SEL1	I	Mode Selection Terminal 1 (H:33.8688MHz) (L:16.9344MHz)
62	/SEL2	I	Mode Selection Terminal 2 (H:APLL) (L:DPLL)
63	/SEL3	I	Mode Selection Terminal 3 (H:CDROM) (L:CDP)
64	/SEL4	I	Mode Selection Terminal 4
65	TEST	I	Test Terminal (L:Normal operating state)
66	EFMI	I	EFM Signal input
67	APDO1	O	Charge Pump output ofr analog PLL
68	/ISTAT	O	The internal status output
69	TRCNT	I	Tracking counter input signal
70	LOCK	O	Output signal of LKFS condition sampled $\frac{PBFR}{16}$ (If LKFS is "H", Lock is "H". If the LKFS is sampled "L" at least 8times by $\frac{PBFR}{16}$, Lock is "L")
71	PBFR	O	Write frame clock (Lock:7.35KHz)
72	SMEF	O	LPF time constant control of the spindle servo error signal
73	SMON	O	ON/OFF control signal for spindle servo
74	DVDD2		Digital Vcc2
75	SMPD	O	Spindle Motor drive (Rough control in the CLV-S mode Phase control in the CLV-P mode)
76	SMSD	O	Spindle Motor dirve (Velocity control in the CLV-P mode)
77	VC001	O	Vco output signal (When the state is Lock by means of PBFR it is 8.6436MHz)
78	VCOI1	I	VCO input signal
79	Dspeed	I	Double speed mode control (H:Normal speed) (L:Double speed)
80	APDO2	O	Analog PLL charge pump output for Doule speed mode

BLOCK DIAGRAM



KA9258D

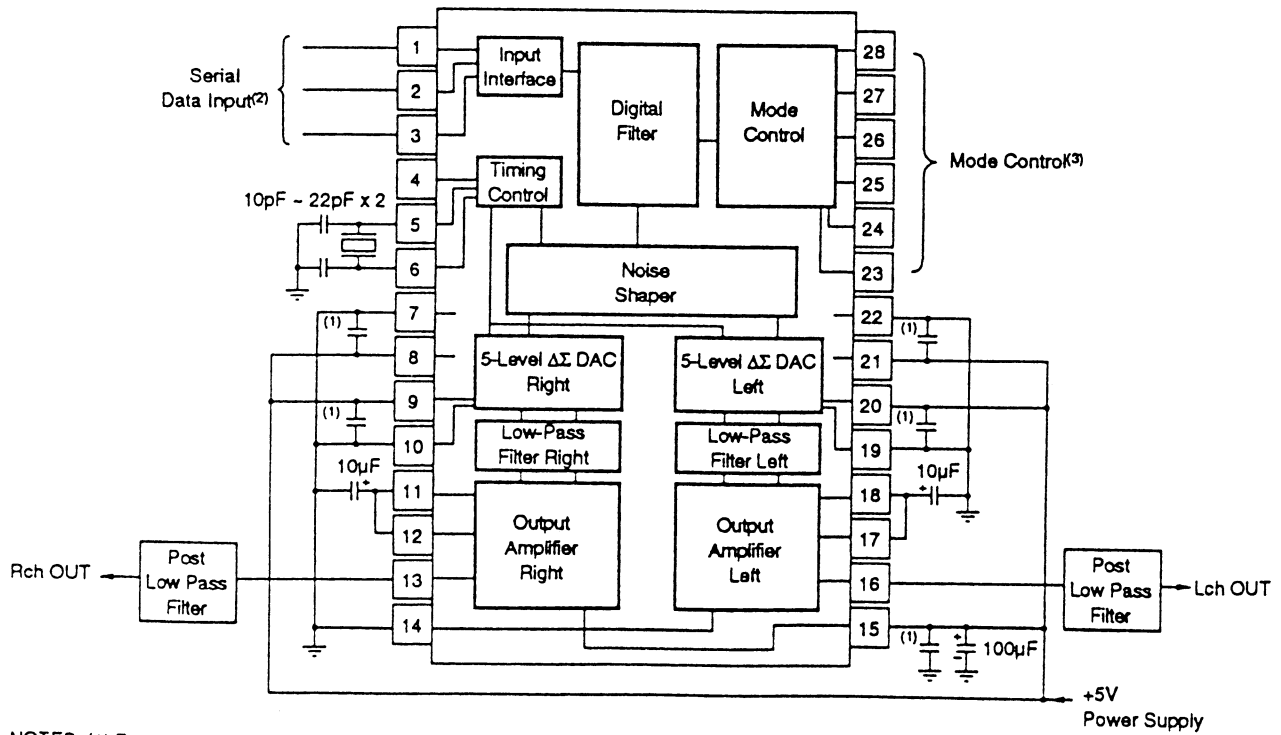
Pin No.	System	I/O	Description
1	DO1.1	O	DRIVE OUTPUT
2	DO1.2	O	DRIVER OUTPUT
3	DI1.1	I	DRIVE INPUT
4	DI1.2	I	DRIVE INPUT
5	REG		REGULATOR
6	REO	O	REGULATOR OUTPUT
7	MUTE		MUTE
8	GND1		GROUND
9	DI2.1	I	DRIVER INPUT
10	DI2.2	I	DRIVE INPUT
11	DO2.1	O	DRIVE OUTPUT
12	DO2.2	O	DRIVE OUTPUT
13	GND2		GROUND
14	OPOUT	O	OPAMP OUTPUT
15	OPIN(-)	I	OPAMP INPUT(-)
16	OPIN(+)	I	OPAMP INPUT(+)
17	DO3.1	O	DRIVE OUTPUT
18	DO3.2	O	DRIVE OUTPUT
19	DI3.1	I	DRIVE INPUT
20	DI3.2	I	DRIVE INPUT
21	VCC1		SUPPLY VOLTAGE
22	VCC2		SUPPLY VOLTAGE
23	VREF		2.5V BIAS VOLTAGE
24	DI4.1	I	DRIVE INPUT
25	DI4.2	I	DRIVE INPUT
26	DO4.1	O	DRIVE OUTPUT
27	DO4.2	O	DRIVE OUTPUT
28	GND 3		GROUND



IC61 PCM1710U (D/A Converter)

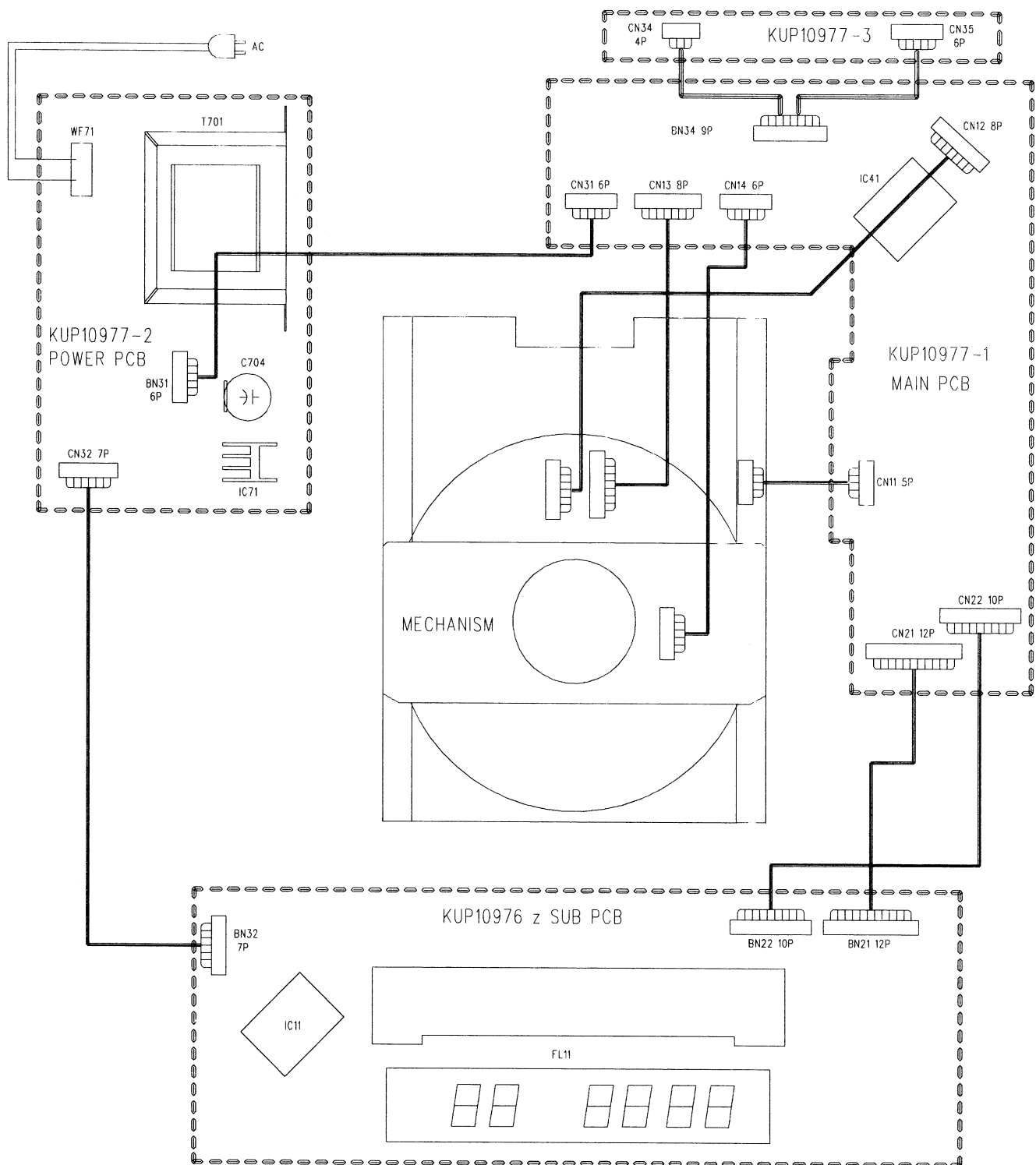
Pin No.	System	I/O	Description
1	LRCIN	I	Sample Rate Clock input, Controls the update rate (fs).
2	DIN	I	Serial Data input. MSB first, right justified format contains a frame of 16-bit or 20-bit data.
3	BCKIN	I	Bit Clock Input. Clocks in the data present on DIN input.
4	CLKO	O	Buffered Output of Oscillator. Equivalent to fs.
5	XTI	I	Oscillator Input
6	XTO	O	Oscillator Output. When using an external clock, leave XTO open.
23	CKSL	I	System Clock Select. For 384fs, tie CKSL "High". For 256fs, tie CKSL "Low"
24	MODE	I	Operation Mode Select. For serial mode, tie MODE "High". For parallel mode, tie MODE "Low"
25	MUTE	I	Mute Control. To disable soft mute, tie MUTE "High". To enable soft mute, tie MUTE "Low".
26	MD/DM1	I	Mode Control for Data/De-emphasis.
27	MD/MD2	I	Mode Control for BCKIN/De-emphasis.
28	ML/DSD	I	Mode Control for WDCK/Double speed dubbing.
13	VOUT R	O	Right Channel Analog Output.
16	VOUT L	O	Left Channel Analog Output.
7, 22	DGND		Digital Ground.
8, 21	VDD		Digital Power Supply (+5V)
9	Vcc 2R		Analog Power Supply(+5V), Right Channel DAC.
10	AGND 2R		Analog Ground (DAC), Right Channel.
11	EXT1R		Output Amplifier Common, Right Channel.
12	EXT2R		Output Amplifier Bias, Right Chanel. Connect to EXT1R.
14	AGND		Analog Ground.
15	Vcc		Analog Power Supply (+5V).
17	EXT2L		Ouput Amplifier Bias, Left Channel. Connect to EXT1L
18	EXT1L		Output Amplifier Common, Left Channel.
19	AGND2L		Analog Ground (DAC), Left Chanel.
20	Vcc 2L		Analog Power Supply (+5V), Left Channel DAC.

CONNECTION DIAGRAM



NOTES: (1) Bypass Capacitor : 1 μ F ~ 10 μ F.
 (2) Input pins require pull-up resistors. (3) Mode control pins require pull-up resistors.

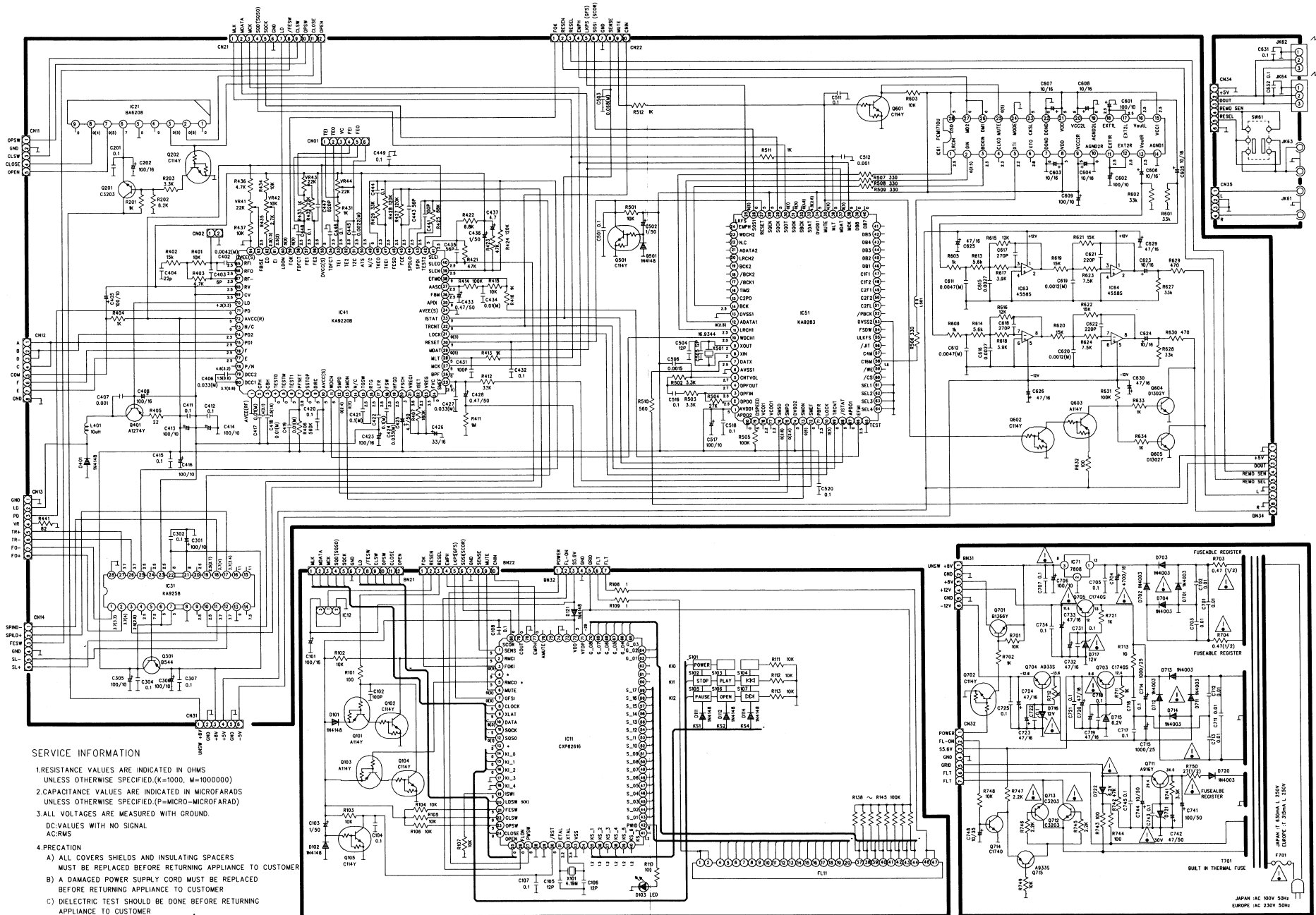
WIRING DIAGRAM



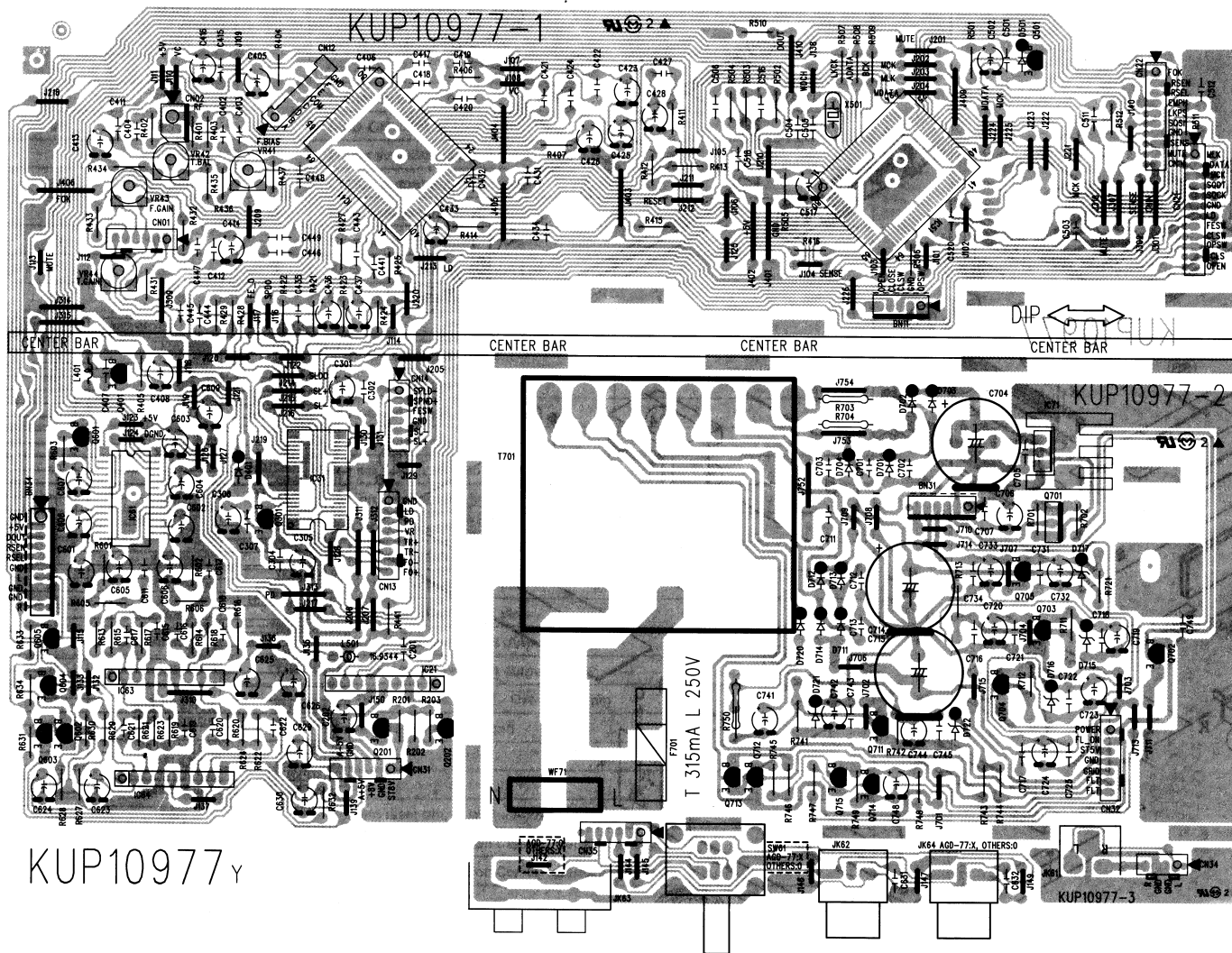
17



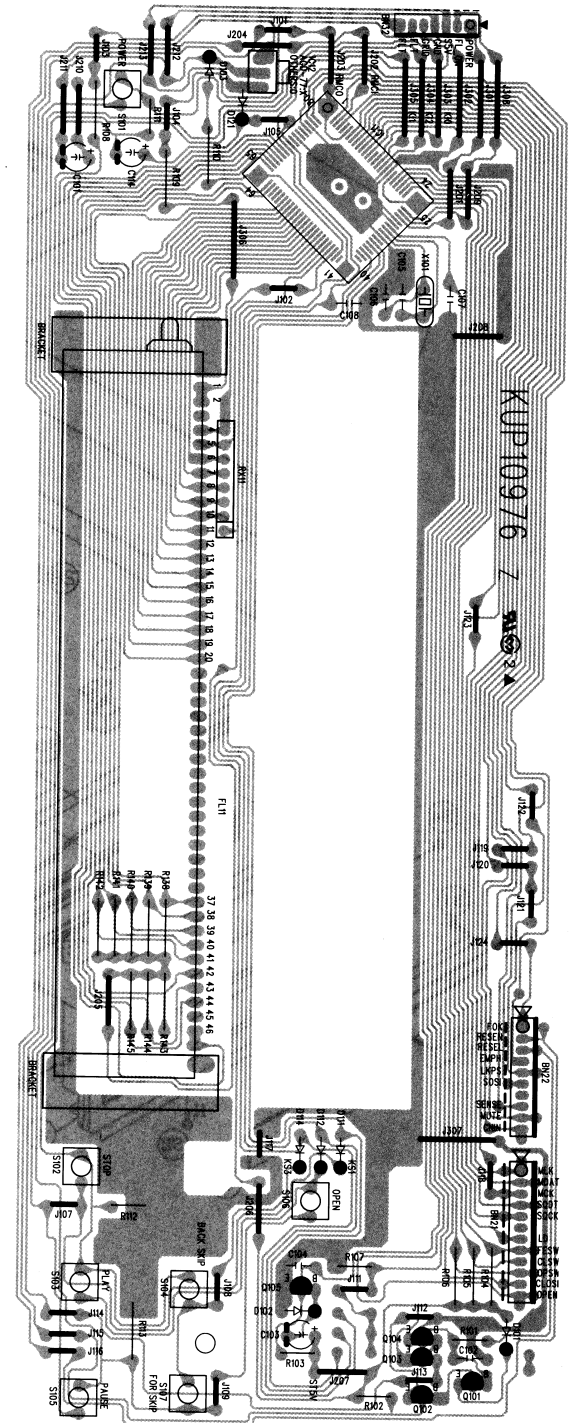
SCHEMATIC DIAGRAM



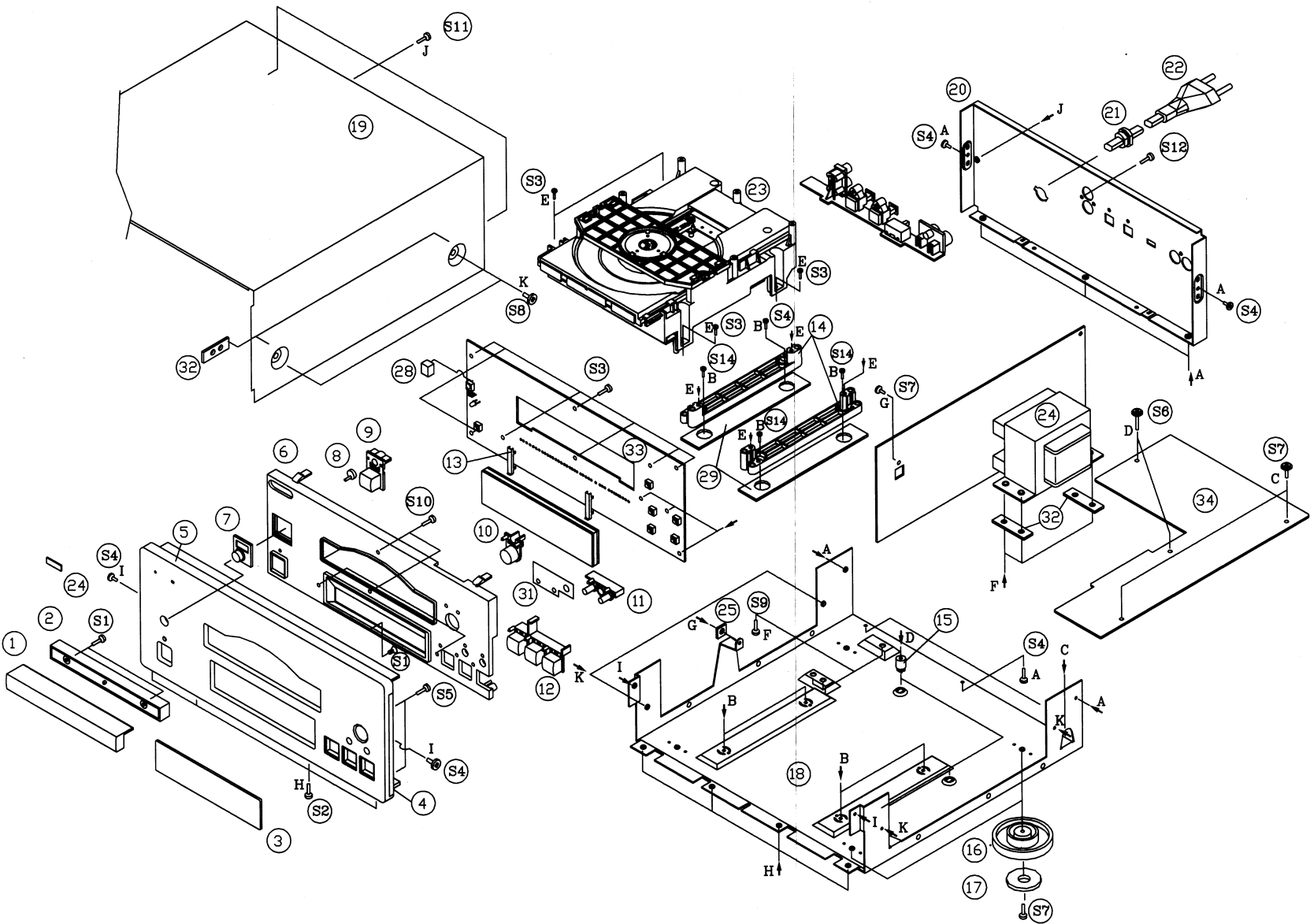
PRINTED CIRCUIT BOARDS



KUP10977_Y



EXPLODED VIEW



NO	PARTS - NO	DESCRIPTION	QTY	MODEL NO.
1	KGHIA221YC11	ORNAMENT,CD	1	
2	KGHIA10SK17	DOOR,CD	1	
3	KGHIA154Y	WINDOW	1	
4	KGHIA053C11	BAR,SIDE	2	
5	KGHIA054YK11	PANEL,AL	1	
6	KGVIA179ZK64	PANEL,SUB	1	
7	KGHIA155	WINDOW,SENSOR	1	
8	KGL1A120	INDICATOR,POWER	1	
9	KRTIA387ZK64	KNDR,POWER	1	
10	KRTIA385YK64	KNDR,TACT	1	
11	KRTIA410C13	KNDR,TACT	1	
12	KRTIA386VK64	KNDR,TACT	1	
13	KHDA209	BRACKET,FILT	2	
14	KHDA1A55	SUPPORT,MECHA	2	
15	KKE1A023	MOUNT,PCB	3	
16	KKL1A147K63	FOOT	4	
17	KHGA10397	FOOT,RUBBER	4	
18	KHGA1A37	MAIN CHASSIS	1	
19	KKC38077S21	CABINET,TOP	1	
20	KKE2A127XK59	PANEL,REAR	1	
21	KHR129	BUSHING,AC CORD	1	
22	KJAPB010Y	CORD,POWER	1	
23	B JUTC0-FL12H	CD MECHANISM ASS'Y	1	
24	BGBIA047	BADGE	1	
25	KHGA1A15	RUBBER	1	
26				
27				
28	KHGA1A32	SUPPORT,SENSOR	1	
29	KHGA1A35	RUBBER,SUPPORT	2	
30				
31	KMCIA139	PLATE,SHIELD	1	
32	KHGA1A43	RUBBER,TRANS	6	
33	KOP10976D	SUB PCB ASS'Y	1	
34	KOP10977D	MAIN PCB ASS'Y	1	

S1	KTS3+6J	SCREW	3	
S2	KTS3+8J	SCREW	4	
S3	KTB3+10G	SCREW	14	
S4	KTB3+8J	SCREW	9	
S5	KKDA106	SCREW	4	
S6	KTB3+14J	SCREW	2	
S7	KTV3+8J	SCREW	7	
S8	KTR4+6FFZ	SCREW	4	
S9	KTR4+8FF	SCREW	4	
S10	KTB3+6F	SCREW	4	
S11	KTB3+8JFZ	SCREW	3	
S12	KTB3+10GFZ	SCREW	5	
S13	KHDA1013FC	SCREW	4	
S14	KHDA1009	SCREW	4	

EXPLODED VIEW [PD-H500]

Ref. No.	Part No.	Description	Remarks
1	9A06240800	ORNAMENT, CD	
2	9A06240400	DOOR, CD	
3	9A06240500	WINDOW	
4	9A06231700	BAR, SIDE	
5	9A06242700	PANEL, AL	
6	9A06316100	PANEL, SUB ACD-77	
7	9A06227600	SENSOR, WINDOW	
8	9A06227400	INDICATOR, POWER	
9	9A06314500	KNOB, POWER	
10	9A06314300	KNOB, TACT (OPEN/CLOSE)	
11	9A06239700	KNOB, TACT	
12	9A06316000	KNOB, TACT	
13	9A05961600	BRACKET, FLT A4-92-1739	
14	9A06243000	SUPPORT, MECHA	
15	9A06229100	MOUNT, PCB A4-92-1728	
16	9A06315500	FOOT	
17	9A06229300	CUSHION, FOOT	
18	9A06244500	CHASSIS, MAIN A1-95-0354	
19	9A06315900	CABINET, TOP	
20	9A06316700	PANEL, REAR [EUR]	
	9A06242500	PANEL, REAR [DM]	
21	9A01376900	BUSHING, AC CORD HEYCO(SR-5N-4)	
22	9A06242000	CORD, POWER [DM]	
	9A05892200	CORD, POWER [EUR]	
23	9A06239000	CD MECHANISM ASS'Y	
24	9A06224200	BADGE, TEAC	
25	9A06229400	RUBBER, MECHA	
26			
27			
28	9A06241300	SUPPORT, SENSOR	
29	9A06241400	RUBBER, SUPPORT	
30			
31		PLATE, SHIELD	
32	9A06241700	RUBBER, TRANS	
33	9A06243200	SUB PCB ASS'Y [DM]	
	9A06243210	SUB PCB ASS'Y [EUR]	
34	9A06243300	MAIN PCB ASS'Y [DM]	
	9A06243310	MAIN PCB ASS'Y [EUR]	
S 1	9A06229000	SCREW, SPECIAL	
S 2	9A01397400	SCREW, KTS3+8J	
S 3	9A01377400	SCREW, KTB3+10G	
S 4	9A01535800	SCREW, KTB3+8J	
S 5	9A06244200	SCREW, KTS3+6J	
S 6	9A06244300	SCREW, KTW3+14J	
S 7	9A05339200	SCREW, KTW3+8J	
S 8	9A05984300	SCREW, KTB4+6FFZ	
S 9	9A06545500	SCREW, SPECIAL	
S10	9A06316300	SCREW, KTB+6F	
S11	9A01377200	SCREW, KTB3+8JFZ	
S12	9A01377300	SCREW, KTB3+10GFZ	
S14	9A06241200	SCREW, SPECIAL	

INCLUDED ACCESSORIES

Ref. No.	Part No.	Description	Remarks
	9A06053900	MANUAL, INSTRUCTION, JAPAN	
	9A06054300	MANUAL, INSTRUCTION, MULTI	

ELECTRICAL PARTS LIST

Ref. No.	Part No.	Description
D101,102	9A01390500	DIODE,1N4148MT
D103	9A05195000	LED,RED SLR342VCF02
D111,112	9A01390500	DIODE,1N4148MT
D114,121	9A01390500	DIODE,1N4148MT
D401,501	9A01390500	DIODE,1N4148MT
D701,702	9A05194600	DIODE,1N4003SRT
D703,704	9A05194600	DIODE,1N4003SRT
D711,712	9A05194600	DIODE,1N4003SRT
D713,714	9A05194600	DIODE,1N4003SRT
D715	9A06236200	DIODE, ZENER MTZJ6.2BT
D716,717	9A05359600	DIODE,ZENER MTZJ12BT
D720	9A05194600	DIODE,1N4003SRT
D721	9A06317000	DIODE MTZJ30BT EUR
D722	9A06236200	DIODE, ZENER MTZJ6.2BT
F701	9A06239400	FUSE 2C0630TLE
FL11	9A06238900	F.I.P. FIP8DRM7
IC11	9A06327200	IC,, U-COM ANAM1206C
IC12	9A06011300	SENSOR,REMOCON
IC21	9A05880700	IC,BA6208 MC-D200
IC31	9A05218500	IC,KA9258D
IC41	9A05432800	IC,KVIKA9220
IC51	9A06244900	IC, KS9283
IC61	9A06239300	IC, PCM1710U
IC63,64	9A05195800	IC,MC4558S
IC71	9A06244800	IC ASS'Y
JK61	9A06242100	JACK, LINE IN TERMINAL
JK62	9A06239100	MODULE, OPTICAL
JK63	9A06242200	JACK, BOARD
JK64	9A06239100	MODULE, OPTICAL
L401	9A05356900	COIL, AXAIL 10UH,K
L501	9A05968600	COIL,BEAD
Q101	9A05196400	TR,DTA114YST
Q102	9A05196500	TR,DTC114YST
Q103	9A05196400	TR,DTA114YST
Q201	9A05197400	TR,KTC3203YT
Q202	9A05196500	TR,DTC114YST
Q301	9A05359900	TR, 2SB544NPF
Q401	9A05895900	TR,KTA1266YT
Q501,601	9A05196500	TR,DTC114YST
Q602	9A05196500	TR,DTC114YST
Q603	9A05196400	TR,DTA114YST
Q604,605	9A05197500	TR,KTD1302T
Q701	9A06245000	T.R KTB1366Y
Q702	9A05196500	TR,DTC114YST
Q703	9A05939500	TR.,2SC1740SR
Q704	9A05911600	TR.,2SA933SR
Q705	9A05939500	TR.,2SC1740SR

Ref. No.	Part No.	Description
Q711	9A05196700	TR,KSA916-Y-SHTA
Q712,713	9A05197400	TR,KTC3203YT
Q714	9A05939500	TR.,2SC1740SR
Q715	9A05911600	TR.,2SA933SR
S101-107	9A04882500	SW,TACT SKHV10910A
SW61	9A06244100	SWITCH, SLIDE
T701	9A06327300	TRANS, POWER [EUR]
T701	9A06242800	TRANS, POWER [DM]
VR41	9A05317700	VR, SEMI FIX EVNDJAA03B24
VR42	9A05317400	R,SEMI FIXED EVNDJAA03B15
VR43,44	9A05317700	VR, SEMI FIX EVNDJAA03B24
WF71	9A05329000	WAFER MOLEX35309-0210
X101	9A05192900	CRYSTAL, DC-D1800
X501	9A05193100	CRYSTAL, DC-D1800



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