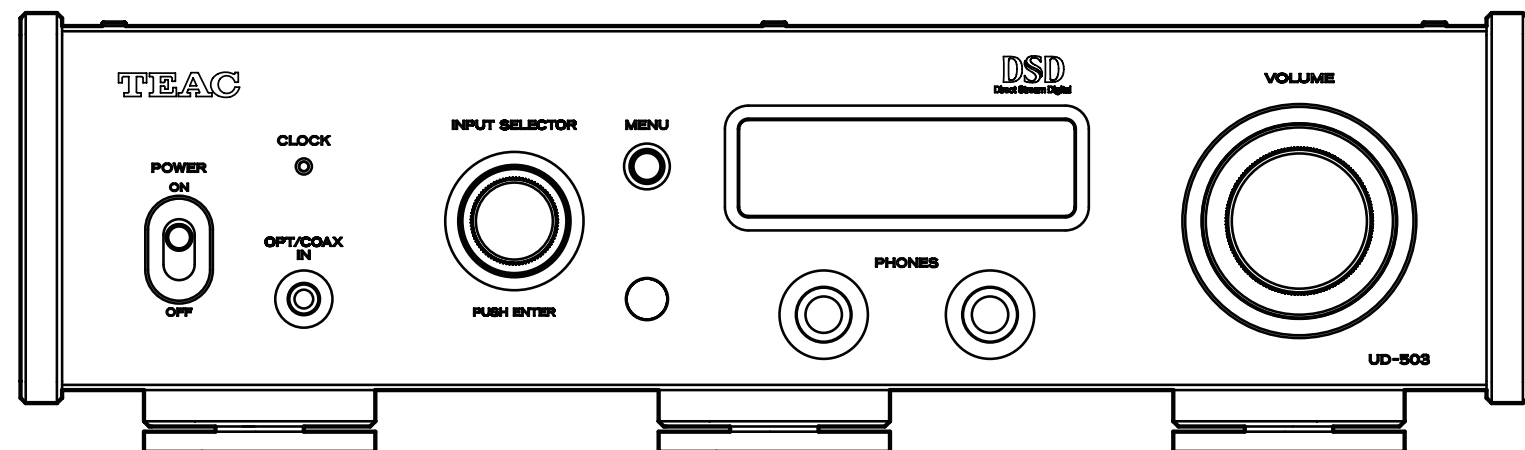
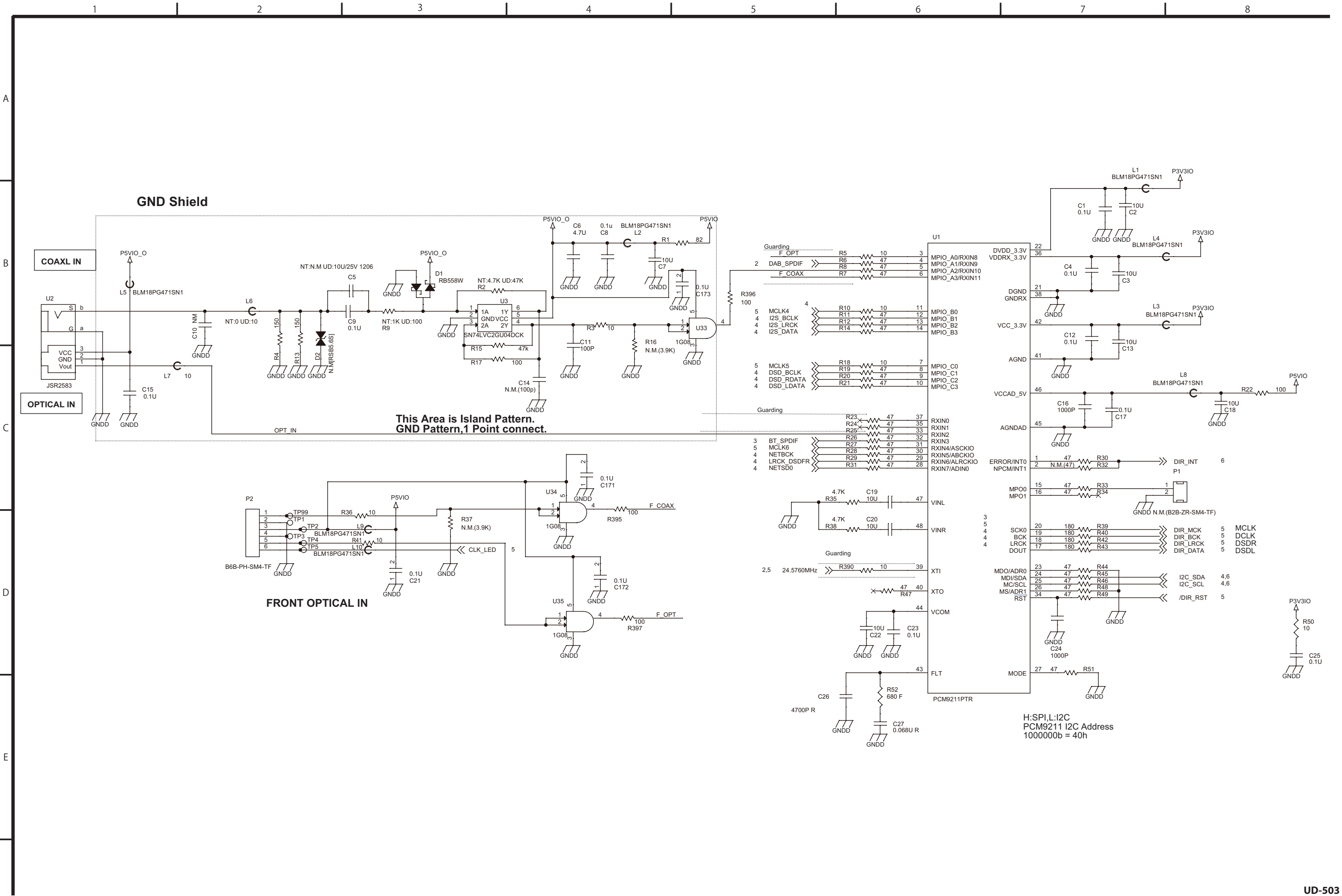
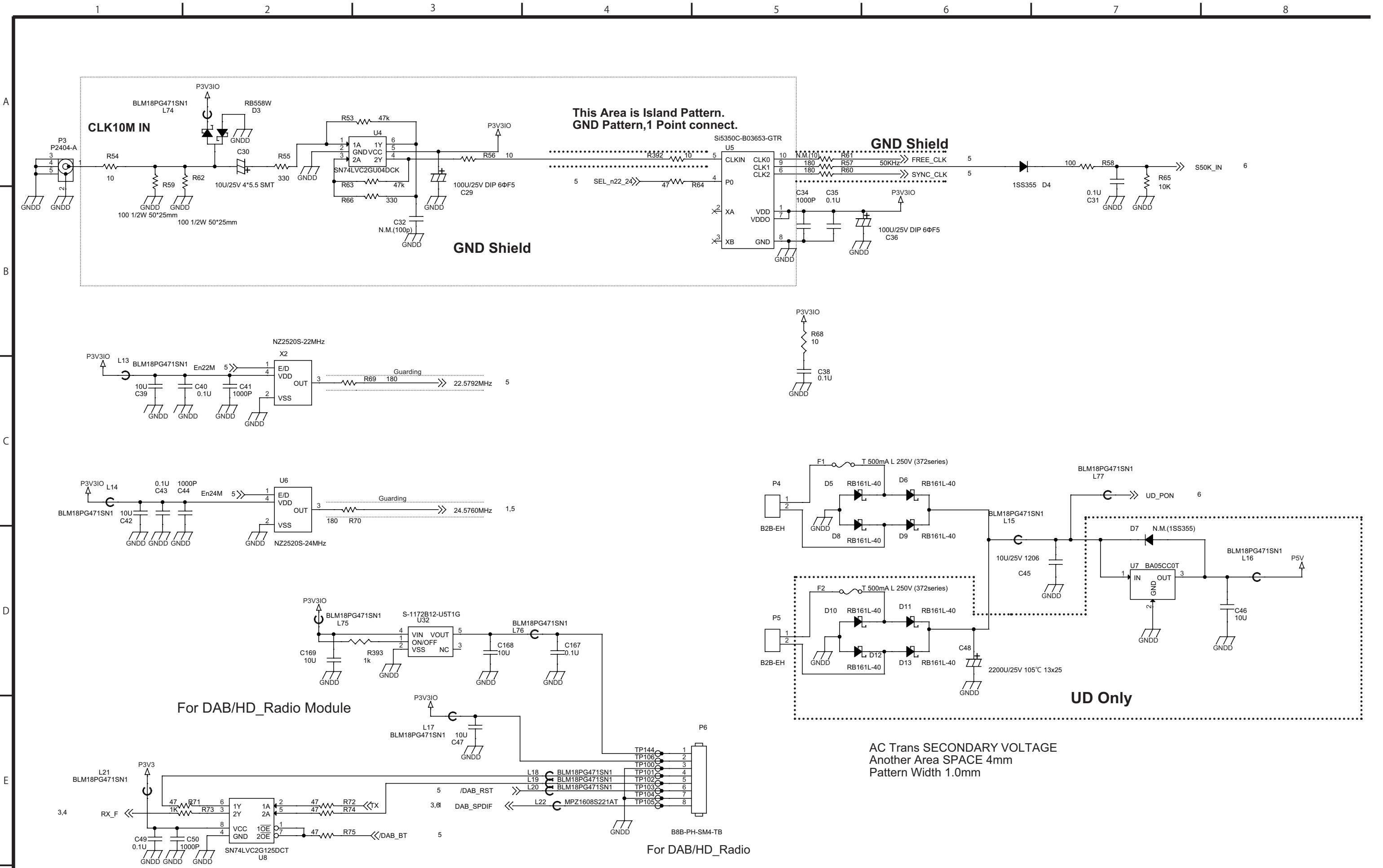


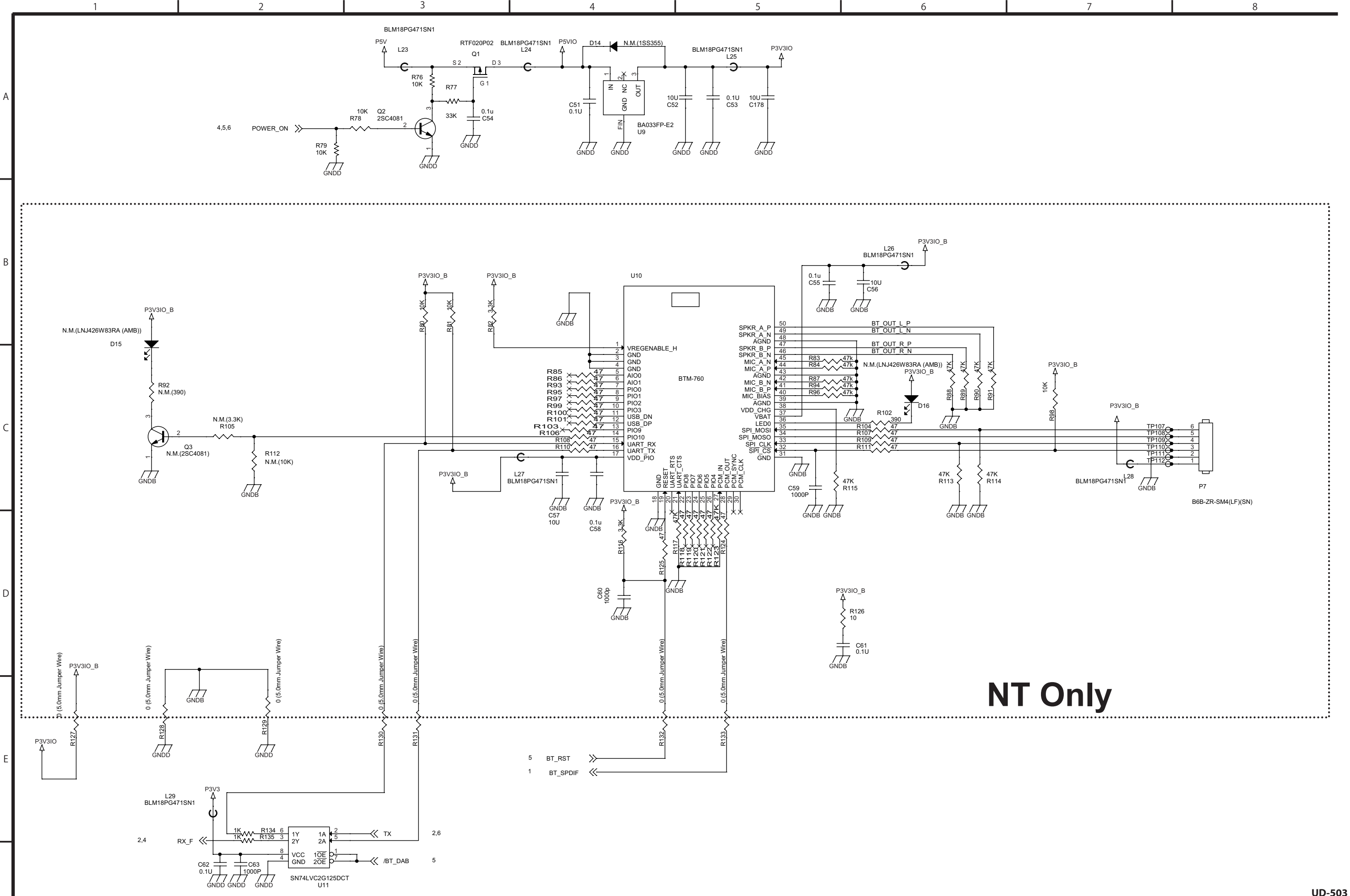
CONTENTS

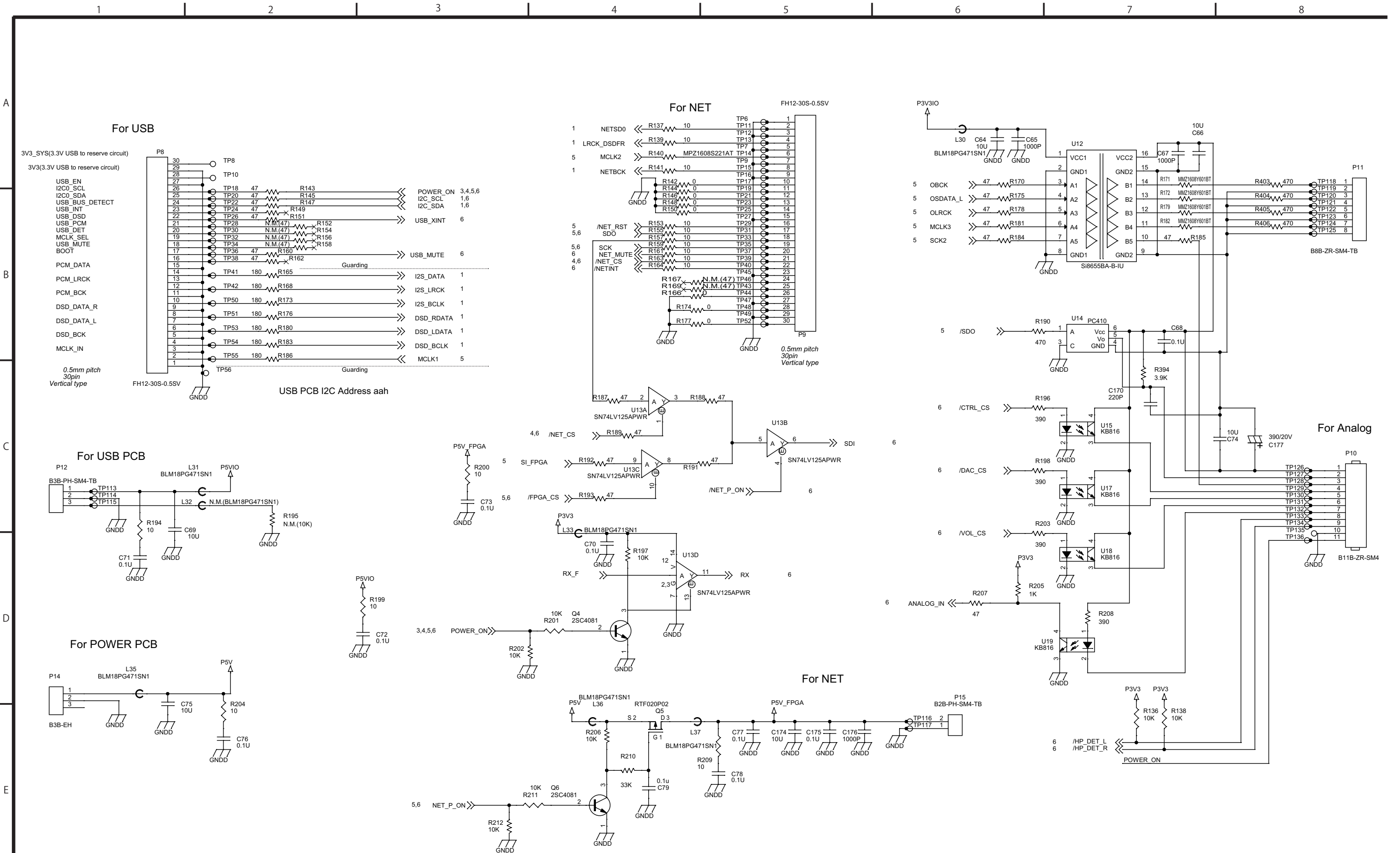
MAIN PCB (1/6).....	2
MAIN PCB (2/6).....	3
MAIN PCB (3/6).....	4
MAIN PCB (4/6).....	5
MAIN PCB (5/6).....	6
MAIN PCB (6/6).....	7
ANALOG PCB (1/5).....	8
ANALOG PCB (2/5).....	9
ANALOG PCB (3/5).....	10
ANALOG PCB (4/5).....	11
ANALOG PCB (5/5).....	12
PHONE PCB	13
DIGITAL/FRONT/VR/PWR SW PCB	14
USB PCB (1/4).....	15
USB PCB (2/4).....	16
USB PCB (3/4).....	17
USB PCB (4/4).....	18
LINEIN/POWER PCB	19



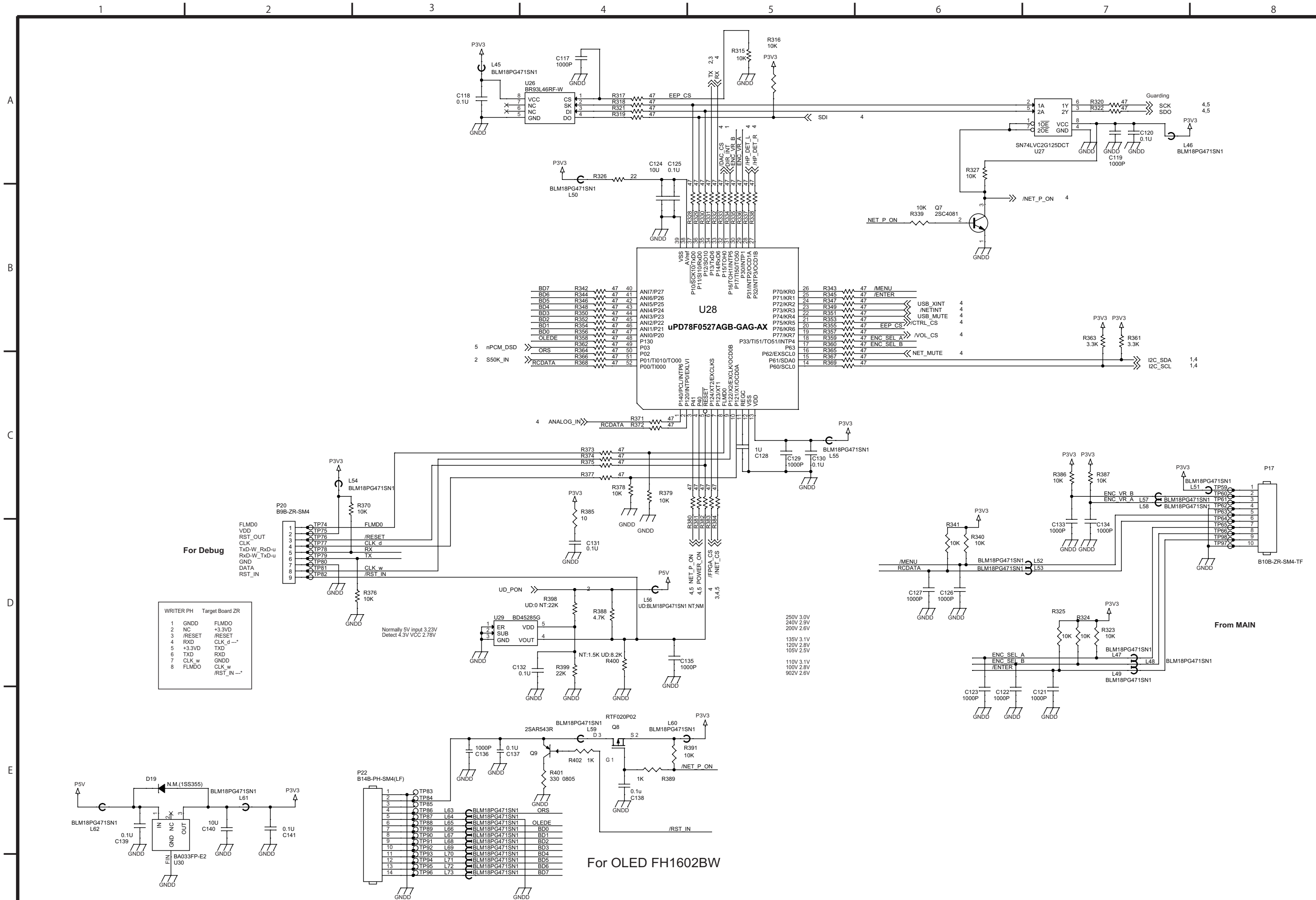


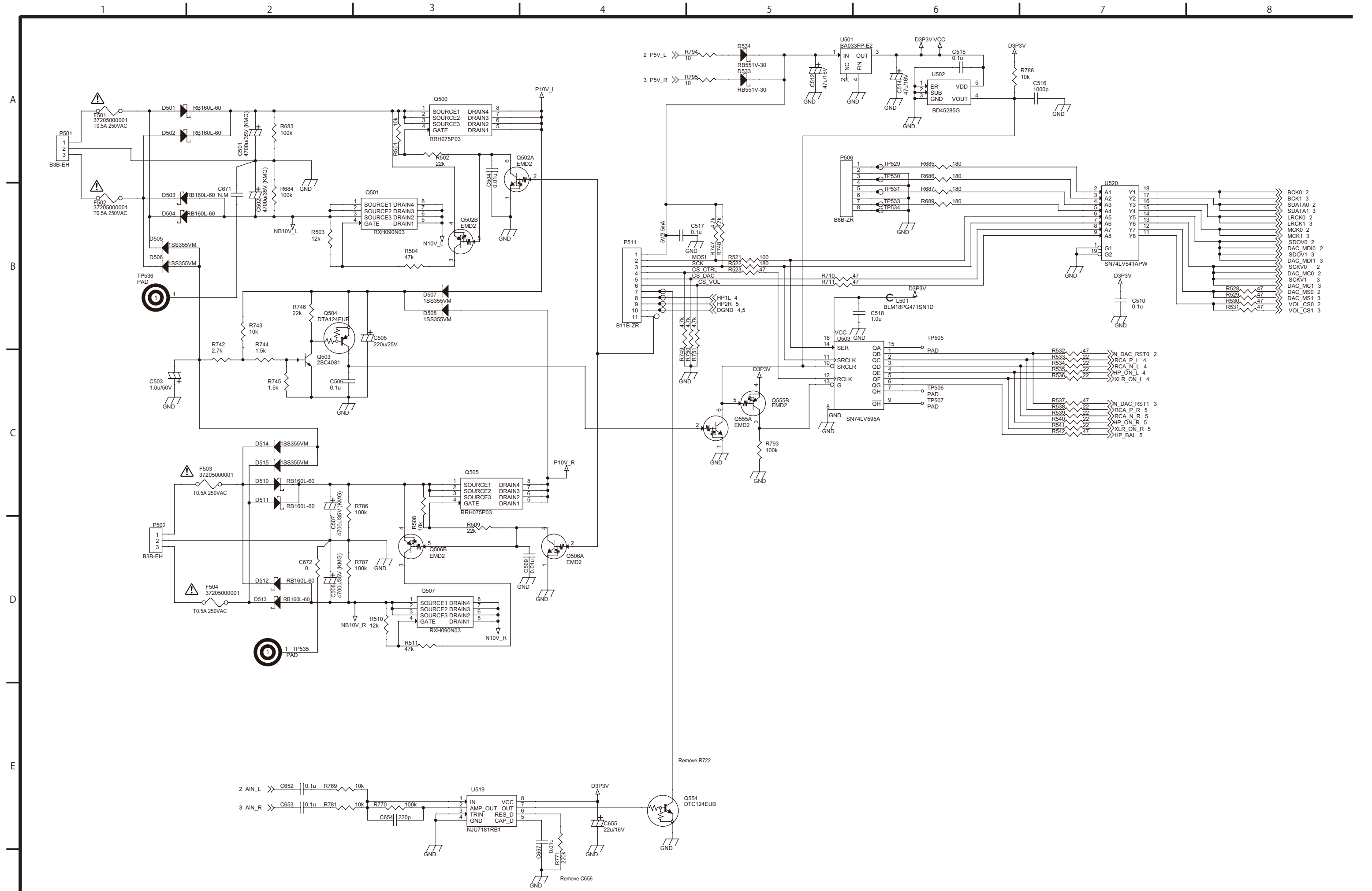


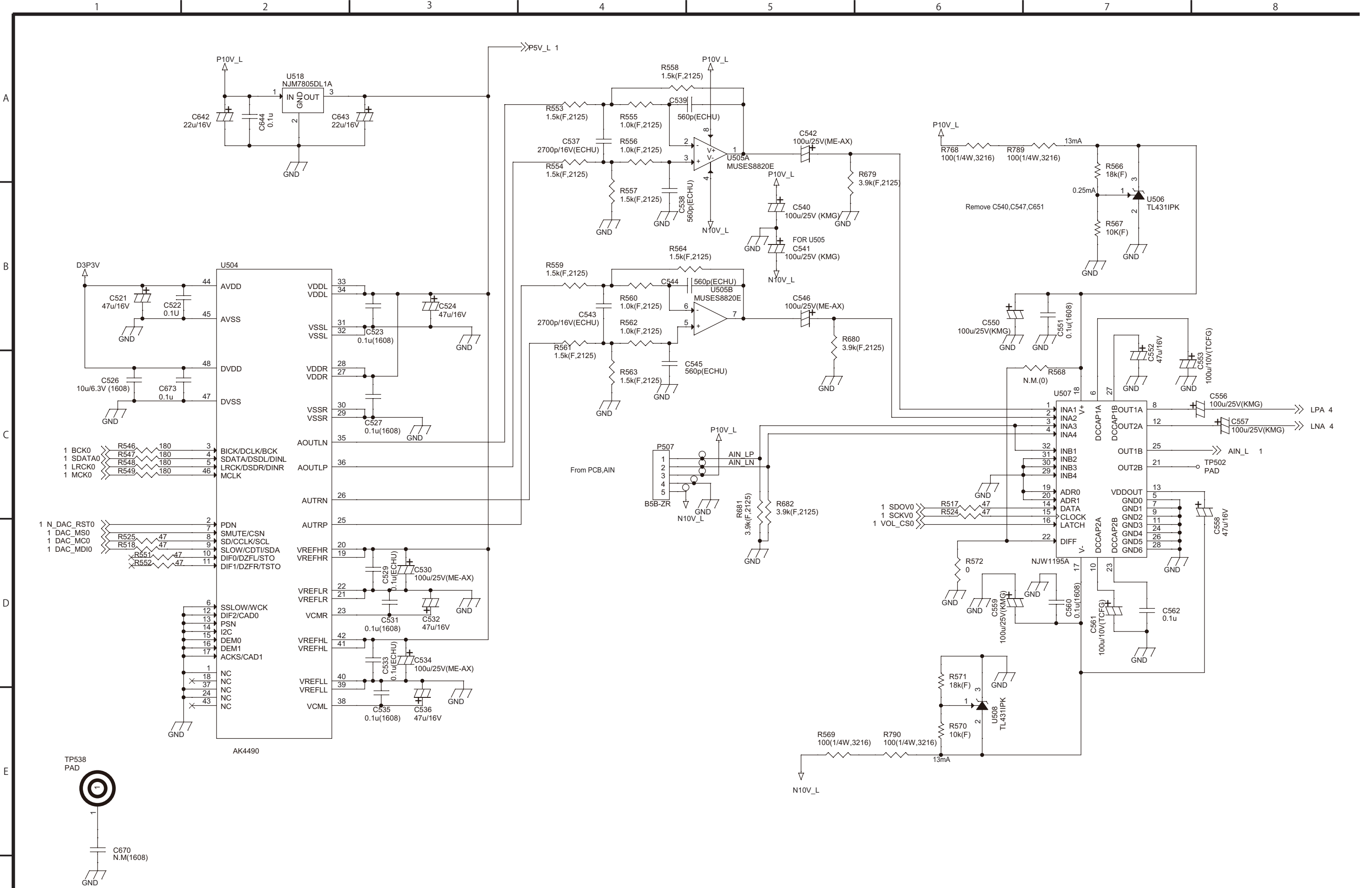


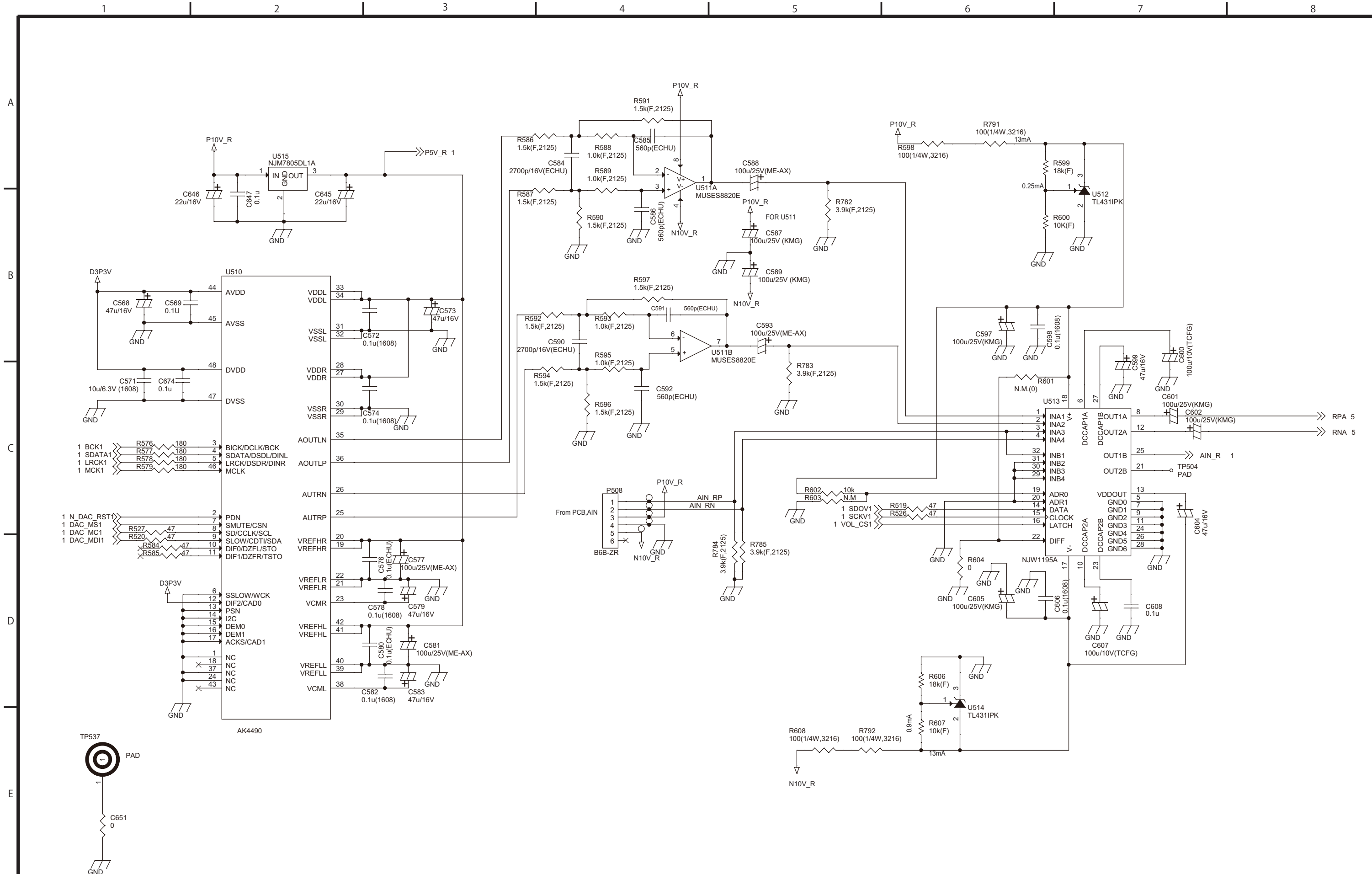


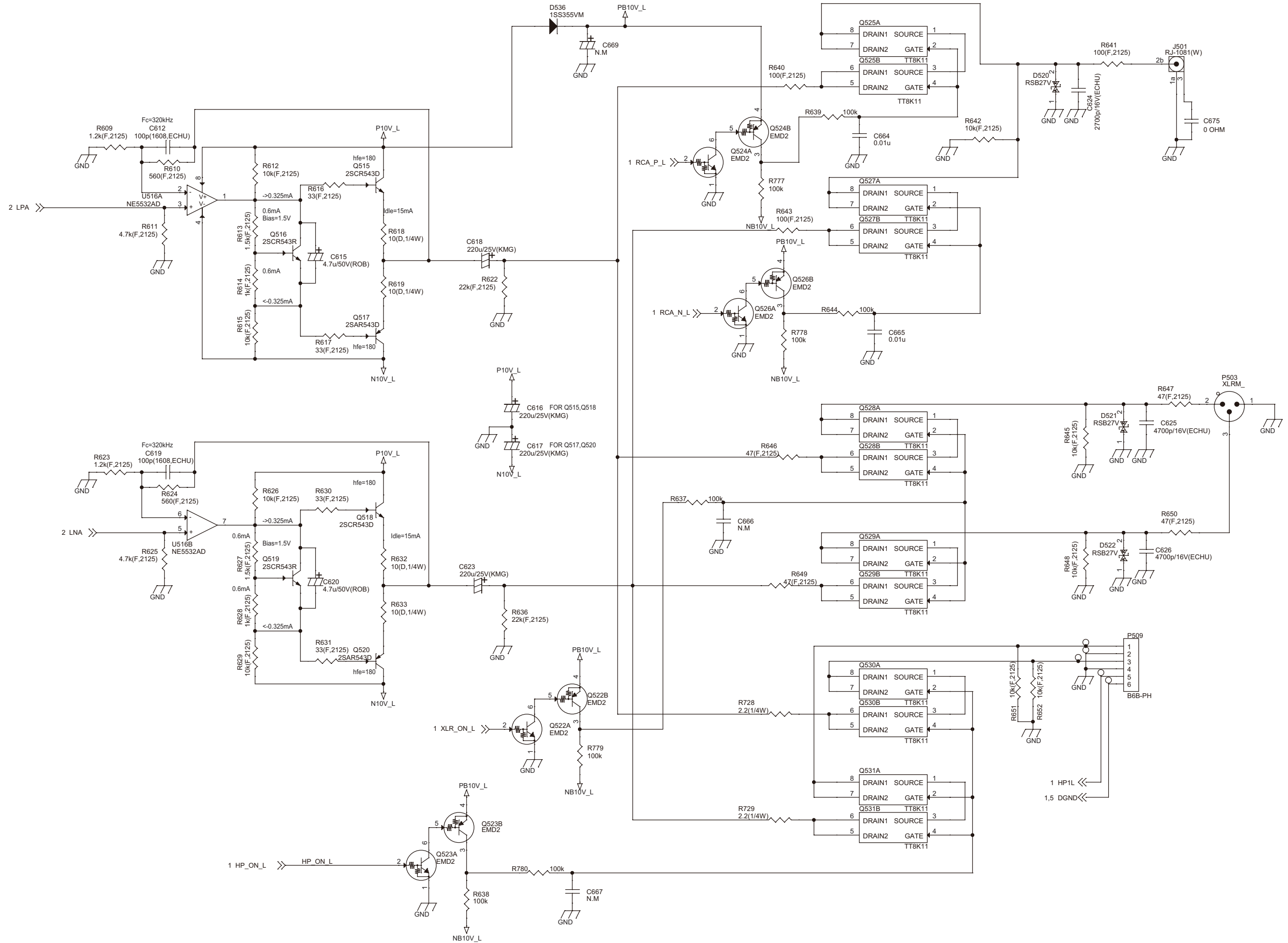


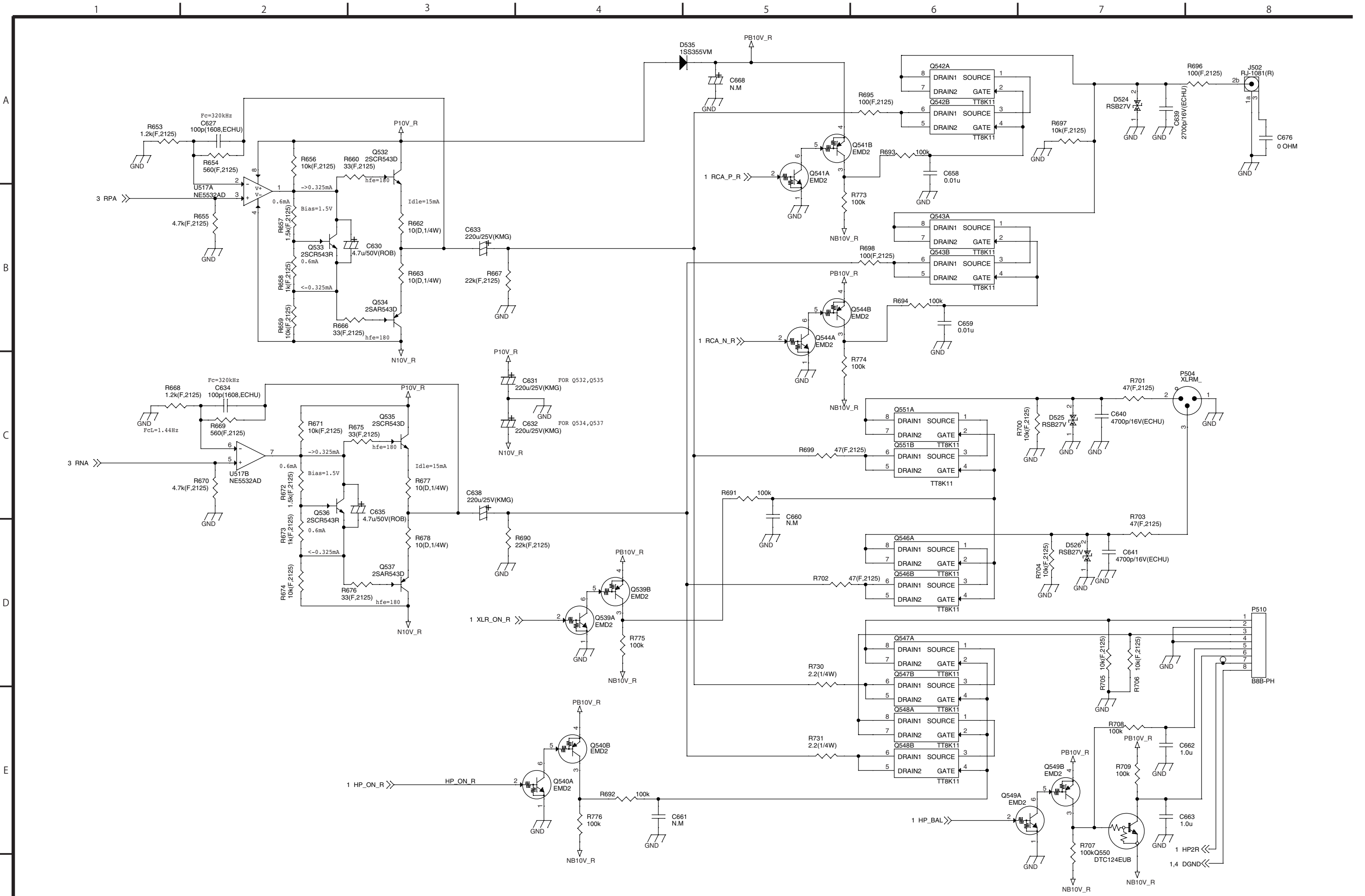


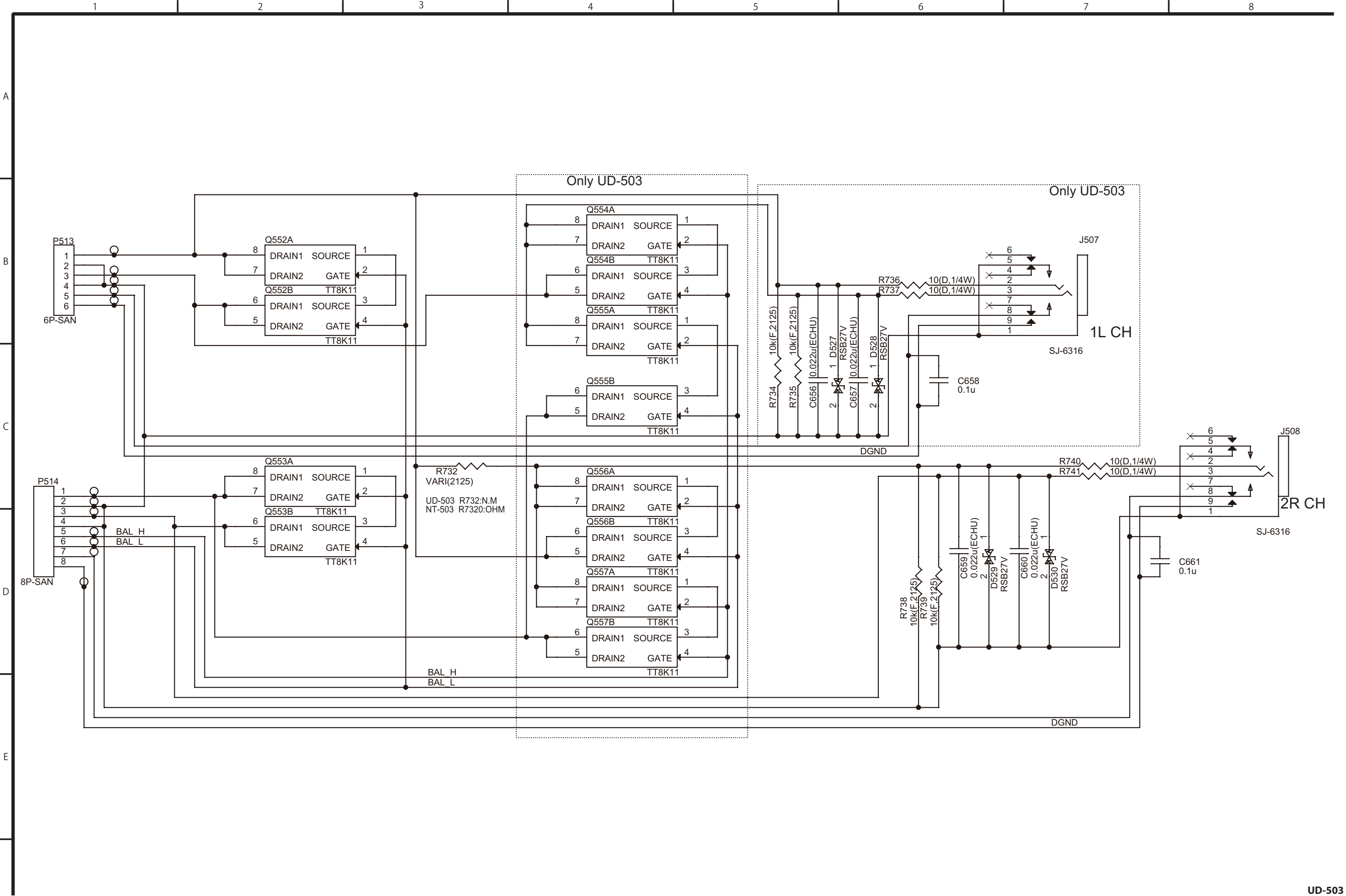


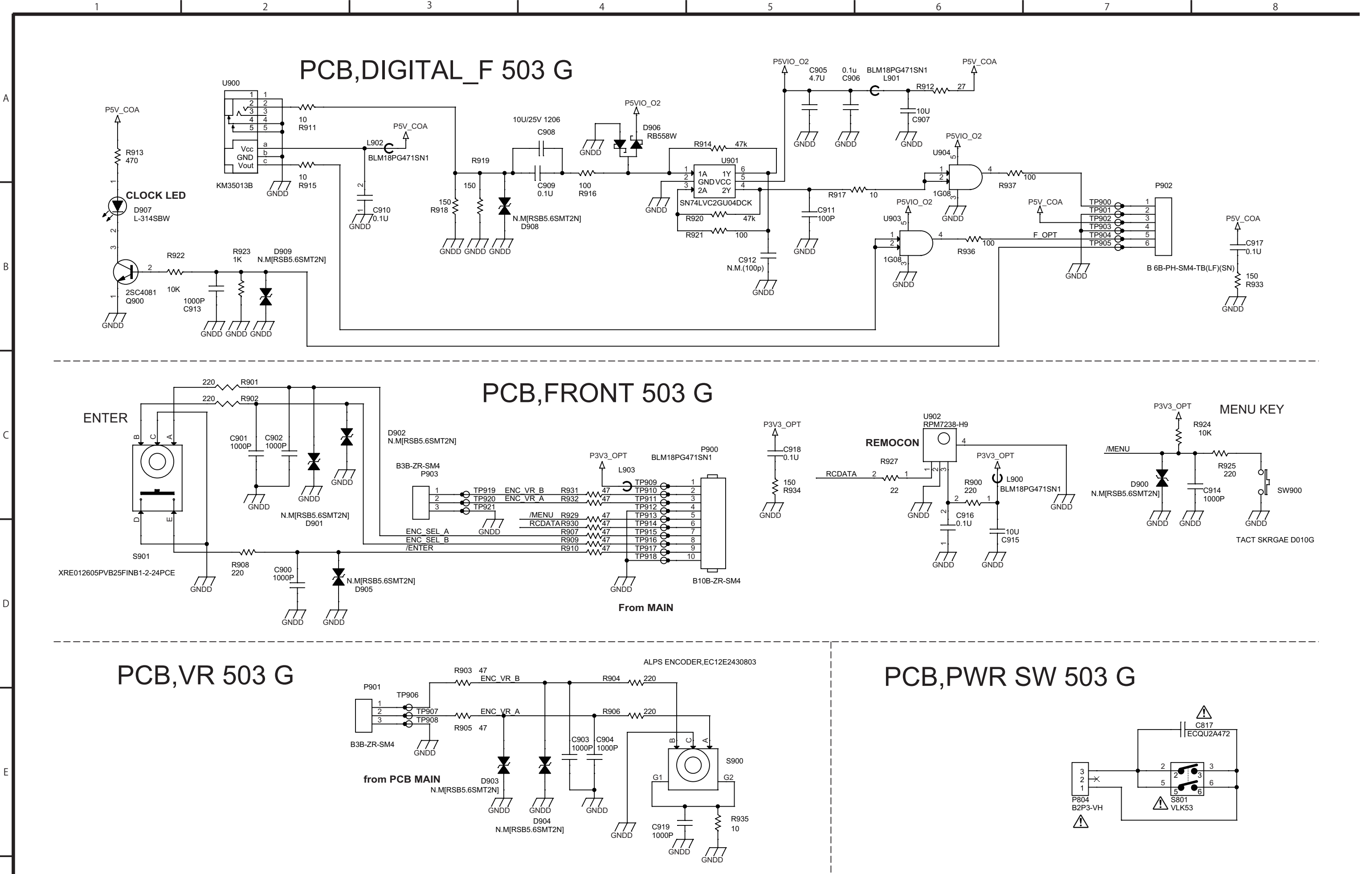


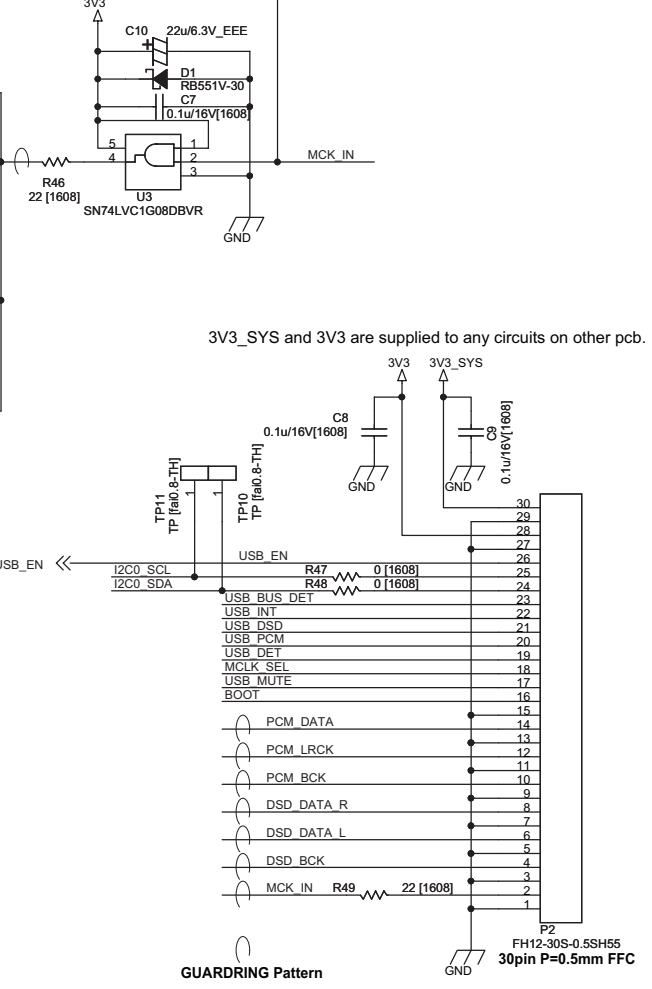
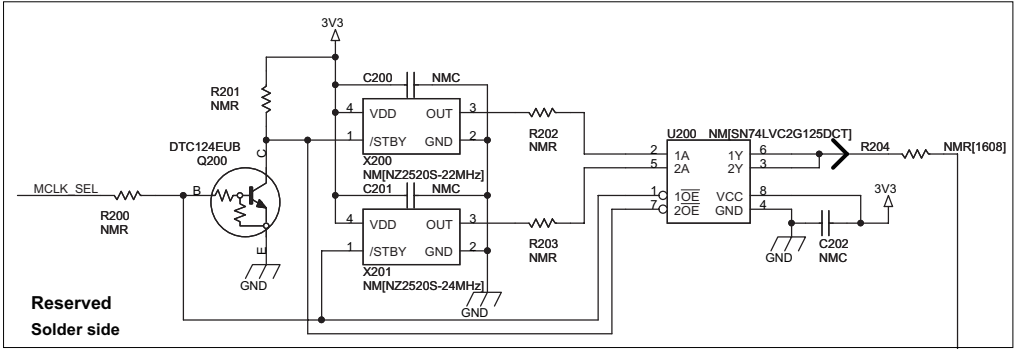
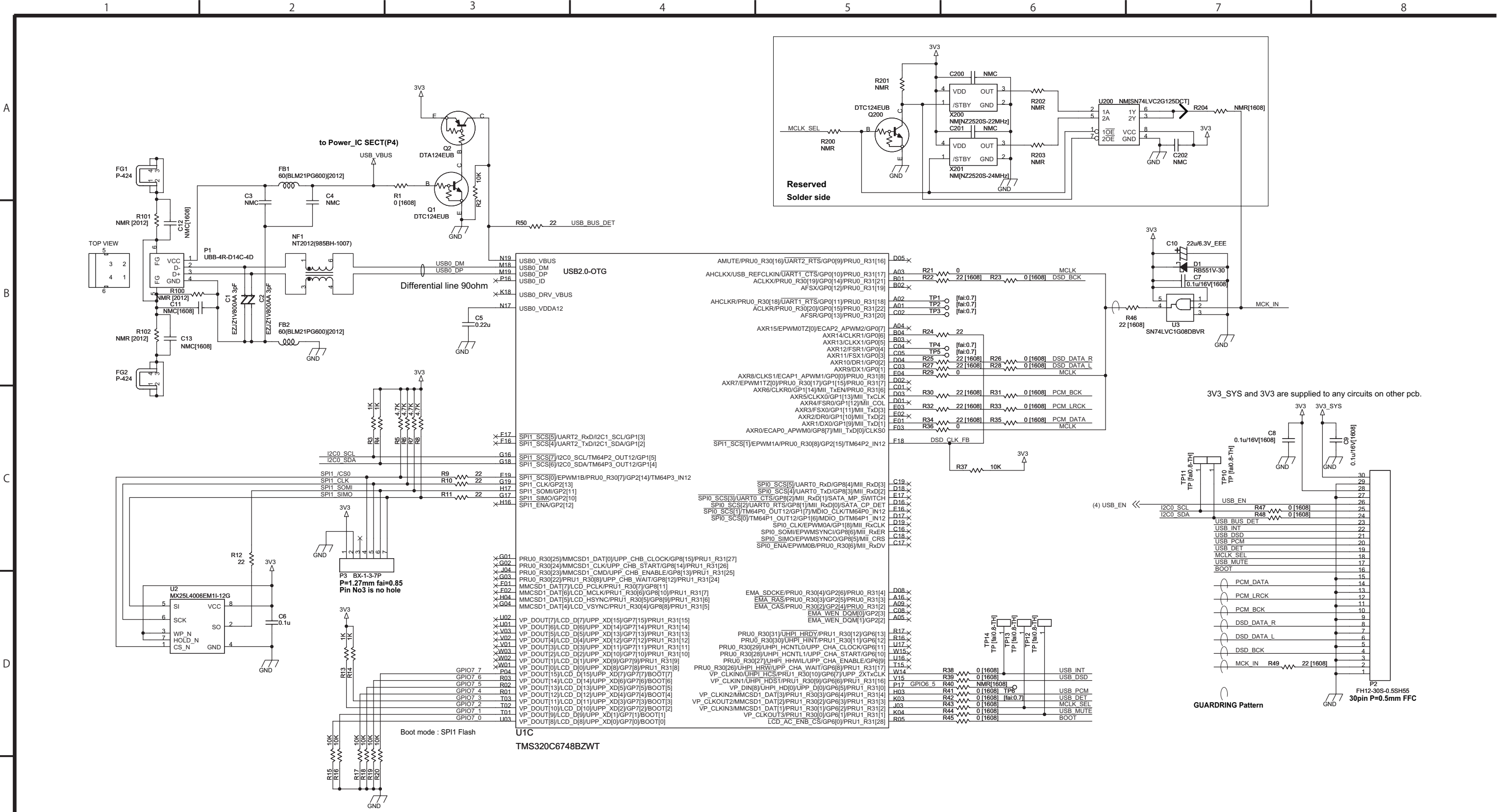










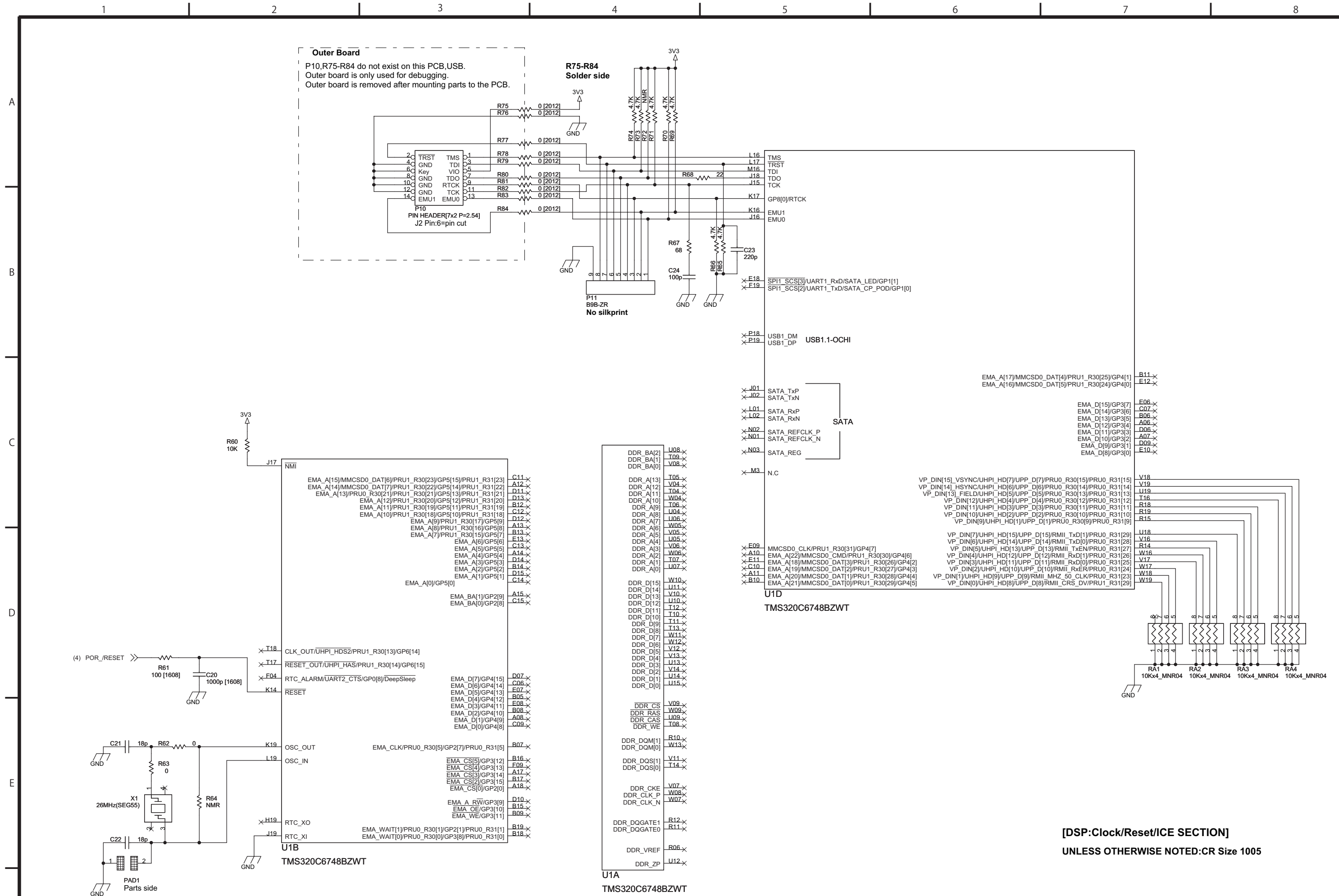


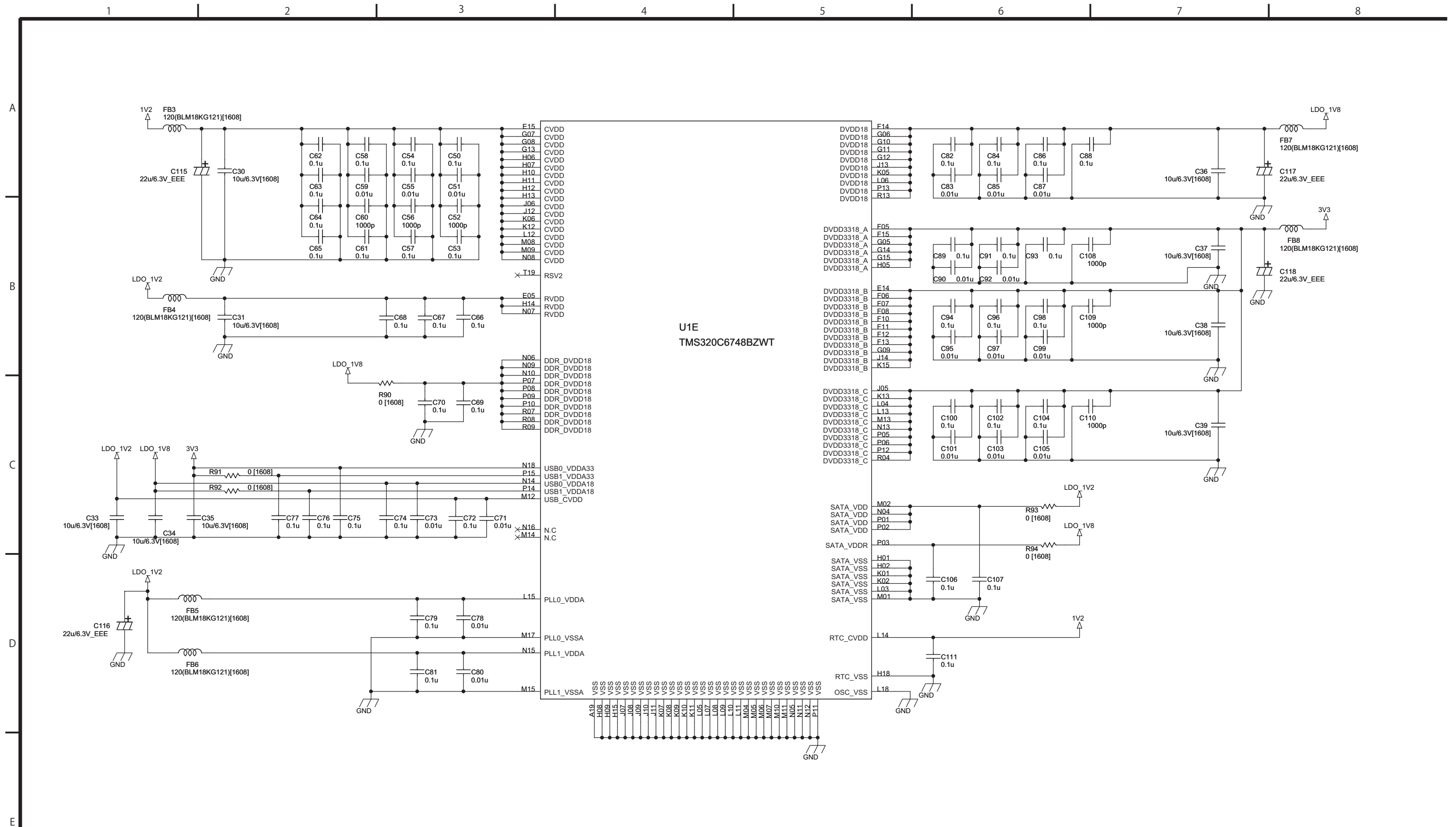
BootStraps		NAND8	
Boot bit	Boot Mode	6	5
7 6 5 4 3 2 1 0		SB	PLL0
0 x x 0 1 1 1 0	NAND8	0	0
0 0 x 1 1 1 0 0	MMC/SDC	0	1
0 0 0 0 0 0 0 0	EEPROM	1	0
0 0 0 0 0 0 0 1	Slave	1	0
0 0 0 0 0 1 1 0	I2C0	1	1
0 0 0 0 0 1 1 1	Slave	1	1
0 0 0 0 1 1 1 0	I2C1	1	1
0 0 0 0 1 1 1 1	Slave	1	1
0 0 0 1 1 0 0 1	EEPROM	1	1
0 0 0 1 1 0 0 1	Slave	1	1
0 0 0 1 1 0 0 1	Flash	1	1
x x x 1 0 1 1 1	UART1	1	1
x x x 1 0 1 0 0	UART2	1	1

SB	:Starting NAND Block
Enable	:PLL0=24
POSTDIV=3	
SYSCLK3=2	

MMC/SDC	
5	Type
0	SDC or MMC
1	MMC Only

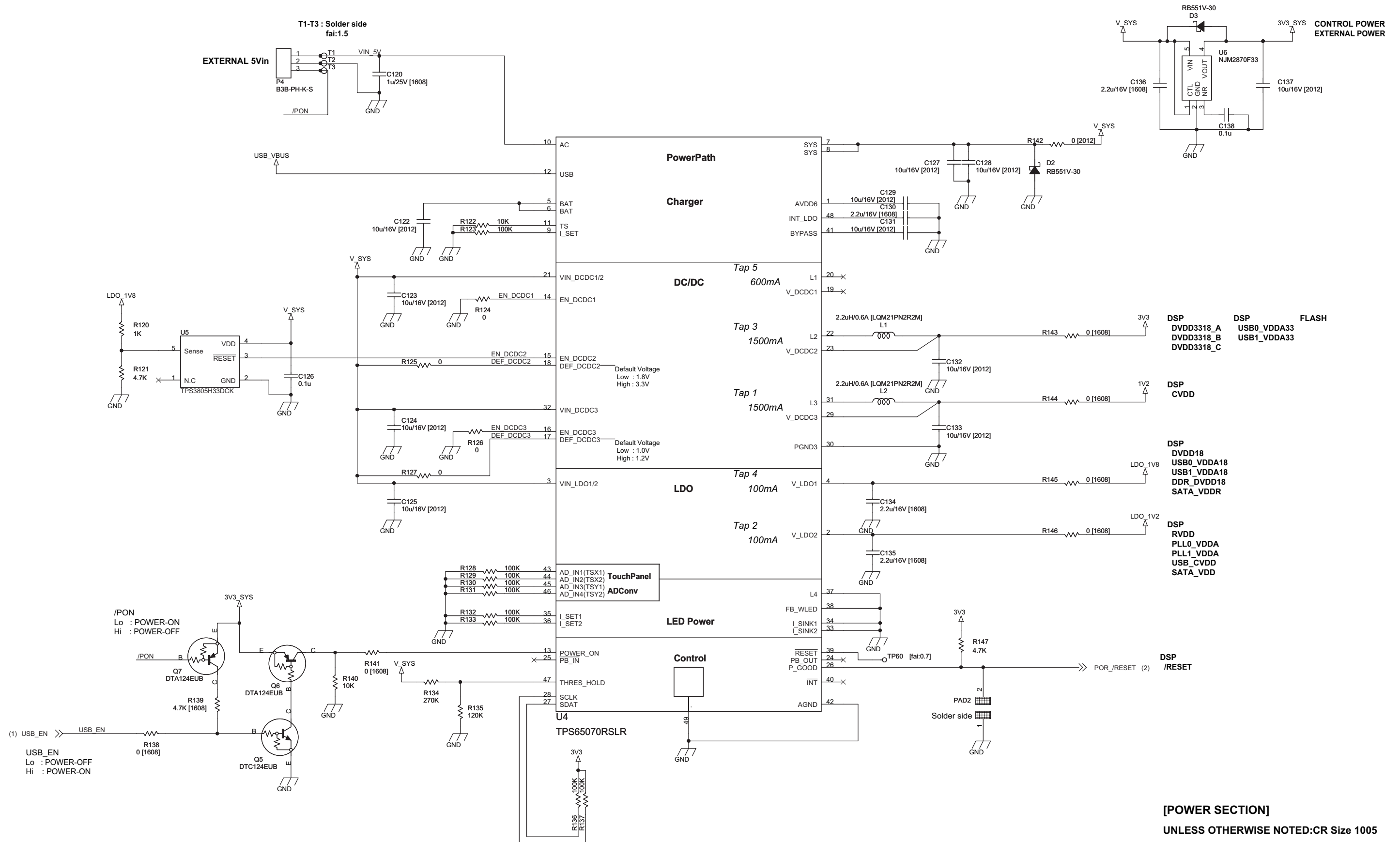
PCB,USB UD501 G
[DSP:MAIN SECTION]
UNLESS OTHERWISE NOTED:CR Size 1005



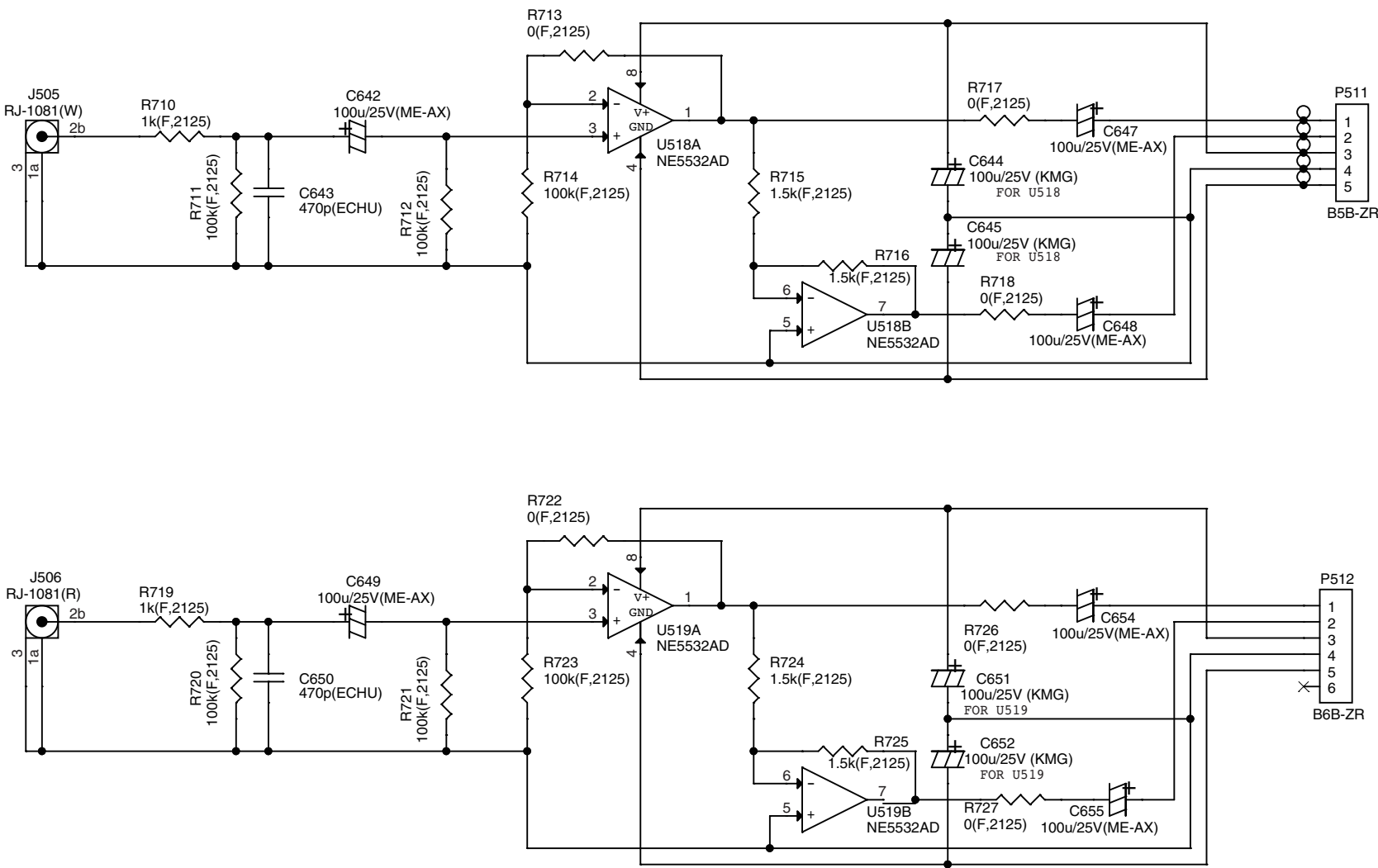


[DSP:POWER SECTION]

UNLESS OTHERWISE NOTED:CR Size 1005



PCB,LINEIN UD503 G



PCB,POWER UD503G

