

Service Manual

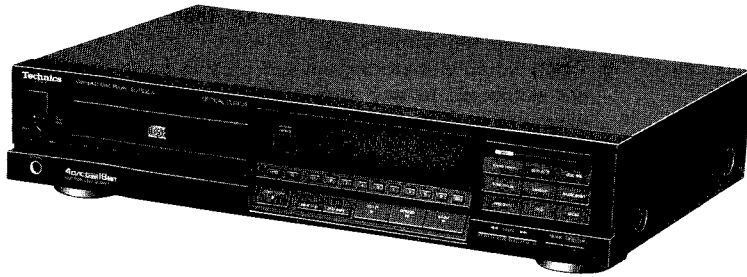
COMPACT
disc
DIGITAL AUDIO

DIGITAL

Compact Disc Player
SL-P222A

Color

(K)... Black Type
(S)... Silver Type



Area

Country Code	Area	Color
(E)	Continental Europe.	(K) (S)
(EK)	United Kingdom.	(K) (S)
(EG)	F.R. Germany.	(K) (S)
(EB)	Belgium.	(K) (S)
(EH)	Holland.	(K) (S)
(EF)	France.	(K) (S)
(Ei)	Italy.	(K) (S)

SPECIFICATIONS

■ Audio

No. of channels	2 (left and right, stereo)
Frequency response	2-20,000 Hz \pm 0.5 dB
Output voltage	2 V (at 0 dB)
Dynamic range	96 dB
S/N ratio	96 dB
Total harmonic distortion	0.005% (1 kHz, 0 dB)
Harmonic distortion	0.003% (1 kHz, 0 dB)
Wow and flutter	Below measurable limit
Output impedance	Approx. 600 Ω
Load impedance	More than 10 k Ω
Headphone output level	15 mW max. 32 Ω (adjustable)

■ Signal Format

Sampling frequency	44.1 kHz
D-A conversion	16-bit linear

■ Pickup

Wavelength	780 nm
------------	--------

■ General

Power supply	
For United Kingdom:	AC 50/60 Hz, 240V
For others:	AC 50/60 Hz, 220V
Power consumption	13 W
Dimensions (W x H x D)	430 x 92 x 278 mm
Weight	3.6 kg

Specifications subject to change without notice.
Weight and dimensions shown are approximate.

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Technics

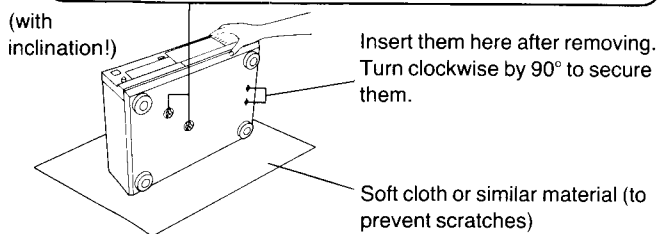
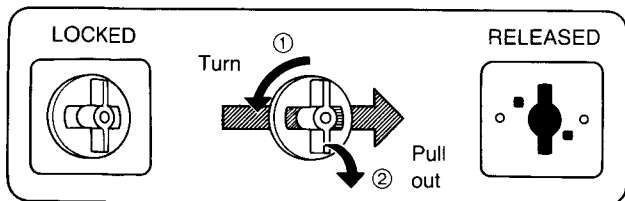
Matsushita Electric Industrial Co., Ltd.
Central P.O. Box 288, Osaka 530-91, Japan

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■ PLACEMENT

Before placement

Two transport security devices are secured to prevent the optical pickup from damage during transport. Be sure to release them before use.



Note:

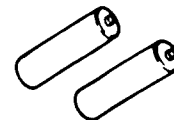
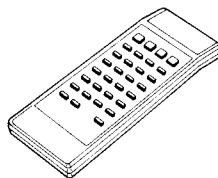
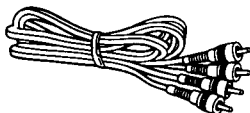
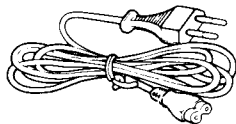
When transporting the unit, be sure to remove the compact disc from inside the unit. And replace the transport security devices again following the reverse order not to damage the optical pickup. (Make sure to place the unit with the rear panel facing downward.)

Notes of placement

- Place on a flat, level surface so that the front-rear inclination does not exceed 5°.
- Avoid places such as the following:
 - Near any equipment or device that generates strong magnetism.
 - On any heat-generating equipment or device, or in any place where the temperature is high (40°C or higher).
 - Extremely cold places (5°C or below).
 - Near a tuner or TV (It may cause noise in the broadcast, or disturbance of the TV picture.)
- Do not place heavy objects, other than system components, on top of the unit.
- When carrying or storing the unit, handle it with care so it is not subjected to any strong bumps. Always remove the disc before storing the unit for any period of time.
- To avoid problems due to vibration.
 - Do not place a book or similar object under this unit.
 - Do not route the connection cables (of this or other units) across the operation panel, across the top, or under the unit.

■ ACCESSORIES

- | | | | |
|--------------------------------|-----------------------------------|--------------------------------------|---------------------|
| ● AC power supply cord ... 1 | ● Stereo connection cable 1 | ● Remote control transmitter 1 | ● Batteries 2 |
| SJA187 (E, EG, EB, EH, EF, Ei) | (SJP2249-3) | (EUR64766) Black type | (UM-4NE/2S) |
| SJA193 (EK) | | (EUR64767) Silver type | |



Note: Configuration of AC power supply cord differs according to area.

■ PRECAUTION OF LASER DIODE

CAUTION: This product utilizes a laser diode with the unit turned "on", invisible laser radiation is emitted from the pick up lens.
Wave length: 780nM
Maximum output radiation power from pick up: 100µW/VDE

Laser radiation from the pick up lens is safety level, but be sure the followings:

1. Do not disassemble the optical pick up unit, since radiation from exposed laser diode is dangerous.
2. Do not adjust the variable resistor on the pickup unit. It was already adjusted.
3. Do not look at the focus lens using optical instruments.
4. Recommend not to look at pick up lens for a long time.

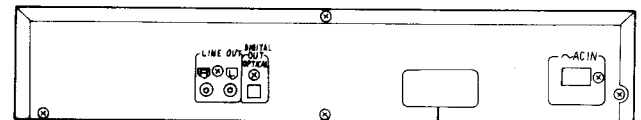
ACHTUNG: Dieses produkt enthält eine laserdioden. Im eingeschalteten zustand wird unsichtbare laserstrahlung von der lasereinheit abgestrahlt.

Wellenlänge: 780nM
Maximale strahlungsleistung der lasereinheit: 100µW/VDE

Die strahlung an der lasereinheit ist ungefährlich, wenn folgende punkte beachtet werden:

1. Die lasereinheit nicht zerlegen, da die strahlung an der freigelegten laserdioden gefährlich ist.
2. Den werkseitig justierten einstellregler der lasereinheit nicht verstellen.
3. Nicht mit optischen instrumenten in die fokussierlinse blicken.
4. Nicht über längere zeit in die fokussierlinse blicken.

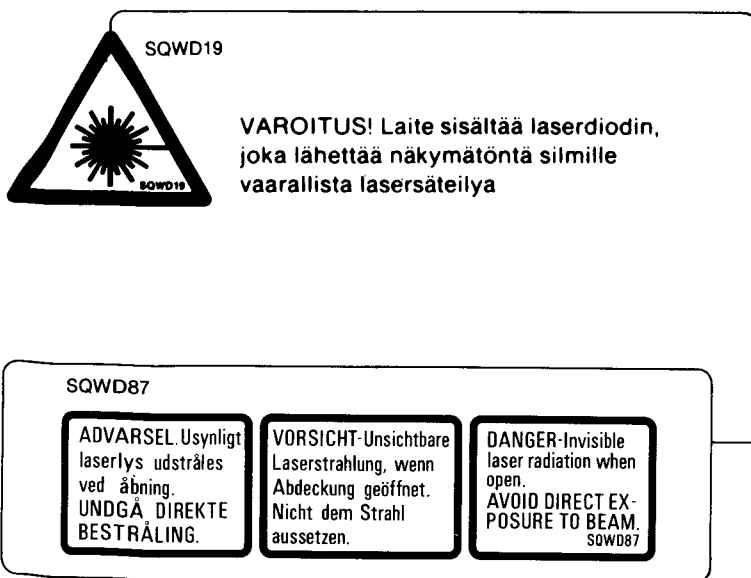
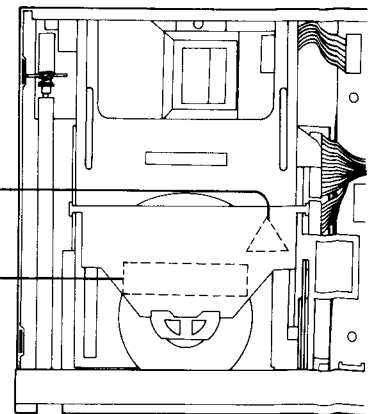
ADVARSEL: I dette a apparat anvendes laser.



SQWD7



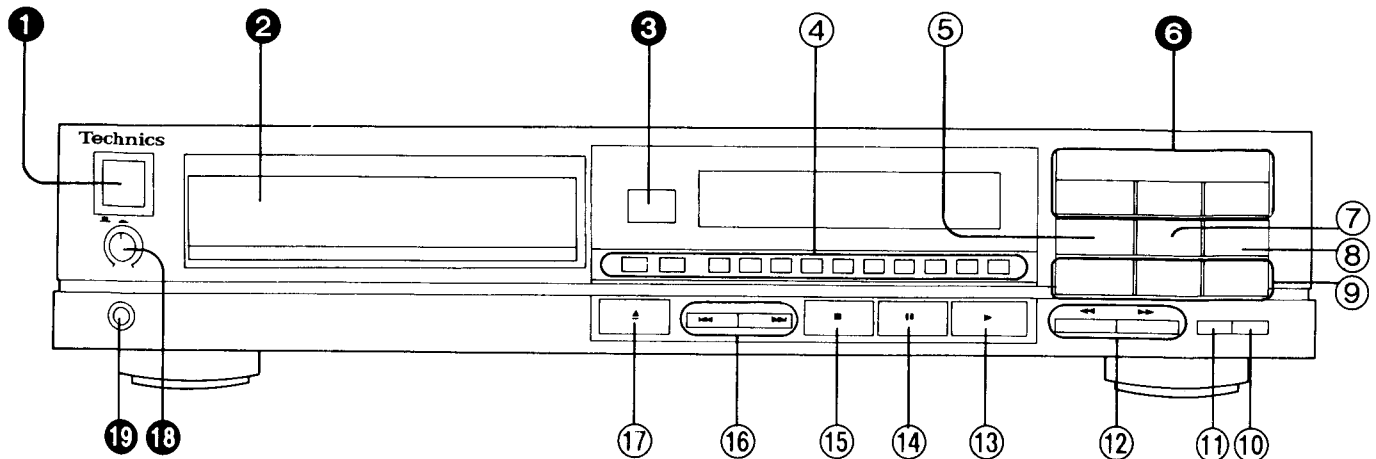
Obs:
Apparaten innehåller laser
Komponent av höger laserklass
än klass 1.



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■ LOCATION OF CONTROLS

The functions indicated by the black numbers (with white background, ④ etc.) can also be activated using the remote control transmitter.



Control section

① Power switch (power)

If a disc has already been loaded, play will begin when the power is turned ON.

② Disc holder

③ Remote control signal sensor (remote sensor)

④ Numeric buttons (+10, 0, 1~10)

⑤ Time mode select button (time mode)

⑥ Buttons for edit function (edit)

•Edit tape length button (tape length)

When compact discs are to be recorded to tape, this button can be used to calculate the number of tracks that can be recorded on each side of the tape, depending on the length of the cassette tape used, so that as little tape as possible is wasted.

•Tape-side select button (side A/B)

When recording compact discs to tape, this button can be used to check the number of tracks and amount of tape left over for side A or B.

•Disc link button (disc link)

This button can be used for edit recording from several discs.

⑦ Random play button (random)

This button can be used to play the tracks on a disc in a random sequence.

⑧ Music scan button (music scan)

This button can be used to play the first part of each track in order.

⑨ Buttons for program function

•Programmed-play button (program)

Pressing this button initiates the programmed play mode. You can then enter specific tracks using the numeric buttons.

•Clear button (clear)

This button can be used to clear tracks from the programmed sequence one at a time.

•Recall button (recall)

This button can be used to display the contents of the programmed track sequence for confirmation.

⑩ Auto cue button (auto cue)

Pressing this button causes the unit to skip automatically to the beginning of the next track on the disc and switch to the play standby mode.

⑪ Repeat button (repeat)

⑫ Search buttons (◀◀ search ▶▶)

These buttons can be used to move rapidly forward or backward on the disc during play. The search speed is slow when the button is pressed at first and becomes faster if the button is pressed and held continuously.

⑬ Play button (▶ play)

⑭ Pause button (⏸ pause)

⑮ Stop button (■ stop)

This button can be used to stop disc play, as well as to cancel the various play modes.

⑯ Skip buttons (◀◀ skip/skip ▶▶)

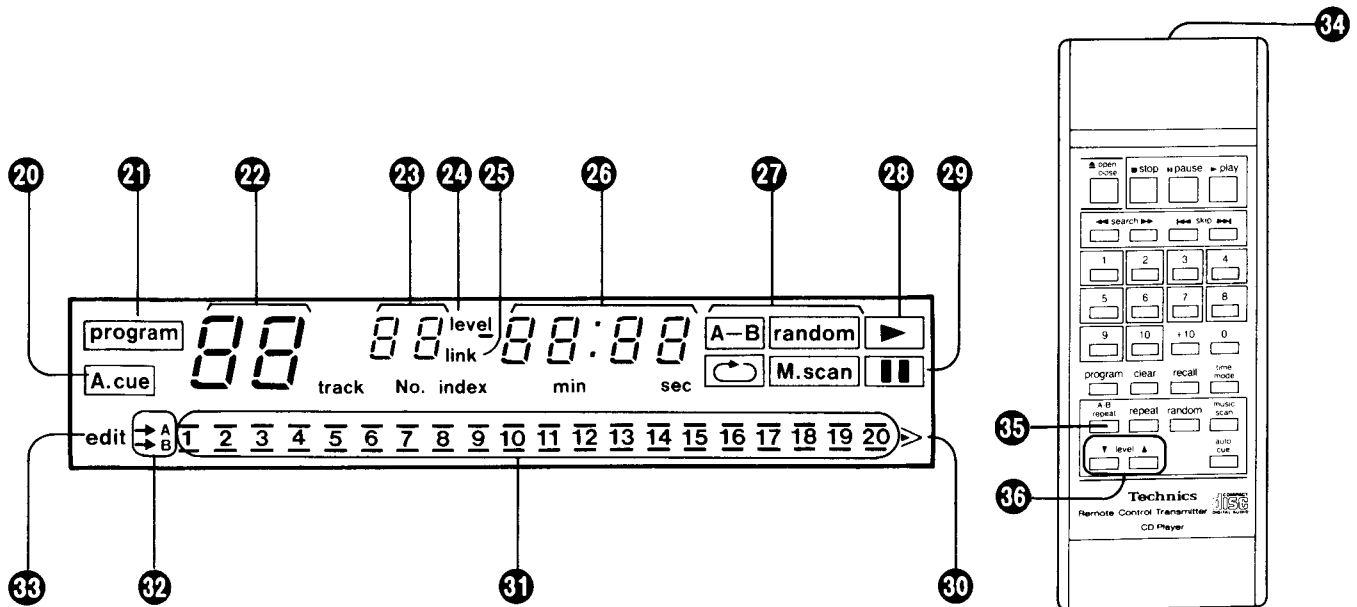
These buttons can be used to skip by track in the forward or reverse direction.

⑰ Disc holder open/close button (▲ open/close)





⑱ Headphones volume control (level)

Avoid listening to music at high volume levels for extended periods of time.



⑲ Headphones jack (phones)



Indicators section

- 20** Auto cue indicator (**A. cue**)
- 21** Programmed-play indicator (**program**)
- 22** Track number display (track)
- 23** Index/program number display (index/No.)
- 24** Level indicator (level)
This indicator lights when the output level is attenuated by the remote control.
- 25** Disc link indicator (link)
- 26** Time display (min/sec)
- 27** Operation indicators
The following indicators light during their respective operations.
 - A-B**: A-B repeat play (remote control operation)
 - random**: Random play
 - : Repeat play
 - M. scan**: Music scan
- 28** Play indicator ()
- 29** Pause indicator ()
- 30** "Over" mark ()
This indicator lights if the total number of tracks on the disc is 21 or more.
- 31** Track number indicator (1-20)
- 32** Tape side indicator (A/B)
- 33** Compact disc edit indicator (edit)

Remote control transmitter

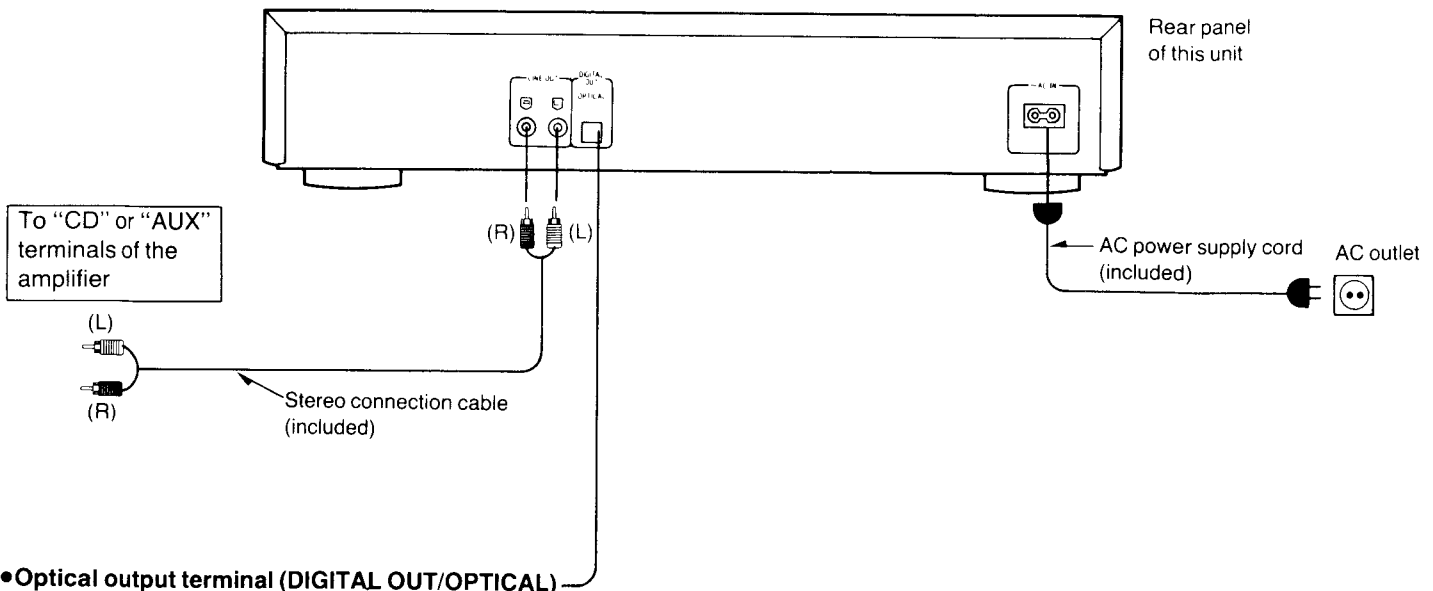
- 34** Remote control signal transmission window
- 35** A-B repeat button (A-B repeat)
This button can be used to play the portion of a disc between two points (A and B) chosen by you.
- 36** Level buttons ( level )
These buttons can be used to control output level (from 0 dB to -12 dB), except for digital output.

Unnumbered buttons on the remote control transmitter function identically to their corresponding parts on the unit.

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■ CONNECTIONS

Turn power off on all components before making connections.



● Optical output terminal (DIGITAL OUT/OPTICAL)

This terminal can be used for connection with other equipment that has a digital input terminal, such as an amplifier, by using an optical cable (optional). A dust-protection cap is inserted in this terminal. Remove this cap only when a connection is to be made to this terminal.

Note:

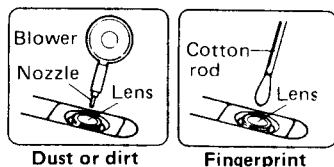
The configuration of the AC outlet and AC power supply cord differs according to area.

■ CLEANING OF LENS

If the lens is stained causing sound skip or operation failure, open the top cover by pressing the open button, and clean the lens.

● To remove dust or dirt

Blow the lens with the blower provided in the cleaning kit to remove dust or dirt.



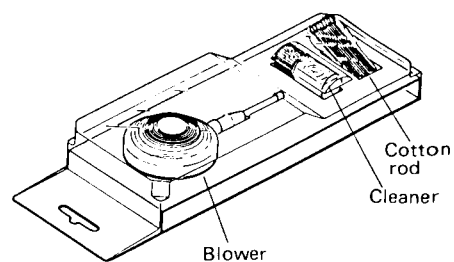
● To remove fingerprint

If the blower is not enough, moisten the cotton rod with the lens cleaner solution and wipe the lens with it from center of the lens to outside.

Cautions:

- Do not directly apply the cleaner solution to the lens. Do not apply too much solution to the cotton rod or otherwise the solution will flow into the player.
- Wipe the lens carefully. Do not give too much stress to the lens or otherwise it may scratch the lens or cause optical pickup trouble.
- If the solution should be too much applied, wipe the lens with a dry cotton rod.

Lens cleaning kit (Part No. : SZZP1038C)



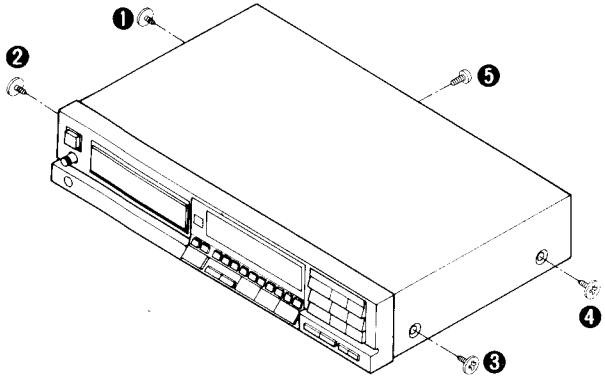
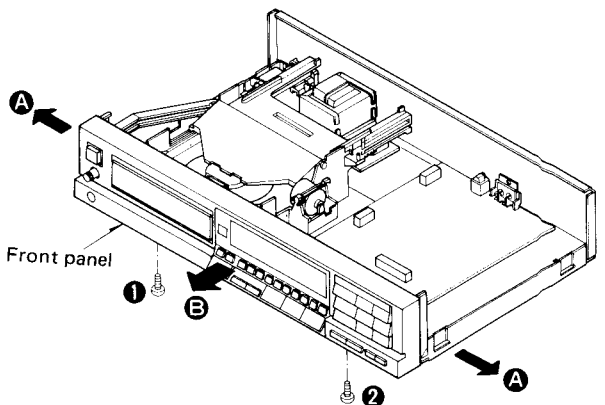
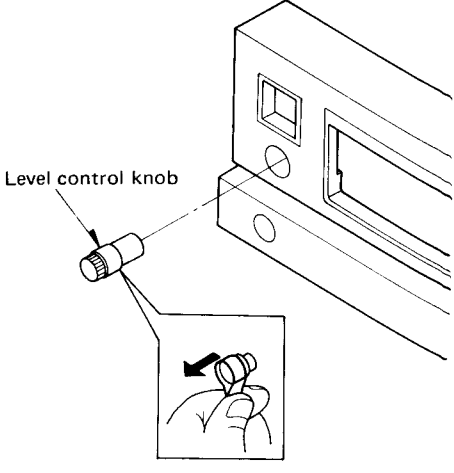
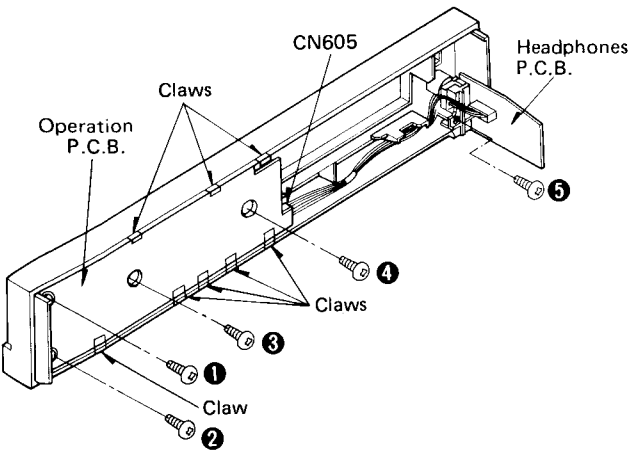
DISASSEMBLY INSTRUCTIONS

Warning: This product uses a laser diodes. Refer to caution statements on page 3.

ACHTUNG: •Die lasereinheit nicht zerlegen.

- Die lasereinheit darf nur gegen eine vom hersteller spezifizierte einheit ausgetauscht werden.

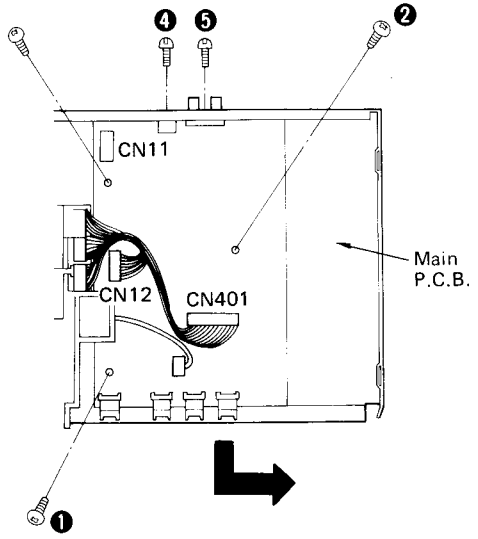
* This CD player is equipped with FPC boards, so handle them with care during disassembly and reassembly.

Ref. No. 1	How to remove the cabinet	Ref. No. 2	How to remove the front panel
Procedure 1	1. Remove the 5 screws (❶ ~ ❺).	Procedure 1→2	1. Remove the 2 screws (❶, ❷). 2. Slightly pull out the front panel in the direction of the arrow (A). 3. Remove the front panel in the direction of the arrow (B).
			
Ref. No. 3	How to remove the operation P.C.B. and headphones P.C.B.		
Procedure 1→2→3			
<p>A. Operation P.C.B.</p> <ol style="list-style-type: none"> 1. Remove the 4 screws (❶ ~ ❹). 2. Remove the connector (CN605). 3. Release the 8 claws. 	<p>B. Headphones P.C.B.</p> <ol style="list-style-type: none"> 1. Remove the level control knob. 2. Remove the screw (❺). 		
			

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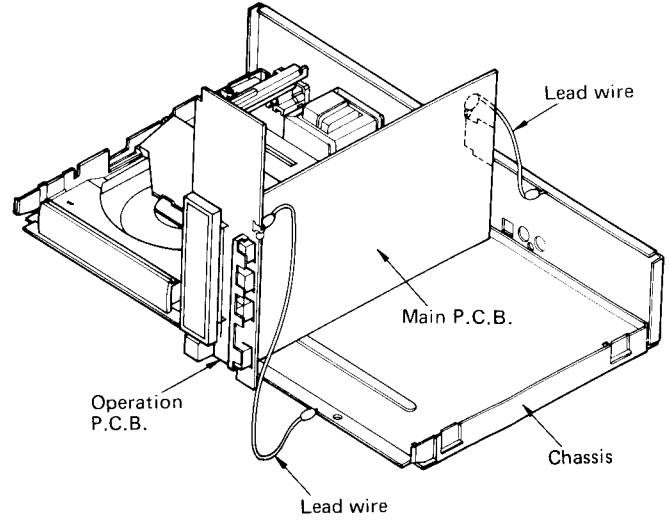
Ref. No. 4
How to remove the main P.C.B.

- Procedure**
1→2→3
→4
1. Remove the 5 screws (❶~❺).
 2. Remove the 3 connectors (CN11, CN12, CN401).
 3. Lift the main P.C.B. off the retention posts on the chassis.
 4. Remove the main P.C.B. in the direction of the arrow.



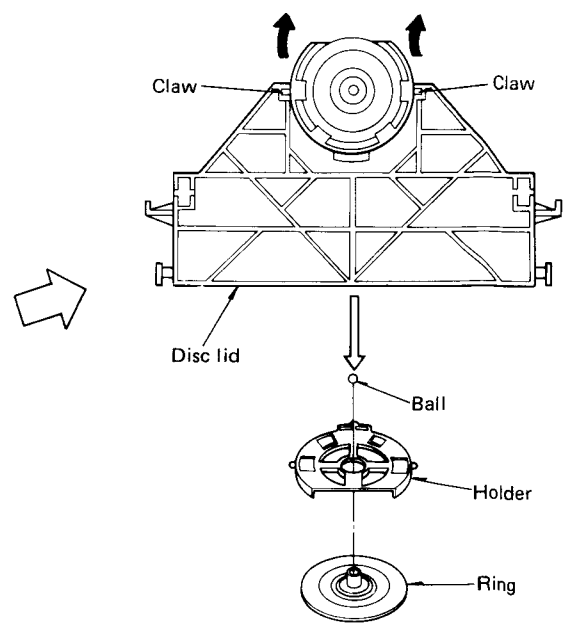
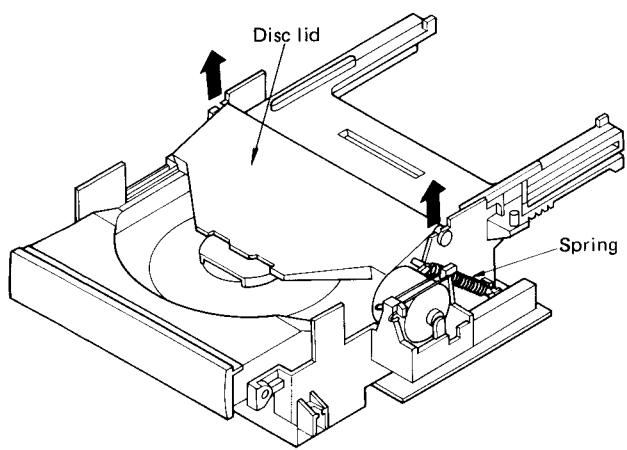
How to check the main P.C.B.

- When checking the soldered surface of the main P.C.B. and replacing the parts, do as shown below.
1. Connect the main P.C.B. ground terminal (LINE OUT terminal) to the chassis with a lead wire.
 2. Connect the operation P.C.B. ground terminal to the chassis with a lead wire.

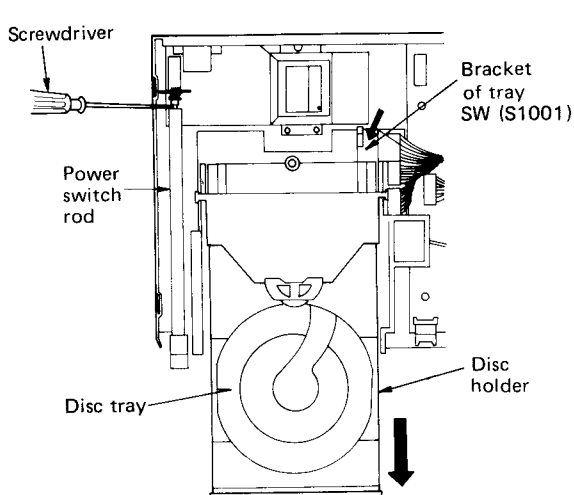
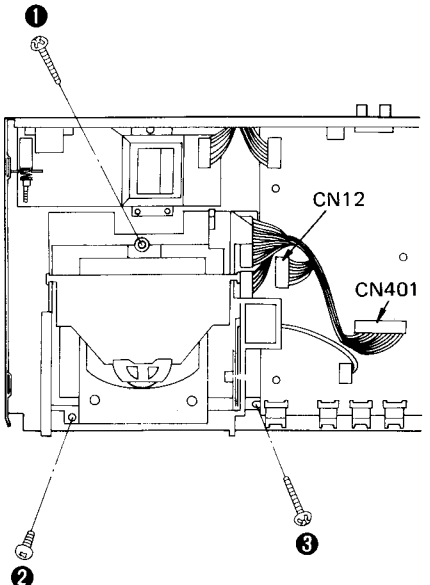


Ref. No. 5
How to remove the disc lid

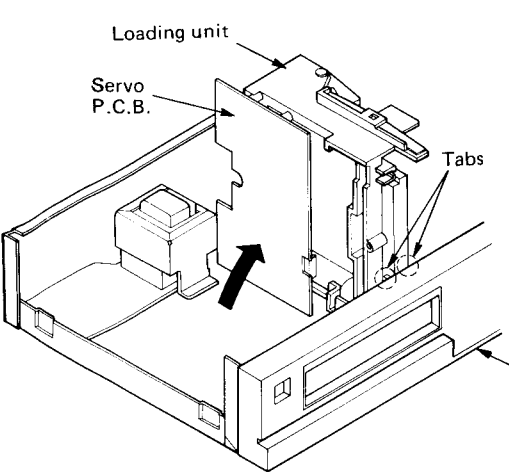
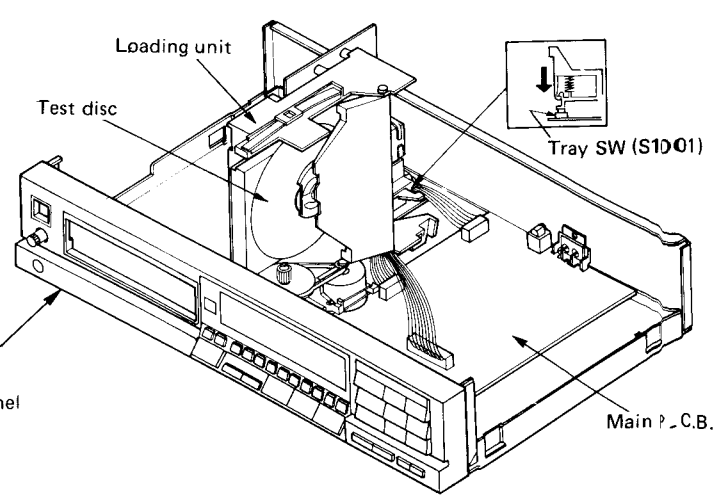
- Procedure**
1→5
1. Remove the spring.
 2. Pull out the disc lid in the direction of the arrow.
 3. Push the 2 claws in the direction of the arrow.



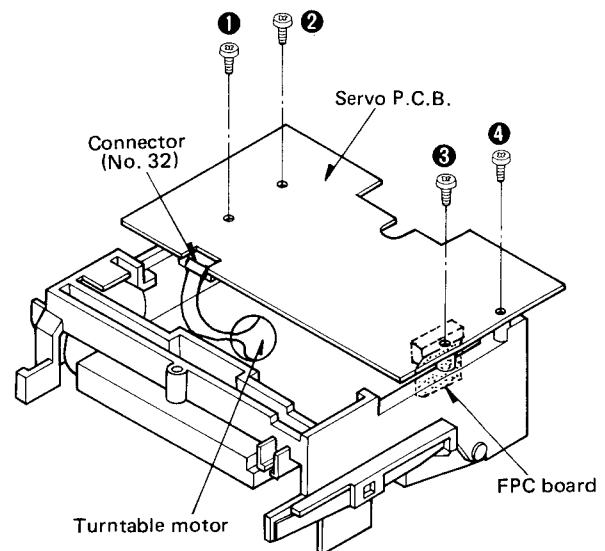
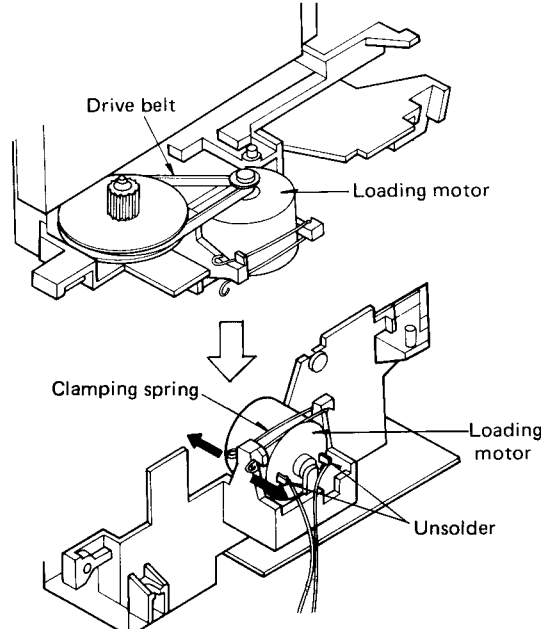
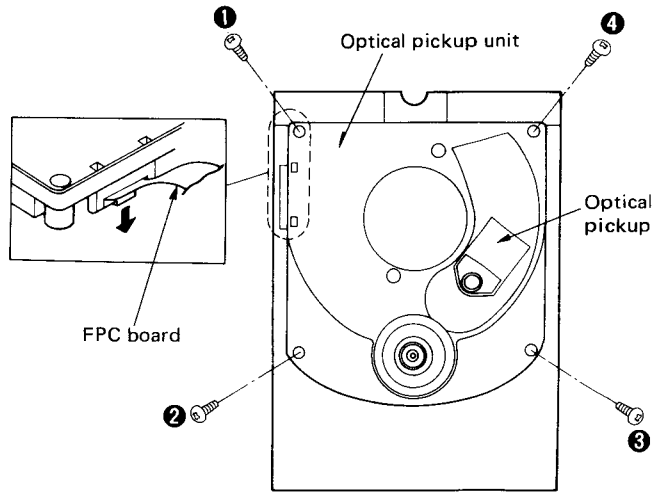
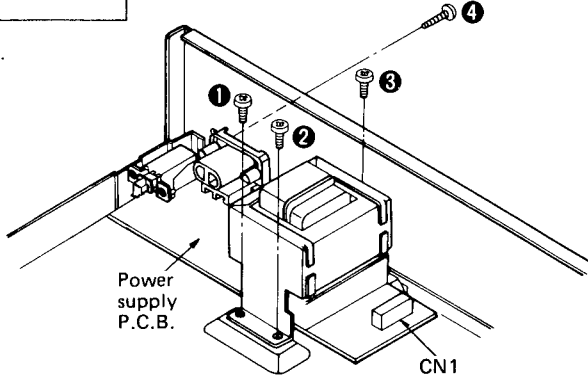
Caution: Be sure to handle the small ball carefully.

<p>Ref. No. 6</p>	<p>How to remove the disc holder and power switch rod</p>	<p>Ref. No. 7</p>	<p>How to remove the loading unit</p>
<p>Procedure 1→2→5→6</p>	<p>A. Disc holder</p> <ol style="list-style-type: none"> 1. Pull the disc holder slowly in the direction of the arrow until the disc tray comes up. 2. Pull the disc holder until it stops. 3. Push the bracket of tray SW (S1001) in the direction of the arrow. 4. Pull out the disc holder further to remove it. <p>B. Power switch</p> <ol style="list-style-type: none"> 1. Set the power switch in the "OFF" position. 2. Remove the power switch rod by using a screwdriver. 	<p>Procedure 1→2→5→6→7</p>	<ol style="list-style-type: none"> 1. Remove the 3 screws (①~③). 2. Remove the 2 connectors (CN12, CN401). 

<p>Ref. No. 8</p>	<p>How to check the servo P.C.B.</p>
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<p>Procedure 1→7→8</p> <ul style="list-style-type: none"> • When checking the soldered surfaces of the servo P.C.B. and replacing the parts, do as shown below. <p>Note: Insert the loading unit into the tabs of the front panel. (Fixed loading unit)</p> 	<p>(To play a disc)</p> <ol style="list-style-type: none"> 1. Place the test disc. 2. Turn "ON" the power switch of the player. 3. Push the bracket of tray SW (S1001) in the direction of the arrow and release it. <p>Note: If the test disc fails to rotate, press the tray switch again.</p> 
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<p>Ref. No. 9</p>	<p>How to remove the servo P.C.B.</p>		
<p>Procedure 1→2→5 →6→7→9</p>	<ol style="list-style-type: none"> 1. Remove the 4 screws (①~④). 2. Remove the FPC board from the optical pickup. 3. Remove the connector (Ref. No. 32) of the turntable motor. <p>Caution: To prevent the breakdown of the laser diode, antistatic shorting pin is inserted into the FPC board.</p>	<p>Ref. No. 10 How to remove the loading motor Ref. No. 11 How to remove the optical pickup unit</p>	
<p>Procedure 1→6→10</p>	<ol style="list-style-type: none"> 1. Remove the drive belt. 2. Release the clamping spring. 3. Unsolder the 2 terminals of the lead wire of the loading motor. 	<p>Procedure 1→2→5→6 →7→9→11</p>	<ol style="list-style-type: none"> 1. Remove the 4 screws (①~④). 2. Remove the FPC board from the optical pickup. 
<p>Ref. No. 12</p>	<p>How to remove the power supply P.C.B.</p>		
<p>Procedure 1→6→12</p>	<ol style="list-style-type: none"> 1. Remove the 4 screws (①~④). 2. Remove the connector (CN1). 		

MEASUREMENTS AND ADJUSTMENTS

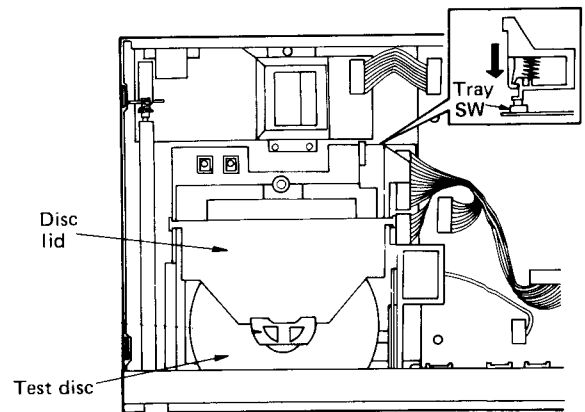
Caution:

It is very dangerous to look at or touch the laser beam. (Laser radiation is invisible.)
With the unit turned "on", laser radiation is emitted from the pickup lens.
Avoid exposure to the laser beam, especially when performing adjustments.

PREPARATION

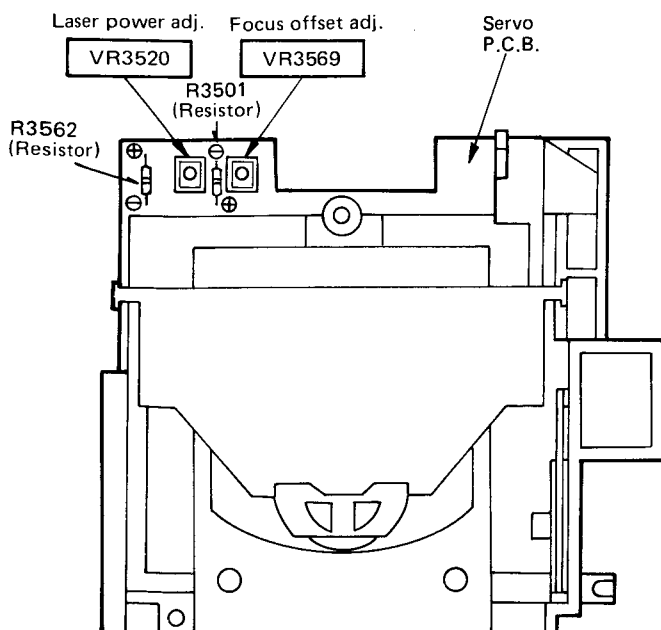
1. Remove the cabinet (see Ref No. 1 of the disassembly instructions.)
2. Remove the disc holder (see Ref No. 6 of the same).
3. Place the test disc on the turntable.
4. Turn "ON" the power switch at the player.
5. Push the bracket of tray SW (S1001) in the direction of the arrow and release it.

Note: If the test disc fails to rotate, press the tray switch again.

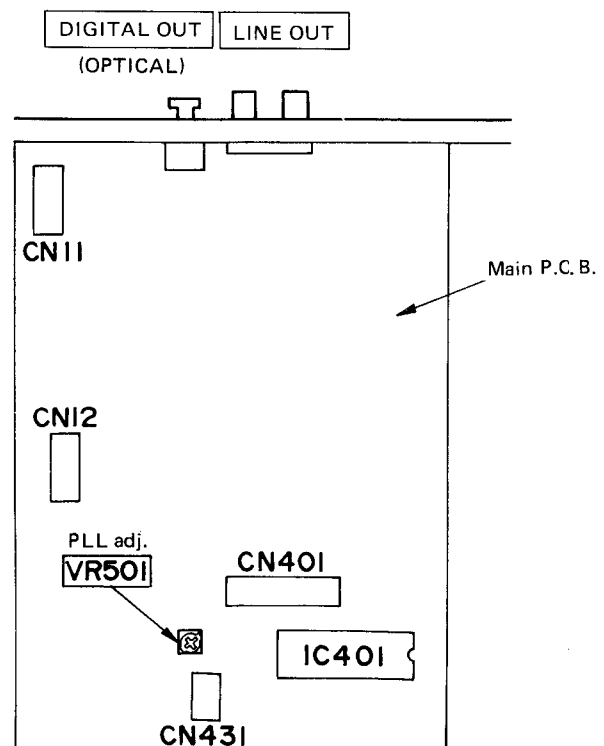


ADJUSTMENT POINTS

• Servo P.C.B.



• Main P.C.B.



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Measuring Instruments

- * Playability test disc (SZZP1054C).
- * Normal disc.

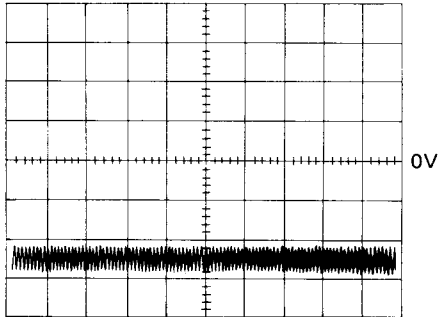
- * Dual-beam oscilloscope with bandwidth of 30MHz or better (with EXT trigger and 1:1 probe).

(1) LASER POWER ADJUSTMENT

1. Connect the oscilloscope's CH1 probe across (+) and (-) of **R3501** (Resistor) on the servo P.C.B.
2. Switch the player power ON, and play track No. 1 on the test disc (SZZP1054C).
3. Adjust **VR3520** so that the voltage is $-50 \pm 2\text{mV}$.

Oscilloscope setting:

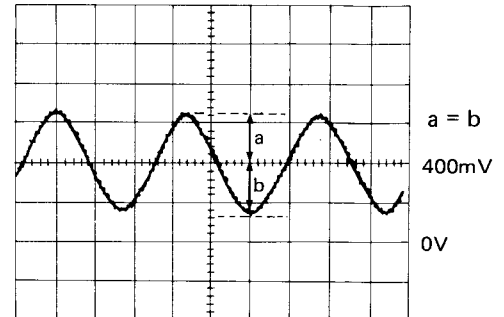
[VOLT 20mV INPUT DC]
 [SWEEP 0.2msec.]

**(2) FOCUS OFFSET ADJUSTMENT**

1. Connect the oscilloscope's CH1 probe across (+) and (-) of **R3562** (Resistor) on the servo P.C.B.
2. Switch the player power ON, and play track No.1 on the test disc (SZZP1054C).
3. Adjust **VR3569** until the signal amplitude become in the center of **400mV**.

Oscilloscope setting:

[VOLT 200 mV INPUT DC]
 [SWEEP 50msec.]

**(3) PLL ADJUSTMENT**

1. Connect CH1 of the oscilloscope to the **LINE OUT terminal** (either of Lch or Rch) and **ground**.

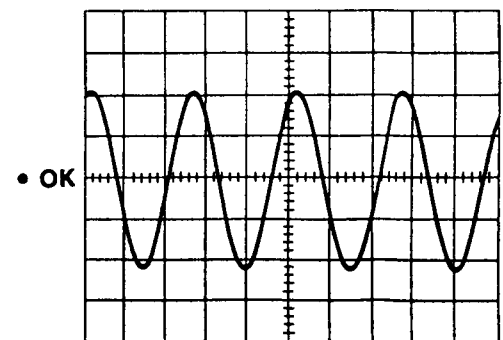
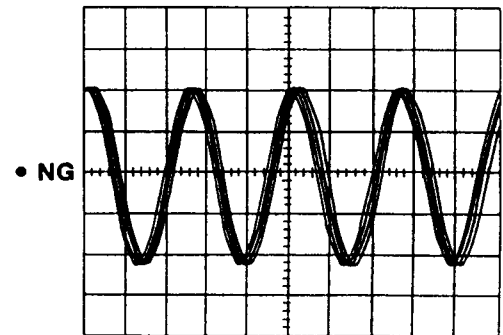
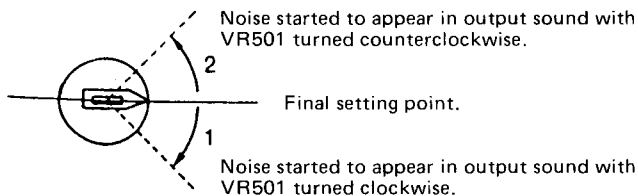
Oscilloscope setting: VOLT 1V
 SWEEP 1msec.
 INPUT DC

2. Switch the player power ON, and play track No. 6 (wedge 0.7mm) on the test disc (SZZP1054C).
3. Check the waveform displayed on the oscilloscope and adjust **VR501** in the following steps.

Step 1. Turn **VR501** clockwise slowly and observe the point at which the waveform on the oscilloscope begins to be disturbed.

Step 2. Turn **VR501** counterclockwise slowly and observe the point at which the waveform on the oscilloscope begins to be disturbed.

Step 3. Set **VR501** in the middle between the points observed in the above steps "1" and "2".

**(4) CHECK OF PLAY OPERATION AFTER ADJUSTMENT***** Checking Skip Search**

1. Play an ordinary musical program disc.
2. Press the skip button to check for normal skip search operation (in both the forward and reverse directions).

*** Checking Manual Search**

1. Play an ordinary musical program disc.
2. Press the manual search button to check for smooth manual search operations at either low or high speed (in both the forward and reverse directions).

*** Playability check by test disc**

1. Play the 0.7 mm black dot and the 0.7 mm wedge on the defect test disc (SZZP1054C) and verify that no sound skip or noise occurs.

■ TERMINAL FUNCTIONS OF IC's

● IC6501 (TDA8808T): Photo diode signal processor

Pin No.	Mark	I/O Division	Function
1	GCHF	I	Gain control input of HF amplifier. Current output from HF amplitude detector
2	Vp	I	Positive supply voltage
3	HFout	O	HF amplifier and equalizer voltage output
4	DET	I	HF detector voltage input
5	Sc	I	Starting up capacitor input
6	Si/RD	I/O	On/off control (start input); ready signal output (starting up procedure successful)
7	Beq	I	Equalizer reference current input
8	Bgc	I	DC and LF gain control reference current input
9	FOC START	I	Focus normalizing circuit starting current
10	PLLH	O	PLL on hold output
11	TL	O	Track loss output
12	DODS	I	Drop out detector suppression input
13	Vext	I	Negative supply connection for FE and FEIag output stage; also substrate connection
14	LPF	O	Low pass filter for Iret, used in track loss (TL) detector and LF gain control

Pin No.	Mark	I/O Division	Function
15	FE	O	Current output of normalized, switched focus error signal
16	FEIag	O	Current output of switched focus error signal, intended for lag network
17	LO	O	Laser amplifier current output
18	LM	I	Laser monitor diode input
19	GCLF	I	Gain control input for AC and LF amplifiers. Current output from LF amplitude detector
20	Re2	O	Summation of amplified currents from D3 and D4
21	Re1	O	Summation of amplified currents from D1 and D2
22, 23	D1, D2	I	Current inputs to DC and LF photo diode amplifier
24, 25	D3, D4	I	Current input to DC and LF photo diode amplifier
26	HFin	I	Current input to HF amplifier
27	GND	I	Ground connection of device
28	DEC	I	Decoupling input (internal bypass)

● IC6503 (TDA8809T): Radial error signal processor

Pin No.	Mark	I/O Division	Function
1	Vp	I	Positive supply voltage
2	Cosc1	I	Frequency setting capacitors for oscillator
3	Cosc2		
4	Rwob	I	Wobble generator input
5	Rosc	I	Biassing resistor for oscillator frequency and internal amplitude
6	DIV4	I	Divide-by-4 input
7	REdig	O	Digital output of sign (Re2 – Re1)
8	B3	I	Input control bits for off-, catch-, plat- status and DAC output current
9	B2		
10	B1		
11	B0		
12	Vext(+)	I	Positive external voltage input
13	Vext(-)	I	Negative external voltage input (also substrate connection)
14	GND	I	GND terminal
15	RADout	O	Current output of amplified (Re2 – Re1) input currents
16	REin	I	Radial error input
17	REIag	O	Voltage output of integrated (Re2 – Re1) input currents

Pin No.	Mark	I/O Division	Function
18	Lag	I	Connection of integrator capacitor for (Re1 – Re2) input currents
19	Lead	O	Lead output
20	Vref	I	Internal reference voltage output
21	AGC	I	Gain control input for radial error signal
22	RDAC	O	Biassing resistor for current output for track jumping (3½ bits)
23	offset in	I	Offset control input for radial offset
24	offset off	O	Offset control output for radial offset
25	CLPF	I	Low-pass filter for Re1 and Re2, used for radial offset control
26	CHPF	I	High-pass filter for Re1 and Re2, used for radial offset control
27	Re1	I	Input for amplified currents from photo-diodes D1 and D2
28	Re2	I	Input for amplified currents from photo diodes D3 and D4

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● IC301 (MN6625): Digital signal processing

Pin No.	Mark	I/O Division	Function	Pin No.	Mark	I/O Division	Function
1	BYTCK	O	Serial data byte clock (Not used, open)	25	MCLK	I	Data clock for MDATA
2	FCLK	O	Crystal frame clock (Not used, open)	26	MDATA	I	Mode control data
3	DEMPH	O	De-emphasis ON signal (de-emphasis ON at "H") (Not used, open)	27	DMUTE	I	Data mute command
4	SRDATA	O	Serial data output (MSB first)	28	TRON	I	Tracking servo ON signal (tracking servo ON at "L")
5	SCK	O	Serial bit clock output	29	STAT	O	Status command for CRC etc. . .
6	LRCK	O	LR discrimination clock (88.2kHz)	30	SUBC	O	Sub-code serial output data (Not used, open)
7	WDCK	O	Serial data output word clock (Not used, open)	31	SBCK	I	Clock for sub-code serial output (Not used, open)
8	LDG	O	L channel deglitch signal (Not used, open)	32	SMCK	O	System clock (4.2336MHz)
9	RDG	O	R channel deglitch signal (Not used, open)	33	VDD	I	Power supply (connected to +5V)
10	IPFLAG	O	Interpolation flag (interpolation at "H") (Not used, open)	34	MEMP	I	Deemphasis command
11	FLAG	O	Error flag terminal (Not used, open)	35	FG	I	Turntable motor FG signal input (Not used, open)
12	XCK	O	Clock (16.9344 MHz) output (Not used, open)	36	PC	O	Turntable motor ON command (ON at "L")
13	TEST	I	Test mode selection (Not used, connected to +5V)	37	EC	O	Turntable motor drive signal
14	TX	O	Digital signal output	38	RESY	O	Resynchronizing signal (Not used, open)
15	SLEEP	I	Mode selector ("L": normal, "H": SLEEP mode) (Not used, connected to GND)	39	DO	I	Drop-out detection signal (Drop-out at "H")
16	CSEL	I	Test terminal ("L": normal) (Not used, connected to GND)	40	SRF	I	Sliced RF signal
17	X1	I	Clock input (16.9344 MHz)	41	EFM	I	Modulation data
18	X2	O	Clock output (16.9344 MHz) (Not used, open)	42	PCK	I	PLL extract clock (4.2336MHz)
19	VSS	I	GND terminal	43	FPC	O	PLL frequency comparison signal
20	BLKCK	O	Sub-code Q data block clock (75 Hz)	44 } 51	D7 } D0	I/O	16K RAM data input/output
21	CLDCK	O	Sub-code frame clock (7.35kHz)	52	RAM OE	O	Read out enable
22	SUBQ	O	Sub-code Q data	53	RAM WE	O	Write enable
23	RST	I	Reset command	54 } 64	RAM A0 } RAM A10	O	16K RAM address signal (RAMA0: LSB, RAMA10: MSB)
24	MLD	I	Load command for mode control data				

● IC401 (MN1554PJE-2): System control

Pin No.	Mark	I/O Division	Function
1	VSS	I	GND terminal
2	DMUTE	O	Data mute command
3	MDATA	O	Mode control data
4	MCLK	O	Data clock for MDATA
5	MLD	O	Load command for mode control data
6	NC	—	Not connected
7	NC	—	Not connected
8	INH	O	Track loss det. signal output
9	MUTE	O	Muting signal output
10	EMPH	O	Deemphasis command
11	NC	—	Not connected
12	SIRQ	I	Track loss det. signal input
13	BLKCK	I	Sub-code Q data block clock (75 Hz)
14	CLDCK	I	Sub-code Q data frame clock (7.35 KHz)
15	NC	—	Not connected
16	SUBQ	I	Sub-code Q data
17	RESET	I	Reset command
18	TL	I	Track loss signal
19	RE DIG	I	Radial error digital
20	HFD	I	HF detector output for PLLH
21	TRAY SW	I	Disc holder open/close det. signal input
22	DIV4	O	Radial error digital divided by four
23	DODS	O	Drop out detector suppression
24	MODEA	—	Not used, open
25	FR/REV	—	Not used, open
26	B0	O	Control bits for radial circuit
27	B1		
28	B2		
29	B3		
30	CLOSE	O	Disc tray "close" detection
31	OPEN	O	Disc tray "open" detection

Pin No.	Mark	I/O Division	Function
32	TR ON	O	Tracking servo ON command
33	VDD	I	Power supply (connected to +5V)
34	NC	—	Not connected
35	NC	—	Not connected
36	P53	—	Not used, open
37	P60	I	Period det. select signal input
38	POL	I	Period det. select signal input
39	SKATE	I	Period det. select signal input
40	STAT	I	Status command for CRC etc. . . .
41	SP0	I	Speed control code input
42	SP1		
43	SP2		
44	SP3		
45	RECV	I	Data receipt command signal
46	SEND	I	Data transmission command signal
47	ACK	I	Data discrimination signal
48	CLK	I	Data clock signal
49	DATA0	I	Key scan signal
50	DATA1		
51	DATA2		
52	DATA3		
53	CMD0	O	Command signal of access mode
54	CMD1		
55	CMD2		
56	CMD3		
57	NC	—	Not connected
58	NC	—	Not connected
59	NC	—	Not connected
60	SI/RD	I/O	On/off control for laser supply and focus circuit
61	OSC2	I	Clock input
62	OSC1	I	Clock input (4.2336 MHz)
63	XI	I	Digital input of signal
64	X0	—	Not used, open

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● IC403 (MN1551 PJF-1):

Pin No.	Mark	I/O Division	Function
1	VDD	I	Power supply (connected to +5V)
2	NC	—	Not connected
3	NC	—	Not connected
4	TL	I	Track loss signal
5	REDIG	I	Radial error digital
6	DIV4	I	Radial error digital divided by four
7	SP3	O	Speed control code output
8	SP2		
9	SP1		
10	SP0		
11	SKATE	O	Period det. select signal output
12	POLAR	O	Period det. select signal output
13	DIVC	O	Period det. select signal output
14	SIRQ	O	Track loss det. signal output

Pin No.	Mark	I/O Division	Function
15	CMD0	I	Command signal of access mode
16	CMD1		
17	CMD2		
18	CMD3		
19	NC	—	Not connected
20	NC	—	Not connected
21	NC	—	Not connected
22	NC	—	Not connected
23	NC	—	Not connected
24	SYNC	—	Not connected
25	RESET	I	Reset command
26	OSC2	I	192 fs (8.4672 MHz) clock input
27	OSC1		
28	VSS	I	GND terminal

● IC601 (MN15283PEY-1): System control and FL drive

Pin No.	Mark	I/O Division	Function
1	VSS	I	GND terminal
2	X0	O	Not used, open
3	X1	I	Optical servo condition input (Not used, open)
4	O0 (RECV)	O	Data receipt command signal
5	O1 (SEND)	O	Data transmission command signal
6	O2 (ACK)	O	Data discrimination signal
7	O3 (CLK)	O	Data lock signal
8 } 11 }	10 (D0) } } 13 (D3)	O	Key scan signal
12	SYNC	O	Not used, open
13	RST	I	Reset command (reset at "L")
14	IRQ/TC1	I	Remote control signal input
15 } 18 }	50 } } 53	I	Key return signal
19	SBT	I	Sub-code frame clock (7.35 kHz) (Not used, open)
20	SBD	I	Sub-code Q data input (Not used, open)
21 } 24 }	20 } } 23	O	Key scan signal
25	30	O	Key scan signal
26	31		

Pin No.	Mark	I/O Division	Function
27	32	—	Not used, open
28	33	O	Key scan signal
29	40	I	Key return signal
30	41		
31	42	—	Not used, open
32	43	—	Not used, open
33	P60	—	Not used, open
34	P61	—	Not used, open
35	DAC	—	Not used, open
36	VPP	I	FL drive power supply (connected to -28.3V)
37 } 52 }	D0 } } • DA } } DF	O	FL grid signal
53 } 61 }	S8 } } S0	O	FL anode signal
62	VDD	I	Power supply (connected to +5V)
63	OSC2	I	Clock terminal
64	OSC1	I	Clock input

● IC501 (AN8371S): Data slice and PLL

Pin No.	Mark	I/O Division	Function
1	VEE	I	Power supply (connected to -5V)
2	SRF	O	Sliced RF signal
3	EFM	O	Modulation data
4	D.GND	I	GND terminal (digital system)
5	PCK	O	PLL extract clock (4.2336MHz)
6	VCC	I	Power supply (connected to +5V)
7	VA	I	VCO free run frequency adjusting current input (Not used, open)
8, 9	VC1, 2	I	Capacitor connection for VCO oscillator frequency
10	VR	I	Resistor connection for VCO oscillator frequency
11	PD	I	Capacitor connection for PLL DO protection
12	PL1	I	PLL loop filter connection

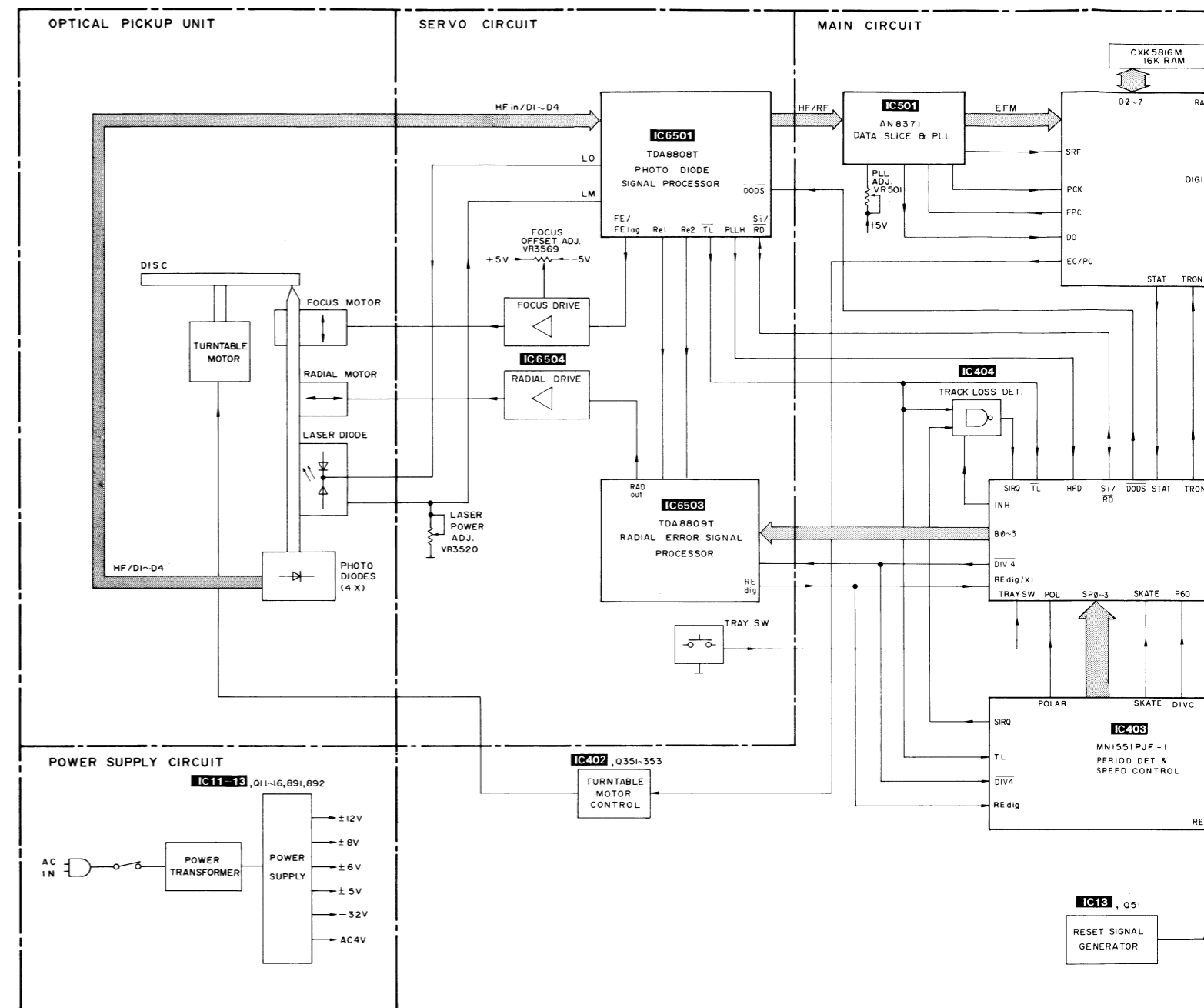
Pin No.	Mark	I/O Division	Function
13	PL2	I	PLL loop filter connection
14	FPC	I	PLL frequency comparison signal
15	RF	I	Data
16	ARF	O	RF signal output with AGC output
17	AGC	I	ARF signal input for AGF drop-out detection input
18	AC	I	Loop filter for AGC connected
19	DO	O	Drop-out detection signal
20	A.GND	I	GND terminal (analog system)
21	DSL	I	RF signal input for data slicing
22	SLC	I	Slicing level control signal input (Not used, connected to GND)
23	FC1	I	Filter capacitor for data slicer connected
24	FC2	I	Filter capacitor for data slicer connected

● IC801 (MN6471): Digital filter and D/A converter

Pin No.	Mark	I/O Division	Function
1	MLD	I	Command load input (load: L)
2	RSTB	I	Reset command
3	IE	I	Not used, connected to GND
4	TP1	—	TEST terminal
5	TP2	—	TEST terminal
6	TEST1	I	TEST terminal 1 (connected to GND)
7	TEST2	I	TEST terminal 2 (connected to GND)
8	NC	—	Not connected
9	NC	—	Not connected
10	AVDD4	I	Power supply (connected to +4.7V)
11	OUTL (-)	O	Lch data output, (-) terminal
12	AVSS4	I	GND terminal
13	AVSS3	I	GND terminal
14	OUTL (+)	O	Lch data output, (+) terminal
15	AVDD3	I	Power supply (connected to +4.7V)
16	NC	—	Not connected
17	AVDD2	I	Power supply (connected to +4.7V)
18	OUTR (+)	O	Rch data output, (+) terminal
19	AVSS2	I	GND terminal (analog system)
20	AVSS1	I	GND terminal (analog system)
21	OUTR (-)	O	Rch data output, (-) terminal

Pin No.	Mark	I/O Division	Function
22	AVDD1	I	Power supply (connected to +4.7V)
23	DVDD1	I	Power supply (connected to +4.3V)
24	DVSS1	I	GND terminal (digital system)
25	X2	O	Clock output
26	X1	I	Clock input
27	NC	—	Connected to GND
28	DVDD2	I	Power supply (connected to +4.3V)
29	DVSS2	I	GND terminal (digital system)
30	NSUB	I	Sub-strate terminal (Not used, connected to +4.3V)
31	ZFLGB	O	Zero input detector terminal (Not used, open)
32	192fs	O	192 fs (8.4672MHz)
33	LRPOL	I	LR clock selector (Not used, connected to +4.3V)
34	LRCLK	I	LR discrimination signal input
35	BCLK	I	Serial bit clock input
36	SRDATA	I	Serial data input (MSB first)
37	DVSS 3	I	GND terminal (digital system)
38	DVDD	I	Power supply (connected to +4.3V)
39	384 fs	O	384 fs (16.9344MHz) output
40	PD	I	Power down terminal (Not used, connected to GND)
41	MDATA	I	Mode control data
42	MCLK	I	Data clock for MDATA

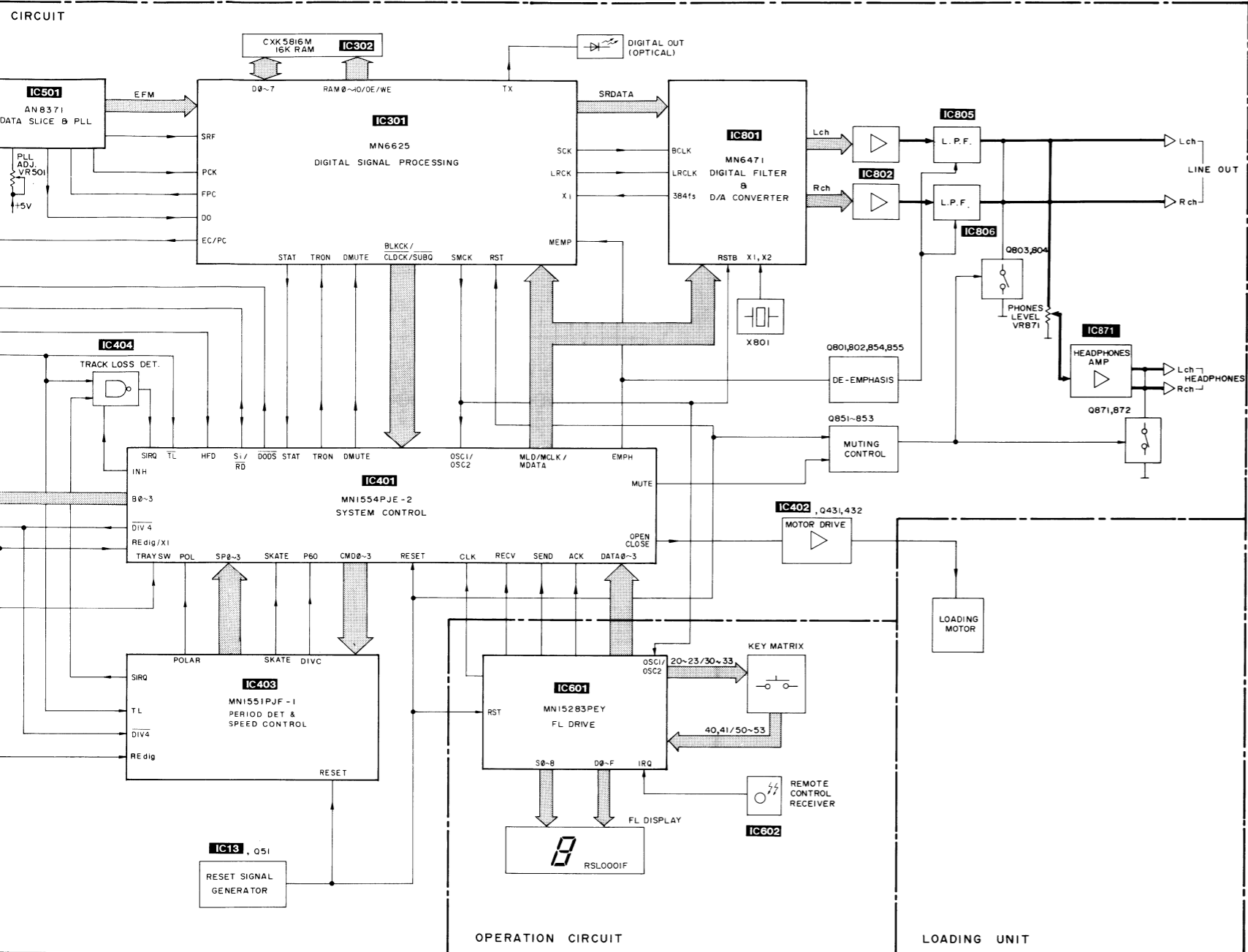
■ BLOCK DIAGRAM



B0-B3 : Control bits for radial circuit.
 DAC : Current output for track jumping. (Digital to Analogue Converted)
 D0D5 : Drop out detector suppression.
 D1-D4 : Photodiode currents.
 FE : Focus error signal.
 FE lag : Laser error signal for LAG network.
 HF : HF output for DEMOD.
 HFD : HF detector output for DEMOD.
 HF-in : HF current input.
 LM : Laser monitor diode input.
 LO : Laser amplifier current output.
 RE : Radial error signal (amplified RE2 - RE1 currents).
 RE1 : Radial error signal 1 (summation of amplified currents D3 and D4).
 RE2 : Radial error signal 2 (summation of amplified currents D1 and D2).

RE dig : Radial error digital.
 RE lag : Radial error signal for LAG network
 RPu : Radial puls after track jumping.
 Si/RD : On/off control for laser supply and focus circuit.
 TL : Track loss signal.
 Div4 : Radial error digital divided by four.
 RF : Data
 DO : Drop-out detection signal (Active High)
 SRF : Sliced RF signal.
 EFM : Modulation data.
 PCK : PLL extract clock (4.2336MHz)
 FPC : PLL frequency comparison signal
 STAT : Status command for CRC etc.
 DMUTE : Data mute command
 MDATA : Mode control data
 MLD : Load command for mode control data (Active Low)

MCLK
 SUBQ
 CLDCK
 BLKCK
 RST
 TRON
 EC
 PC
 SMCK
 OE
 WE
 LRCK
 SRDATA
 SCK
 MEMP

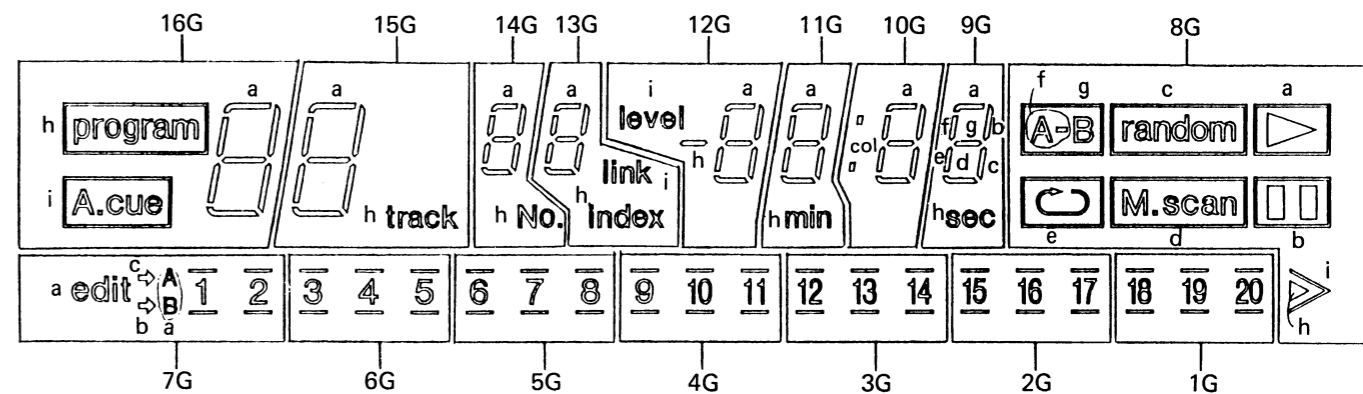


- MCLK : Data clock for MDATA
- SUBQ : Sub-code Q data
- CLDCK : Data frame clock (7.35KHz)
- BLKCK : Sub-code Q data block clock (75 Hz)
- RST : Reset command (Active Low)
- TRON : Tracking servo ON command (Active Low)
- EC : Spindle motor drive signal
- PC : Spindle motor ON command (Active Low)
- SMCK : System clock (4.2336MHz)
- OE : Read out enable
- WE : Write enable
- LRCK : L/R data discrimination clock (88.2KHz)
- SRDATA : Serial data output (MSB first)
- SCK : Serial bit clock (2.82MHz)
- MEMP : Deemphasis command (Active High)

Note)
• → Audio signal.

INTERNAL CONNECTION OF FL

Grid connection diagram



Pin connection table

42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
F	F	N	a	b	c	d	N	N	N	e	f	g	h	i	16	15	14	13	N	N	N	N	12	11	10	9	8	7	6	5	4	N	N	N	N	3	2	1	N	F	F
2	2	P	a	b	c	d	P	P	P	e	f	g	h	i	G	G	G	G	P	P	P	P	G	G	G	G	G	G	G	G	G	P	P	P	P	G	G	G	P	1	1

Anode connection table

	16G	15G	14G	13G	12G	11G	10G	9G	8G	7G	6G	5G	4G	3G	2G	1G	
a	a	a	a	a	a	a	a	a	▶	edit	A	3	6	9	12	15	18
b	b	b	b	b	b	b	b	b	▬▬▬	➡	(down)	3	6	9	12	15	18
c	c	c	c	c	c	c	c	c	random	➡	(up)	3	6	9	12	15	18
d	d	d	d	d	d	d	d	d	M.scan	1	4	7	10	13	16	19	
e	e	e	e	e	e	e	e	e	↻	(down) 1	4	7	10	13	16	19	
f	f	f	f	f	f	f	f	f	Λ-	(up) 1	4	7	10	13	16	19	
g	g	g	g	g	g	g	g	g	B	2	5	8	11	14	17	20	
h	program	track	No.	index	—	min	col	sec	▶	(down) 2	5	8	11	14	17	20	
i	A.cue	-	-	link	level	-	-	-	>	(up) 2	5	8	11	14	17	20	

SCHEMATIC DIAGRAM

(Parts list on pages 41 ~ 46.)

(This schematic diagram may be modified at any time with development of new technology.)

- Notes:**
- S1 : Power switch in "on" position.
 - S601 : Disc holder open/close (▲ open/close) switch.
 - S602, 603 : Skip (◀◀ skip/skip ▶▶) switches.
S602: ◀◀ (R), S603: ▶▶ (F)
 - S604 : Stop (■ stop) switch.
 - S605 : Pause (■ pause) switch.
 - S606 : Play (▶ play) switch.
 - S607 ~ 618 : Numeric (+10, 0, 1 ~ 10) switches.
[S607: +10, S608: 0, S609: 1, S610: 2,
S611: 3, S612: 4, S613: 5, S614: 6,
S615: 7, S616: 8, S617: 9, S618: 10]
 - S619 : Edit tape length (tape length) switch.
 - S620 : Tape-side (side A/B) switch.
 - S621 : Disc link (disc link) switch.
 - S622 : Time mode select (time mode) switch.
 - S623 : Programmed-play (program) switch.
 - S624 : Clear (clear) switch.
 - S625 : Recall (recall) switch.
 - S626 : Repeat (repeat) switch.
 - S627 : Auto cue (auto cue) switch.
 - S628 : Random play (random) switch.
 - S629 : Music scan (music scan) switch.
 - S630, 631 : Search (◀◀ search ▶▶) switches.
S630:◀◀(R), S631:▶▶(F)
 - S1001 : Tray (open/close) switch.

The voltage value and waveforms are the reference voltage of this unit measured by DC electronic voltmeter (high impedance) and oscilloscope on the basis of chassis. Accordingly, there may arise some error in voltage values and waveforms depending upon the internal impedance of the tester or the measuring unit.

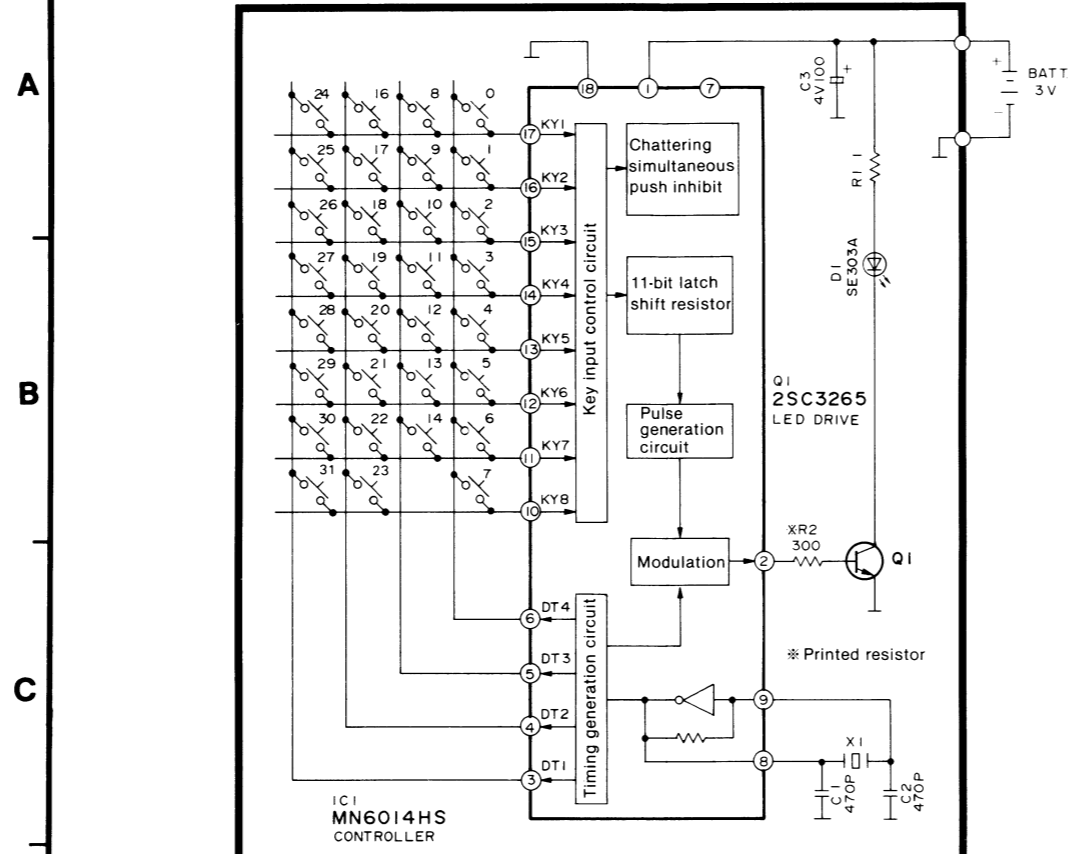
*The parenthesized are the values of voltage generated during playing (Test disc 1kHz, L+R, 0dB), others are voltage values in stop mode.

Important safety notice:
Components identified by ▲ mark have special characteristics important for safety. When replacing any of these components, use only manufacturer's specified parts.

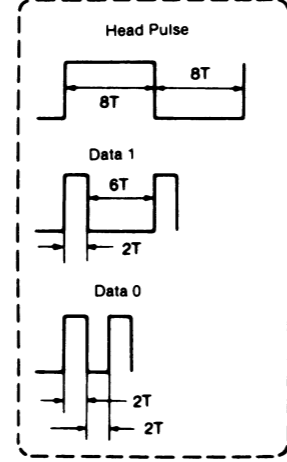
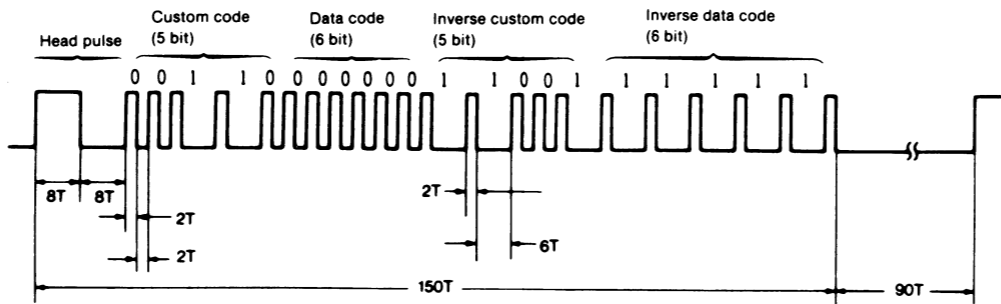
◀ / --> / >--> : Positive voltage lines and negative voltage lines.
⚡ : Audio signal lines.

Caution!
IC and LSI are sensitive to static electricity. Secondary trouble can be prevented by taking care during repair.
• Cover the parts boxes made of plastics with aluminum foil.
• Ground the soldering iron.
• Put a conductive mat on the work table.
• Do not touch the pins of IC or LSI with fingers directly.

Remote control transmitter



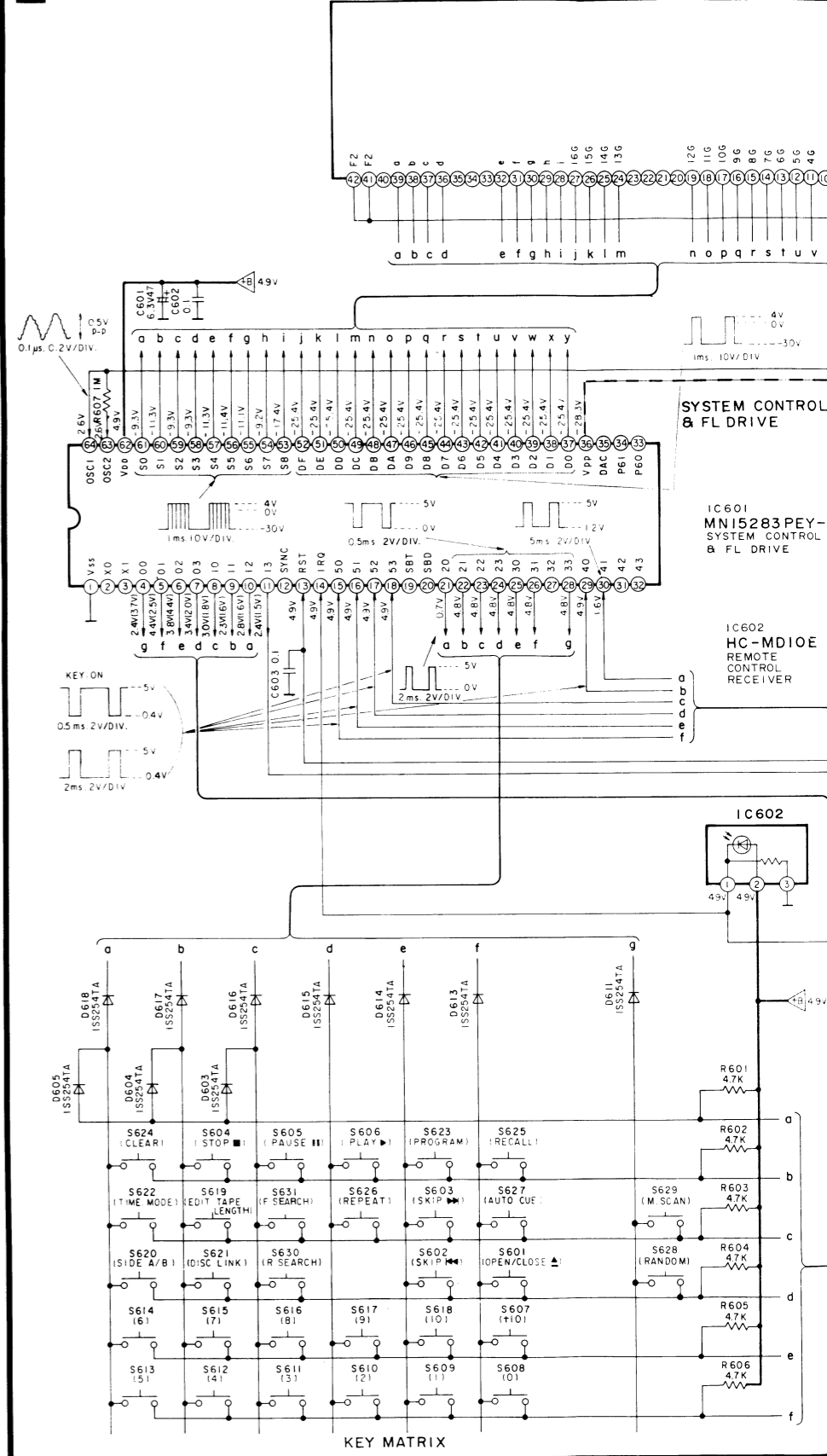
Key number description and data code

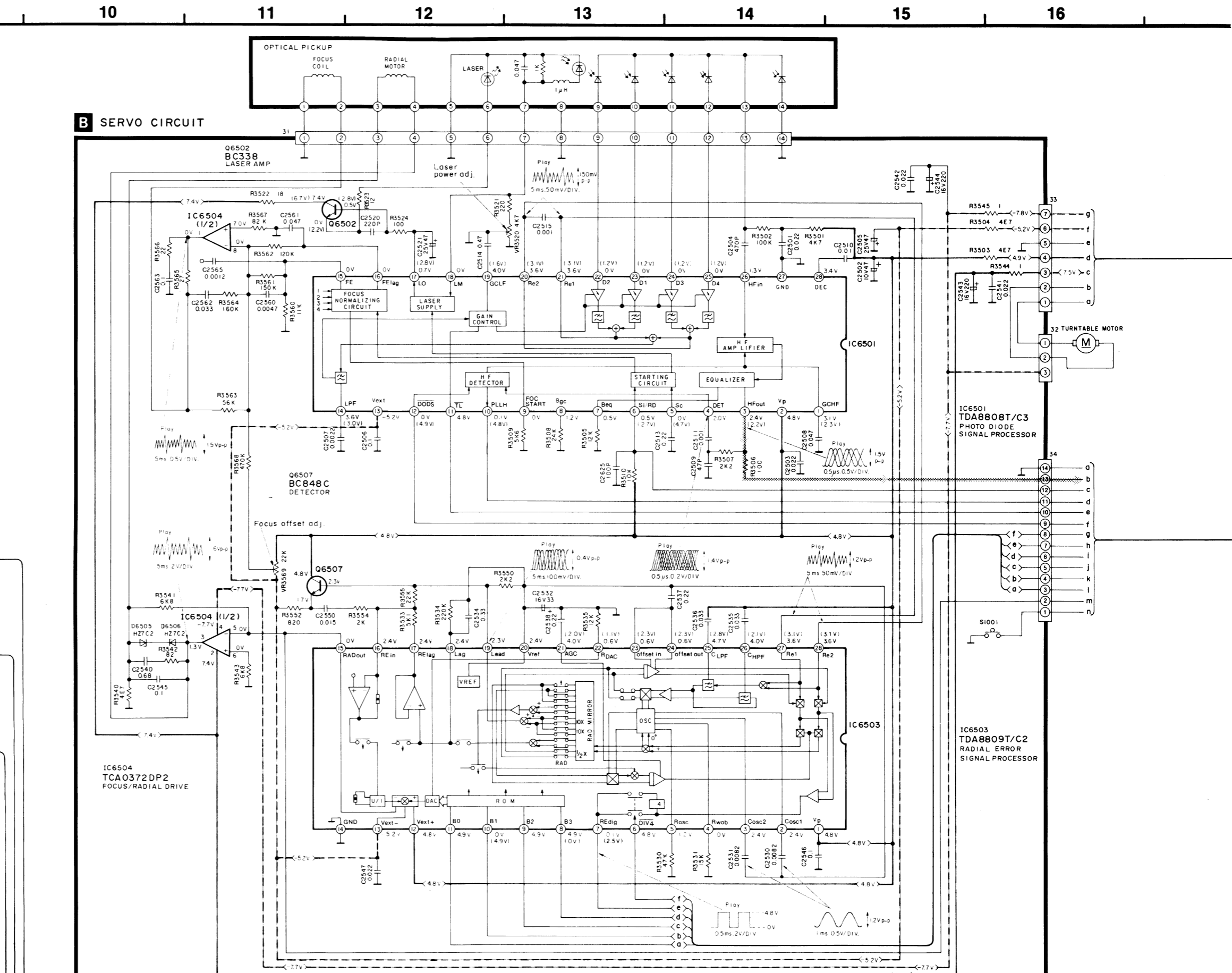
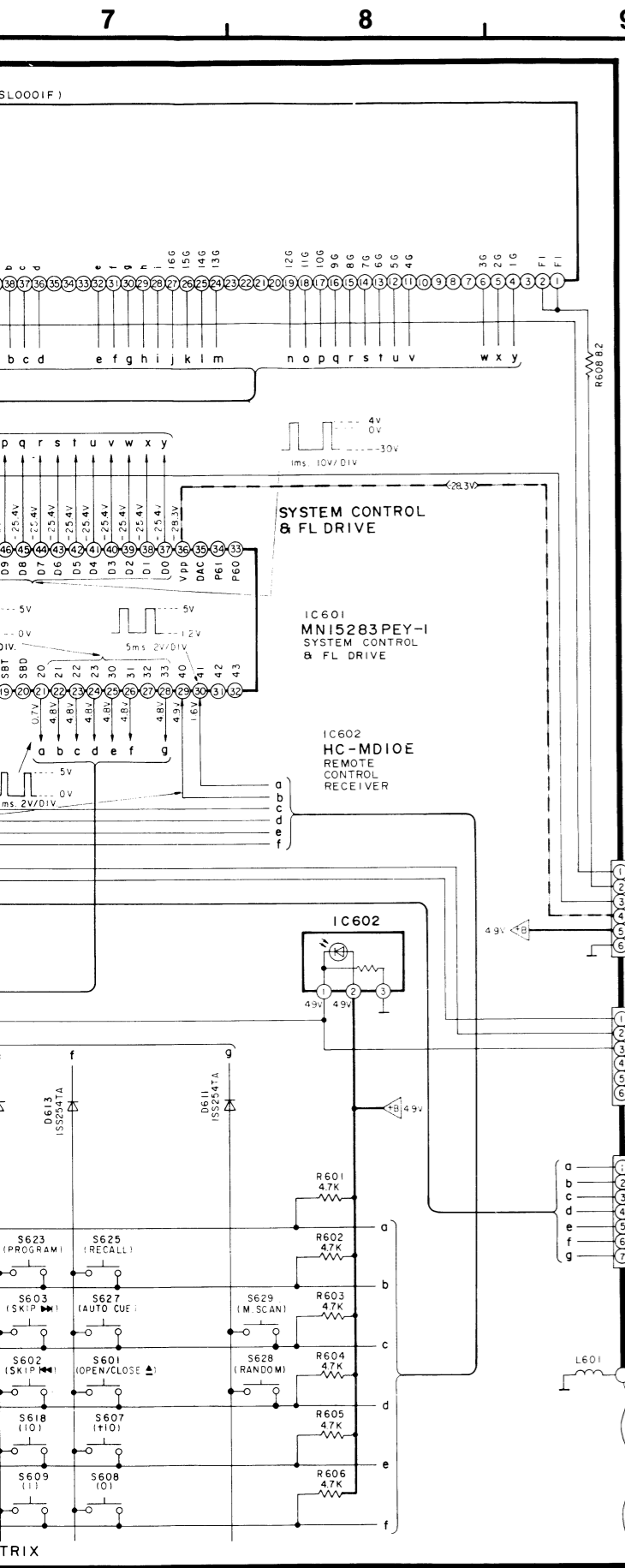


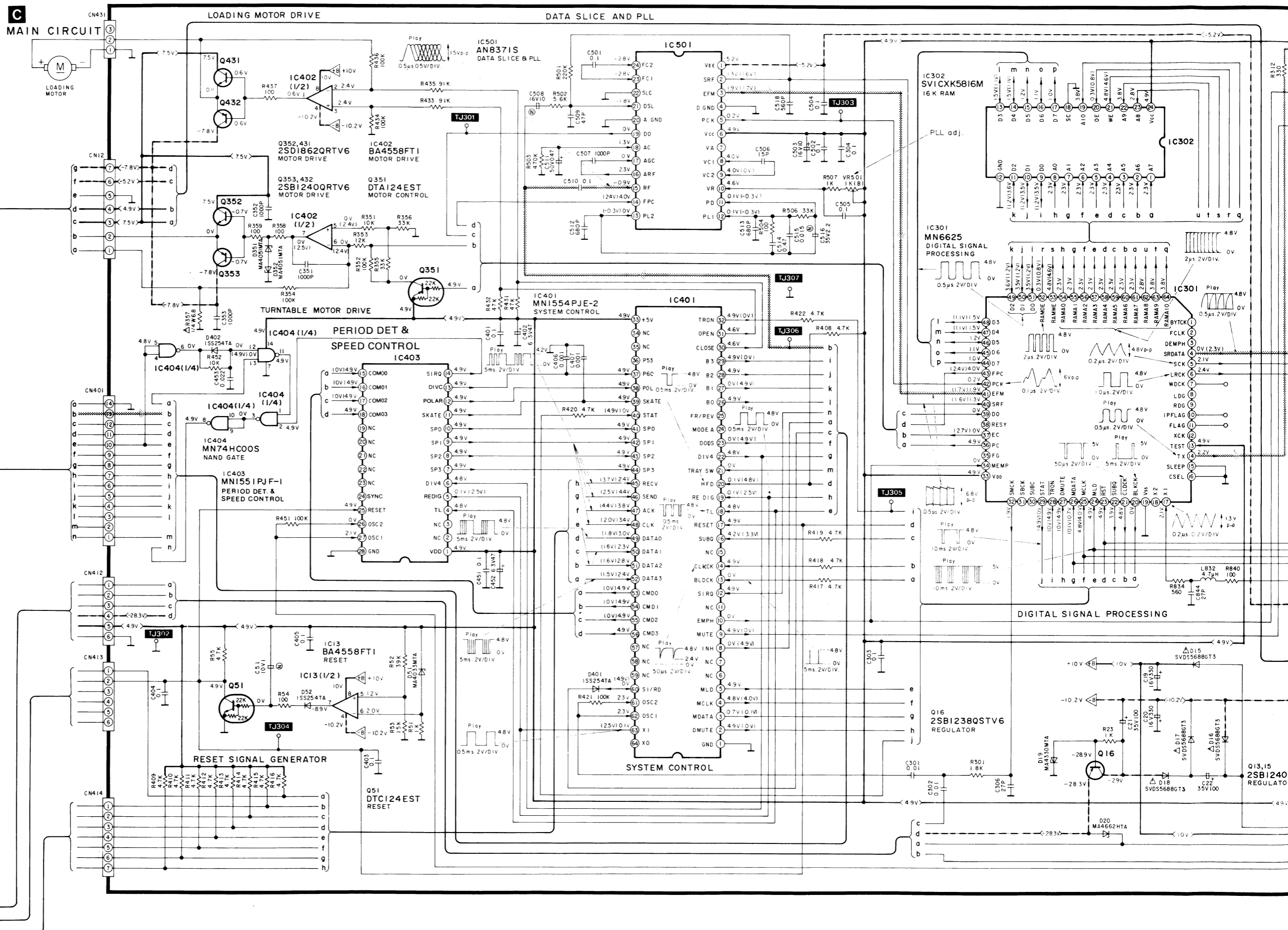
Key No.	Function	Data code	Key No.	Function	Data code
0	▶ play	001010	17	search ▶▶	000101
1	skip ▶▶	000011	18	2	010001
2	4	010011	19	6	010101
3	8	010111	20	10	110000
4	0	011001	21	clear	001011
5	time mode	101001	22	repeat	000111
6	music scan	001111	23	level ▲	100100
7	auto cue	100011	24	▲ open/close	001110
8	■ pause	000110	25	◀◀ search	000100
9	◀◀ skip	000010	26	1	010000
10	3	010010	27	5	010100
11	7	010110	28	9	011000
12	+10	011010	29	program	011101
13	recall	001001	30	A-B repeat	001000
14	random	011111	31	▼ level	100101
16	■ stop	000000			

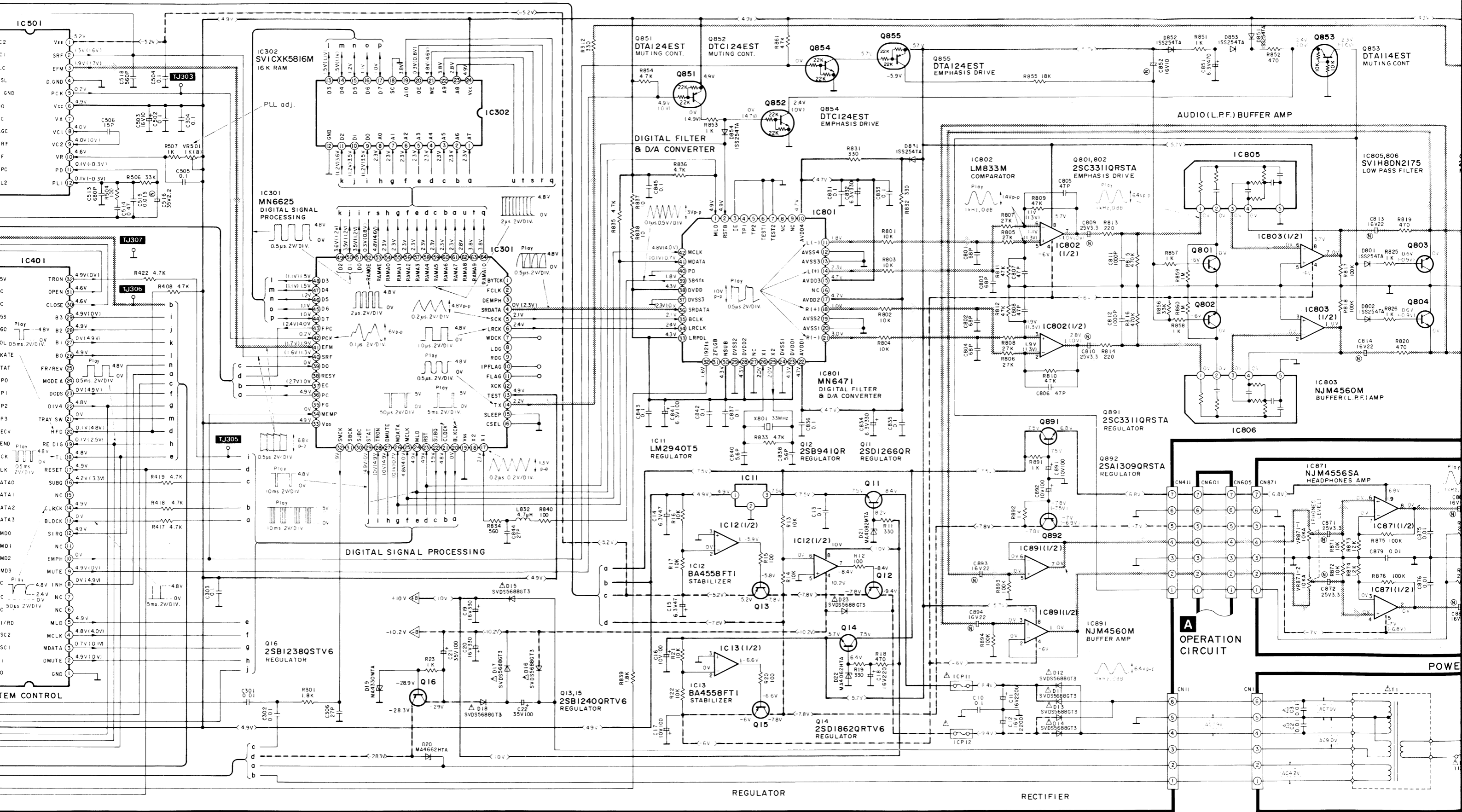
*Custom code: 01100

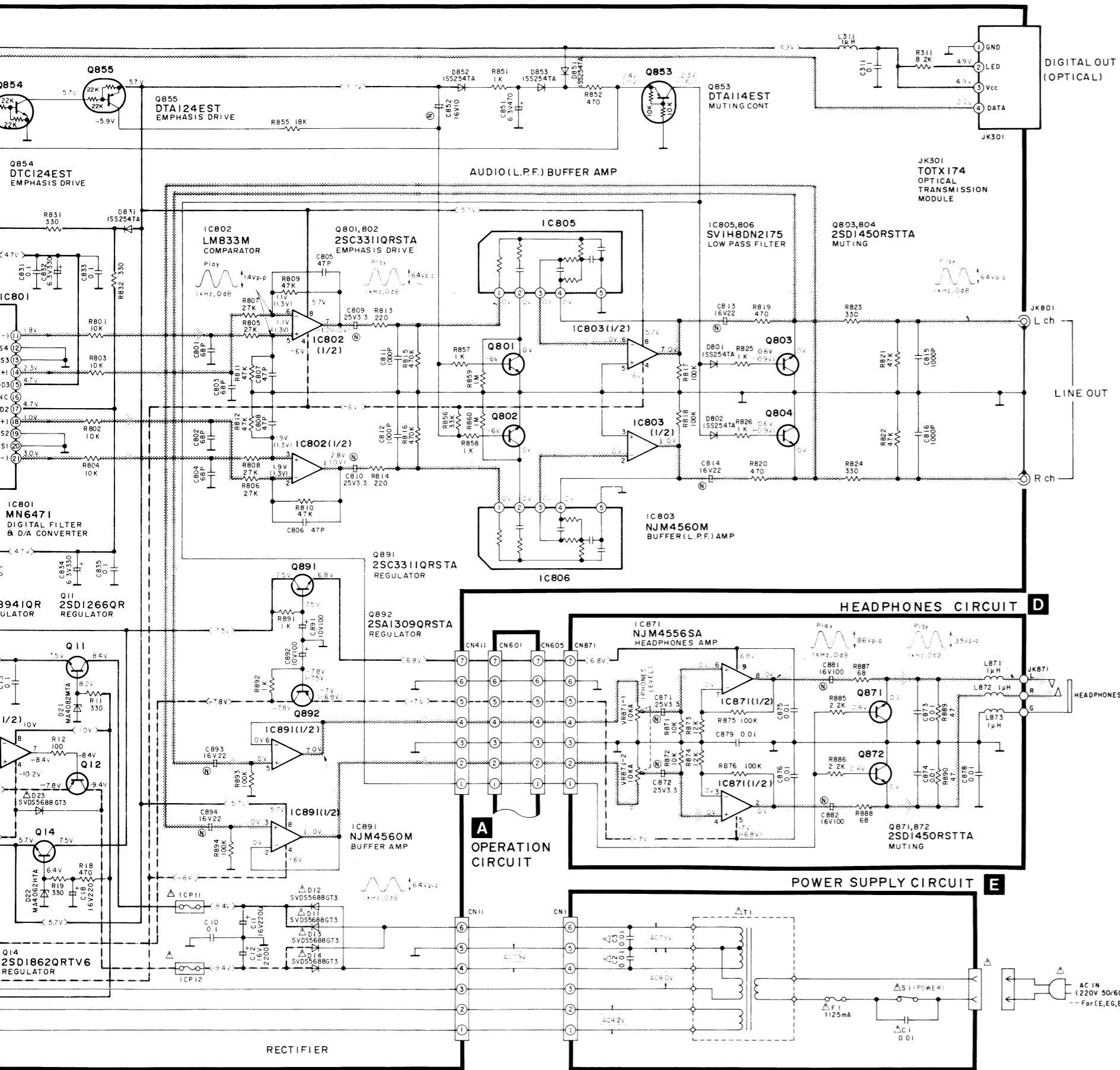
OPERATION CIRCUIT





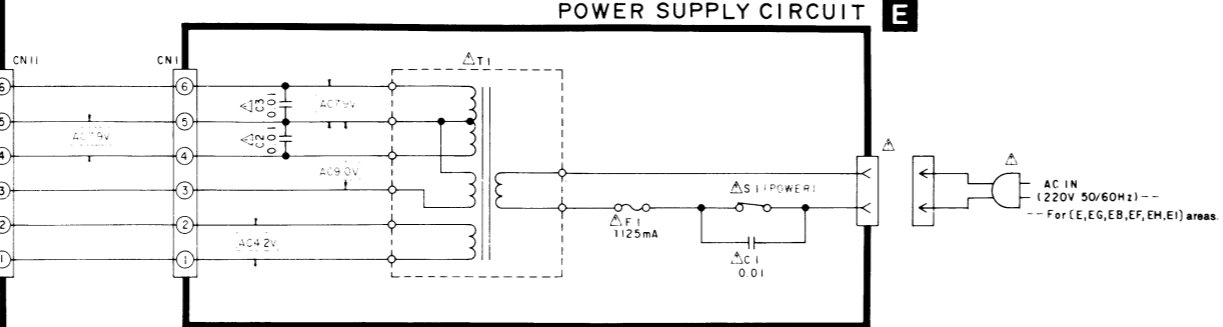
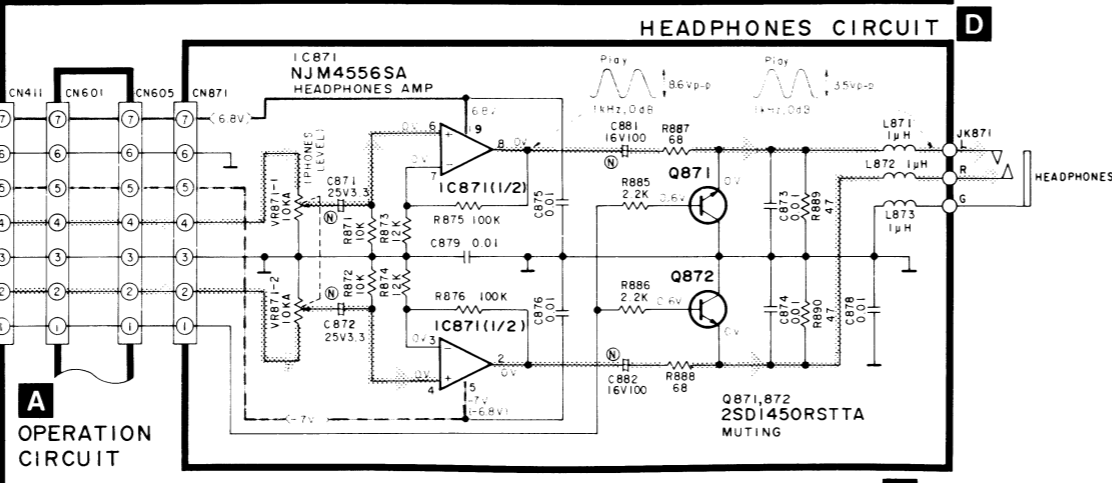
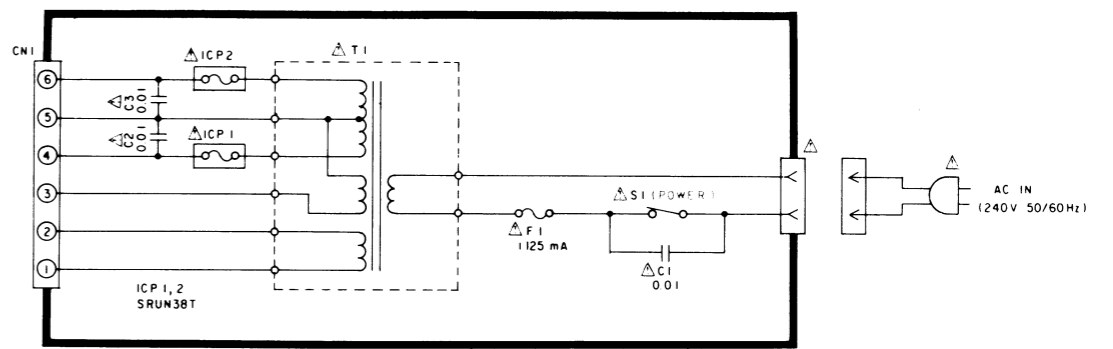




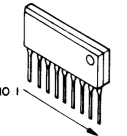
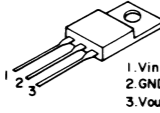
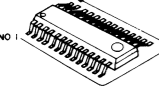
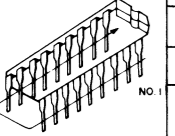
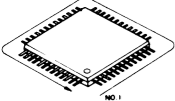
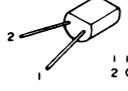
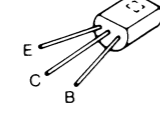
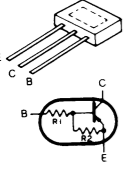
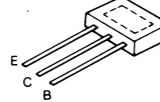
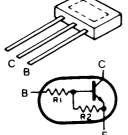
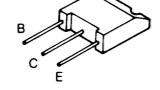
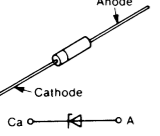
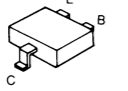


• Power supply circuit for [EK] area.

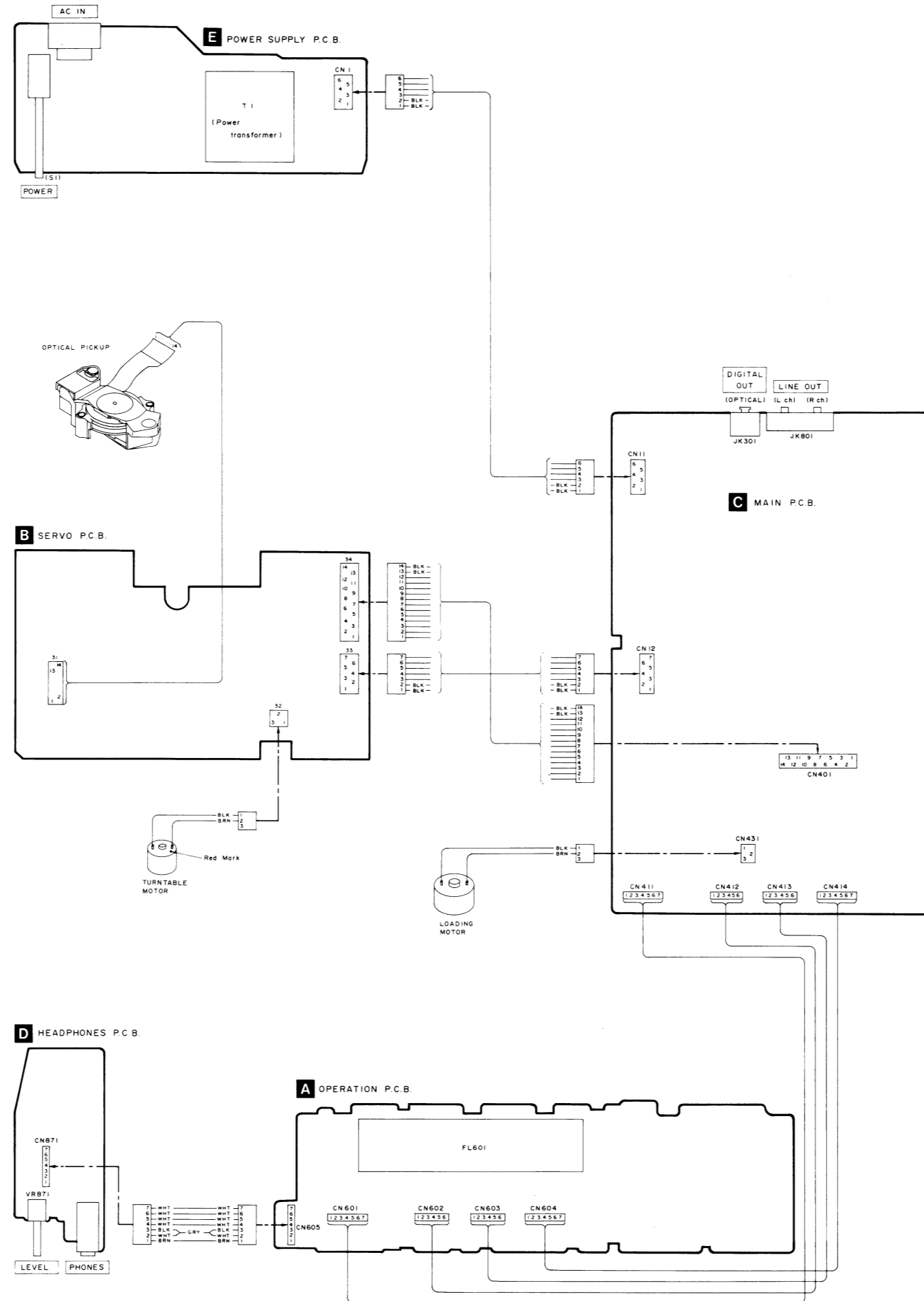
For [EK] area.
E POWER SUPPLY CIRCUIT



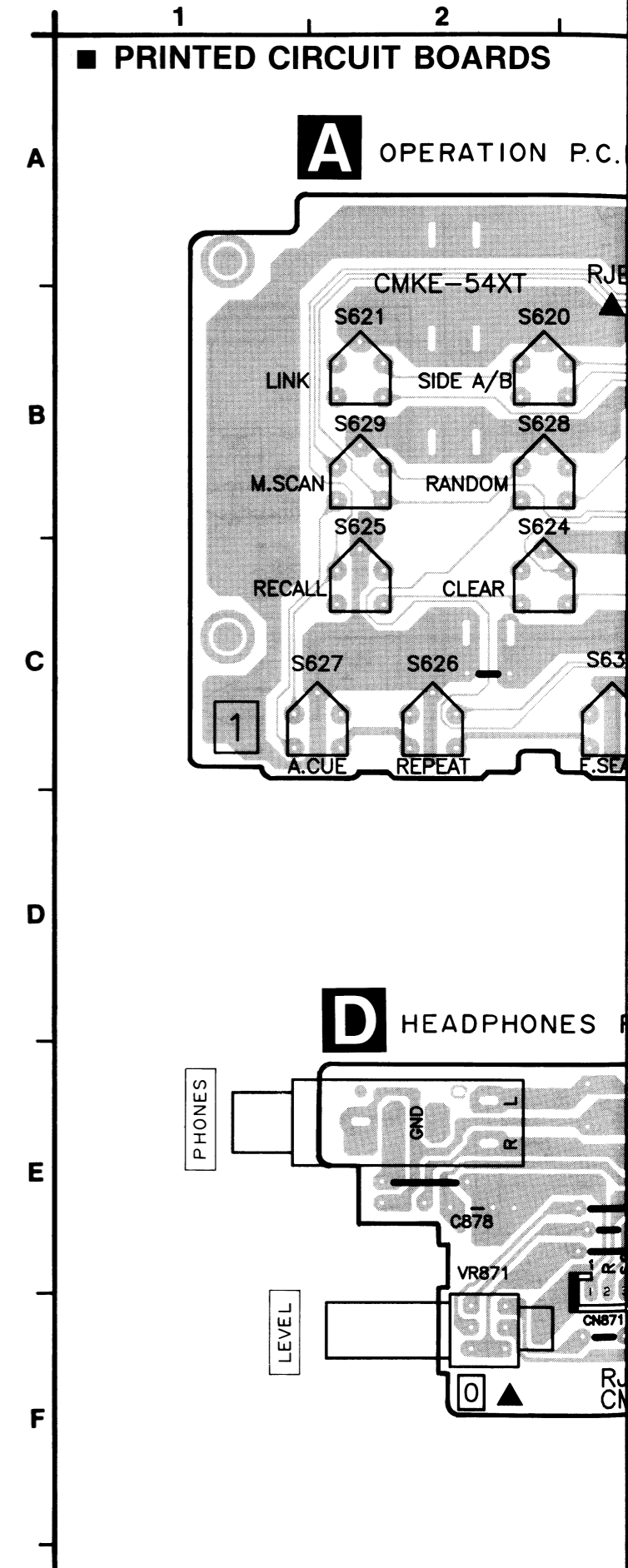
■ TERMINAL GUIDE OF IC's, TRANSISTORS AND DIODES

		LM2940T5 SVIH8DN2175 5 Pin NJM4556SA 9 Pin	 1. Vin 2. GND 3. Vout
		LM833M NJM4560M 8 Pin BA4558FT1 MN74HC00S 14 Pin	AN8371S 24 Pin SVICXK5816M 24 Pin 4822 209 73234 (TDA8808T) 28 Pin 4822 209 73235 (TDA8809T) 28 Pin
		MN15283PEY-1 64 Pin MN1551PJF-1 28 Pin MN1554PJE-2 64 Pin 4822 209 72587 (TCA0372) 8 Pin	2SB941QR, 2SD1266QR
MN6471 42 Pin MN6625 64 Pin 		SRUF38T, SRUN15  1 Input 2 Output	4822 130 44121 (BC338) 
		DTA124EST, DTA114EST	2SD1450R, 2SC3311 A-Q, 2SA1309 
DTC124EST 		2SB1238QSTV6, 2SD1862QRTV6, 2SB1240-P 	1SS254, SVDS5688GT3 Anode Cathode Ca \rightarrow A
 Anode Cathode Ca \rightarrow A		MA4330M, MA4062H, MA4033M, MA4051M, MA4082M	5322 130 42136 (BC848C) 

■ WIRING CONNECTION DIAGRAM



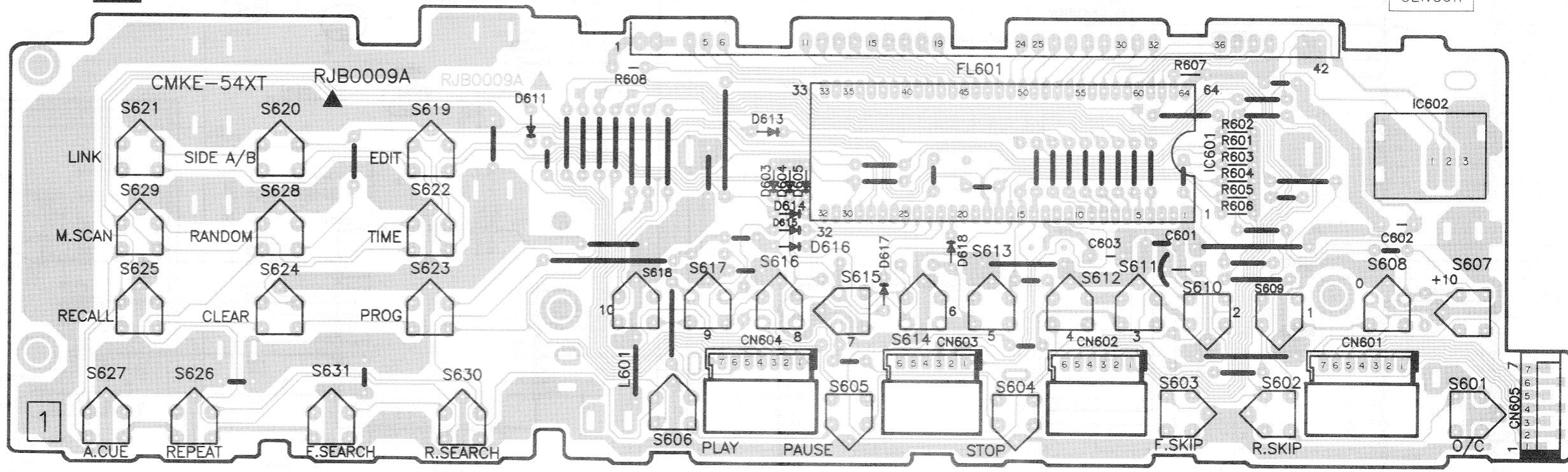
■ PRINTED CIRCUIT BOARDS



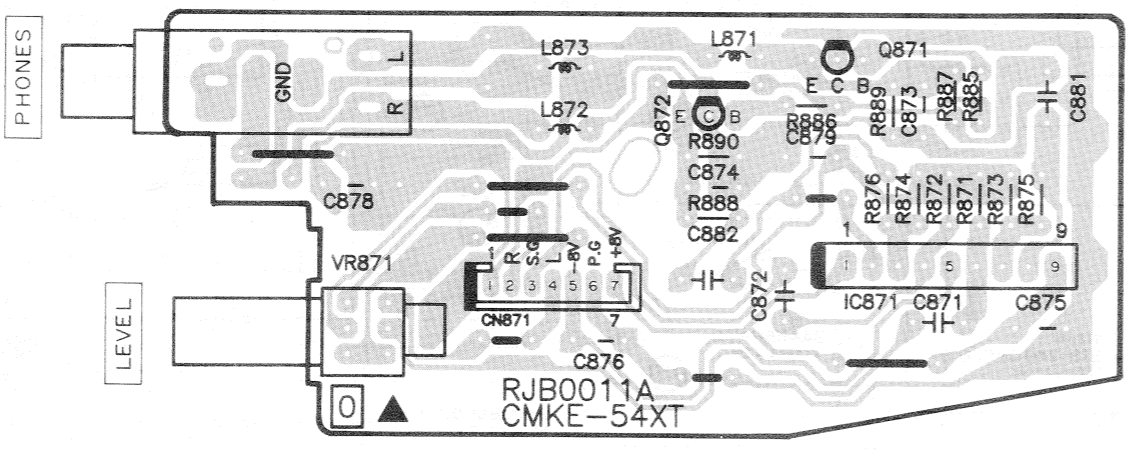
PRINTED CIRCUIT BOARDS

A
B
C
D
E
F

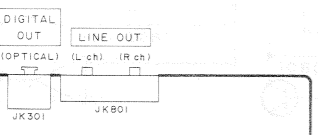
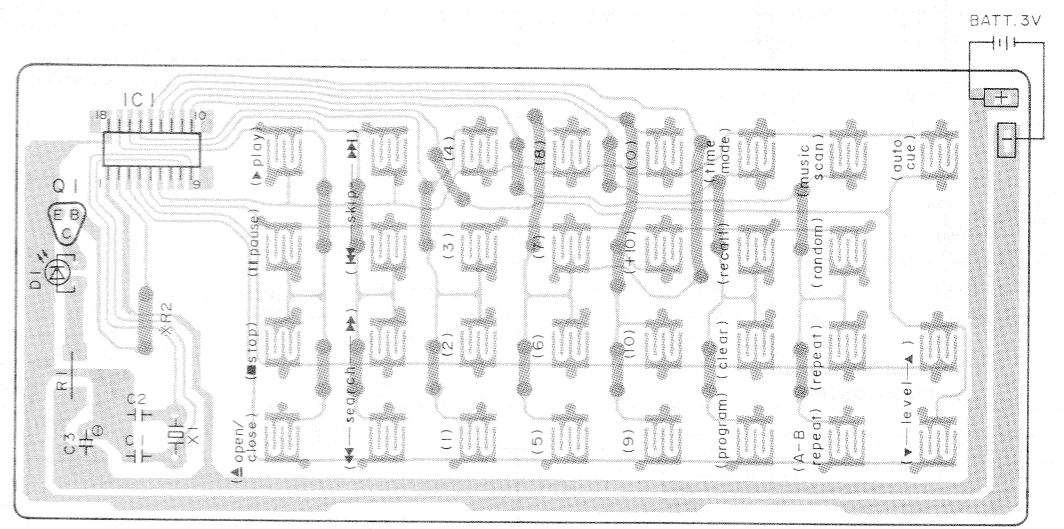
A OPERATION P.C.B.



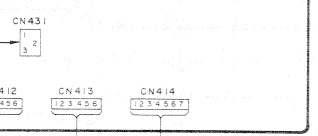
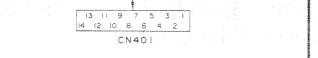
D HEADPHONES P.C.B.



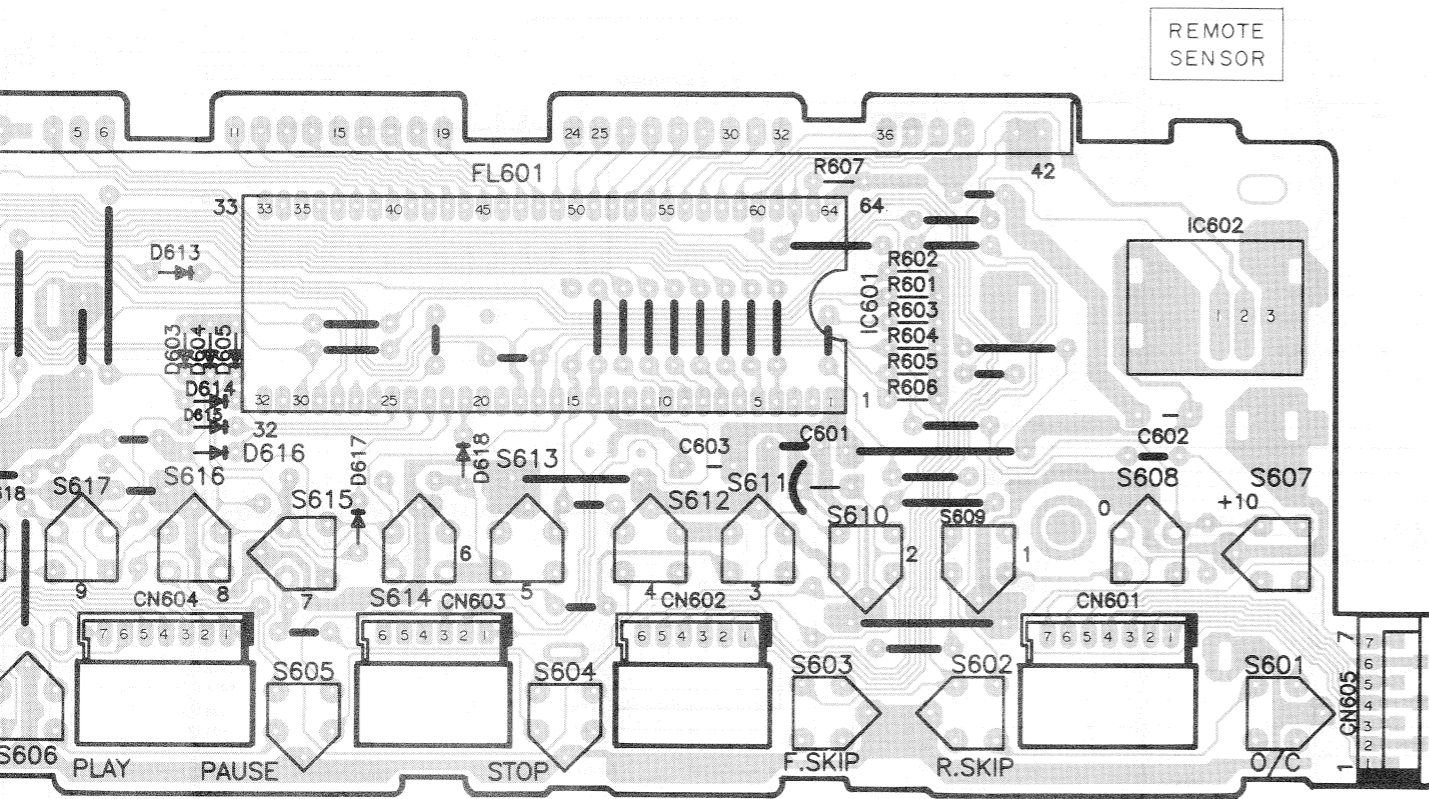
Remote control transmitter



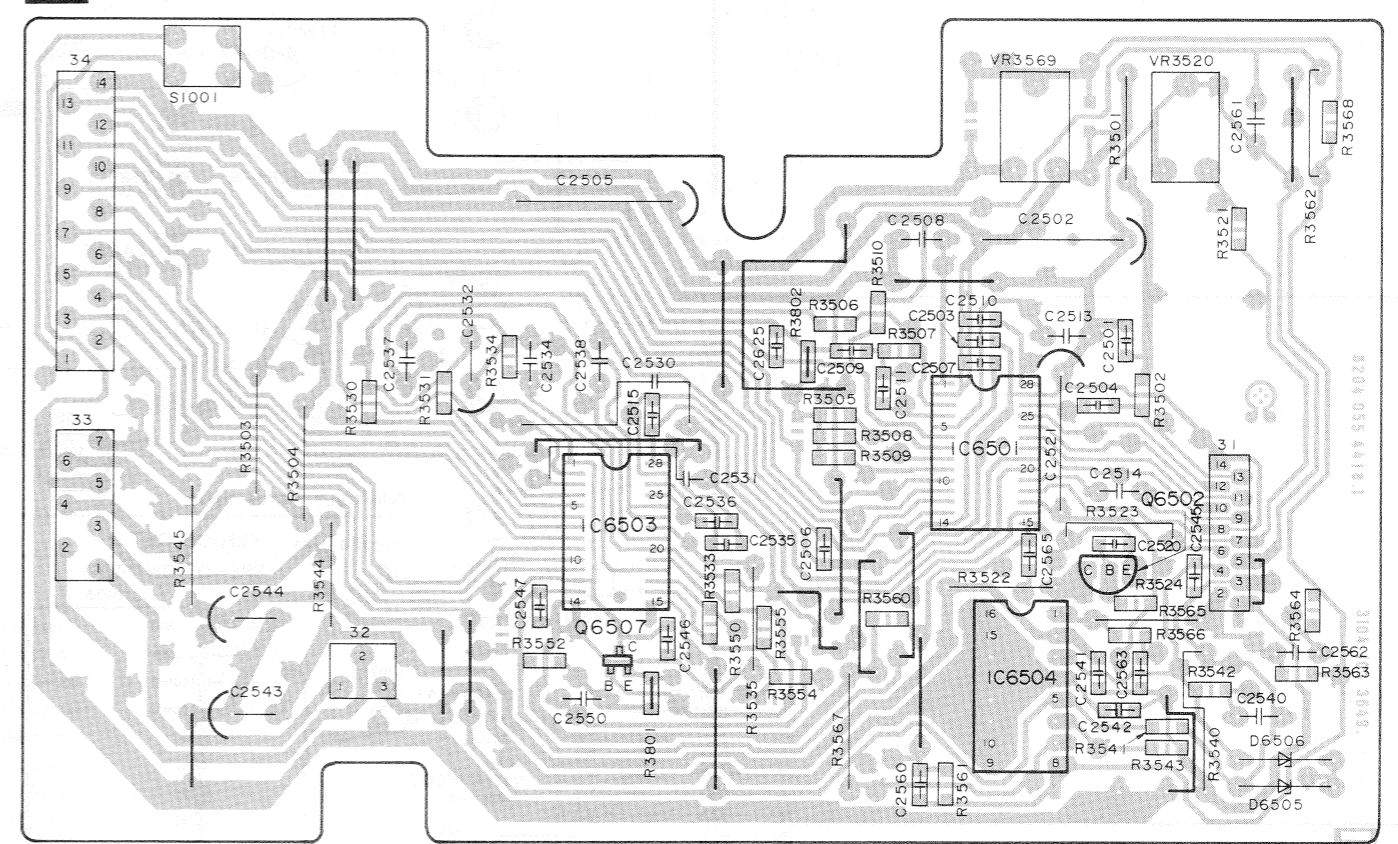
C MAIN P.C.B.



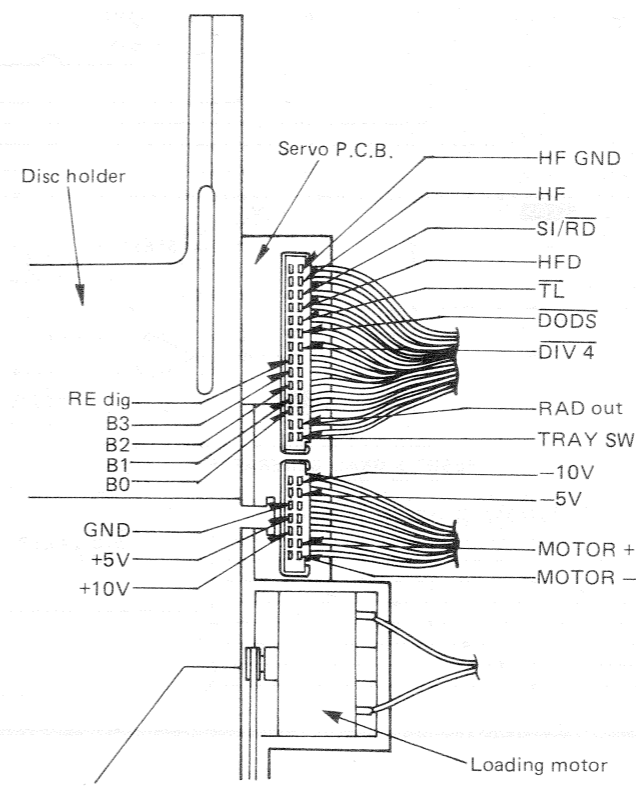
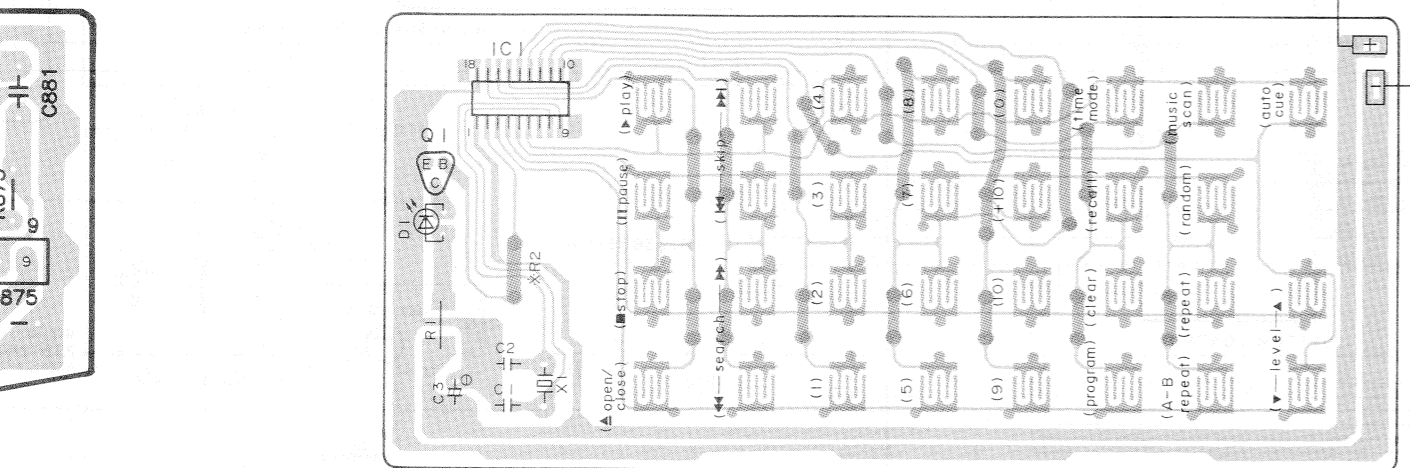
5 6 7 8 9 10 11 12 13 14



B SERVO P.C.B.



• Remote control transmitter

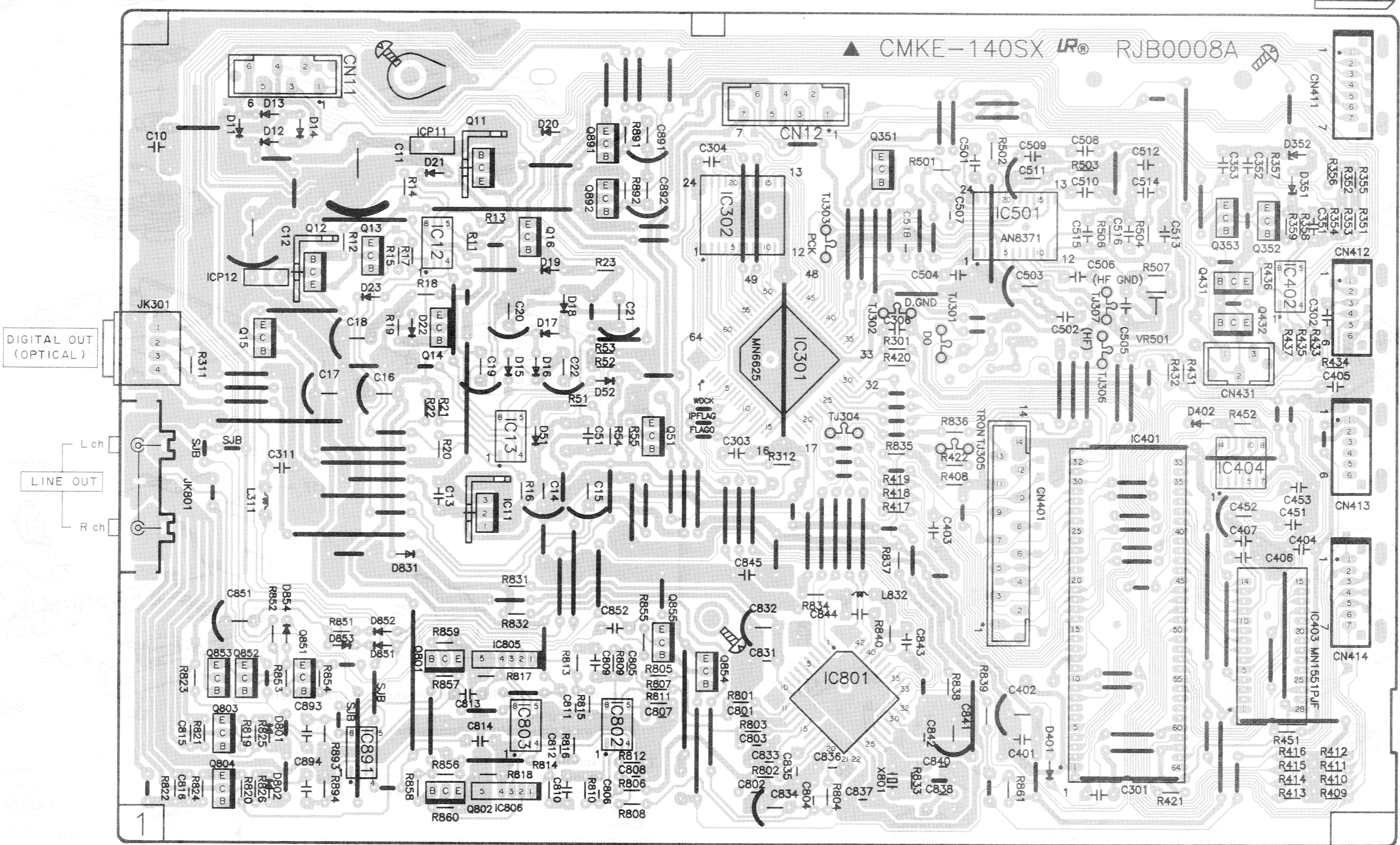


Note: Use connector pins to check servo circuit voltages and waveforms.

15 16 17 18 19 20 21 22 23 24

C MAIN P.C.B.

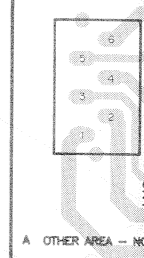
▲ CMKE-140SX LR® RJB0008A



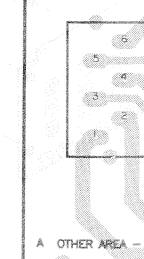
DIGITAL OUT (OPTICAL)

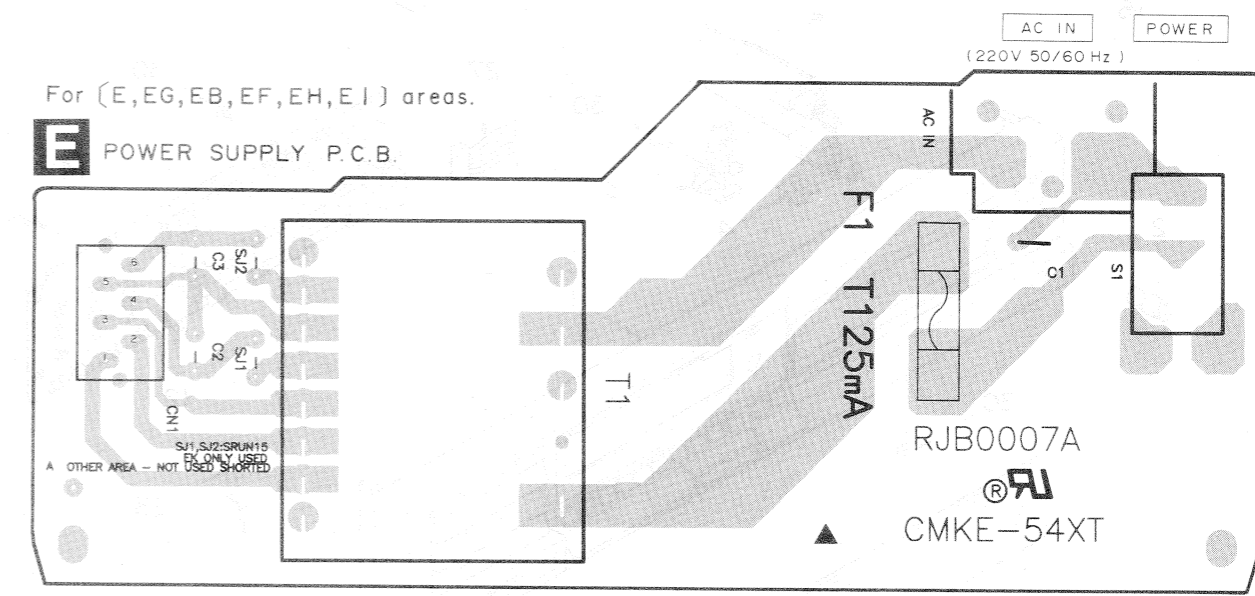
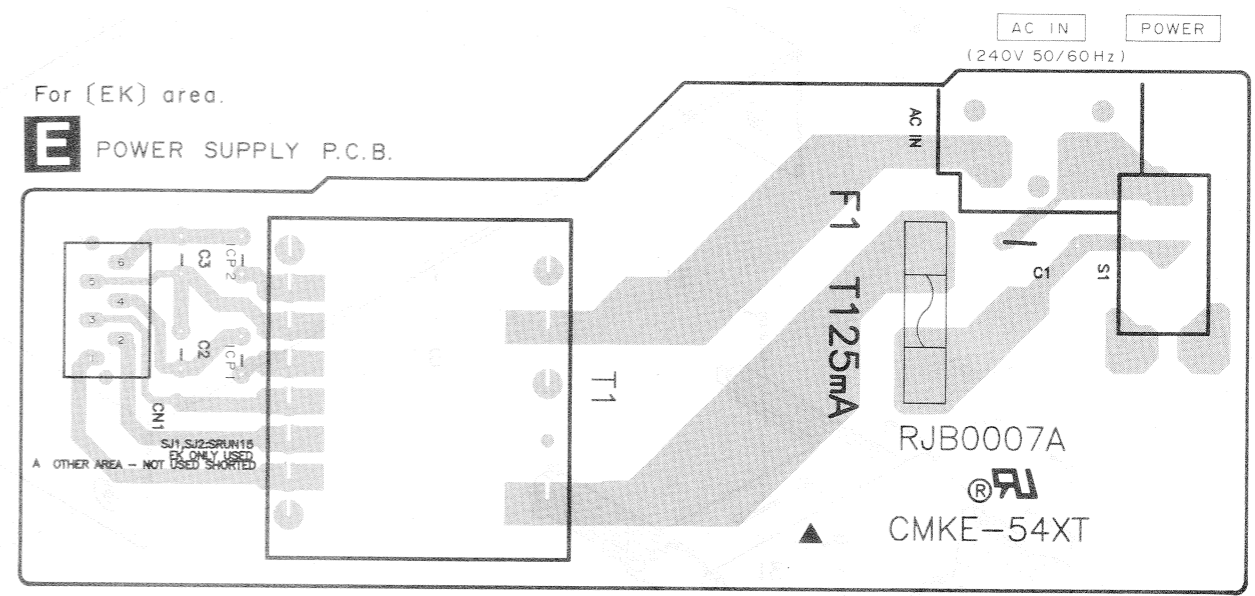
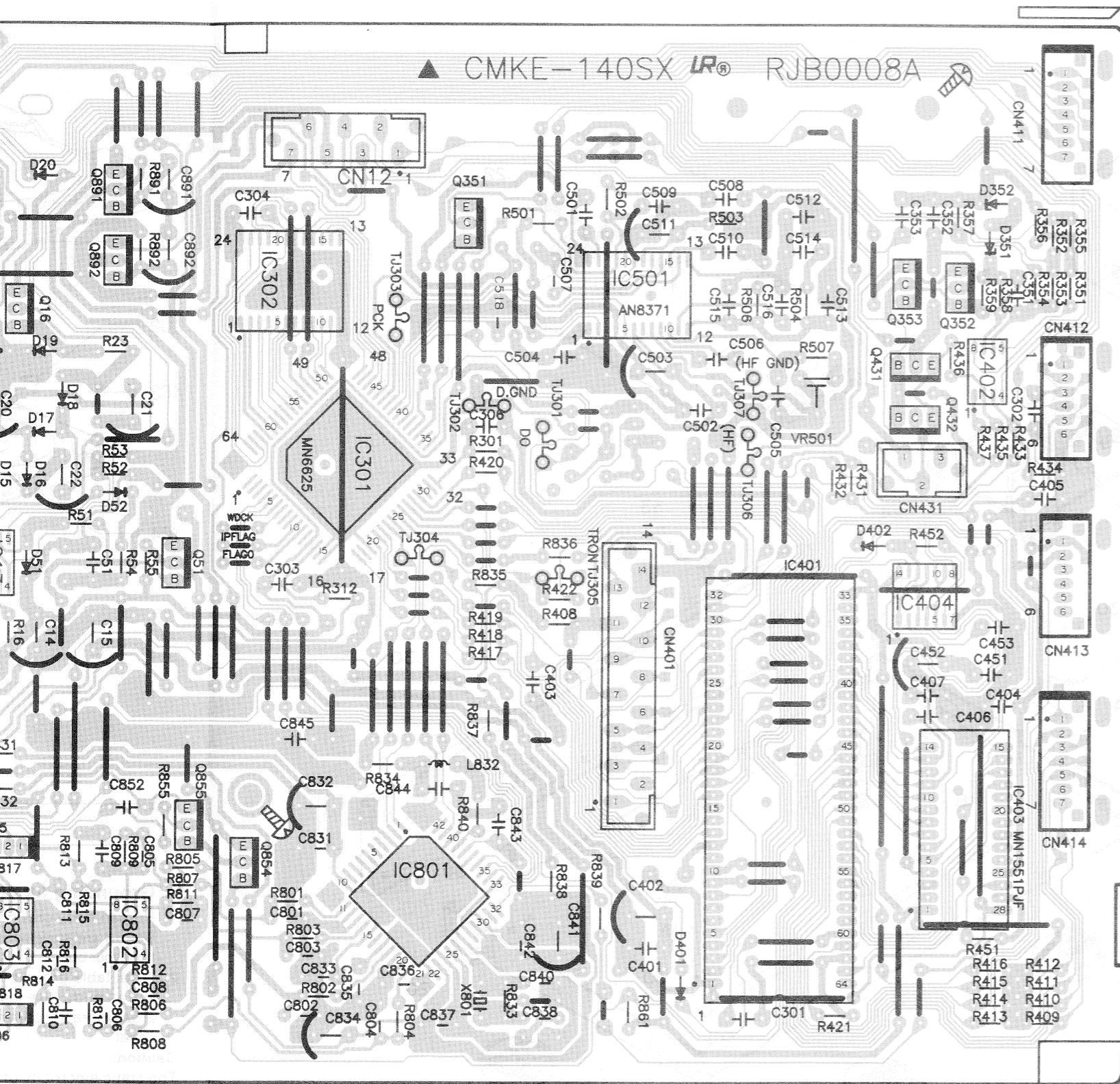
LINE OUT
L ch
R ch

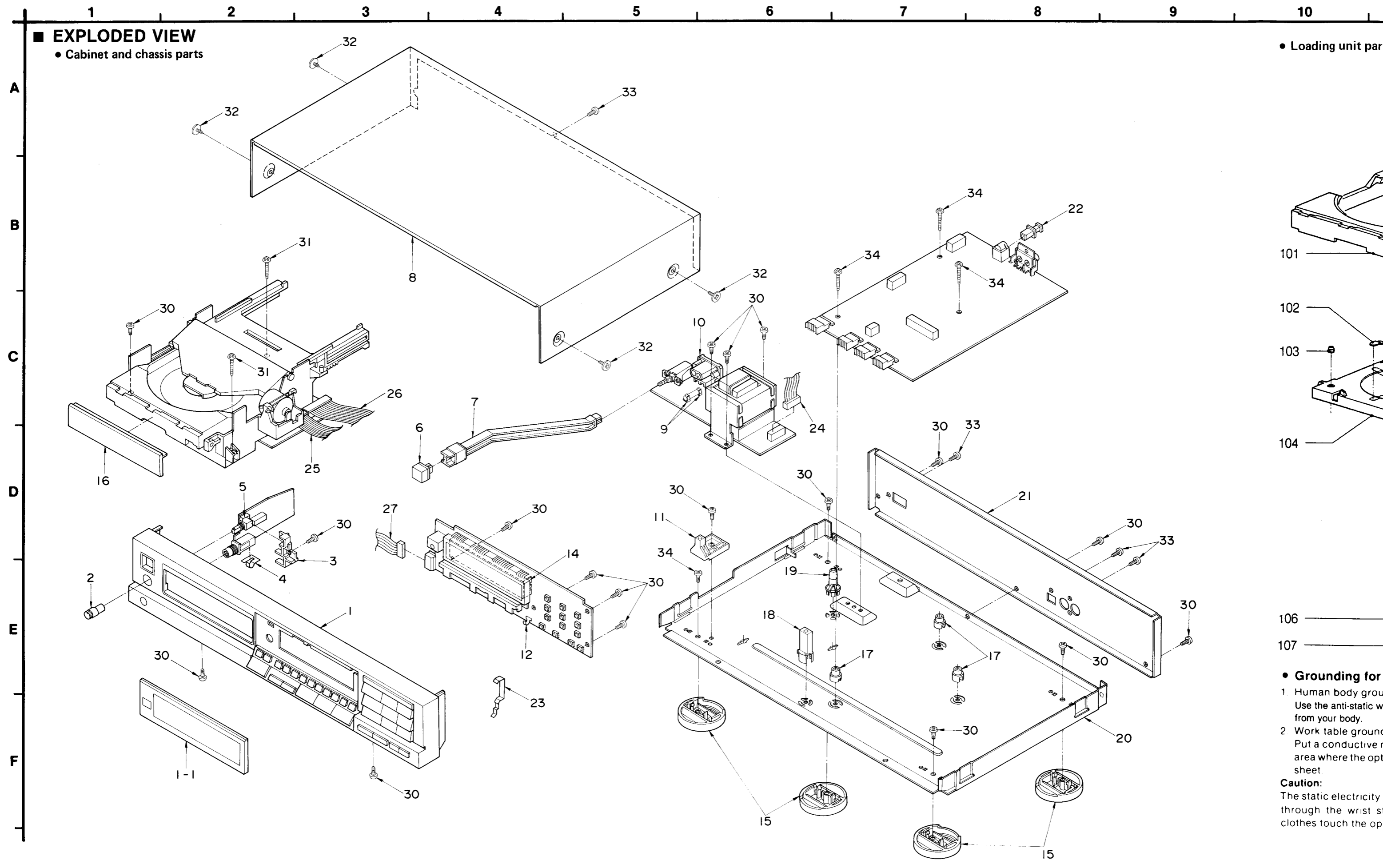
For (EK) POWER



For (E, EG) POWER

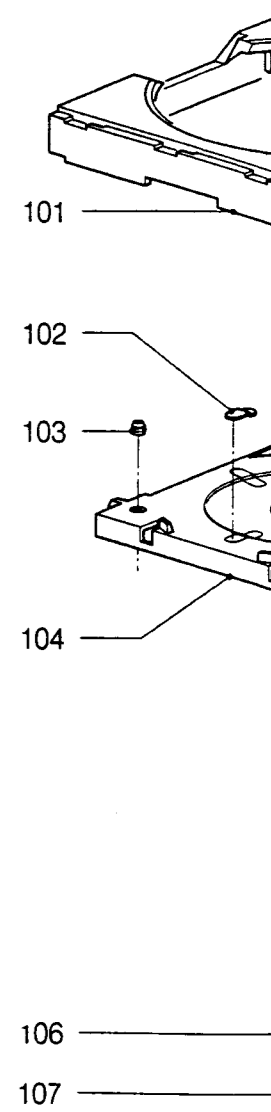




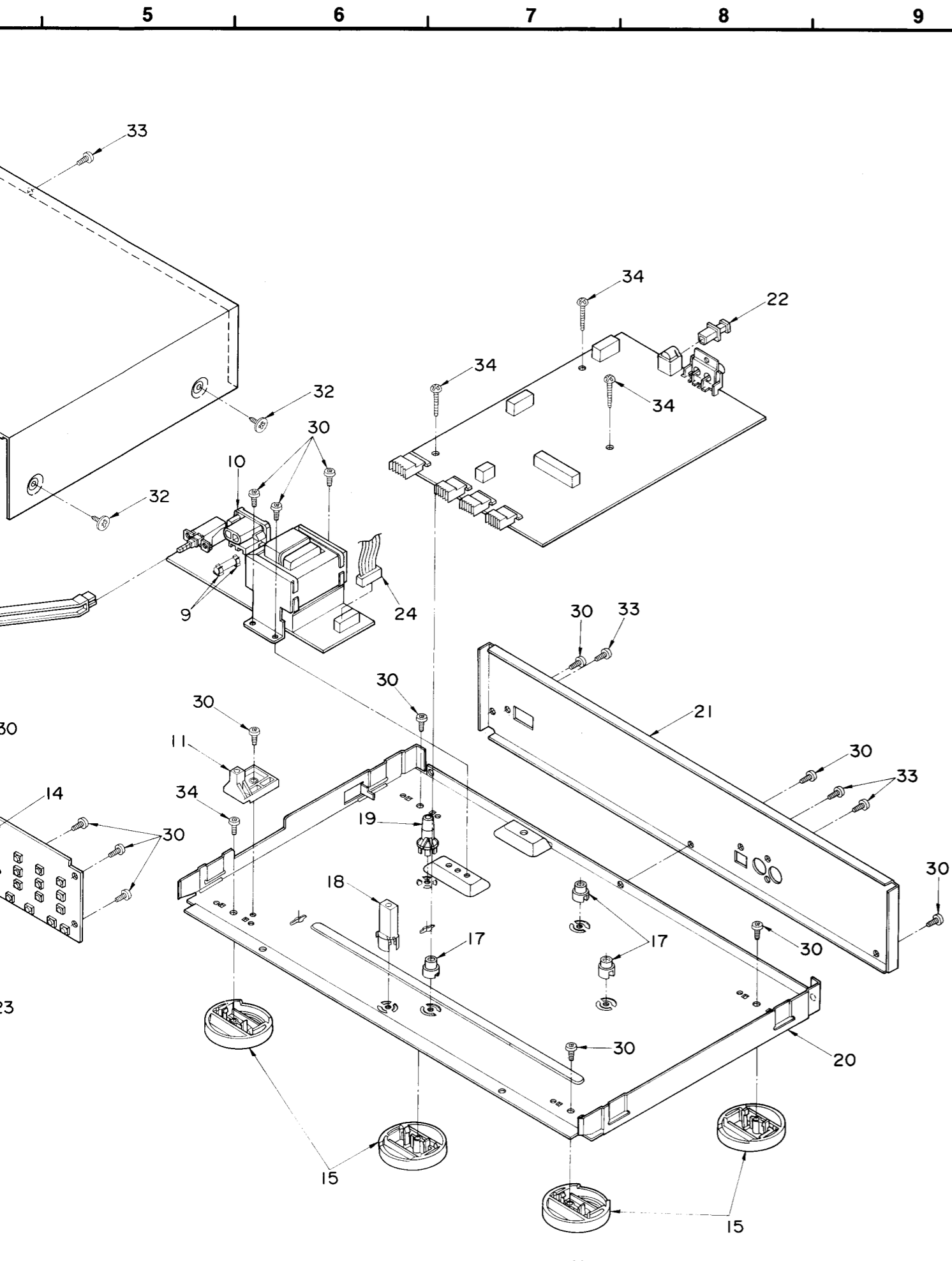


EXPLODED VIEW
 • Cabinet and chassis parts

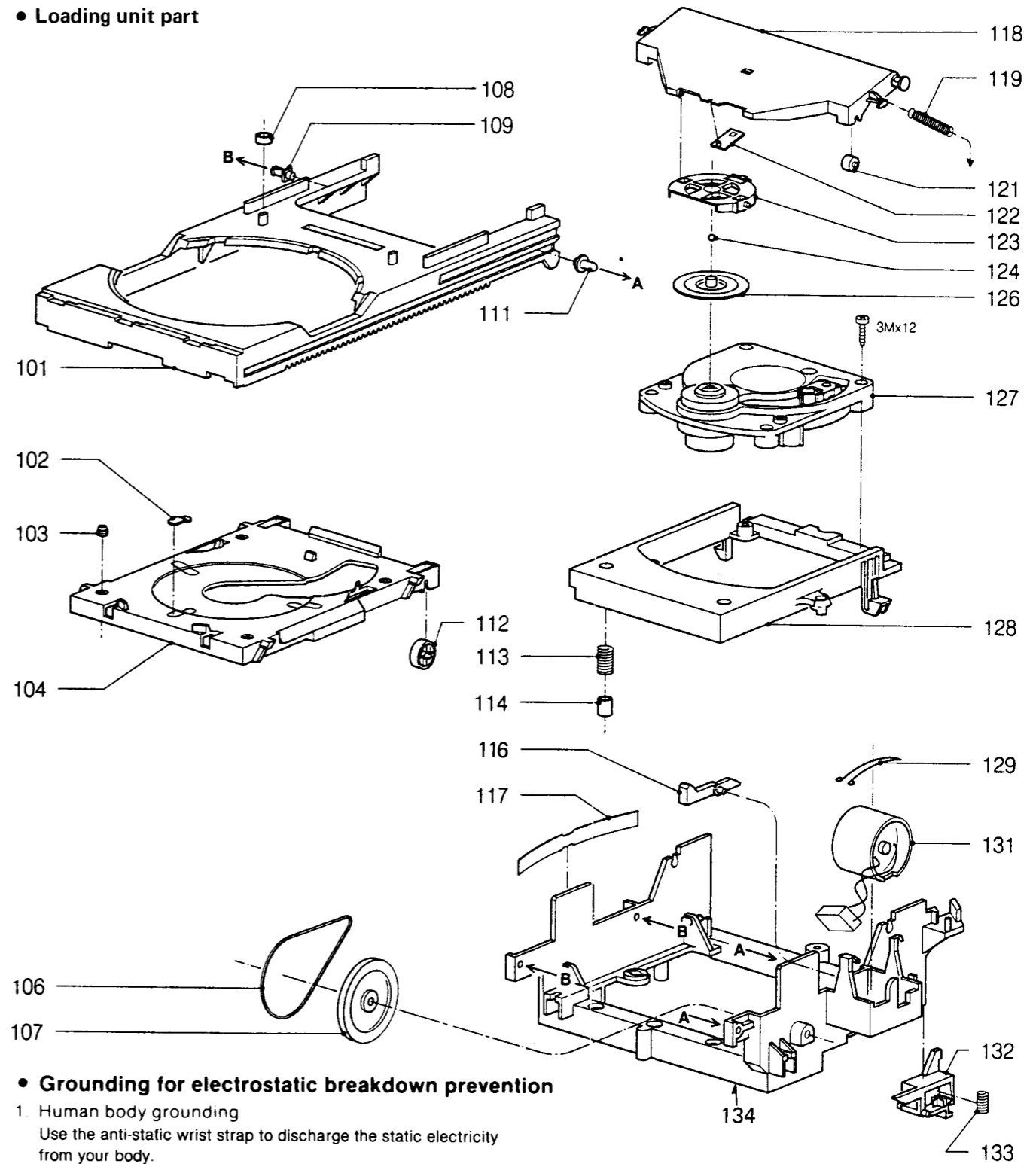
• Loading unit part



- **Grounding for el**
1. Human body ground
Use the anti-static wrist from your body.
 2. Work table ground
Put a conductive mat area where the optical sheet.
- Caution:**
The static electricity of through the wrist strap clothes touch the optical



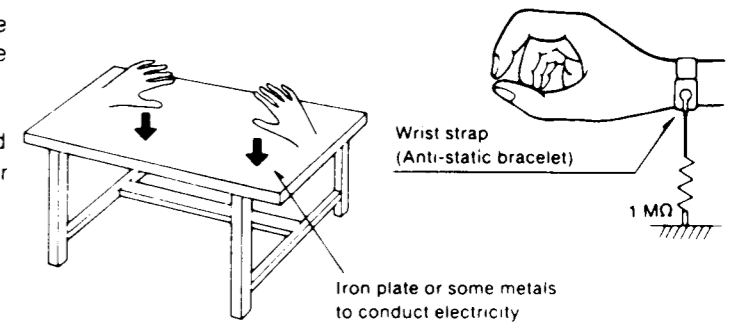
• Loading unit part



• Grounding for electrostatic breakdown prevention

1. Human body grounding
Use the anti-static wrist strap to discharge the static electricity from your body.
2. Work table grounding
Put a conductive material (sheet) or steel sheet on the area where the optical pickup is placed, and ground the sheet.

Caution:
The static electricity of your clothes will not be grounded through the wrist strap. So, take care not to let your clothes touch the optical pickup



■ REPLACEMENT PARTS LIST

Notes : * Important safety notice :

Components identified by Δ mark have special characteristics important for safety. When replacing any of these components use only manufacturer's specified parts.

* Bracketed indications in Ref. No. columns specify the area. (Refer to the first page for area.)
Parts without these indications can be used for all areas.

* "K" mark parts are used for black type only.

* "S" mark parts are used for silver type only.

Parts other than "K" and "S" marked are used for all color types

* Warning : This product uses a laser diode. Refer to caution statements on page 3.

* ACHTUNG :

Die lasereinheit nicht zerlegen.

Die lasereinheit darf nur gegen eine vom hersteller spezifizizierte einheit ausgetauscht werden.

* Remote control ass'y : Supply period for three years from termination of production.

* \boxed{MB} indicates parts that are supplied by MBV.

Ref. No.	Part No.	Description	Ref. No.	Part No.	Description
CABINET AND CHASSIS					
1	$\text{\textcircled{K}}$ SYD222KEE	FRONT PANEL ASS'Y	18	RMR0021	HOLDER \boxed{MB}
1	$\text{\textcircled{S}}$ SYD222SEE	FRONT PANEL ASS'Y	19	RMR0020	HOLDER \boxed{MB}
1-1	SGUD224	ORNAMENT	20	SKULP212A-KE	BOTTOM BOARD \boxed{MB}
2	$\text{\textcircled{K}}$ SBN1161-2	KNOB	21	SGPLP222A-KB	REAR PANEL \boxed{MB}
2	$\text{\textcircled{S}}$ SBN1161-3	KNOB	(EB, EH, EF)		
3	SHRD201	PLATE	(E1)		
4	SUSD165	SPRING	21	SGPLP222A-KE	REAR PANEL \boxed{MB}
5	SUSD141	COIL SPRING	(E)		
6	$\text{\textcircled{K}}$ SBC666-1	BUTTON	21	SGPLP222A-KG	REAR PANEL \boxed{MB}
6	$\text{\textcircled{S}}$ SBC666	BUTTON, POWER	(EG)		
7	SUBD15-1	ROD	21	SGPLP222A-KK	REAR PANEL \boxed{MB}
8	$\text{\textcircled{K}}$ RKM0011-K	CABINET \boxed{MB}	(EK)		
8	$\text{\textcircled{S}}$ RKM0011-S	CABINET \boxed{MB}	22	VJA1024	CAP
9	SJT330	FUSE HOLDER	23	$\text{\textcircled{S}}$ RMC0013	BRACKET \boxed{MB}
10	Δ SJS9236	AC INLET	24	REX0006	CONNECTOR \boxed{MB}
11	RMR0022	HOLDER \boxed{MB}	25	REX0007	CONNECTOR \boxed{MB}
12	SUSD144	EARTH PLATE	26	REX0008	CONNECTOR \boxed{MB}
14	SUMD139	BRACKET	27	SWKD552071-1	CORD
15	SKLD8-E	FOOT	30	XTB3+8JFZ	SCREW
16	$\text{\textcircled{K}}$ R GK0015	ORNAMENT	31	XTB3+35JFZ	SCREW
16	$\text{\textcircled{S}}$ R GK0015A	ORNAMENT	32	$\text{\textcircled{K}}$ SNE2129-1	SCREW
17	SHE185-2	HOLDER	32	$\text{\textcircled{S}}$ SNE2129	SCREW
			33	XTBS3+8JFZ1	SCREW
			34	XTB3+16JFZ	SCREW

Ref. No.	Part No.	Description	Ref. No.	Part No.	Description
LOADING UNIT PARTS					
101	4822 444 50603	TRAY \boxed{MB}	119	4822 492 32883	SPRING, TENSION \boxed{MB}
102	4822 325 50176	GROMMET, CABLE \boxed{MB}	121	4822 528 90639	ROLLER \boxed{MB}
103	4822 325 50177	GROMMET, CABLE \boxed{MB}	122	4822 466 92257	PLATE \boxed{MB}
104	4822 466 92251	PLATE \boxed{MB}	123	4822 402 61207	HOLDER \boxed{MB}
106	4822 358 10115	BELT, DRIVING \boxed{MB}	124	4822 520 40177	BALL \boxed{MB}
107	4822 522 32359	WHEEL, GEAR \boxed{MB}	126	4822 530 80503	RING, PRESSURE \boxed{MB}
108	4822 532 51518	RING, RUBBER \boxed{MB}	127	4822 691 30209	Optical pickup unit \boxed{MB}
109	4822 402 61081	GUIDE \boxed{MB}	128	4822 402 61196	SUPPORT \boxed{MB}
111	4822 402 61132	GUIDE \boxed{MB}	129	4822 492 63746	SPRING, CLAMPING \boxed{MB}
112	4822 528 90638	ROLLER \boxed{MB}	131	4822 361 20998	MOTOR \boxed{MB}
113	4822 492 51902	SPRING, COMPRES. \boxed{MB}	132	4822 402 50244	BRACKET \boxed{MB}
114	4822 466 61587	FOAM \boxed{MB}	133	4822 492 51935	SPRING, COMPRES. \boxed{MB}
116	4822 402 61107	LEVER \boxed{MB}			
117	4822 492 63659	SPRING, BLADE \boxed{MB}			
118	4822 444 60568	LID \boxed{MB}	134	4822 464 50715	CHASSIS \boxed{MB}

SL-P222A

Ref. No.	Part No.	Description	Ref. No.	Part No.	Description
INTEGRATED CIRCUITS			D51	MA4033M	DIODE
IC11	LM2940T5	I.C. REGULATOR	D52	MA165	DIODE
IC12	SV1BA4558F	I.C. STABILIZER	D351	MA4051-M	DIODE
IC13	SV1BA4558F	I.C. RESET	D352	MA4051-M	DIODE
IC301	MN6625	I.C. DIGITAL SIGNAL PROCESSOR	D401	MA165	DIODE
IC302	SV1CXK5816M	I.C. 16K RAM	D402	MA165	DIODE
IC401	MN1554PJE-2	I.C. SYSTEM CONTROL	D603	MA165	DIODE
IC402	SV1BA4558F	I.C. MOTOR DRIVE	D604	MA165	DIODE
IC403	MN1551PJF-1	I.C. PERIOD DET/SPEED CONT	D605	MA165	DIODE
IC404	MN74HC00S	I.C. NAND GATE	D611	MA165	DIODE
IC501	AN8371S	I.C. DATA SLICE&PLL	D613	MA165	DIODE
IC601	MN15283PEY-1	I.C. SYSTEM CONTROL/FL DRIVE	D614	MA165	DIODE
IC602	HC-MD10E	I.C. REMOTE CONTROL	D615	MA165	DIODE
IC801	MN6471	I.C. DIGITAL FILTER	D616	MA165	DIODE
IC802	LM833M	I.C. COMPARATOR	D617	MA165	DIODE
IC803	NJM4560M	I.C. BUFFER(L.P.F.)	D618	MA165	DIODE
IC805	SV1H8DN2175	I.C. LOW PASS FILTER	D801	MA165	DIODE
IC806	SV1H8DN2175	I.C. LOW PASS FILTER	D802	MA165	DIODE
IC871	NJM4566SA	I.C. HEADPHONES	D831	MA165	DIODE
IC891	NJM4560M	I.C. BUFFER	D851	MA165	DIODE
TRANSISTORS			D852	MA165	DIODE
Q11	2SD1266QR	TRANSISTOR, REGULATOR	D853	MA165	DIODE
Q12	2SB941QR	TRANSISTOR, REGULATOR	D854	MA165	DIODE
Q13	2SB1240-P	TRANSISTOR, REGULATOR	I.C. PROTECTORS		
Q14	2SD1862QRTV6	TRANSISTOR, REGULATOR	ICP1	△ SRUN38T	I.C. PROTECTOR
Q15	2SB1240-P	TRANSISTOR, REGULATOR	(EK)		
Q16	2SB1239QSTV6	TRANSISTOR, REGULATOR	ICP2	△ SRUN38T	I.C. PROTECTOR
Q51	DTC124EST	TRANSISTOR, RESET	(EK)		
Q351	DTA124EST	TRANSISTOR, MOTOR CONTROL	ICP11	△ SRUN15	I.C. PROTECTOR
Q352	2SD1862QRTV6	TRANSISTOR, MOTOR DRIVE	ICP12	△ SRUN15	I.C. PROTECTOR
Q353	2SB1240-P	TRANSISTOR, MOTOR DRIVE	VARIABLE RESISTORS		
Q431	2SD1862QRTV6	TRANSISTOR, MOTOR DRIVE	VR501	EVND8AA02B13	V.R. PLL ADJ.
Q432	2SB1240-P	TRANSISTOR, MOTOR DRIVE	VR871	EVU57A022A14	V.R. HEADPHONES
Q801	2SC3311A-Q	TRANSISTOR, EMPHASIS DRIVE	COILS AND TRANSFORMERS		
Q802	2SC3311A-Q	TRANSISTOR, EMPHASIS DRIVE	L311	ELEPK1R0KA	COIL
Q803	2SD1450R	TRANSISTOR, MUTING	L601	VLP0053	COIL
Q804	2SD1450R	TRANSISTOR, MUTING	L832	ELEPK4R7KA	COIL
Q851	DTA124EST	TRANSISTOR, MUTING CONT.	L871	ELEPK1R0KA	COIL
Q852	DTC124EST	TRANSISTOR, MUTING CONT.	L872	ELEPK1R0KA	COIL
Q853	DTA114EST	TRANSISTOR, MUTING CONT.	L873	ELEPK1R0KA	COIL
Q854	DTC124EST	TRANSISTOR, EMPHASIS DIVE	T1	△ RTP1V4B002-J	POWER TRANSFORMER MB
Q855	DTA124EST	TRANSISTOR, EMPHASIS DRIVE	(EK)		
Q871	2SD1450R	TRANSISTOR, MUTING	T1	△ RTP1V4E001-J	POWER TRANSFORMER MB
Q872	2SD1450R	TRANSISTOR, MUTING	(E. EG. EB, EF)		
Q891	2SC3311A-Q	TRANSISTOR, REGULATOR	(EH, EI)		
Q892	2SA1309A-R	TRANSISTOR, REGULATOR	OSCILLATORS		
DIODES			X801	SVQ49U338S	OSCILLATOR
D11	△ SVDS5688GT3	DIODE	DISPLAYS		
D12	△ SVDS5688GT3	DIODE	FL601	RSL0001F	DISPLAY
D13	△ SVDS5688GT3	DIODE	FUSES		
D14	△ SVDS5688GT3	DIODE	F1	△ XBA2C012TB0S	FUSE(250V, T125mA)
D15	△ SVDS5688GT3	DIODE	SWITCHES		
D16	△ SVDS5688GT3	DIODE	S1	△ ES88249V	SW. POWER
D17	△ SVDS5688GT3	DIODE	S601	EVQQS705G	SW. OPEN/CLOSE
D18	△ SVDS5688GT3	DIODE	S602	EVQQS705G	SW. SKIP
D19	MA4330M	DIODE	S603	EVQQS705G	SW. SKIP
D20	MA4062-H	DIODE	S604	EVQQS705G	SW. STOP
D21	MA4062	DIODE			
D22	MA4062-H	DIODE			
D23	△ SVDS5688GT3	DIODE			

Ref. No.	Part No.	Description	Ref. No.	Part No.	Description
S605	EVQQS705G	SW, PAUSE	S629	EVQQS705G	SW, M.SCAN
S606	EVQQS705G	SW, PLAY	S630	EVQQS705G	SW, R.SEARCH
S607	EVQQS705G	SW, +10	S631	EVQQS705G	SW, F.SEARCH
S608	EVQQS705G	SW, NO 0	JACKS		
S609	EVQQS705G	SW, NO 1	JK301	TOTX174	I.C. OPTICAL TRANSMISSION MODULE
S610	EVQQS705G	SW, NO 2	JK801	SJFD4	OUTPUT TERMINAL
S611	EVQQS705G	SW, NO 3	JK871	SJJ146B	JACK, HEADPHONES
S612	EVQQS705G	SW, NO 4	OTHERS		
S613	EVQQS705G	SW, NO 5	CN1	RJT001H006	CONNECTOR <input type="checkbox"/>
S614	EVQQS705G	SW, NO 6	CN11	RJT001H006	CONNECTOR <input type="checkbox"/>
S615	EVQQS705G	SW, NO 7	CN12	RJT001H007	CONNECTOR <input type="checkbox"/>
S616	EVQQS705G	SW, NO 8	CN401	RJT001H014	CONNECTOR <input type="checkbox"/>
S617	EVQQS705G	SW, NO 9	CN411	SJS50780WL	CONNECTOR(7P)
S618	EVQQS705G	SW, NO10	CN412	SJS50680WL	SOCKET(6P)
S619	EVQQS705G	SW, EDIT	CN413	SJS50680WL	SOCKET(6P)
S620	EVQQS705G	SW, SIDE A/B	CN414	SJS50780WL	CONNECTOR(7P)
S621	EVQQS705G	SW, DISC LINK	CN431	RJT001H003	CONNECTOR <input type="checkbox"/>
S622	EVQQS705G	SW, TIME MODE	CN601	SJT30747WL	CONNECTOR(7P)
S623	EVQQS705G	SW, PROGRAM	CN602	SJT30647WL	CONNECTOR(6P)
S624	EVQQS705G	SW, CLEAR	CN603	SJT30647WL	CONNECTOR(6P)
S625	EVQQS705G	SW, RECALL	CN604	SJT30747WL	CONNECTOR(7P)
S626	EVQQS705G	SW, REPEAT	CN605	EMCS0750ZL	CONNECTOR(7P)
S627	EVQQS705G	SW, AUTO CUE	CN871	EMCS0750Z	CONNECTOR(7P)
S628	EVQQS705G	SW, RANDOM			

Ref. No.	Part No.	Description	Ref. No.	Part No.	Description
INTEGRATED CIRCUITS			DIODES		
IC6501	4822 209 73234	I.C. PHOTO DIODE SIGNAL PROCESSOR <input type="checkbox"/>	D6505	4822 130 81101	DIODE <input type="checkbox"/>
IC6503	4822 209 73235	I.C. RADIAL ERROR SIGNAL PROCESSOR <input type="checkbox"/>	D6506	4822 130 81101	DIODE <input type="checkbox"/>
IC6504	4822 209 72587	I.C. FOCUS/RADIAL DRIVE <input type="checkbox"/>	VARIABLE RESISTORS		
TRANSISTORS			VR3520	4822 101 10685	V.R. LASER POWER ADJ. <input type="checkbox"/>
Q6502	4822 130 44121	TRANSISTOR <input type="checkbox"/>	VR3569	4822 100 20522	V.R. FOCUS OFFSET ADJ. <input type="checkbox"/>
Q6507	5322 130 42136	TRANSISTOR <input type="checkbox"/>	SWITCH		
			S1001	4822 276 12523	SW, TRAY <input type="checkbox"/>

Ref. No.	Part No.	Description	Ref. No.	Part No.	Description	
PACKING MATERIAL			A1	RQT0012G	INSTRUCTION MANUAL <input type="checkbox"/>	
P1	Ⓚ	RPG0010	(E, EB, EF, EH)			
P1	Ⓢ	RPG0076	A1	RQT0012V	INSTRUCTION MANUAL <input type="checkbox"/>	
P2		RPNO045A	{E1}			
P3		RPNO045B	A2	△	SJA187	POWER CORD
P4		XZB60X60A01	{E, EH, EB, EF}			
ACCESSORIES			A2	△	SJA193	POWER CORD
A1		RQT0012B	{E1, EG}			
(EK)			A2	△		
A1		RQT0012D	{EK}			
(EG)			A3		SJP2249-3	OUTPUT CORD
A1		RQT0012E	A4		XZB23X35C03	PROTECTION BAG
(E, EH)			A5		XZB26X17C03	PROTECTION BAG(CORDS)
			A6		RQCA0002	CAUTION <input type="checkbox"/>
			A7		RMRO024	STOPPER <input type="checkbox"/>
			A8		UM-4NE/2S	BATTERY

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RESISTORS AND CAPACITORS

Notes : * Important safety notice :

Components identified by Δ mark have special characteristics important for safety. When replacing any of these components use only manufacturer's specified parts.

* Bracketed indications in Ref. No. columns specify the area. (Refer to the first page for area.) Parts without these indications can be used for all areas.

Numbering System For Resistors

Example:

ERD	25	F	J	102
Type	Wattage (1/4W)	Shape	Tolerance	Value (1K Ω)
ERX	2	AN	J	471
Type	Wattage (2W)	Shape	Tolerance	Value (470 Ω)

Numbering System For Capacitors

Example:

ECKD	1H	102	Z	F
Type	Voltage (50V)	Value (0.001 μ F)	Tolerance	Unique
ECEA	50	M		330
Type	Voltage (50V)	Characteristics		Value (33 μ F)

- Capacity values are in microfarads (μ F) unless specified otherwise, P=Pico-farads (pF) F=Farads (F).
- Resistance values are in ohms (Ω), unless specified otherwise, 1K = 1,000 Ω , 1M = 1,000k Ω .

Resistor Type	Wattage		Tolerance
ERD : Carbon	10 : 1/8W	12 : 1/2W	J : \pm 5%
ERG : Metal Oxide	14 : 1/4W	25 : 1/4W	F : \pm 1%
ERQ : Fuse Type Metal	1A : 1W	18 : 1/8W	G : \pm 2%
ERX : Metal Film	S2 : 1/4W	S1 : 1/2W	J : \pm 5%
ERD L : Carbon (chip)	2F : 1/4W	50 : 1/2W	K : \pm 10%
ERD K : Metal Film (chip)	2A : 2W	3A : 3W	M : \pm 20%
ERC : Solid	6G : 1/10W	8G : 1/8W	
ERF : Incombustible Box-Shaped			
ERM : Wire-Wound			
RRJ : Chip Resistor			
ERJ : Chip Resistor			

Capacitor Type	Voltage		Tolerance
ECE : Electrolytic	0J : 6.3V	1A : 10V	K : \pm 10%
ECCD : Ceramic	1C : 16V	1E : 25V	M : \pm 20%
ECKD : Ceramic Capacitor	1H : 50V	1V : 35V	Z : +80% -20%
EQM : Polyester	50 : 50V	05 : 50V	J : \pm 5%
ECQP : Polypropylene	2H : 500V	2A : 100V	G : \pm 2%
EGG : Ceramic	1 : 100V	1J : 63V	F : \pm 1%
ECEA N : Non Polar Electrolytic	KC : 400V AC		C : \pm 0.25pF
QCU : Ceramic (Chip Type)	KC : 125V AC (UL)		D : \pm 0.5pF
ECUX : Ceramic (Chip Type)			
ECF : Semiconductor			
EECW : Liquid electrolyte double layer capacitor			

Ref. No.	Part No.	Value.	Ref. No.	Part No.	Value.	Ref. No.	Part No.	Value.
RESISTORS(VALUE.WATTAGE)			R418	ERDS2T J472	4.7K 1/4	R815	ERDS2T J474	470K 1/4
R11	ERDS2T J331	330 1/4	R419	ERDS2T J472	4.7K 1/4	R816	ERDS2T J474	470K 1/4
R12	ERDS2T J101	100 1/4	R420	ERDS2T J472	4.7K 1/4	R817	ERDS2T J104	100K 1/4
R13	ERDS2T J103	10K 1/4	R421	ERDS2T J104	100K 1/4	R818	ERDS2T J104	100K 1/4
R14	ERDS2T J103	10K 1/4	R422	ERDS2T J472	4.7K 1/4	R819	ERDS2T J471	470 1/4
R15	ERDS2T J101	100 1/4	R431	ERDS2T J473	47K 1/4	R820	ERDS2T J471	470 1/4
R16	ERDS2T J103	10K 1/4	R432	ERDS2T J473	47K 1/4	R821	ERDS2T J473	47K 1/4
R17	ERDS2T J103	10K 1/4	R433	ERDS2T J913T	91K 1/4	R822	ERDS2T J473	47K 1/4
R18	ERDS2T J471	470 1/4	R434	ERDS2T J104	100K 1/4	R823	ERDS2T J331	330 1/4
R19	ERDS2T J331	330 1/4	R435	ERDS2T J913T	91K 1/4	R824	ERDS2T J331	330 1/4
R20	ERDS2T J101	100 1/4	R436	ERDS2T J104	100K 1/4	R825	ERDS2T J102	1K 1/4
R21	ERDS2T J103	10K 1/4	R437	ERDS2T J101	100 1/4	R826	ERDS2T J102	1K 1/4
R22	ERDS2T J103	10K 1/4	R451	ERDS2T J104	100K 1/4	R831	ERDS2T J331	330 1/4
R23	ERDS2T J102	1K 1/4	R452	ERDS2T J103	10K 1/4	R832	ERDS2T J331	330 1/4
R51	ERDS2T J102	1K 1/4	R501	ERDS2T J224	220K 1/4	R833	ERDS2T J472	4.7K 1/4
R52	ERDS2T J333	33K 1/4	R502	ERDS2T J562	5.6K 1/4	R834	ERDS2T J561	560 1/4
R53	ERDS2T J153	15K 1/4	R503	ERDS2T J474	470K 1/4	R835	ERDS2T J472	4.7K 1/4
R54	ERDS2T J101	100 1/4	R504	ERDS2T J101	100 1/4	R836	ERDS2T J472	4.7K 1/4
R55	ERDS2T J472	4.7K 1/4	R506	ERDS2T J333	33K 1/4	R837	ERDS2T J100	10 1/4
R301	ERDS2T J182	1.8K 1/4	R507	ERDS2T J102	1K 1/4	R838	ERDS2T J100	10 1/4
R311	ERDS2T J822	8.2K 1/4	R601	ERDS2T J472	4.7K 1/4	R839	ERDS2T J182	1.8K 1/4
R312	ERDS2T J331	330 1/4	R602	ERDS2T J472	4.7K 1/4	R840	ERDS2T J101	100 1/4
R351	ERDS2T J103	10K 1/4	R603	ERDS2T J472	4.7K 1/4	R851	ERDS2T J102	1K 1/4
R352	ERDS2T J104	100K 1/4	R604	ERDS2T J472	4.7K 1/4	R852	ERDS2T J471	470 1/4
R353	ERDS2T J123	12K 1/4	R605	ERDS2T J472	4.7K 1/4	R853	ERDS2T J102	1K 1/4
R354	ERDS2T J104	100K 1/4	R606	ERDS2T J472	4.7K 1/4	R854	ERDS2T J472	4.7K 1/4
R355	ERDS2T J333	33K 1/4	R607	ERDS2T J105	1M 1/4	R855	ERDS2T J183	18K 1/4
R356	ERDS2T J333	33K 1/4	R608	ERDS2T J8R2	8.2 1/4	R856	ERDS2T J333	33K 1/4
R357 Δ	ERD25F J6R8	6.8 1/4	R801	ERDS2T J103	10K 1/4	R857	ERDS2T J102	1K 1/4
R358	ERDS2T J101	100 1/4	R802	ERDS2T J103	10K 1/4	R858	ERDS2T J102	1K 1/4
R359	ERDS2T J101	100 1/4	R803	ERDS2T J103	10K 1/4	R859	ERDS2T J105	1M 1/4
R408	ERDS2T J472	4.7K 1/4	R804	ERDS2T J103	10K 1/4	R860	ERDS2T J105	1M 1/4
R409	ERDS2T J472	4.7K 1/4	R805	ERDS2T J273	27K 1/4	R861	ERDS2T J472	4.7K 1/4
R410	ERDS2T J472	4.7K 1/4	R806	ERDS2T J273	27K 1/4	R871	ERDS2T J103	10K 1/4
R411	ERDS2T J472	4.7K 1/4	R807	ERDS2T J273	27K 1/4	R872	ERDS2T J103	10K 1/4
R412	ERDS2T J472	4.7K 1/4	R808	ERDS2T J273	27K 1/4	R873	ERDS2T J123	12K 1/4
R413	ERDS2T J472	4.7K 1/4	R809	ERDS2T J473	47K 1/4	R874	ERDS2T J123	12K 1/4
R414	ERDS2T J472	4.7K 1/4	R810	ERDS2T J473	47K 1/4	R875	ERDS2T J104	100K 1/4
R415	ERDS2T J472	4.7K 1/4	R811	ERDS2T J473	47K 1/4	R876	ERDS2T J104	100K 1/4
R416	ERDS2T J472	4.7K 1/4	R812	ERDS2T J473	47K 1/4	R885	ERDS2T J222	2.2K 1/4
R417	ERDS2T J472	4.7K 1/4	R813	ERDS2T J221	220 1/4	R886	ERDS2T J222	2.2K 1/4
			R814	ERDS2T J221	220 1/4	R887	ERDS2T J680	68 1/4

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Ref. No.	Part No.	Value.	Ref. No.	Part No.	Value.	Ref. No.	Part No.	Value.
R888	ERDS2TJ680	68 1/4	C403	ECFR1E104ZF5	0.1 25	C809	ECEA1EKN3R3B	3.3 25
R889	ERDS2TJ470	47 1/4	C404	ECFR1E104ZF5	0.1 25	C810	ECEA1EKN3R3B	3.3 25
R890	ERDS2TJ470	47 1/4	C405	ECFR1E104ZF5	0.1 25	C811	ECBT1H102KB5	0.001 50
R891	ERDS2TJ102	1K 1/4	C406	ECBT1H102KB5	0.001 50	C812	ECBT1H102KB5	0.001 50
R892	ERDS2TJ102	1K 1/4	C407	ECBT1H102KB5	0.001 50	C813	ECEA1CKN220	22 16
R893	ERDS2TJ104	100K 1/4	C451	ECFR1E104ZF5	0.1 25	C814	ECEA1CKN220	22 16
R894	ERDS2TJ104	100K 1/4	C452	ECEA0JK470	47 6.3	C815	ECBT1H102KB5	0.001 50
CAPACITORS(VALUE,VOLTAGE)								
C1	ECKDKC103PF2	0.01 125	C453	ECFR1H223KD	0.022 50	C816	ECBT1H102KB5	0.001 50
C2	ECFTD103KXL	0.01 25	C501	ECQM1H104KF	0.1 50	C831	ECFR1E104ZF5	0.1 25
C3	ECFTD103KXL	0.01 25	C502	ECFR1E104ZF5	0.1 25	C832	ECEA0JS331	330 6.3
C10	ECFR1E104ZF5	0.1 25	C503	ECEA1CKS100	10 16	C833	ECFR1E104ZF5	0.1 25
C11	ECEA1CU222	2200 16	C504	ECFR1E104ZF5	0.1 25	C834	ECEA0JS331	330 6.3
C12	ECEA1CU222	2200 16	C505	ECFR1E104ZF5	0.1 25	C835	ECFR1E104ZF5	0.1 25
C13	ECFR1E104ZF5	0.1 25	C506	ECBT1H150JC	15P 50	C836	ECFR1E104ZF5	0.1 25
C14	ECEA0JK470	47 6.3	C507	ECBT1H102KB5	0.001 50	C837	ECFR1E104ZF5	0.1 25
C15	ECEA0JK470	47 6.3	C508	ECEA1CKN100B	10 16	C838	ECBT1H5R6K5	5.6P 50
C16	ECEA1AK101	100 10	C509	ECBT1H470J5	47P 50	C840	ECBT1H5R6K5	5.6P 50
C17	ECEA1AK101	100 10	C510	ECQM1H104KF	0.1 50	C841	ECEA0JK101	100 6.3
C18	ECEA1CU221	220 16	C511	ECEA1HKR47	0.47 50	C842	ECFR1E104ZF5	0.1 25
C19	ECEA1CU331	330 16	C512	ECBA1H681KB5	680P 50	C843	ECFR1E104ZF5	0.1 25
C20	ECEA1CU331	330 16	C513	ECBA1H681KB5	680P 50	C844	ECBT1H270J5	27P 50
C21	ECEA1VU101	100 35	C514	ECQV1H474J23	0.47 50	C845	ECFR1E104ZF5	0.1 25
C22	ECEA1VU101	100 35	C515	ECQM1H153KF	0.015 50	C851	ECEA0JU471	470 6.3
C51	ECEA1HKNO10B	1 50	C516	ECEA1VSN2R21	2.2 35	C852	ECEA1CKN100B	10 16
C301	ECBT1C103NS5	0.01 16	C518	ECBT1H561KB5	560P 50	C871	ECEA1EKN3R3B	3.3 25
C302	ECBT1C103NS5	0.01 16	C601	ECEA0JK470	47 6.3	C872	ECEA1EKN3R3B	3.3 25
C303	ECFR1E104ZF5	0.1 25	C602	ECFR1E104ZF5	0.1 25	C873	ECQM1H103KF	0.01 50
C304	ECFR1E104ZF5	0.1 25	C603	ECFR1E104ZF5	0.1 25	C874	ECQM1H103KF	0.01 50
C306	ECBT1H270J5	27P 50	C801	ECBT1H680J5	68P 50	C875	ECBT1C103NS5	0.01 16
C351	ECBT1H102KB5	0.001 50	C802	ECBT1H680J5	68P 50	C876	ECBT1C103NS5	0.01 16
C352	ECBT1H102KB5	0.001 50	C803	ECBT1H680J5	68P 50	C878	ECBT1C103NS5	0.01 16
C353	ECBT1H102KB5	0.001 50	C804	ECBT1H680J5	68P 50	C879	ECBT1C103NS5	0.01 16
C401	ECFR1E104ZF5	0.1 25	C905	ECBT1H470J5	47P 50	C881	ECEA1CN101SE	100 16
C402	ECEA0JK470	47 6.3	C806	ECBT1H470J5	47P 50	C882	ECEA1CN101SE	100 16
			C807	ECBT1H470J5	47P 50	C891	ECEA1AK101	100 10
			C808	ECBT1H470J5	47P 50	C892	ECEA1AK101	100 10
						C893	ECEA1CKN220	22 16
						C894	ECEA1CKN220	22 16

Ref. No.	Part No.	Value.	Ref. No.	Part No.	Value.	Ref. No.	Part No.	Value.	
RESISTORS									
R3501	4822 116 52426	4K7 1/2	R3552	4822 111 90171	820 1/8	C2514	4822 121 51252	0.47 100	
R3502	4822 111 90214	100K 1/8	R3554	4822 116 90421	2K 1/8	C2515	4822 122 31746	0.001 50	
R3503	4822 111 30499	4E7 1/3	R3555	4822 111 90251	22K 1/8	C2520	4822 122 31965	220P 60	
R3504	4822 111 30499	4E7 1/3	R3560	4822 111 91494	11K 1/8	C2521	4822 124 22027	47 25	
R3505	4822 111 90253	12K 1/8	R3561	4822 116 90417	150K 1/8	C2530	4822 121 51321	0.0082 60	
R3506	5322 111 90091	100 1/8	R3562	4822 116 52845	120K 3/5	C2531	4822 121 51321	0.0082 60	
R3507	4822 111 90248	2K2 1/8	R3563	4822 111 90573	56K 1/8	C2532	4822 124 40272	33 10	
R3508	4822 111 90512	24K 1/8	R3564	4822 111 91495	160K 1/8	C2534	5322 121 42661	0.33 60	
R3509	4822 111 90572	5K6 1/8	R3565	4822 116 52354	27 1/2	C2535	5322 122 31848	0.033 60	
R3510	4822 111 90249	10K 1/8	R3566	4822 111 90186	22 1/8	C2536	5322 122 31848	0.033 60	
R3521	4822 111 90178	220 1/8	R3567	4822 116 52478	82K 1/2	C2537	4822 121 42245	0.22 60	
R3522	4822 111 30515	18 1/3	R3568	4822 111 90161	470K 1/8	C2538	4822 121 42245	0.22 60	
R3523	4822 111 30511	12 1/3	R3801	4822 111 90163	0E	C2540	4822 124 41583	0.68 50	
R3524	5322 111 90091	100 1/8	R3802	4822 111 90163	0E	C2541	4822 122 33147	0.022 50	
R3530	4822 111 90543	47K 1/8	CAPACITORS				C2542	4822 122 33147	0.022 50
R3531	4822 111 90344	15K 1/8	C2501	4822 122 33147	0.022 50	C2543	4822 124 40196	220 10	
R3533	5322 111 90268	5K1 1/8	C2502	4822 124 22027	47 10	C2544	4822 124 40196	220µ 10	
R3534	4822 111 90197	220K 1/8	C2503	4822 122 33147	0.022 50	C2545	4822 122 33104	0.1 60	
R3535	4822 116 53081	12K 3/5	C2504	4822 122 31727	470P 63	C2546	4822 122 33104	0.1 60	
R3540	4822 116 52858	4E7 3/5	C2505	4822 124 22027	47 25	C2547	4822 122 33147	0.022 50	
R3541	4822 111 90544	6K8 1/8	C2506	4822 122 33104	100NF 63	C2550	4822 121 51049	0.015 60	
R3542	4822 111 90124	82 1/8	C2507	4822 122 31644	0.0022 63	C2551	4822 121 51225	18NF 60	
R3543	4822 111 90544	6K8 1/8	C2508	5322 121 42491	0.047 100	C2560	4822 122 31784	0.0047 50	
R3544	4822 111 30483	1 1/3	C2509	4822 122 31772	47P 50	C2561	4822 121 51252	0.047 10	
R3545	4822 111 30483	1 1/3	C2510	4822 122 32442	0.01 50	C2562	5322 121 42661	0.033 60	
R3550	4822 111 90248	2K2 1/8	C2511	4822 122 31746	0.001 50	C2563	4822 122 33104	100NF 60	
R3551	4822 116 90417	15K 1/8	C2513	4822 121 42245	0.22 63	C2565	4822 122 32808	0.0012 50	

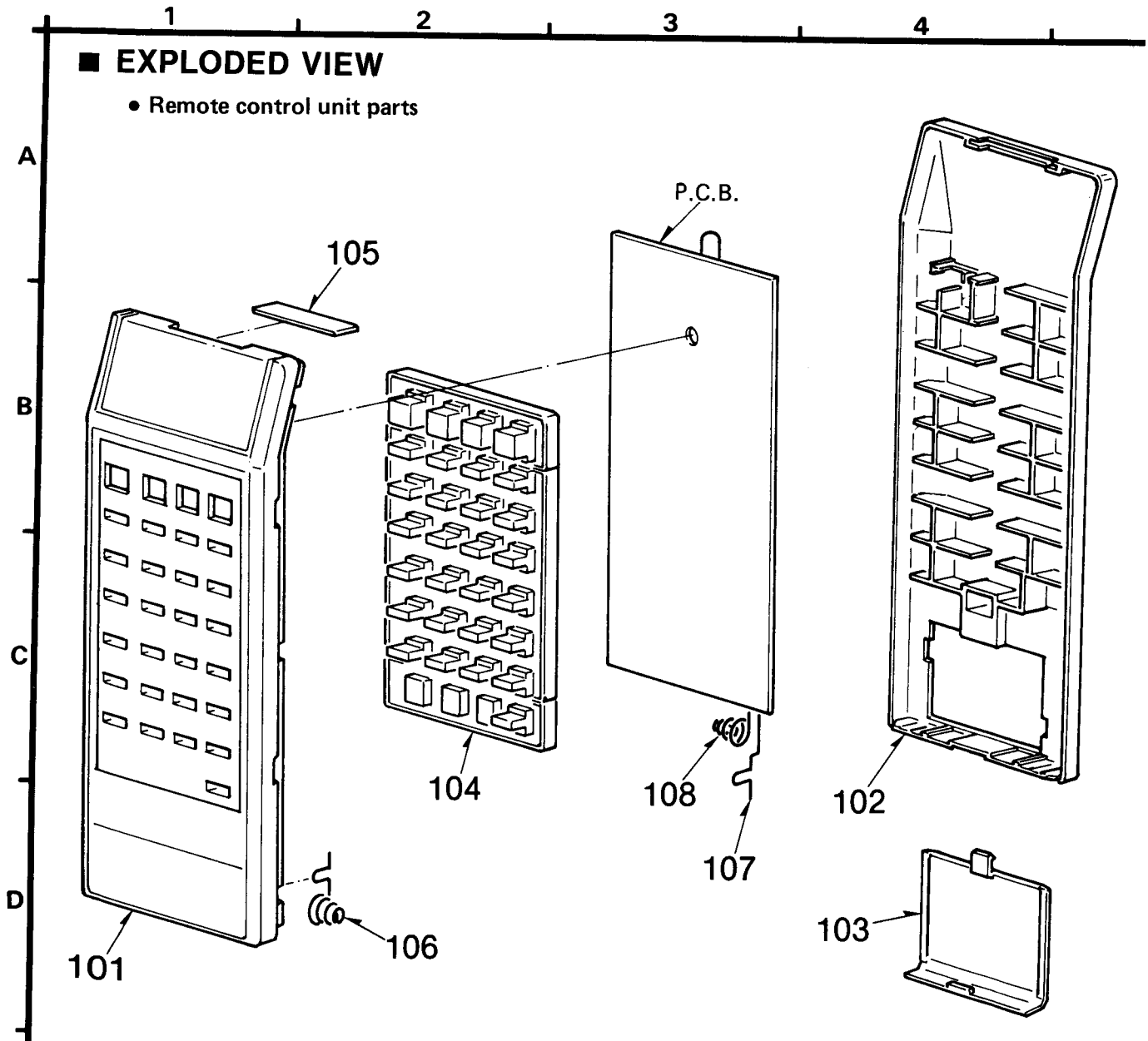
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REPLACEMENT PARTS LIST

Ref. No.	Part No.	Description	Ref. No.	Part No.	Description
REMOTE CONTROL			MECHANISM PARTS		
INTEGRATED CIRCUITS			C2	ECUV1H471KCG	CAPACITOR
IC1	MN6014HS	I.C.	C3	ECEA0GK101	ELECTROLYT I.C. 100 μ F, 4V
TRANSISTORS			101	UR64VCS710	UPPER CABINET
Q1	2SC3265Y	TRANSISTOR	101	UR64VCS712	UPPER CABINET
DIODES			102	UR64CS803A	LOWER CABINET
D1	LN66-S	L.E.D.	102	UR64CS803B	LOWER CABINET
OSCILLATOR			103	UR64EC804	BATTERY COVER
X1	CSB420PB6	OSCILLATOR	103	UR64EC804A	BATTERY COVER
RESISTORS			104	UR64CT805K	BUTTON
R1	ERD25TLJ1R0U	RESISTOR	105	UR52SB327	PLATE(SMOKE)
CAPACITORS			106	UR64TD374	BATTERY TERMINAL(COMMON)
C1	ECUV1H471KCG	CAPACITOR	107	UR64TD808	TERMINAL (+)
			108	UR64TD809	TERMINAL (-)
			REMOTE CONTROL ASS'Y		
			RC1	UR64766	REMOTE CONTROL
			RC1	UR64767	REMOTE CONTROL

EXPLODED VIEW

• Remote control unit parts

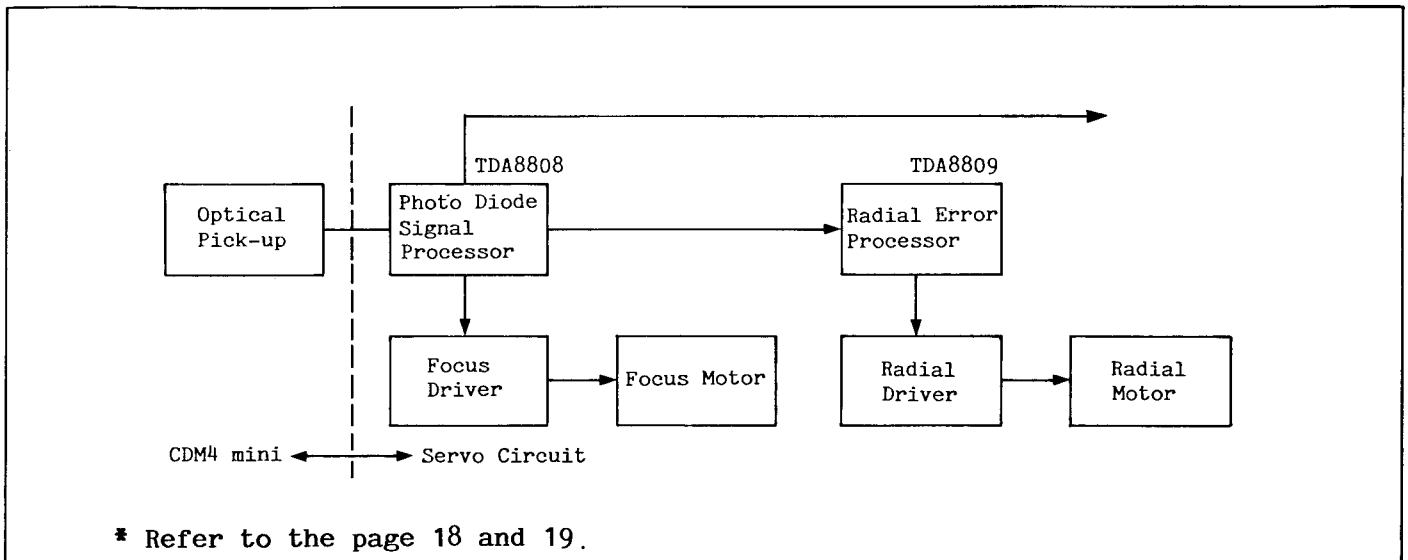


■ TECHNICAL INFORMATION

Explanation on the CDM4 mini system and servo circuit for service engineers

1. System Configuration

1-1. Block Diagram Rough



2. Rough Explanation of the Functions of the CDM4 mini System Part.

2-1. Servo electronics CDM4 mini system.

TDA8808: bipolar photo diode signal processor

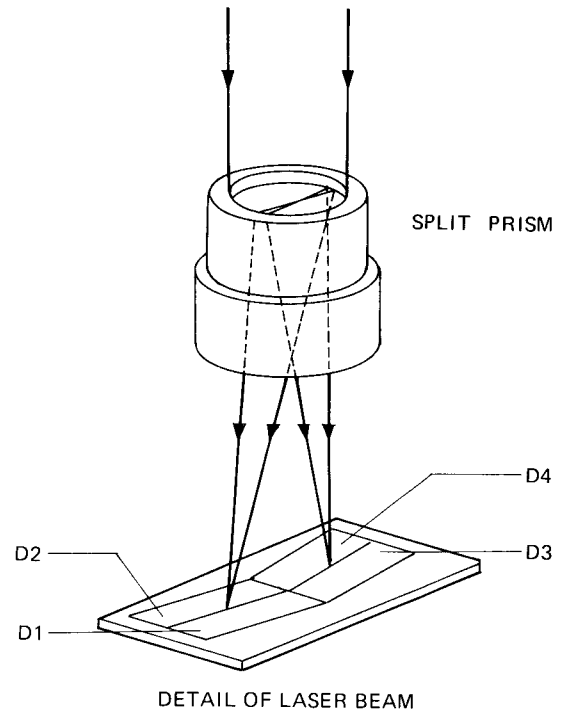
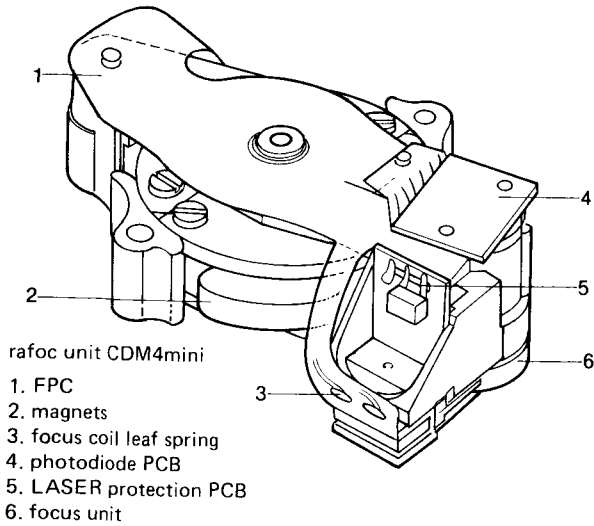
- Photodiode signal preamplifiers with separate h.f. and d.c. a.g.c. for optimum generation of the h.f. data signal and the focusing/tracking error signals.
- Tracking error signal amplifier.
- Focus error signal processor with focus normalizing and start-up circuit.
- Data equalizer.
h.f. level and track-loss detectors.
- Regulated supply for the reading laser diode.
- Low current consumption: 15mA (8V - 14V).

TDA8809: bipolar radial error processor

- Tracking error processor with automatic asymmetry control.
- a.g.c. circuitry with automatic start-up wobble generator.
- Tracking control for fast forward/reverse scan, search, repeat and pause functions.
- Low current consumption: 10mA (8V - 14V).

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2-2.CDM4 mini Optical Pick-up

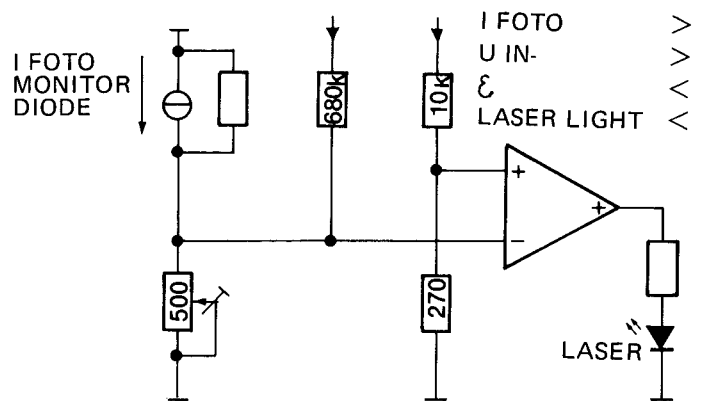
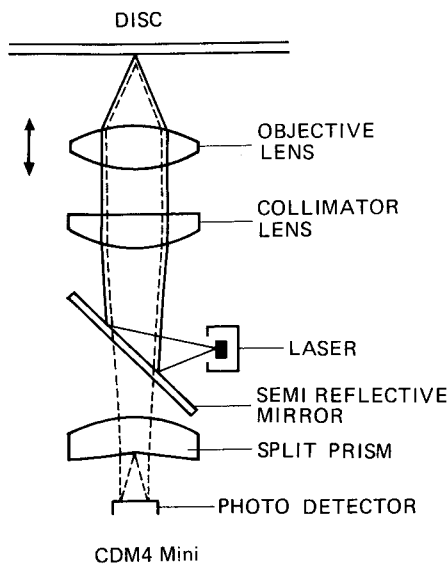


A feature of the CDM4 mini pick-up is its simple diffraction limited optics. The laser point source is focused on the information layer of the disc by two lenses: a spherical glass objective with a plastic aspherical skin, and a spherical glass collimator.

3.Detailed Explanation of the Functions in the Servo.

3-1-1 The LASER supply

The LASER supply circuit has a working principle like the basic schematic below.

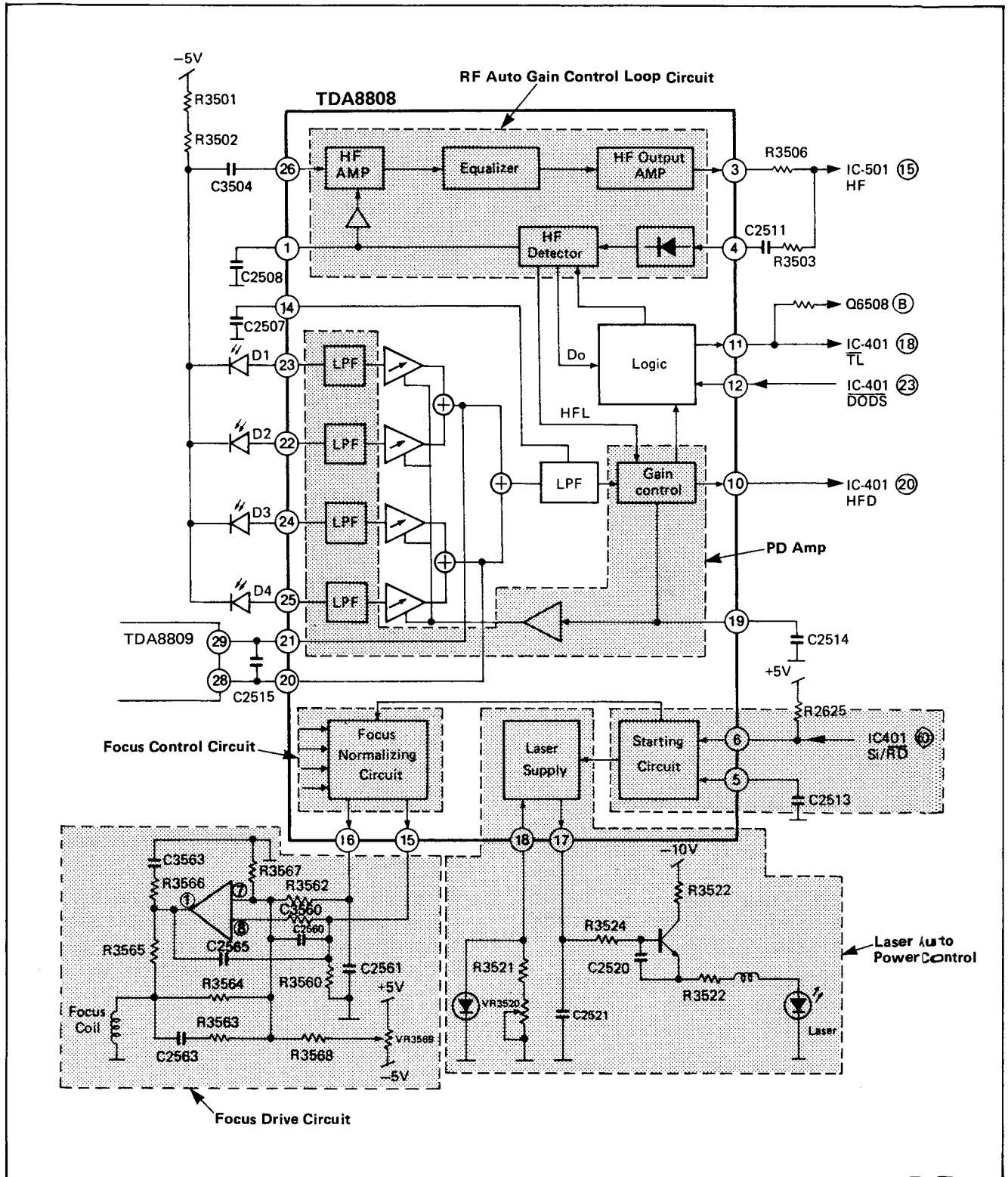


Furthermore, a semi-transparent mirror is used instead of a beam-splitter cube. Astigmatism introduced into the reflected beam by the mirror is corrected by a plastic component (the wedge) which also dissects the beam into the two halves from which the tracking and focus error signals are generated.

The LASER supply function integrated in the TDA8808 has as an extra an overload protection and a turn on transient protection. The LASER current is regulated by sensing the monitor photodiode current.

The threshold voltage of the LASER diode is approximately 1.75V and the sense voltage is approx. 200mV. It is not allowed to measure the sense voltage. A distortion at this end of the loop might cause severe damage to the LASER diode.

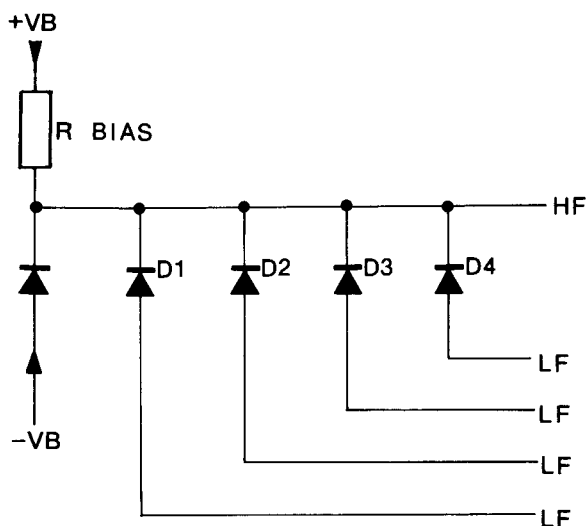
3-1-2 h.f. Signal handling, h.f. a.g.c. and data equalizer.



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The photodiodes used in the optical pick-up are connected in reverse direction. The photodiodes require a reverse bias of at least 5V. Bias current is set by R3501 and R3502. Supply noise and interference is kept away by R3501 and C2521. Coupling of the h.f. signal is performed by C2504, R3502 and C2521 also prevent h.f. coming into the supply.

The h.f. signal is effectively separated; from the I.f. signals by deriving the h.f. signal from the total substrate current of the four photodiodes and only the I.f. error signals from the individual anode currents. (see figure below).



The h.f. signal is effectively separated; from the I.f. signals by deriving the h.f. signal from the total substrate current of the four photodiodes and only the I.f. error signals from the individual anode currents. (see figure below).

The h.f. amplifier and a.g.c. circuit have to meet the correct transfer function.

The parameters of the transfer function are set by R3505, R3508, R3507, C2509, and C2508.

With internal girators the equalizer provides a 3rd order bessel filter for making the correct modular transfer function to compensate the optical pick-up system. The total IC bias current is set by the value of R3508 and decoupled by C2510. The girator circuits are set by the value of R3505.

The 3rd pole of the modular transfer function is made by the RC circuit containing R3507, C2511 and the capacitance of the h.f. wire from the CDM to the demodulator circuits.

3-1-3 The amplitude level detector

For the a.g.c. the amplitude of the h.f. signal is measured in a level detector by comparing a full-wave rectified and filtered version of the h.f. signal with an internal reference level. The level detector is not data path but parallel to the data path as in the CDM4 system. For fast response to drop-out the time constant of the high pass filter formed by R3507, C2511 and the detector input resistance has to be 10 sec. In the h.f. amplitude slicer the amplitude of the h.f. signal is sliced in 3 levels.

62% and below is HFD
12% and below is DO

The HFD output is used to switch the PLL on and off ensuring minimum locking loss while having a drop-out or during searching. This provides fast relocking afterwards.

Capacitor C2508 in combination with an internal switch provides a more constant gain control during drop-outs. The switch is operated by the HFD signal. This set-up restricts the h.f. signal and gives the loop a bandwidth of 50Hz.

When the pick-up is searching for a track the DODS (drop-out detection suppression) is activated to keep the h.f. signal level clamped on the normal

3-1-4 The photodiode current amplifiers and I.f. a.g.c.

The low pass filters for the photodiode inputs D1 - D4 are integrated in the photodiode signal processor.

The outputs for RE1 (RE1 = D1 + D2) and RE2 (RE2 = D3 + D4) are open collector outputs. In case an external pull-up has to be connected, it has to be connected to Vdd.

Normally the pull-up function is done by the inputs of the radial error processor.

The a.g.c. for the photodiode amplifiers keeps the d.c. sum current constant for all discs. The control loop has a LPF which is set by the value of C2507.

3-1-5 The track loss detector

During playback of a disc, the radial error signals keep the pick-up on track. In case of searching for a certain spot of music, the pick-up has to jump over the tracks. To maintain the control of the pick-up during a search operation, the microprocessor must be informed whether the pick-up is on track or off track. This function is performed by the track-loss detector.

Condition to be met for the track-loss:

- The gain controlled h.f. signal has to be between 12% and 62% of the nominal value (100% is set by the h.f. a.g.c.).
- Average light intensity of the I.f. signal is more than 120% of the nominal value (100% is set by the I.f.a.g.c.).

All values are set during track following.

Use of the TL signal:

1. Counting tracks when track jumping.
2. In case a TL is detected during normal playback the control microprocessor looks to the RE dig signal from the radial error processor and compensates the tracking error by means of the DAC signal.

Such a situation can only occur due to large knocks. This set up makes the system extremely tolerant for bumps.

(Formula $TL = FS > 120\%$ and $63\% > HFL > 12\%$).

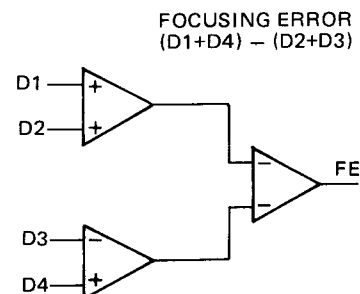
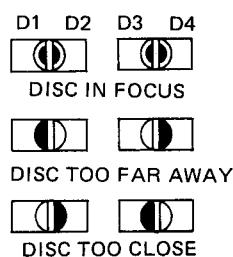
3-1-6 Focus processing

The CDM4 pick-up uses two focuault knife focus pick-ups to keep the reading spot focused on the data layer of the disc.

The TDA8808 has two focus outputs: FE and FE lag.

FE lag is the basic focus error signal. FE is a normalized version of FE to remove the effect of the unequal illumination of photodiode pairs D1, D2 and D3, D4 which occurs in case of tracking errors.

PRINCIPLE



$$FE \text{ lag} = (I_1 + I_4) - (I_2 + I_3) \cdot F_{\text{transfer}}$$

$$FE = \frac{I_1 - I_2}{I_1 + I_2} + \frac{I_4 - I_3}{I_4 + I_3} \cdot I \text{ control}$$

Since in focus $I_1 + I_2 \approx I_3 + I_4$

and $I_1 \approx I_2 \approx I_3 \approx I_4$ with a.g.c. on

$FE = FE \text{ lag on trace}$

Either one signal or both signals can be used to control the focus actuator. The FE lag signal gives the loop the correct gain and phase margin. The FE signal has an important role in the start-up procedure.

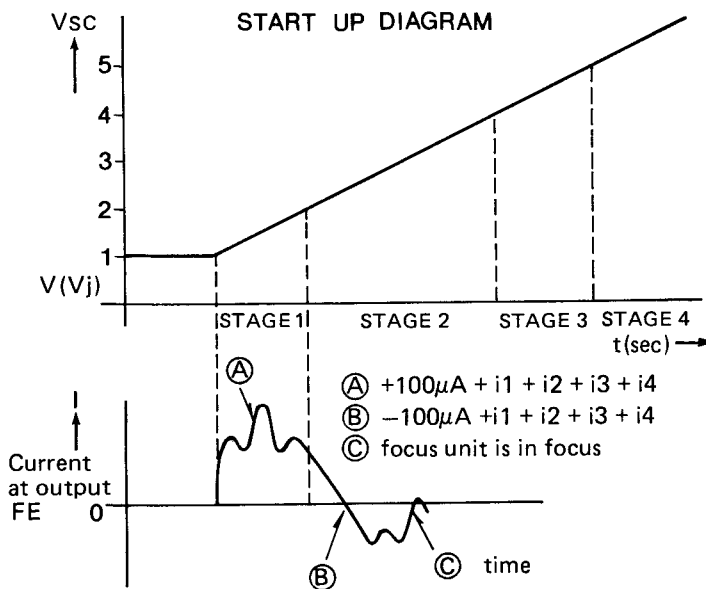
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3-1-7 Start-up procedure

The whole start-up procedure is sequenced by the voltage across capacitor C2513. Capacitor C2513 is charged by an internal current source of 1 μA nom.

Together with a starting capacitor of 270 nF, a starting-up time of approximately 0.3 sec. is reached.

The start-up procedure is divided in stages. The procedure is started by putting a logical low level at the Si/RD input.



STAGE 1

Laser turns on and the objective is moved to the center of the disc. The turntable motor is switched on. The FE pin delivers a positive going starting current of 100 μA + the sum current ($I_1 + I_2 + I_3 + I_4$).

Note the I.f. a.g.c. does not see light, so max amplification.

STAGE 2

The starting current is reversed, gradually causing the objective to go down. This process is stopped when the starting current equals the amplified sum current ($I_1 + I_2 + I_3 + I_4$).

Note: $I_1 + I_2 + I_3 + I_4 = 100\mu\text{A}$

STAGE 3

A smooth transition is made from the amplified sum current ($I_1 + I_2 + I_3 + I_4$) to the normalized focus error signal

$$\frac{I_1 - I_2}{I_1 + I_2} + \frac{I_4 - I_3}{I_4 + I_3}$$

The spot is now almost focused on the reflective layer of the disc.

The signals to the FE pin.

STAGE 1 (starting-up current + $I_1 + I_2 + I_3 + I_4$)

STAGE 2 (starting-up current + $I_1 + I_2 + I_3 + I_4$)

Stage 3 $\frac{I_1 - I_2}{I_1 + I_2} + \frac{I_4 - I_3}{I_4 + I_3}$

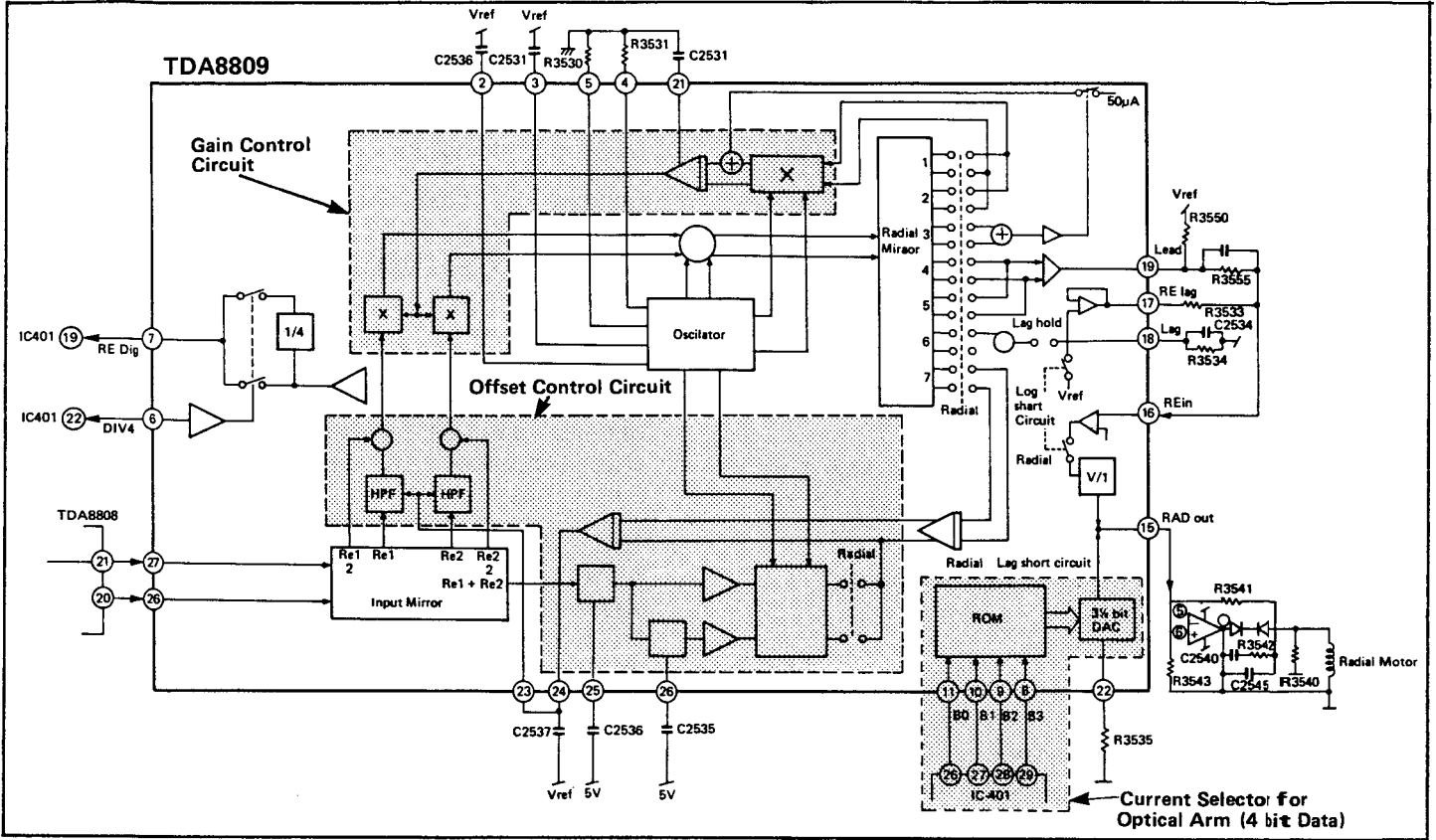
This should be almost 0

The signal to the FE lag pin.

Stage 4 $(I_1 + I_4) - (I_3 + I_2) +$ the normalized FE (like in stage 3)

The arm is kept at the inside recess by means of a binary word from the microprocessor at B0, B1, B2, and B3 of the radial error processor.

3-2 TDA8809 RADIAL ERROR PROCESSOR



- The resistor R3535 sets the amount of output current of the DAC signal.
- The other components setting the oscillator frequency are R3530, R3531, C2530, C2531.
- The inputs B0 - B3 are used to control the operations which have to be performed by the radial error processor.
- The DAC output is a current source output used to move the radial arm. The normal control is RE, but to jump beyond tracks and to compensate bumps the DAC signal is used.
- The RE lag output gives the loop a more powerful control. This output is only activated during normal playback.
- The RE dig is a 1-bit quantized version of the RE signal. The function of RE dig is not in all sets the same. Below is the function in a audio player explained. If a TL occurs the microprocessor looks to RE dig, the value of RE dig indicates whether the microprocessor has to compensate for a right or left bump via DAC. The maximum hold range of RE is normally

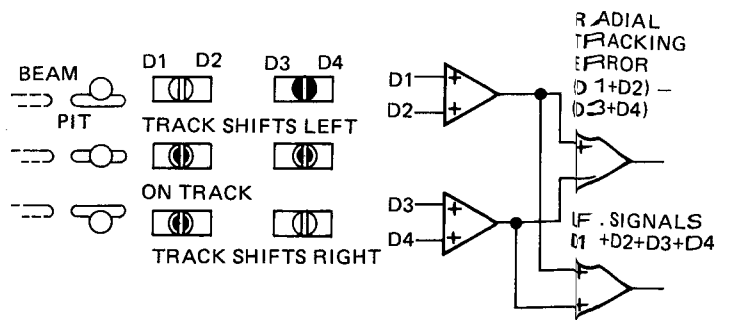
1/4 track pitch due to the sinusoidal form of the radial error signal. By means of TL and RE dig the hold range is extended to 3/4 track pitch.

- For high speed track crossing, four dived RE dig signal is given by pulling Div 4 input to low level.

3-2-1 Radial processing

The radial error processor has to process the basic input signals RE1 and RE2 and to generate the full radial error signal RE. The RE signal is the basic signal to keep the reading beam on track.

RE1 and RE2 are caused by more or less light of the laser falling onto diodes D1, D2 or D3, D4 (see figure below)



$RE1 = i1+i2$ and $RE2 = i3+i4$

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The processing formula produced by the radial error processor is $RE = k d (i_1+i_2+i_3+i_4) - k(i_1+i_2)$.

The factors k and d are detected from the loop itself by processing a low frequency wobble fault signal, which is injected into the loop. Injection of this causes a displacement of $\pm 0.05 \mu\text{m}$ of the arm.

The k is again factor and has to compensate:

- The tracking angle fault (varies between 90 degrees, smallest radius, and 45 degrees, largest radius).
- The pit geometry of disc (causing the amplitude of radial error signals to vary up to 100% from disc to disc).
- Deterioration of the laserdiode.

3-2-2 Automatic gain control (k-factor)

Detection of the desired value is done by detecting the phase angle of an injected wobble signal in the radial loop.

The closed loop transfer function has a phase shift of 135 degrees at 650 Hz (the wobble frequency).

To maintain the loop gain, the phase of the closed loop signal of RE is compared with a -45 degrees shifted signal derived from the 650 Hz wobble injection oscillator. If the gain is nominal the output of this phase comparator is zero. In other cases a positive or negative phase error signal is detected.

The detected signal is integrated by C2538. The value of this integrated signal sets the gain. This completes the set up for the k factor in TDA8809.

Note: The phase comparator is a linear multiplier which ensures that the a.g.c. loop is unaffected by spurious signals near the odd harmonics of the wobble frequency.

The open-loop gain control is clamped to an internal reference level during starting-up and searching.

3-2-3 Automatic asymmetry control (d-factor)

The radial error signal in a single-beam pick-up is derived from a comparison of the light intensity in each pupil half.

Errors in this signal can be caused by:

- Asymmetry in the far-field pattern of the laser.
- Light path to the disc.
- Imperfections in the beam splitter.

Errors can be corrected by bringing the d -factor in the total transfer function.

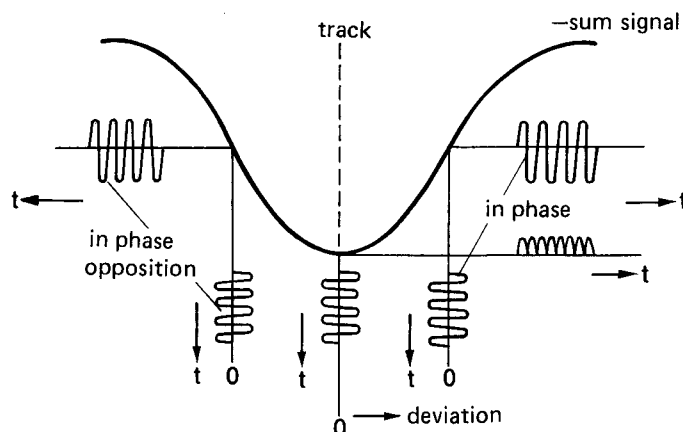
The wobble used to measure the k -factor is also used to measure the asymmetry. The signal causes the beam to wobble with an amplitude of $0.05 \mu\text{m}$. When the reading spot is to the right of the track the total amount of L.F. light ($i_{TOT} = i_1+i_2+i_3+i_4 = RE_1 + RE_2$) is in phase with the wobble in the radial error signal.

When the reading spot is to the left the I.f. sum signal ($RE_1 + RE_2$) is 180 degrees out of phase with the wobble.

The sum signal is in practice multiplied by a 90 degrees shifted version of the wobble signal. The multiplier is again linear giving the above-mentioned benefits. The loop has a filter which is set by the value of C2537.

The imperfections mentioned above make the beam follow the track at one of its edges. The wobble component in the RE signal for such a situation is visualised below.

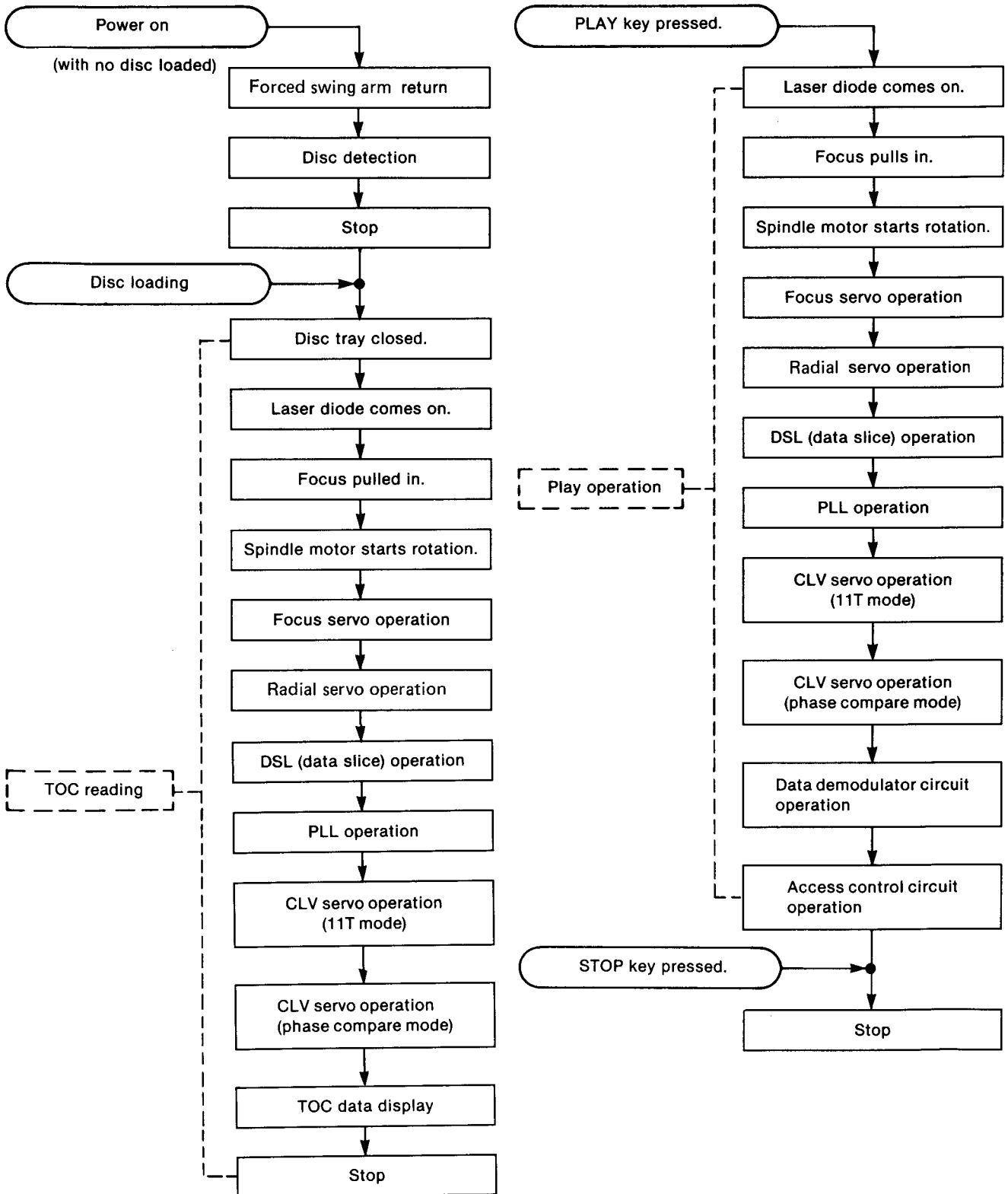
Note: C2537 is the capacitor which integrates the d -factor.



■ TROUBLESHOOTING GUIDE

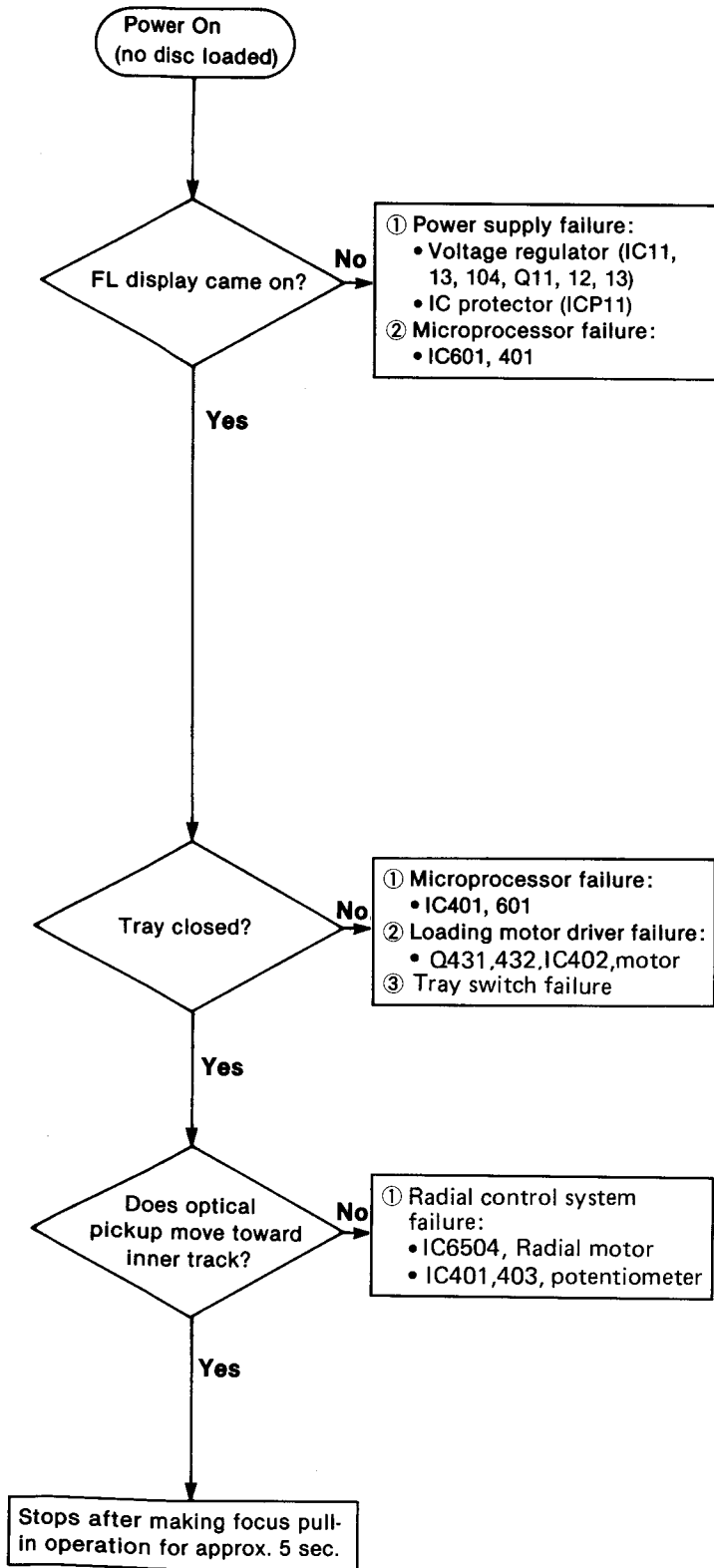
SL-P222A Operation Sequence Check Sheet

Play Operation Sequence



SL P222A

(Operation Sequence Just After Power On)



(TOC Read Operation-PLAY Operation)

